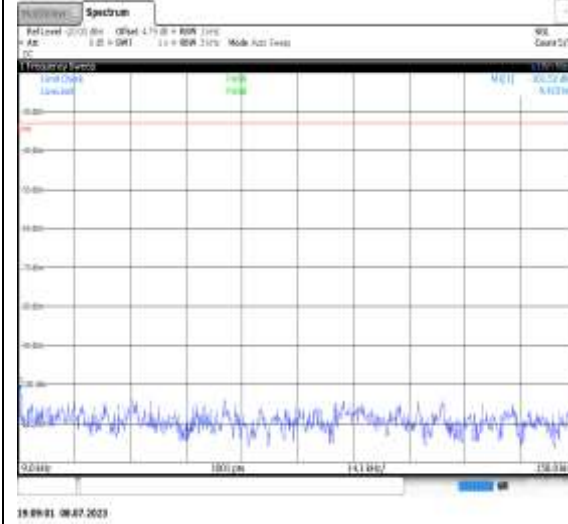
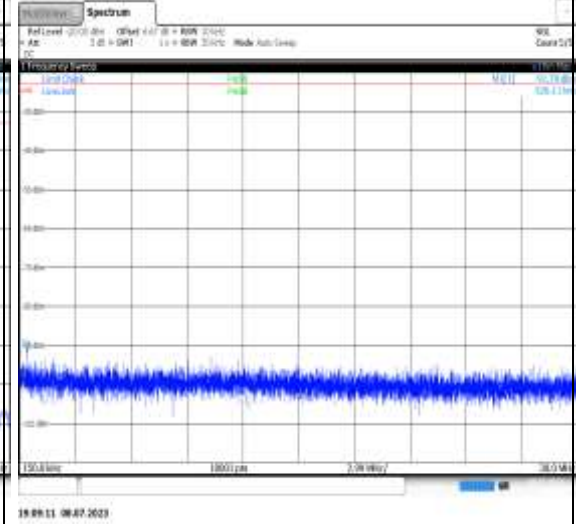


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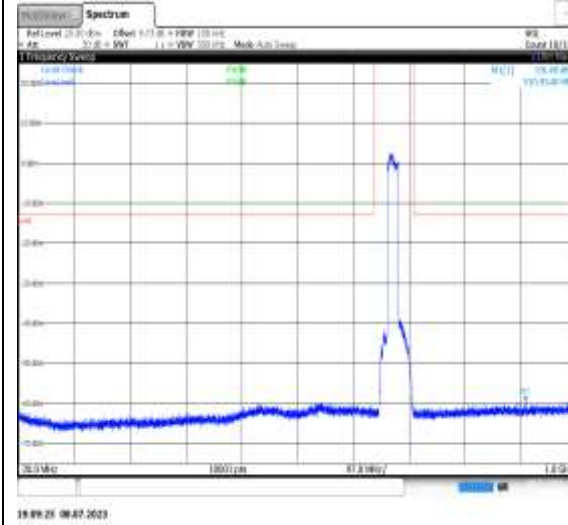
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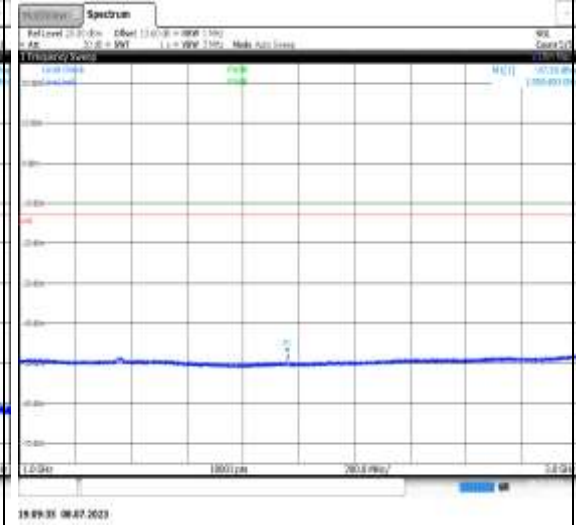
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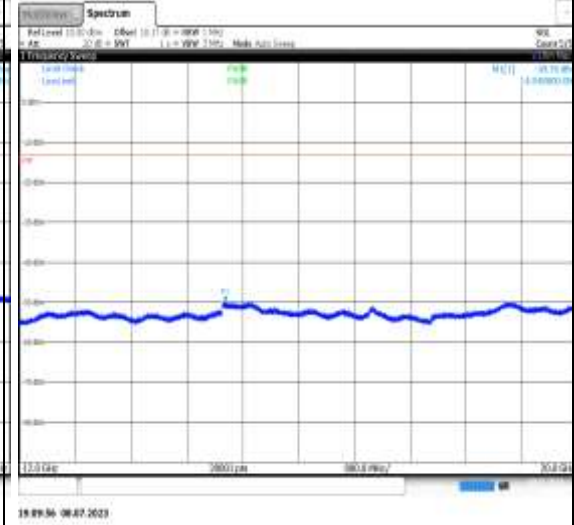
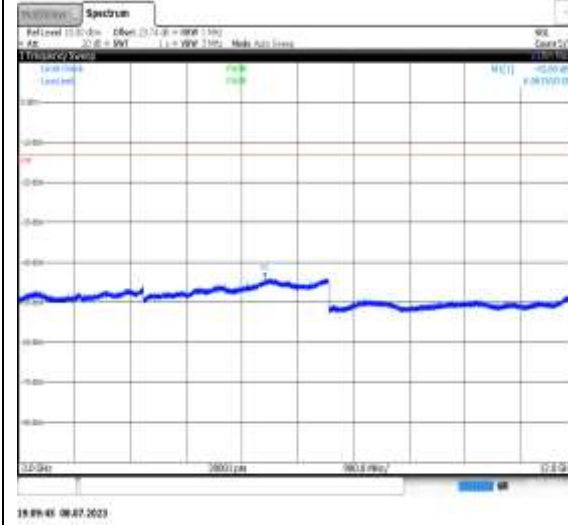


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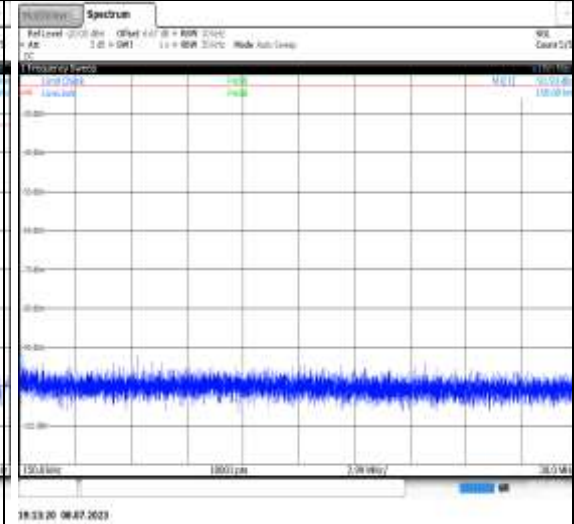
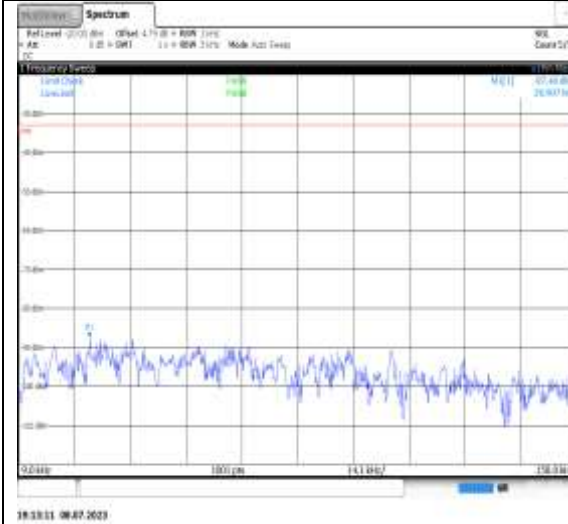
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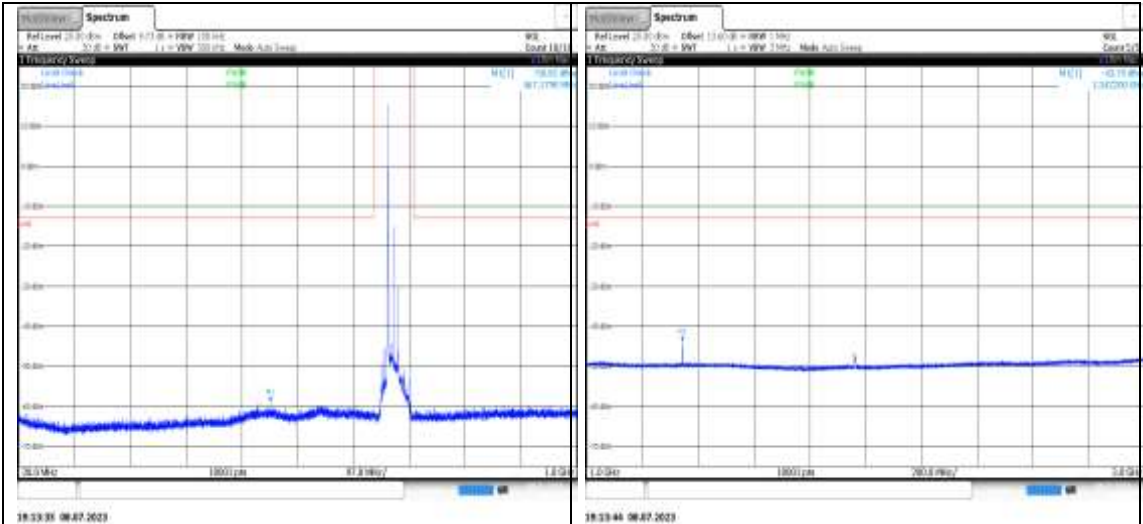
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1-DC_2A_n71A-PC3-15-5+20-M+M-24-NS_01-N/A-N/A-N/A-CP-64QAM-Outer_Full-106@0-12000-20000-14948.80--49.76--13-PASS



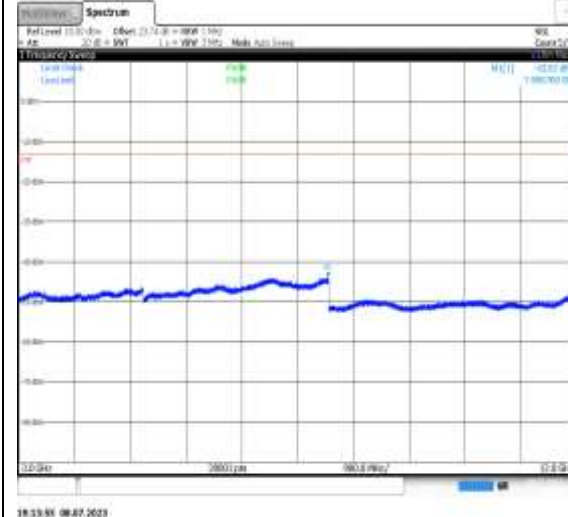
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1-DC_2A_n71A-PC3-15-5+20-M+M-25-NS_01-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-0.15-30-0.15--91.93--23-PASS

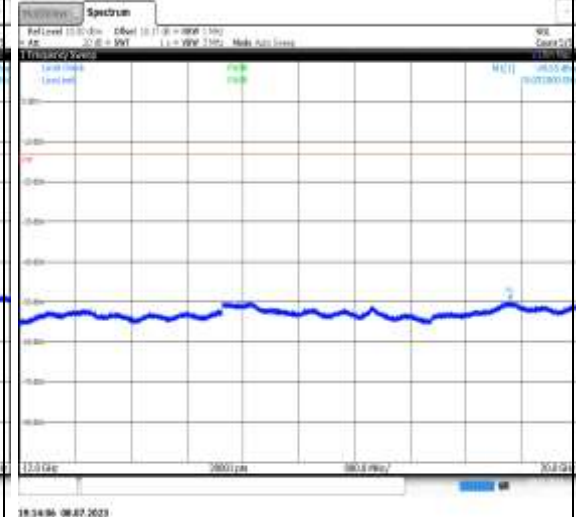


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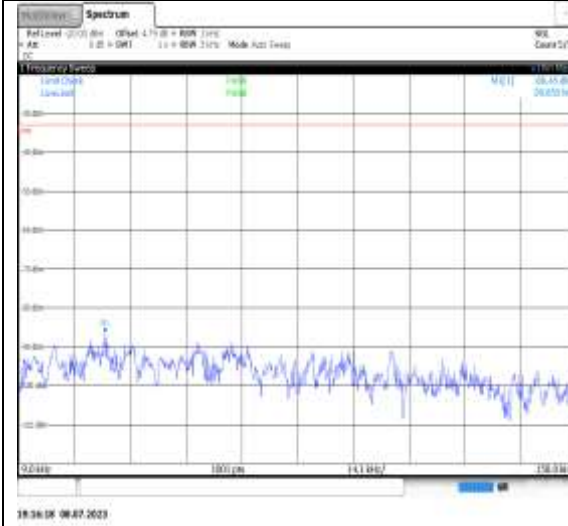
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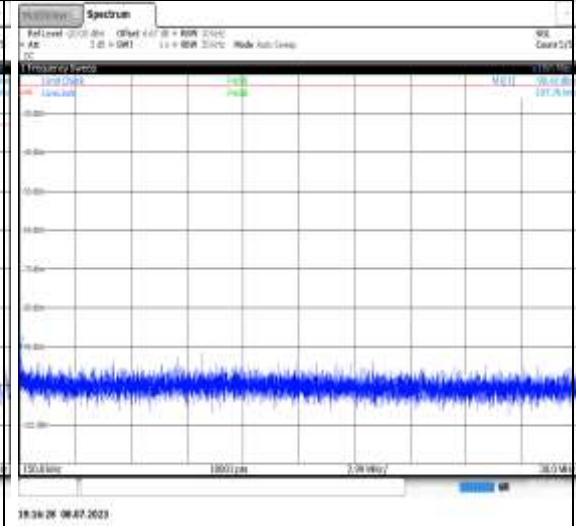
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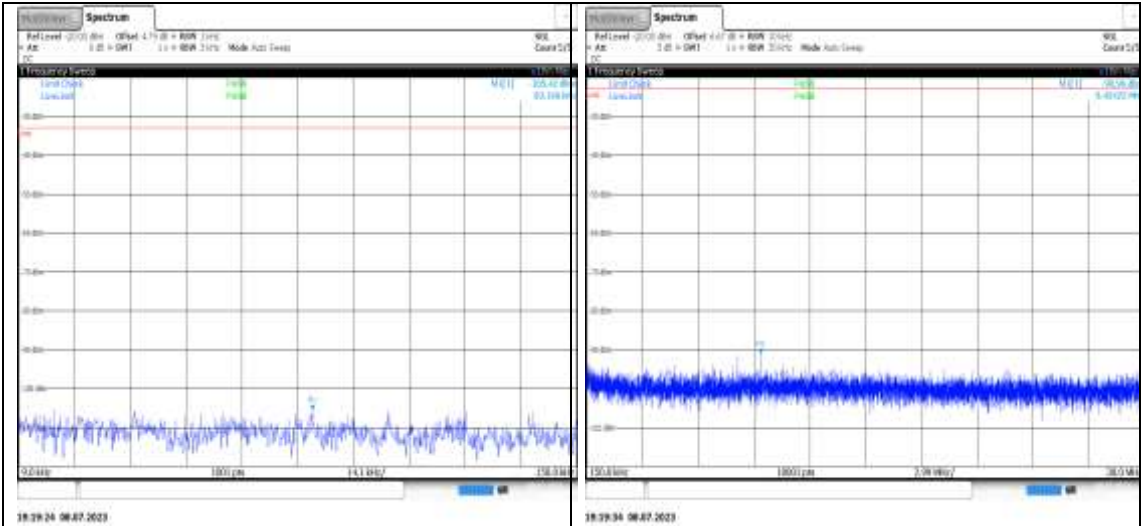


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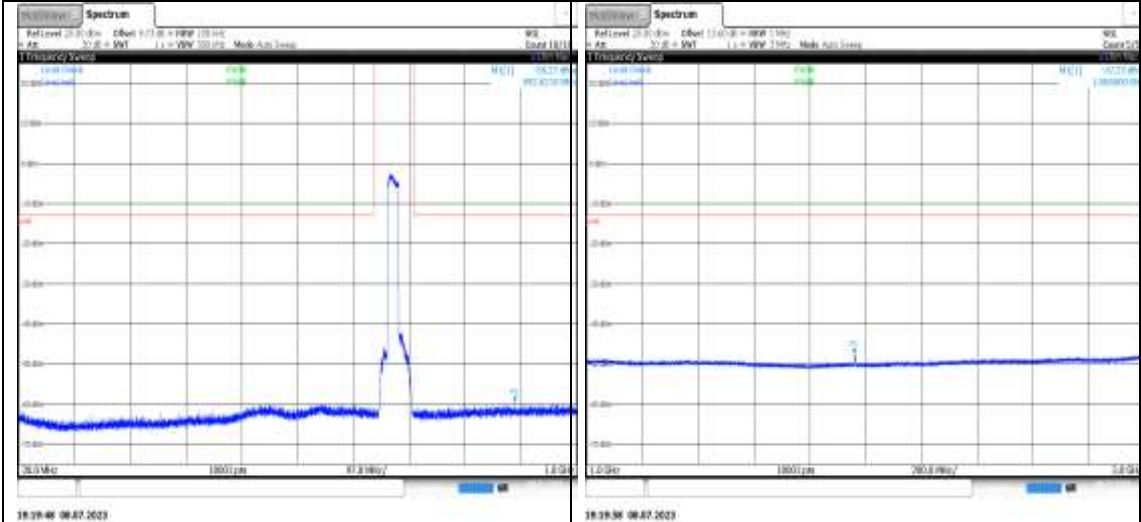
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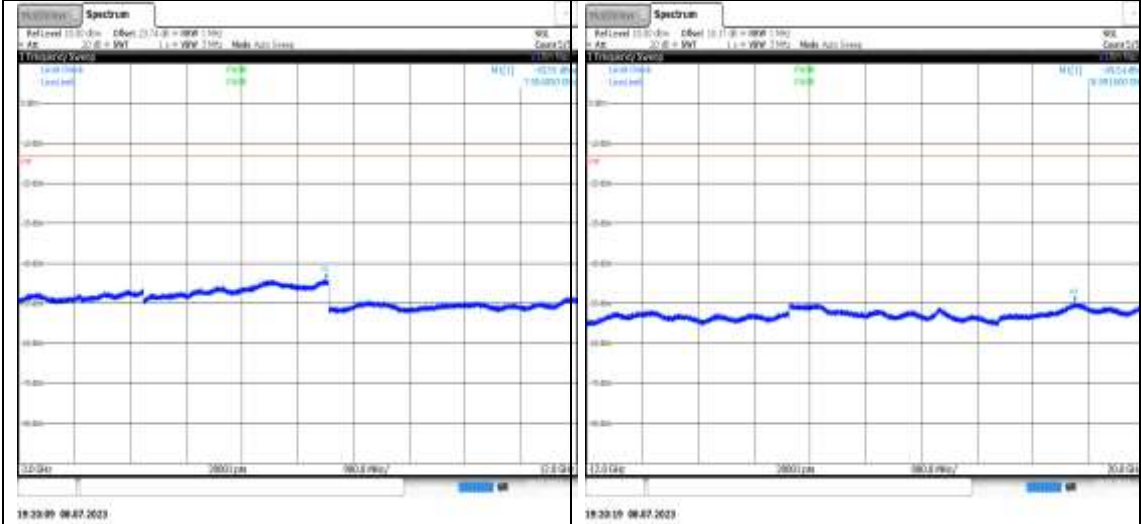
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33-PASS

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33-PASS



1-DC_2A_n71A-PC3-15-5+20-M+M-27-
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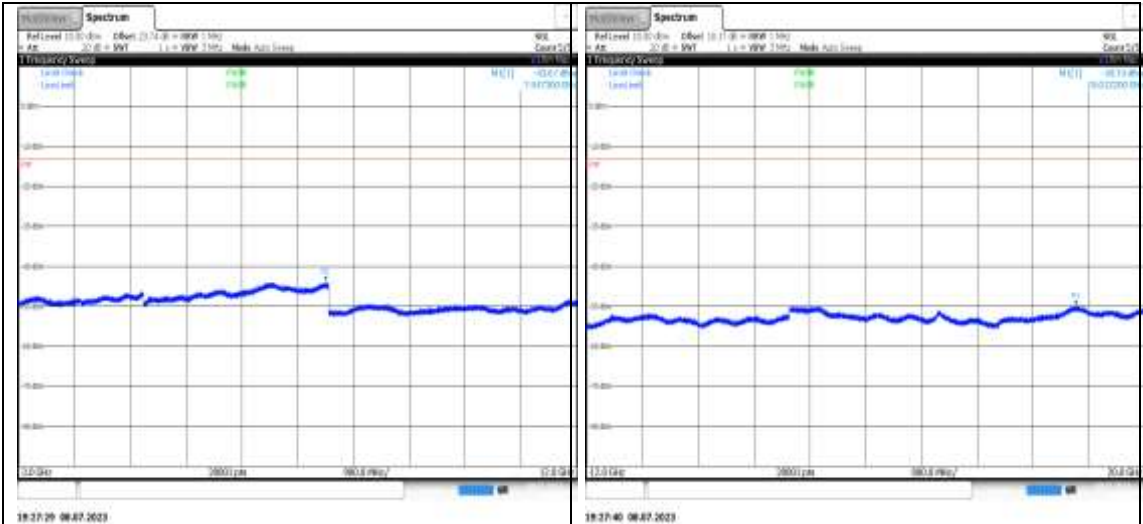
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18:20:09 08/07/2023

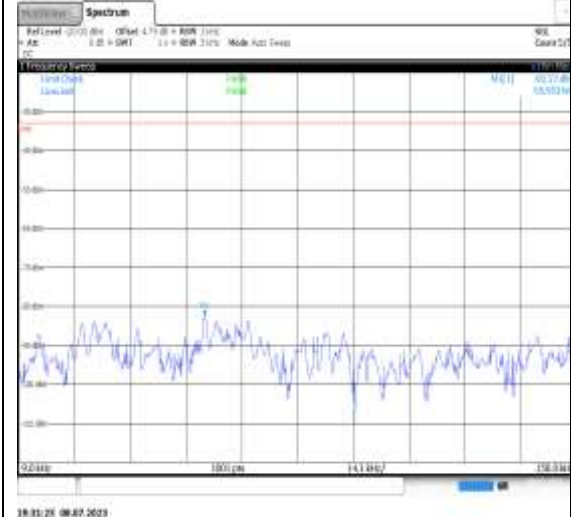
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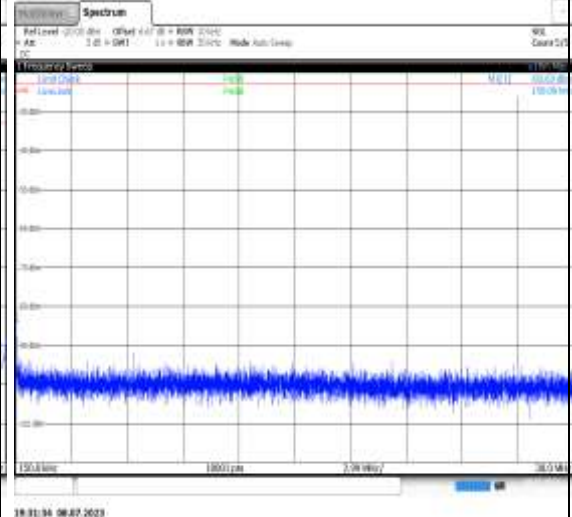


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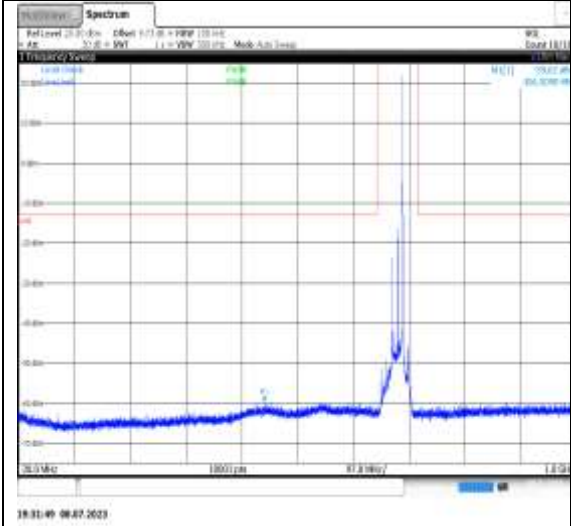
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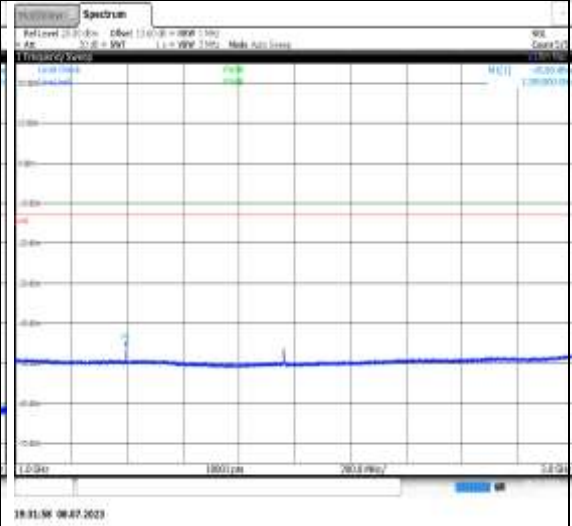
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1-DC_2A_n71A-PC3-15-5+20-M+H-2-NS_01-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_Right-1@105-0.15-30-0.15--83.63--23-PASS

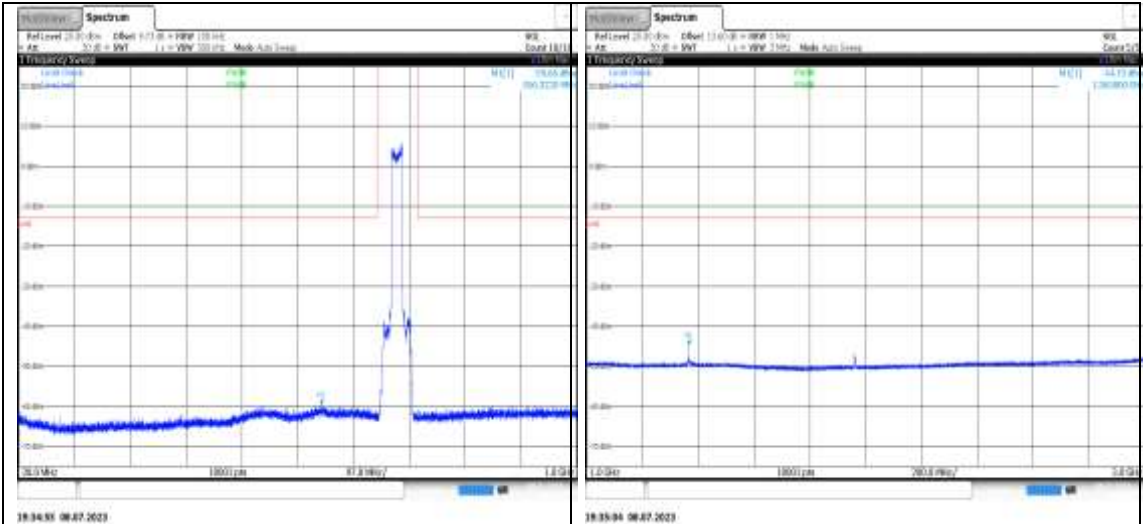


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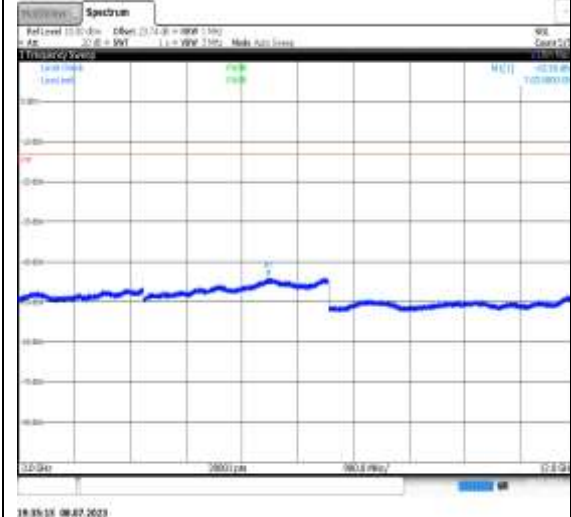
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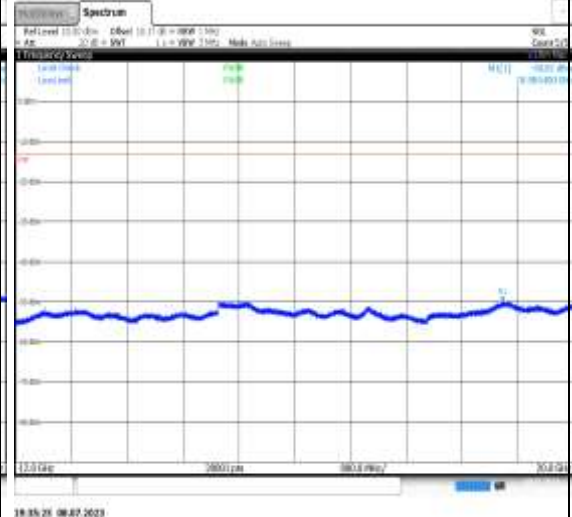


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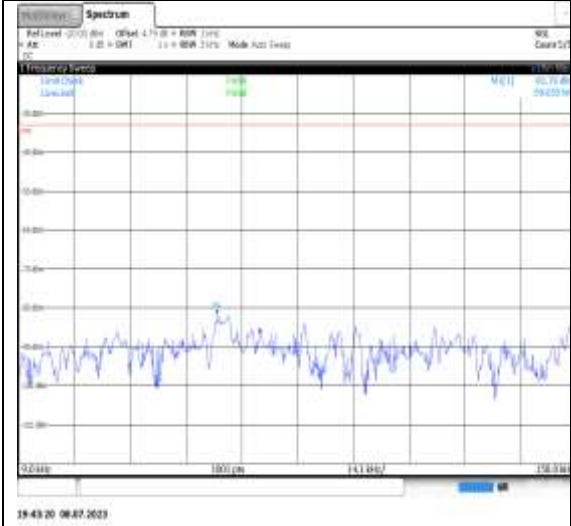
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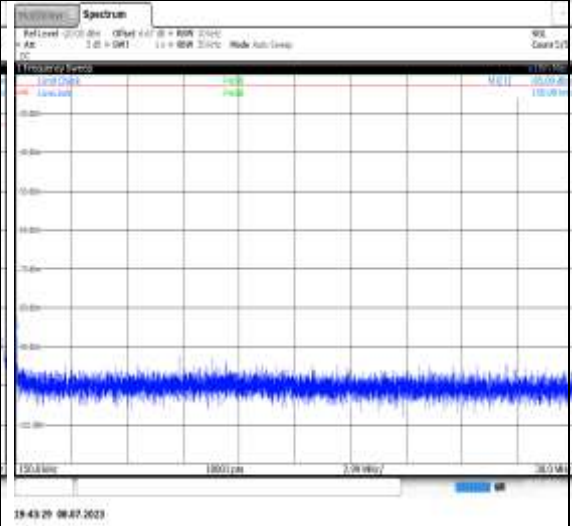
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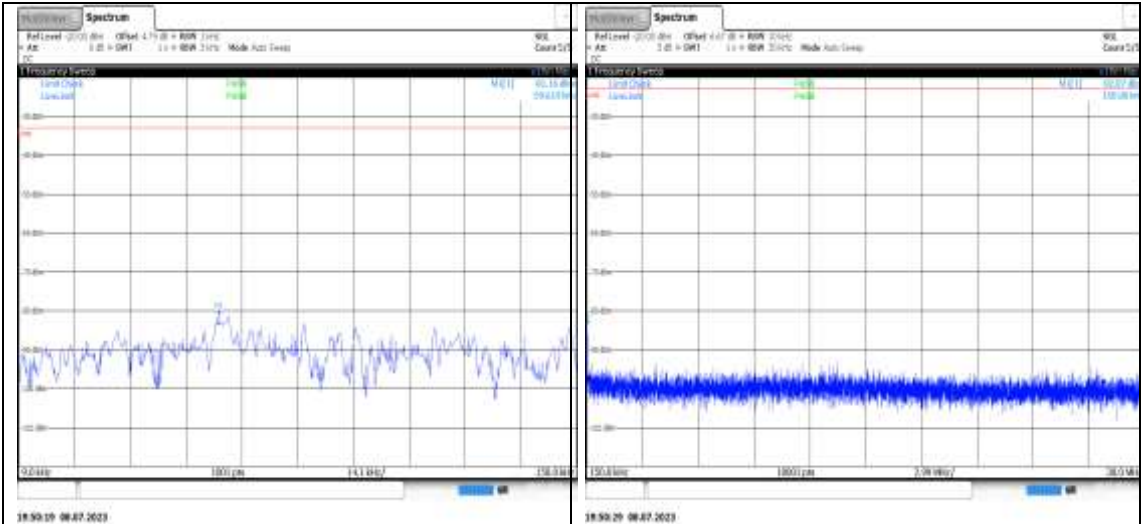


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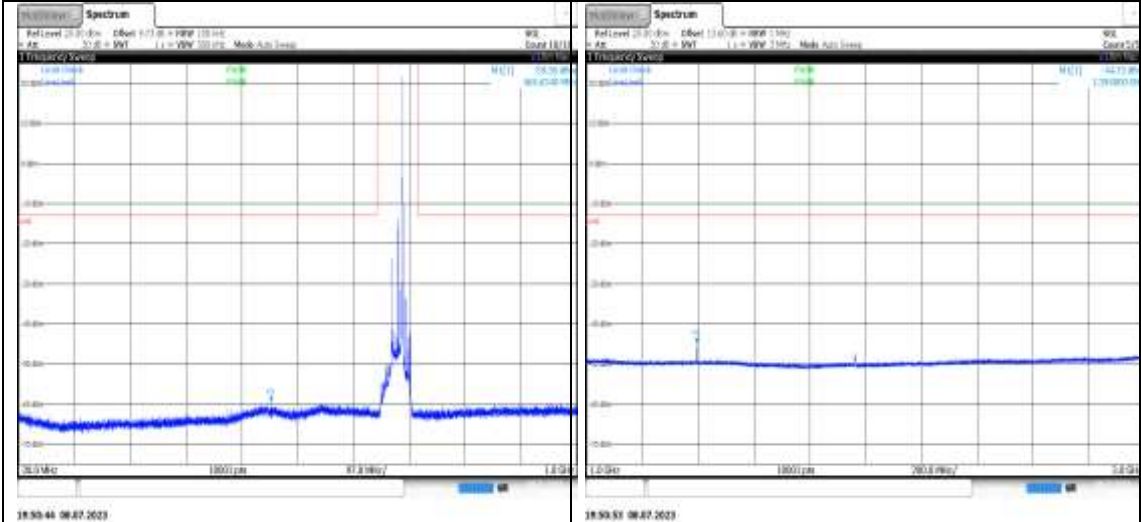
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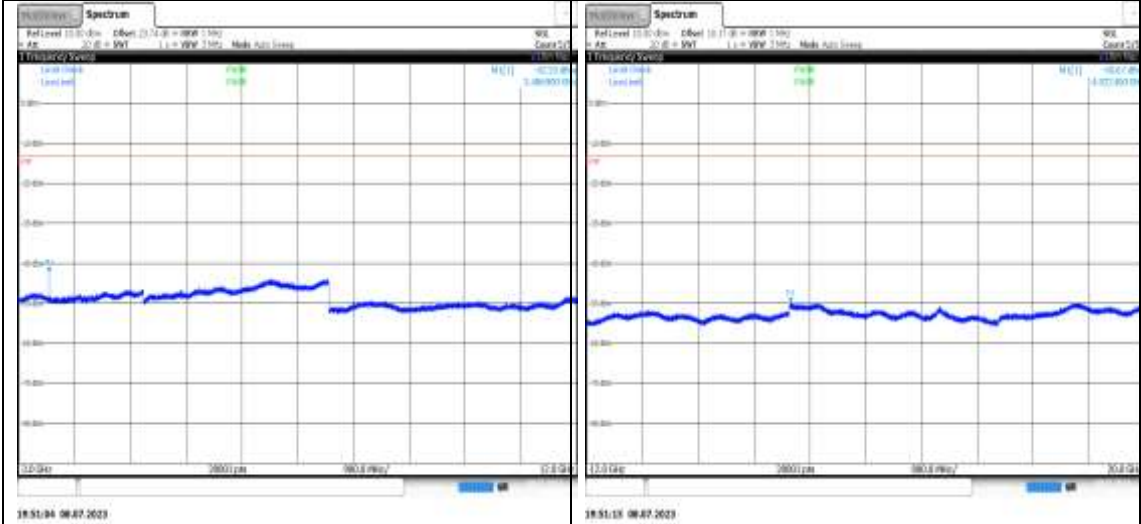
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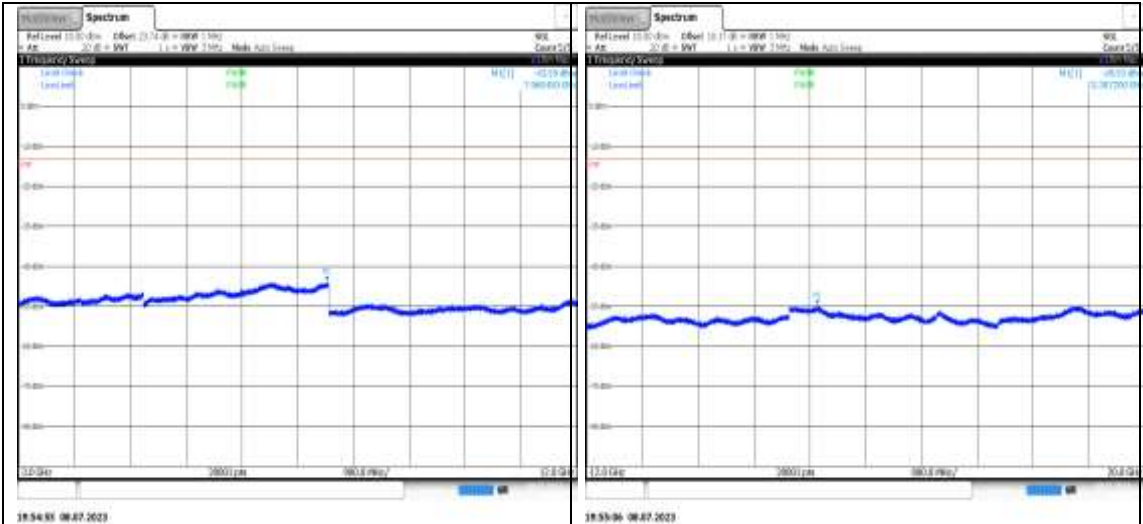
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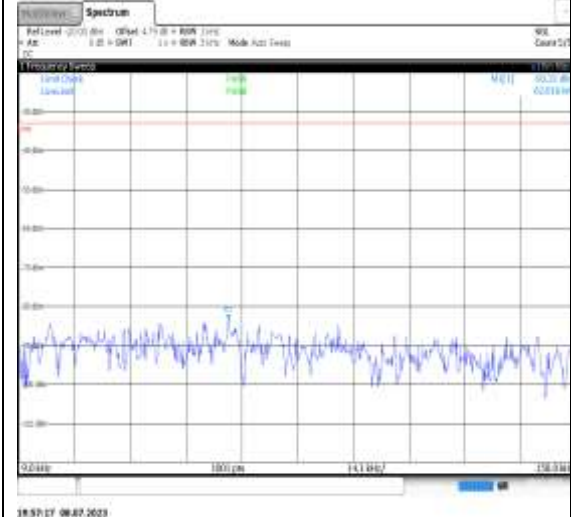
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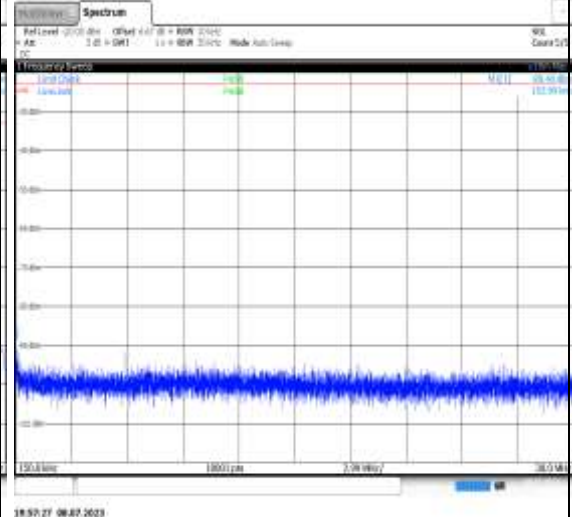


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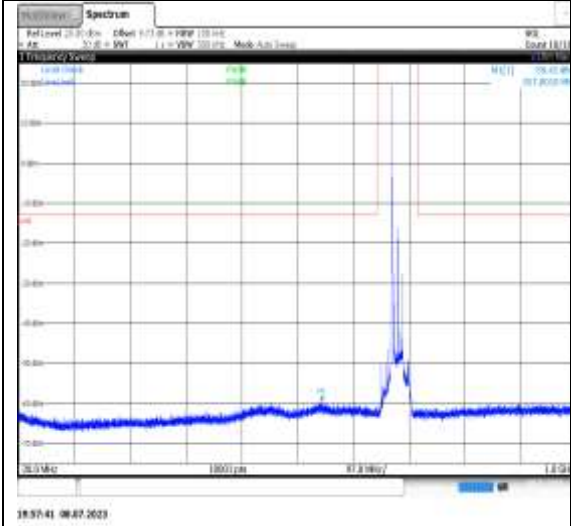
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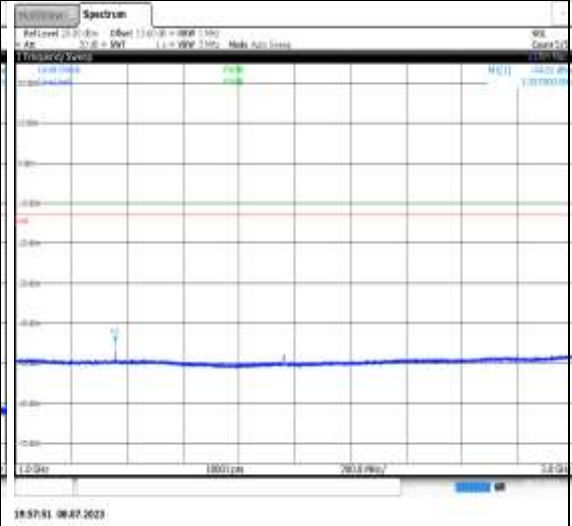
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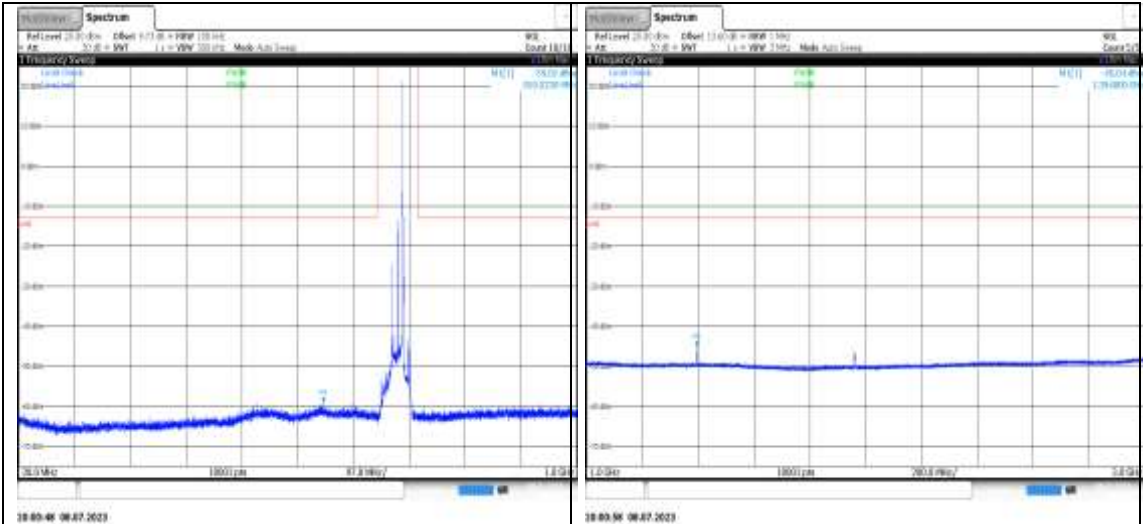


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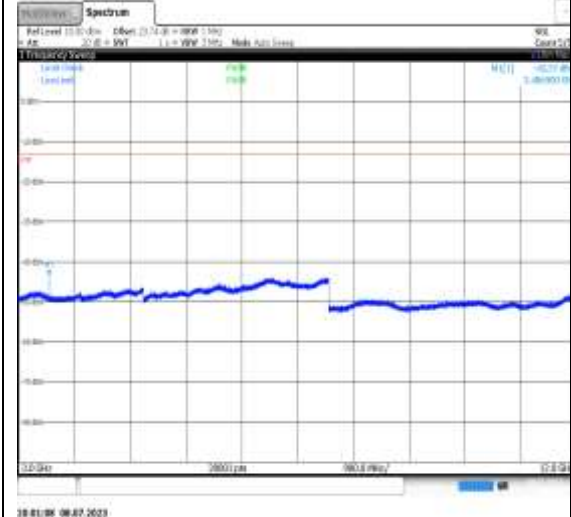
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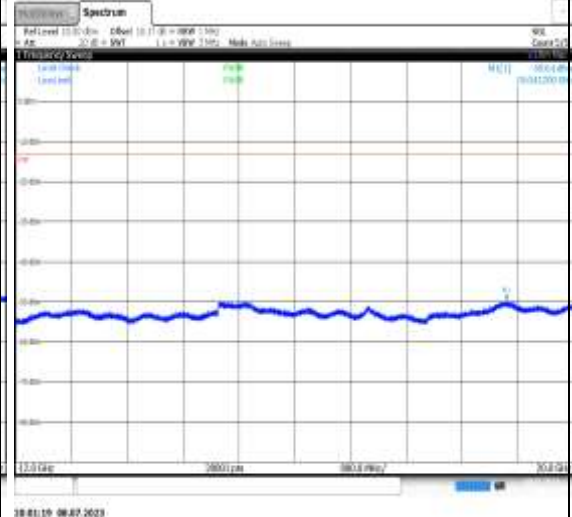


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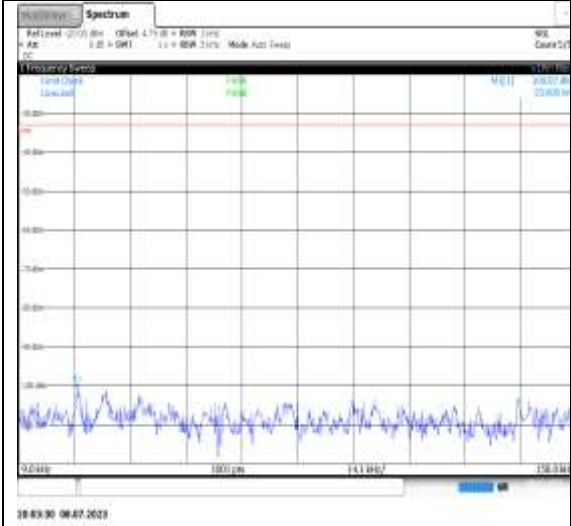
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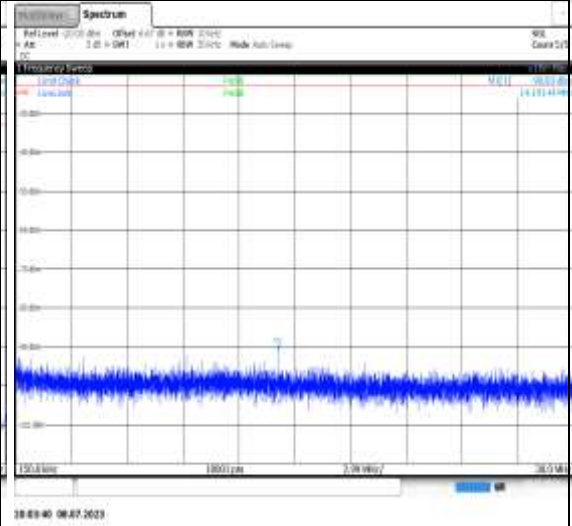
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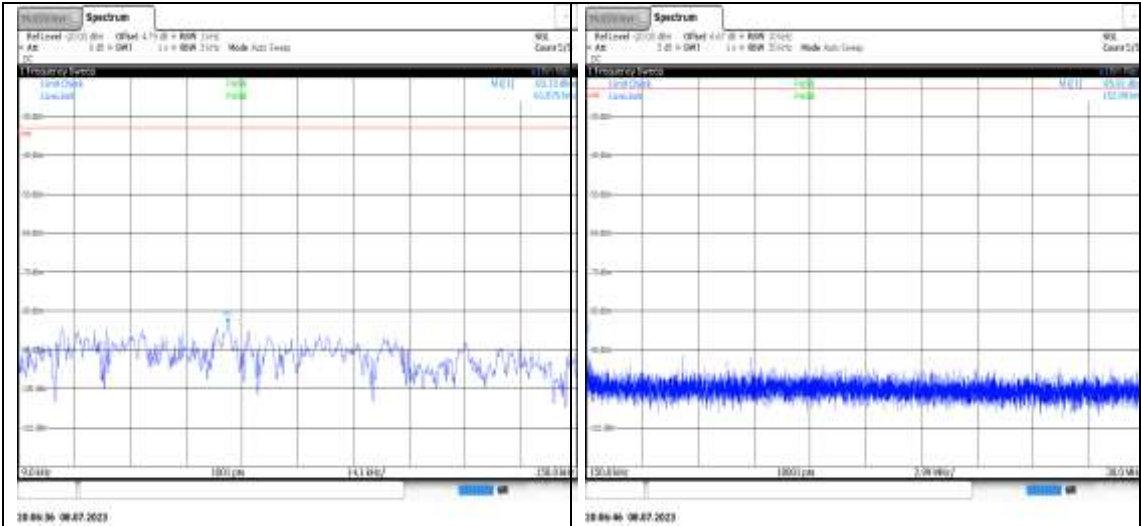


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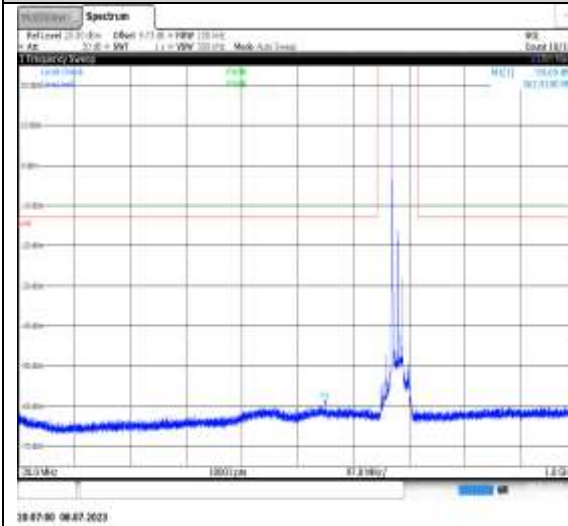
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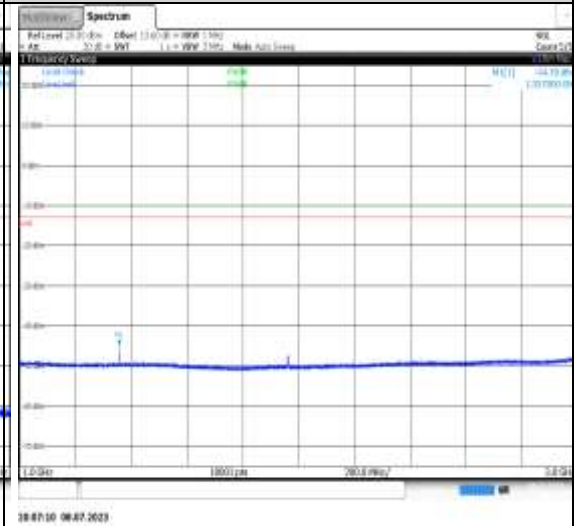


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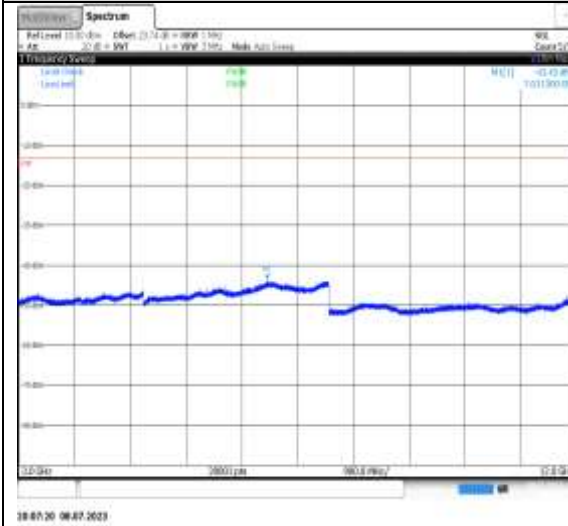
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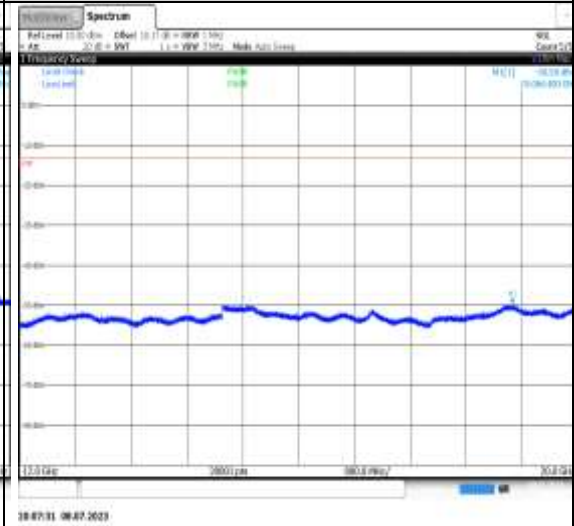
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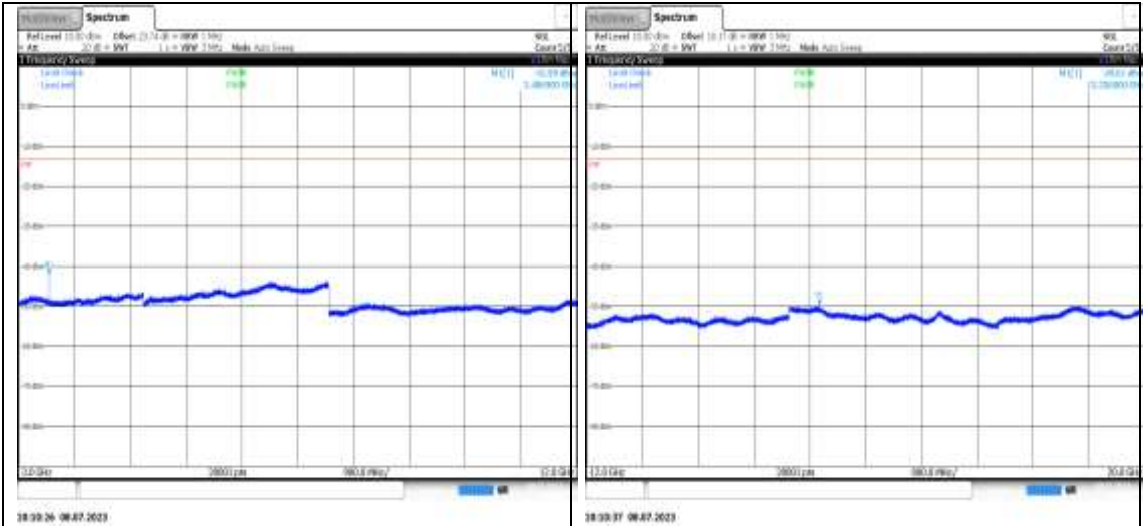


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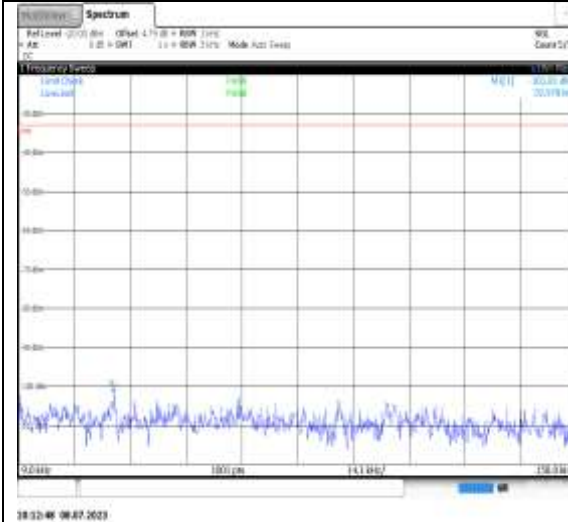
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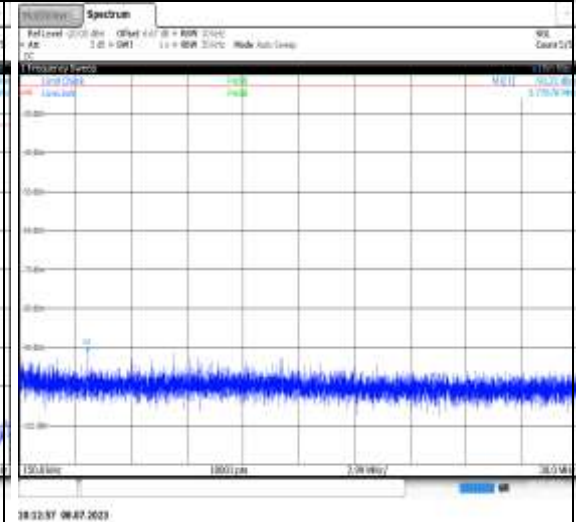


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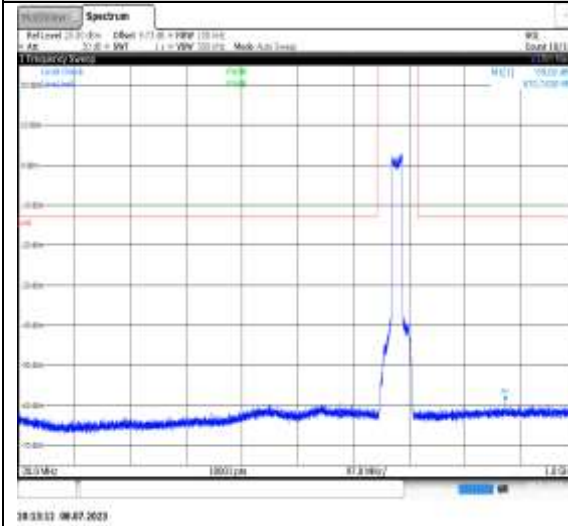
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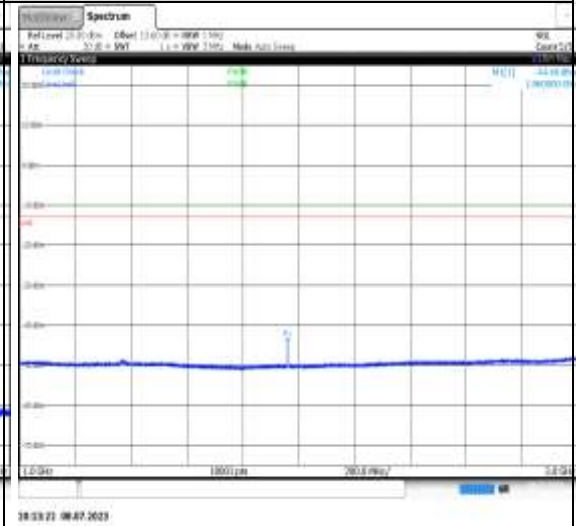
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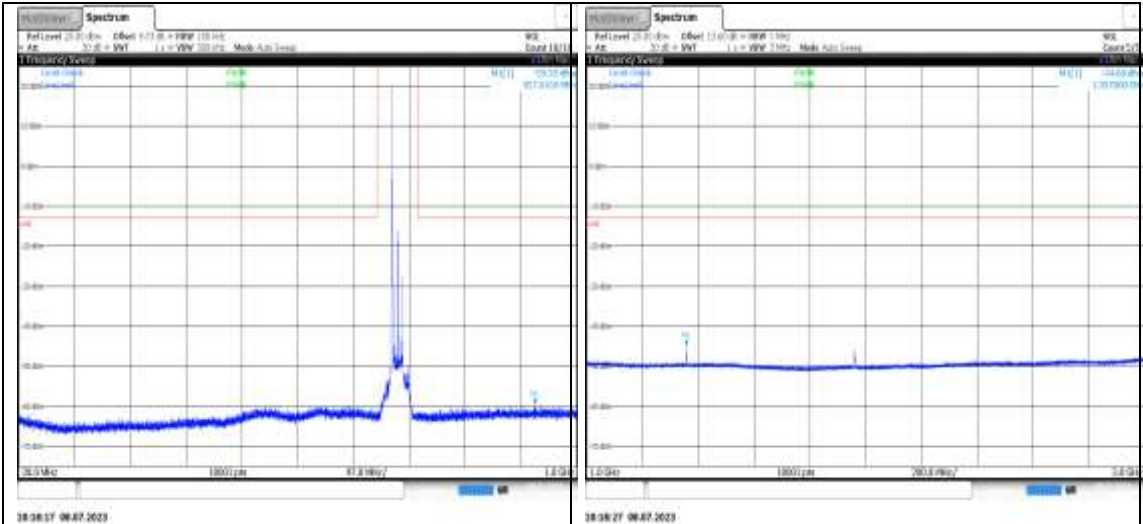


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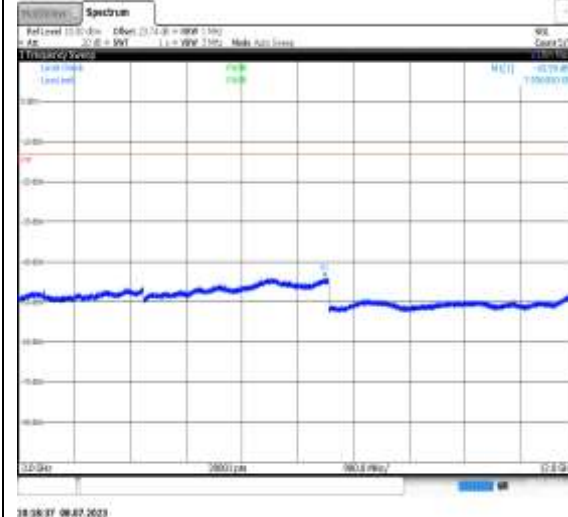
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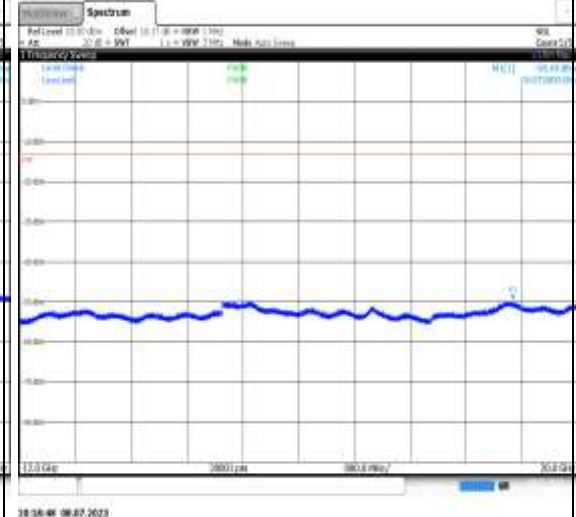


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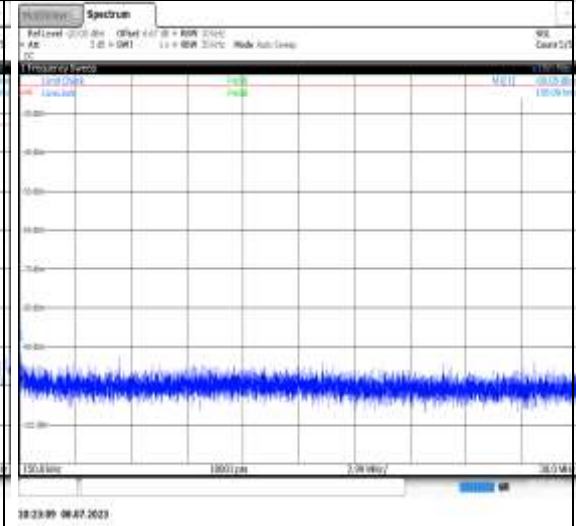
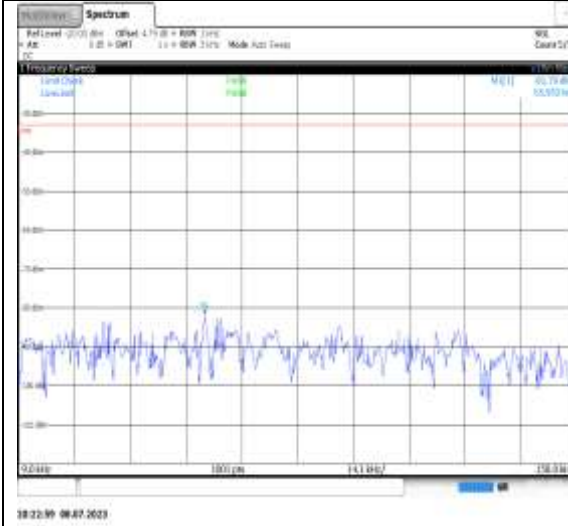
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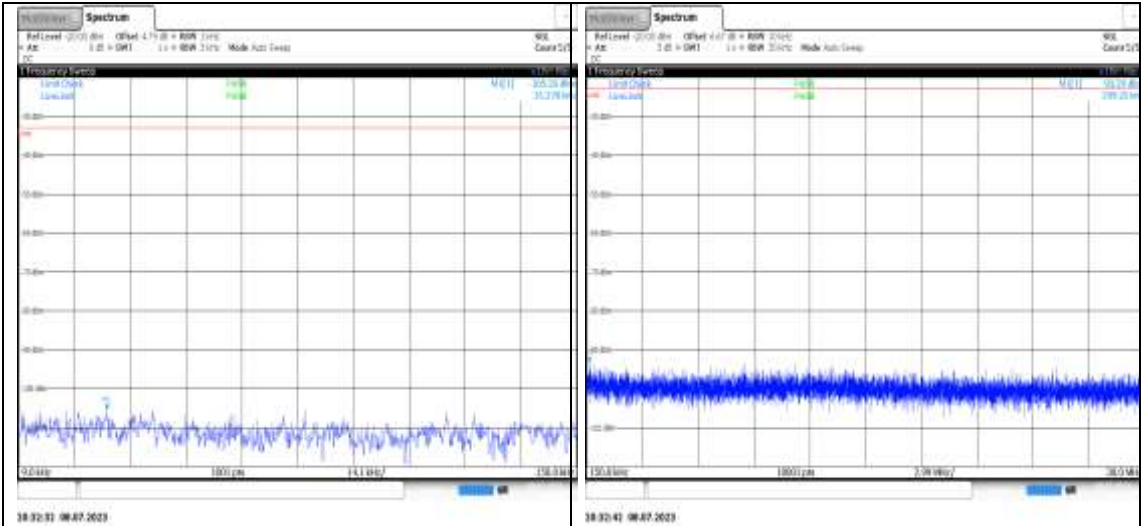
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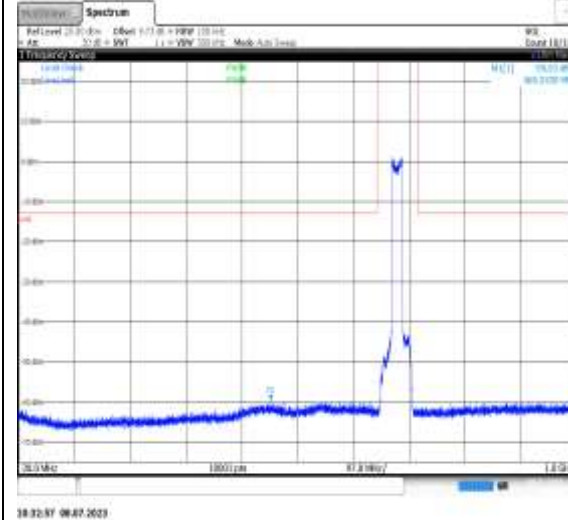


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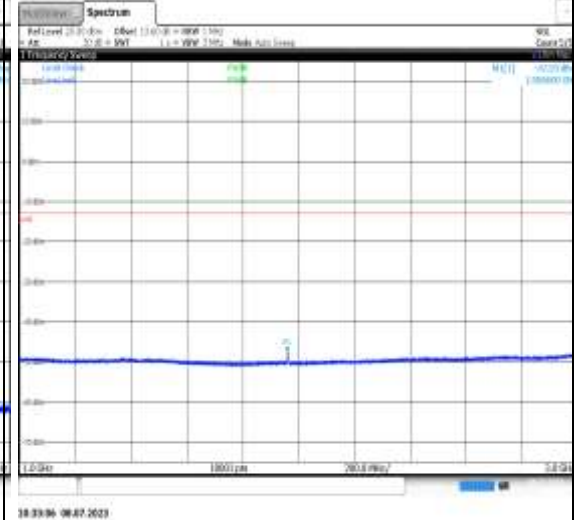


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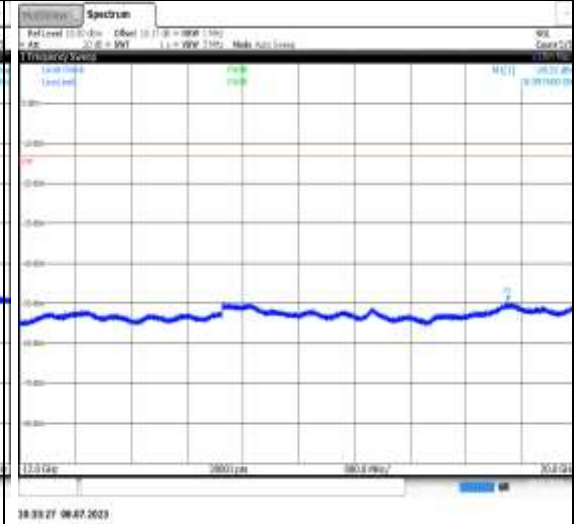
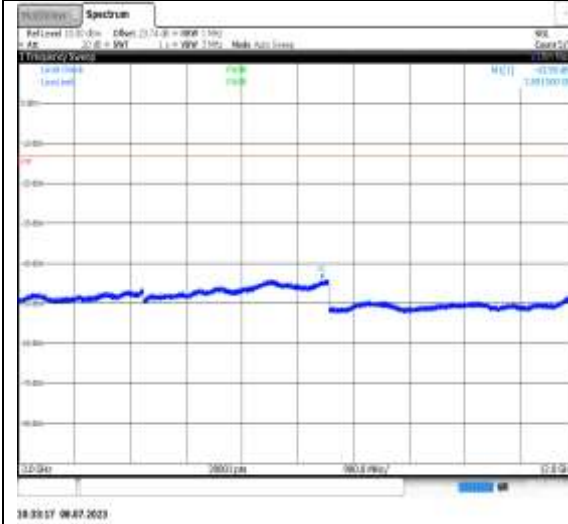
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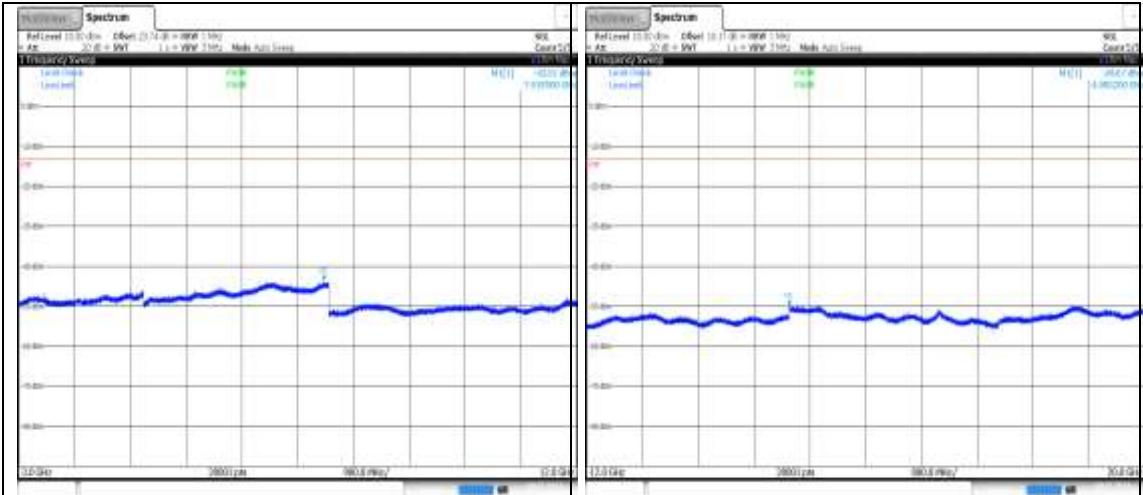
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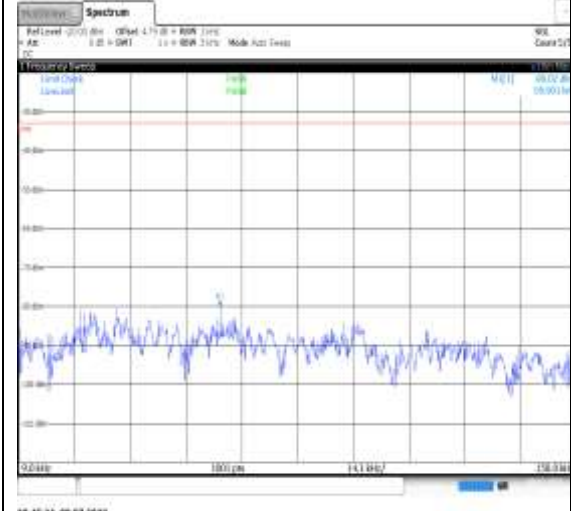


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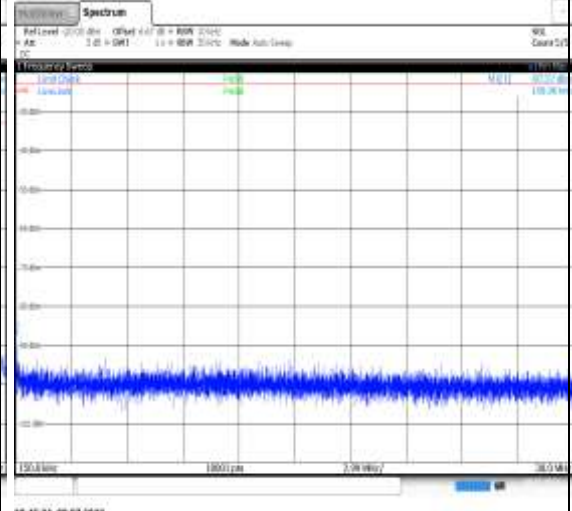


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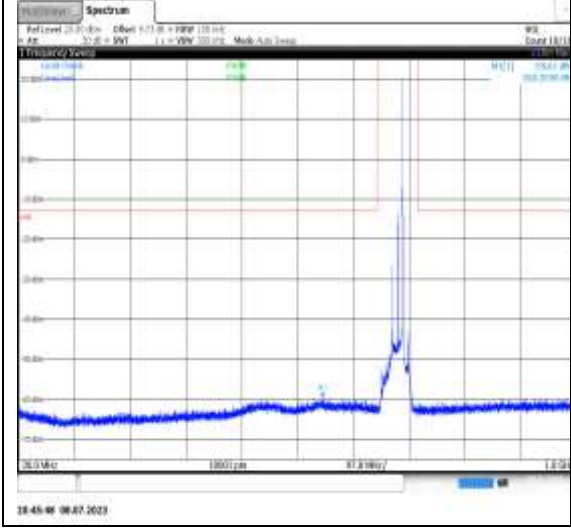
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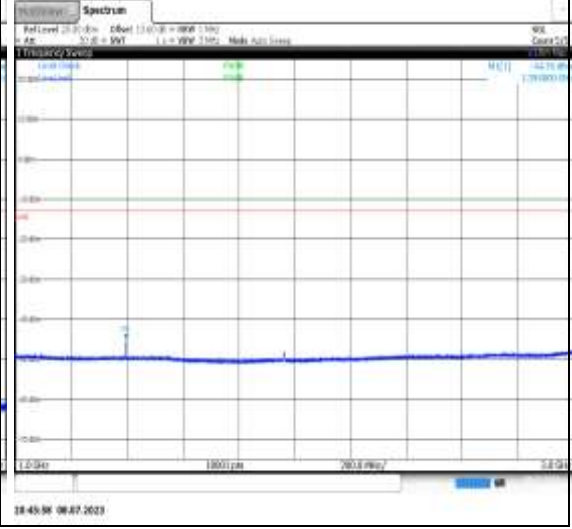
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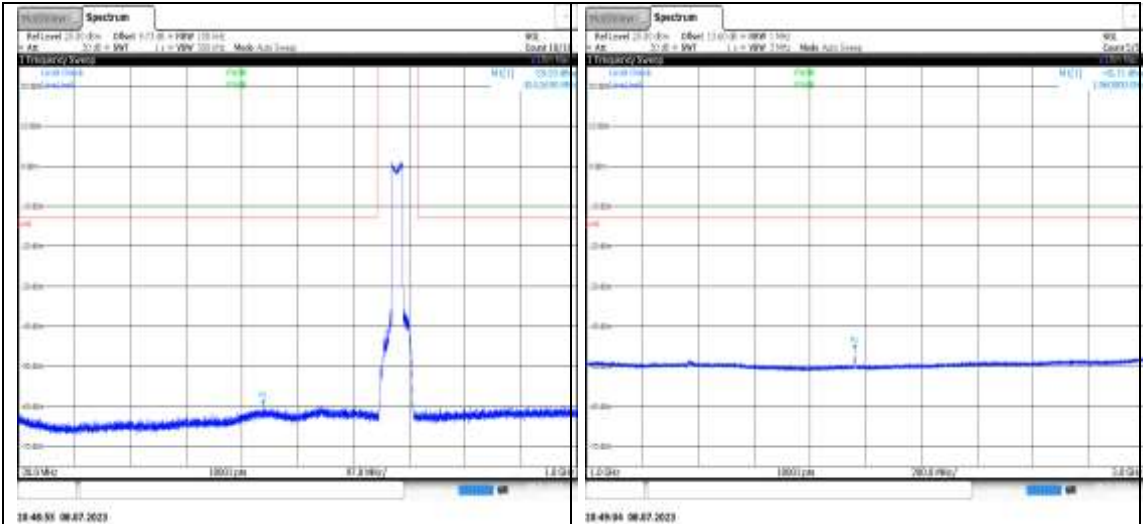


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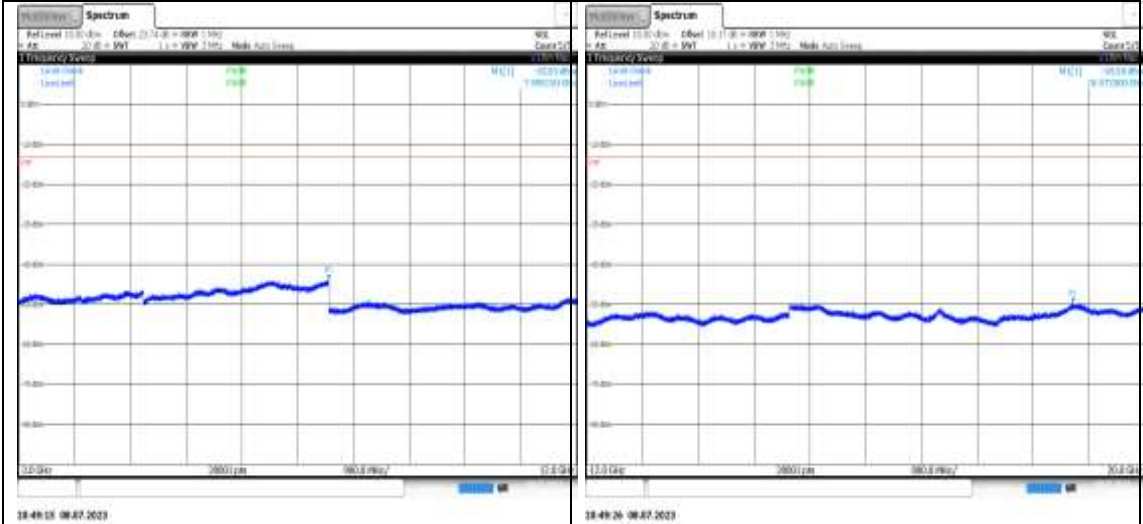
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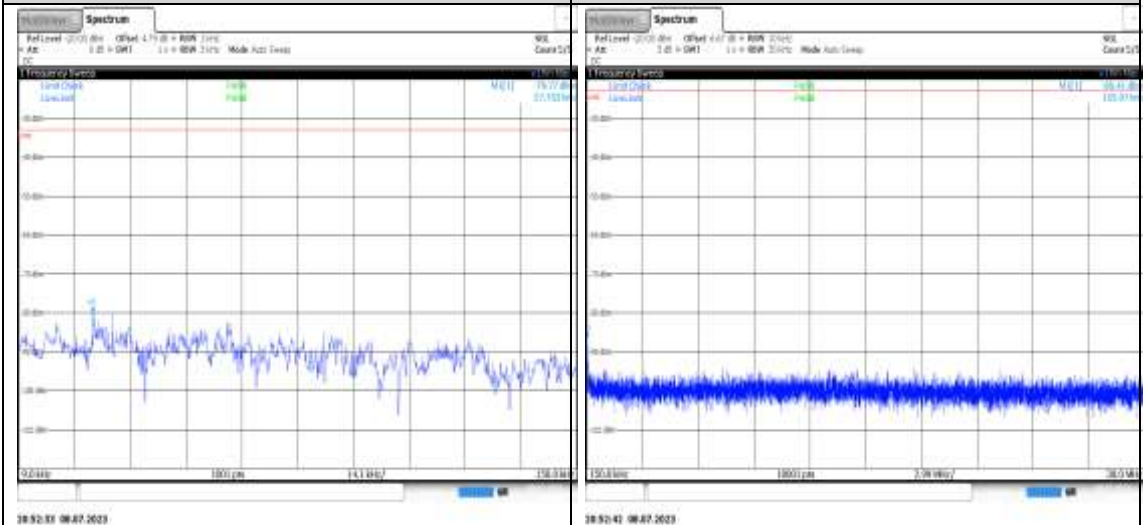
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1-DC_2A_n71A-PC3-15-5+20-M+H-18-NS_01-N/A-N/A-N/A-CP-QPSK-Outer_Full-106@0-3000-12000-7998.15--43.83--13-PASS

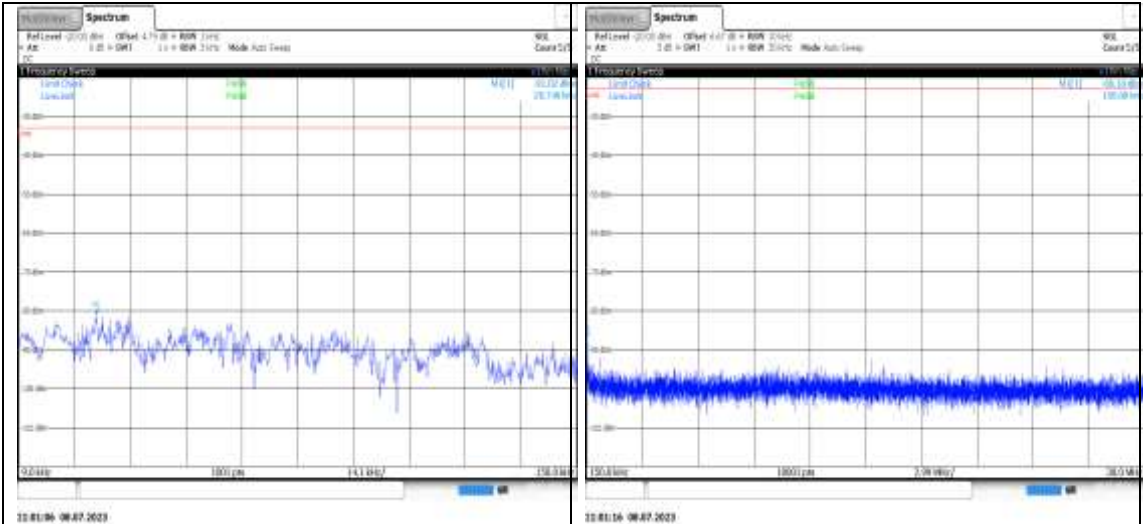
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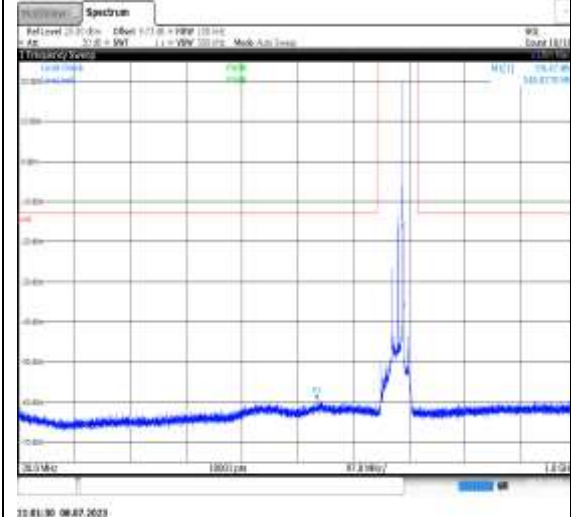
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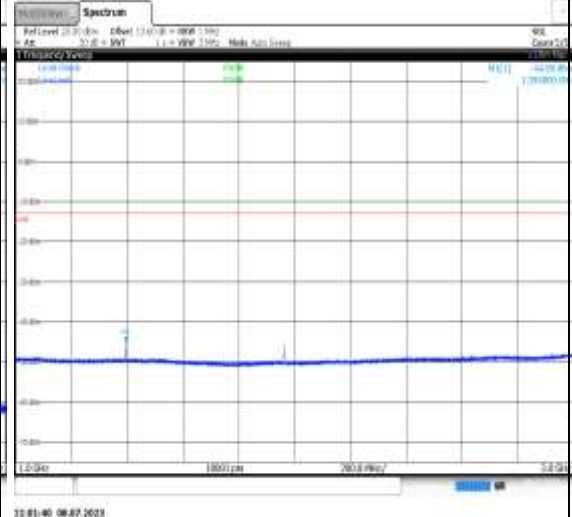


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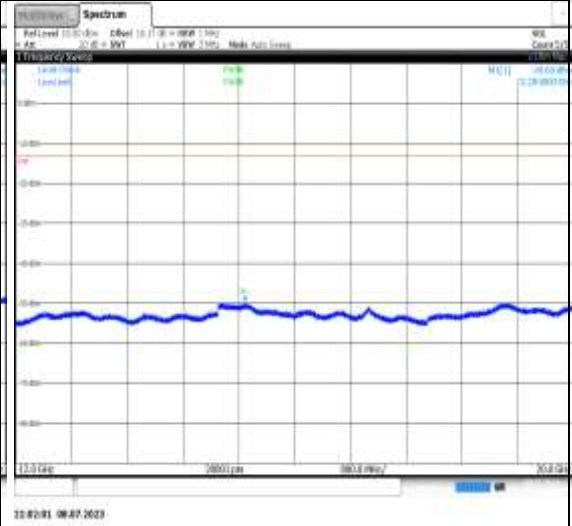
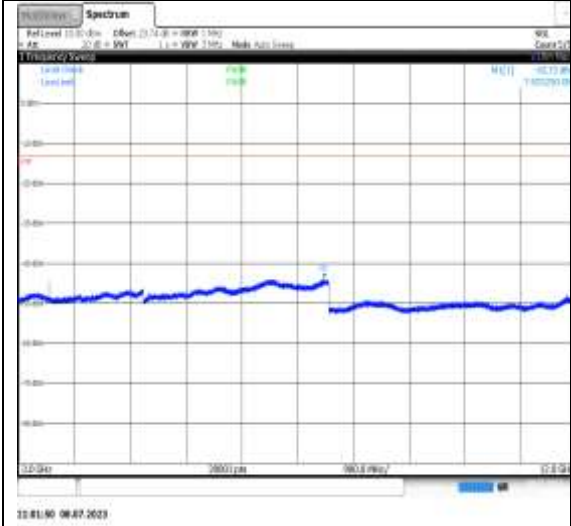
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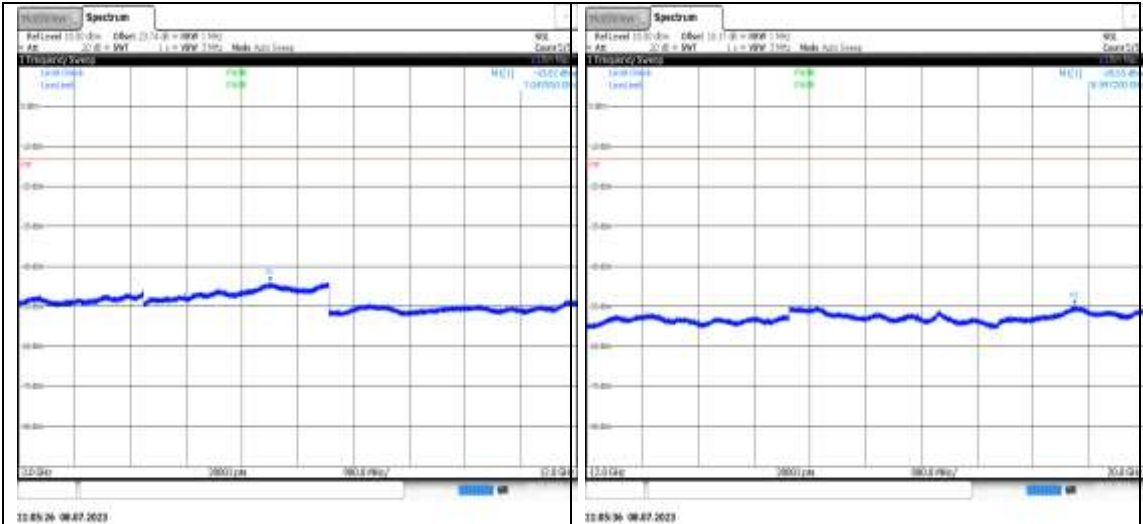
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1-DC_2A_n71A-PC3-15-5+20-M+H-20-NS_01-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Right-1@105-1000-3000-1394.80--44.98--13-PASS

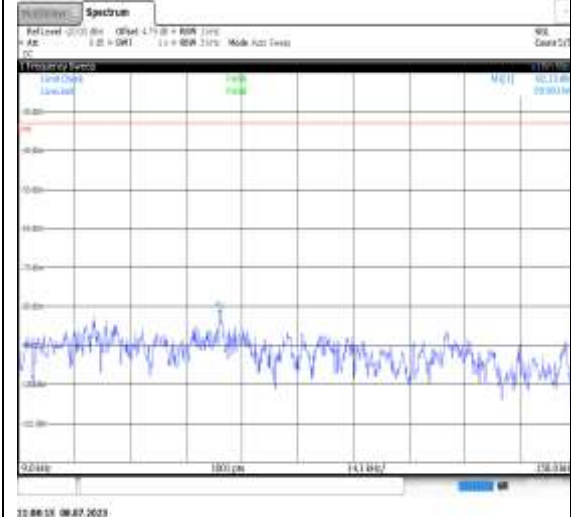


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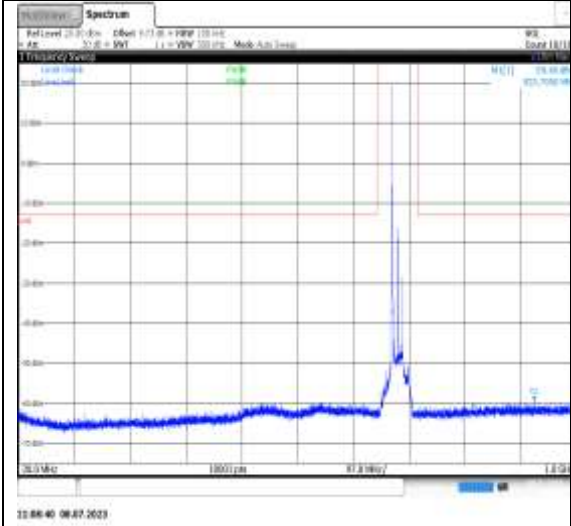
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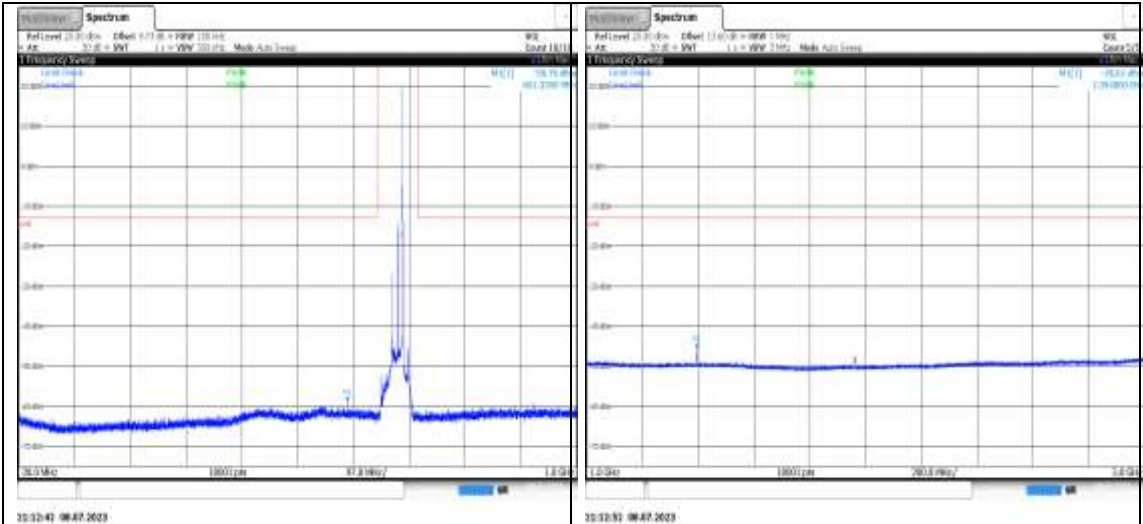
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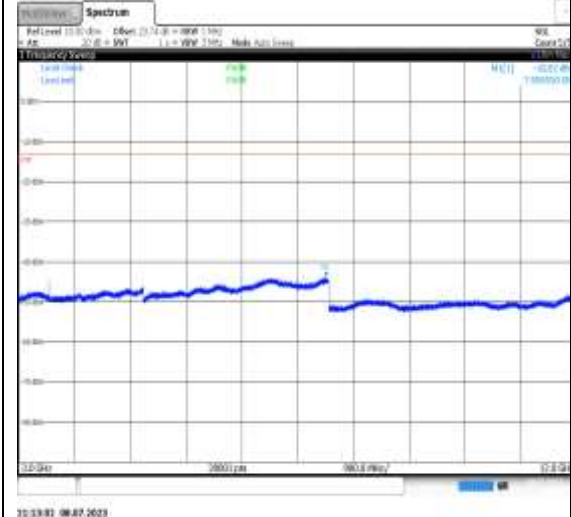
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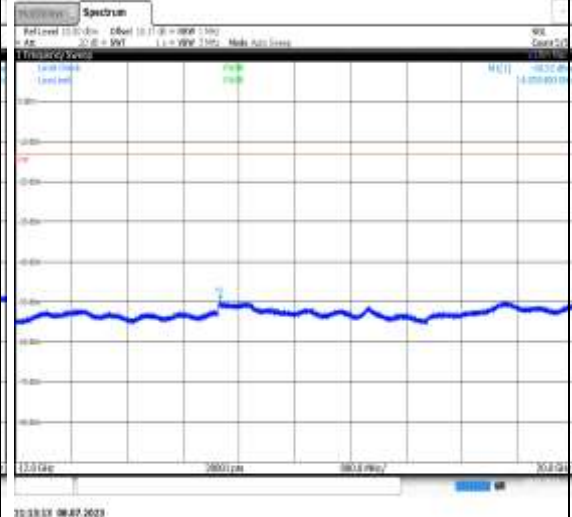


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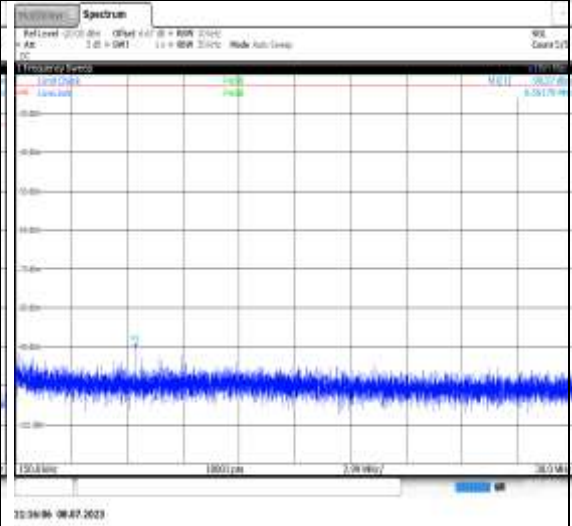
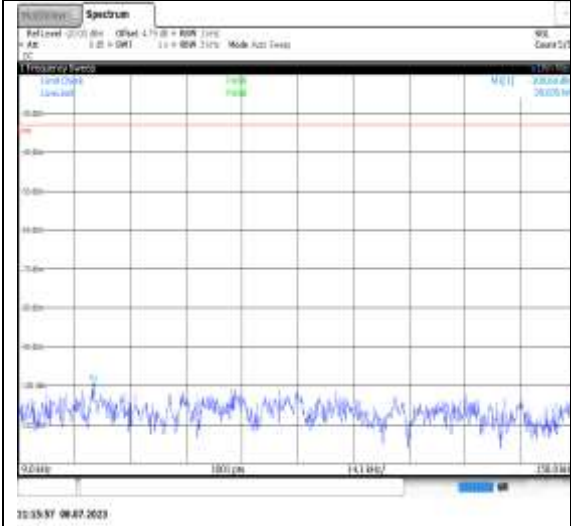
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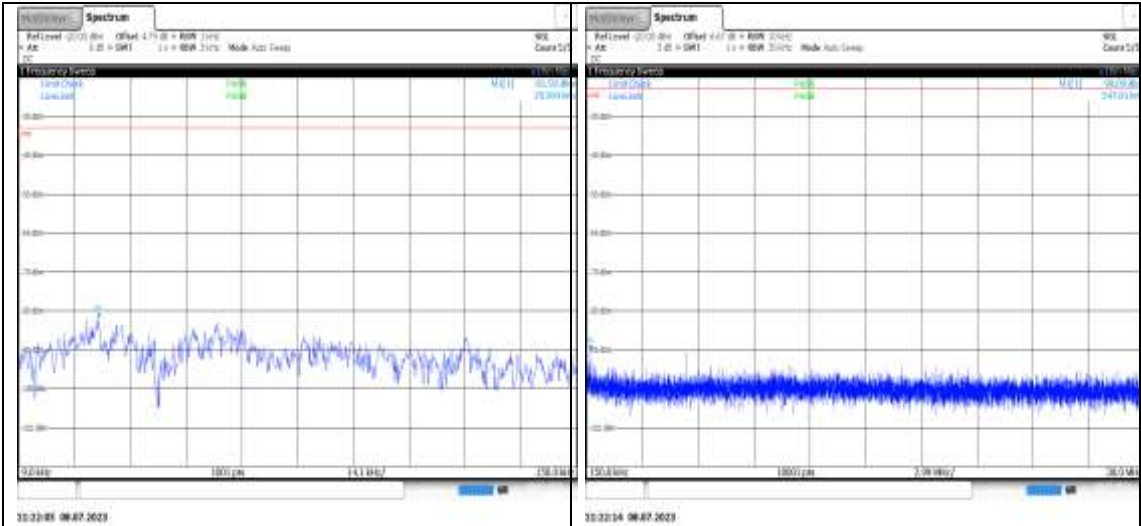
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1-DC_2A_n71A-PC3-15-5+20-M+H-23-NS_01-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Right-1@105-12000-20000-14928.40--49.52--13-PASS

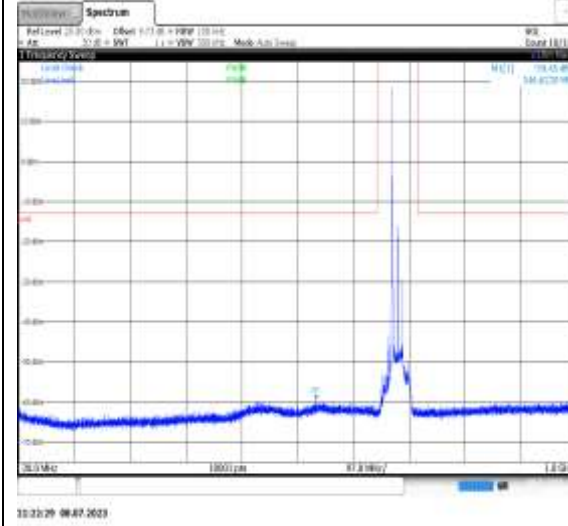


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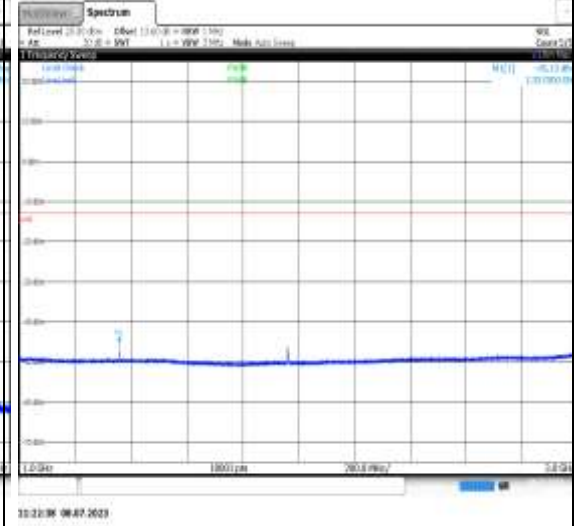


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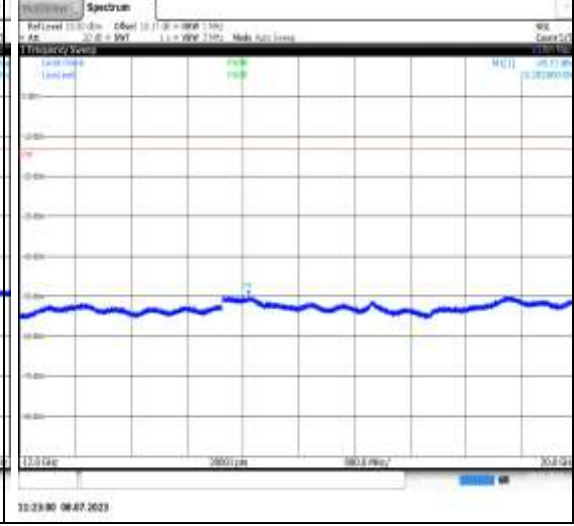
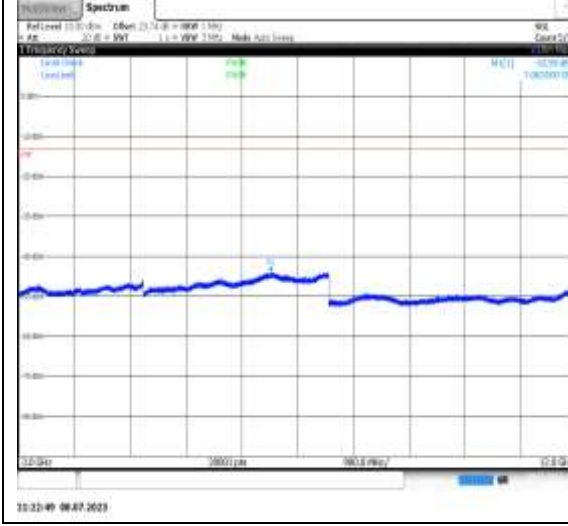
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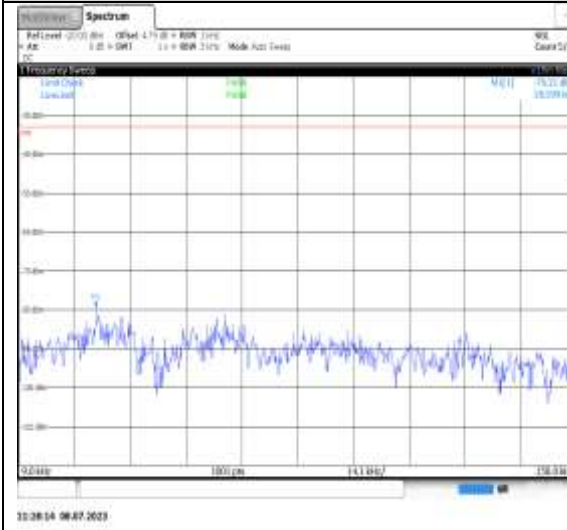
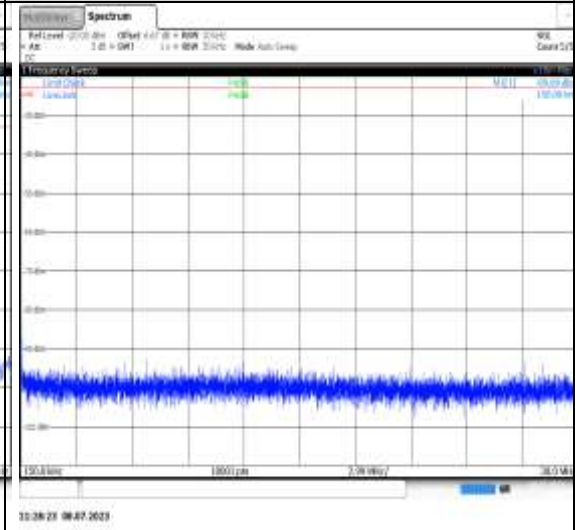
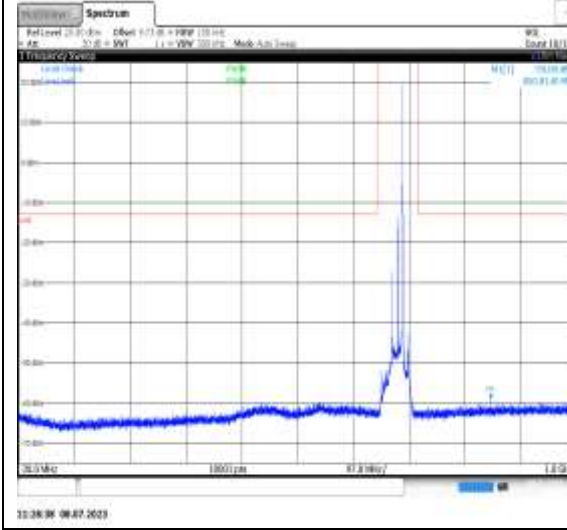
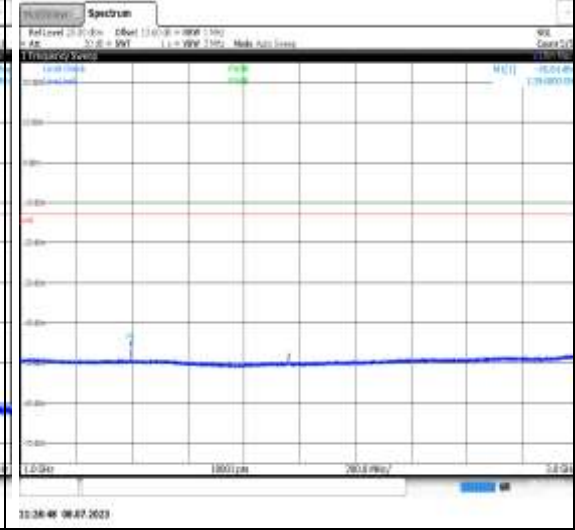


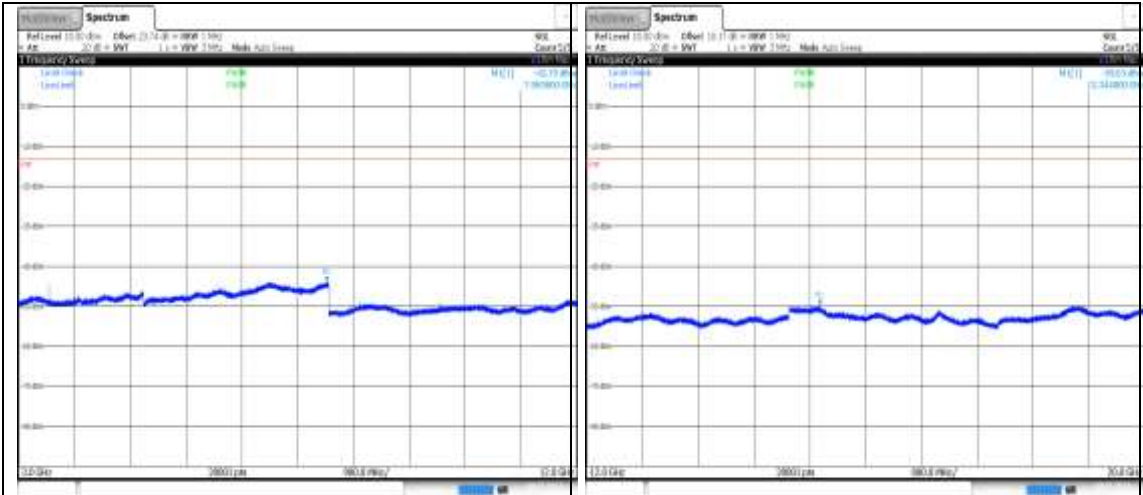
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1-DC_2A_n71A-PC3-15-5+20-M+H-25-NS_01-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-1000-3000-1357.00--45.13--13-PASS

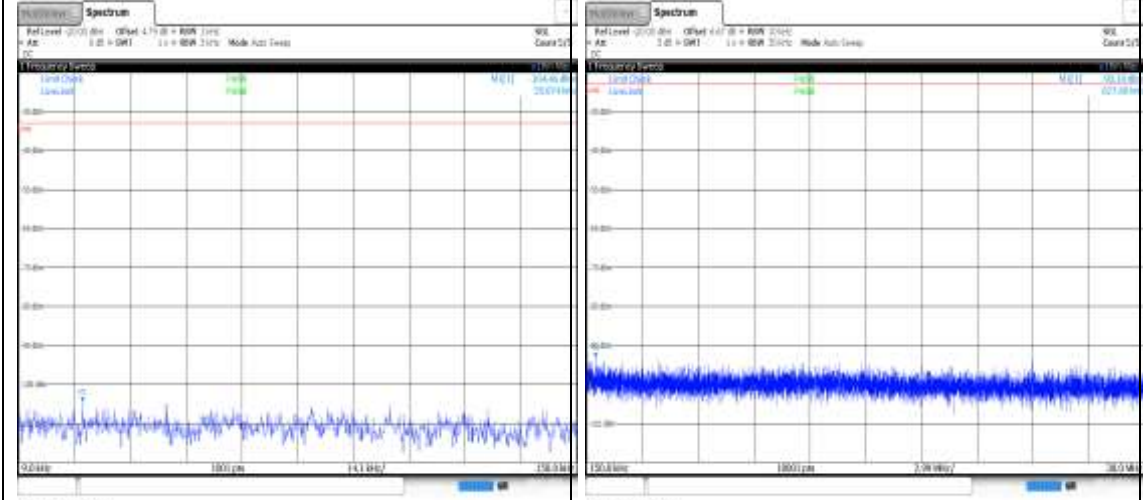


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<p>1-DC_2A_n71A-PC3-15-5+20-M+H-26-NS_01-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@105-30-1000-850.81--59.00--13-PASS</p>	<p>1-DC_2A_n71A-PC3-15-5+20-M+H-26-NS_01-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@105-1000-3000-1394.80--45.84--13-PASS</p>



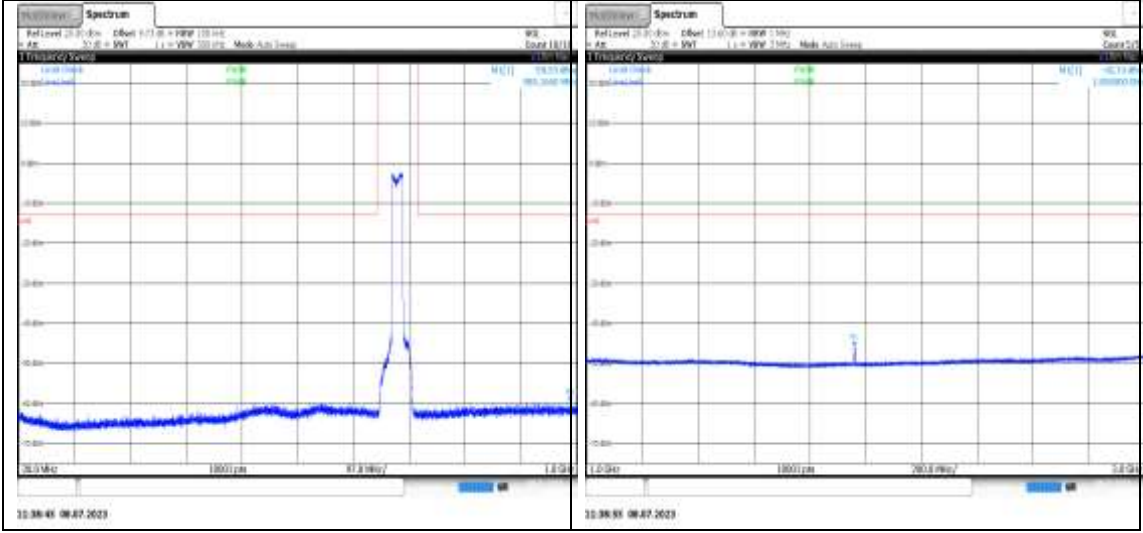
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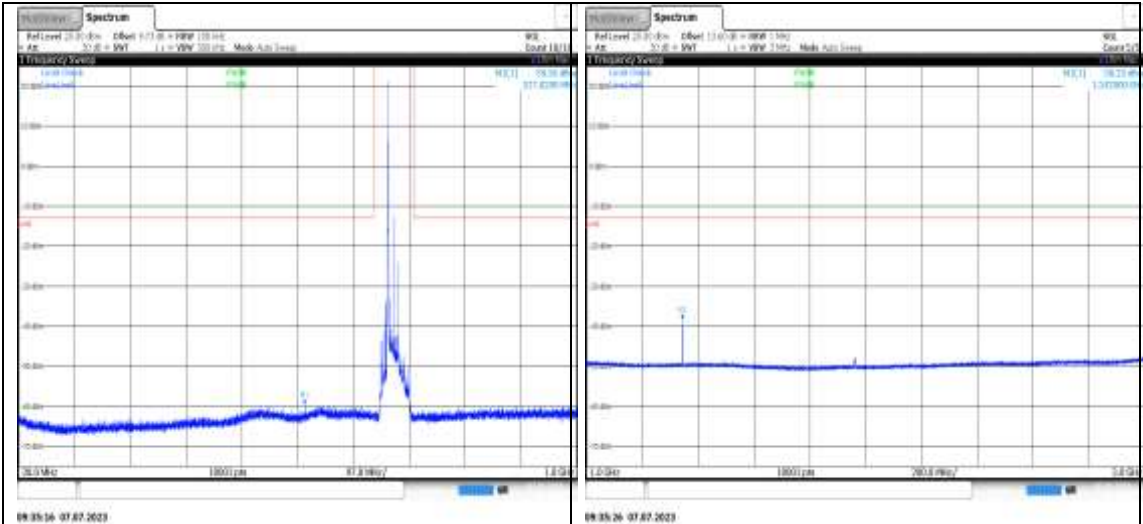
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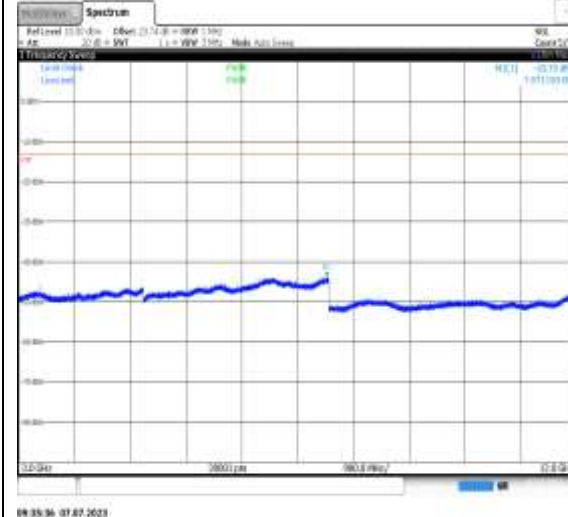
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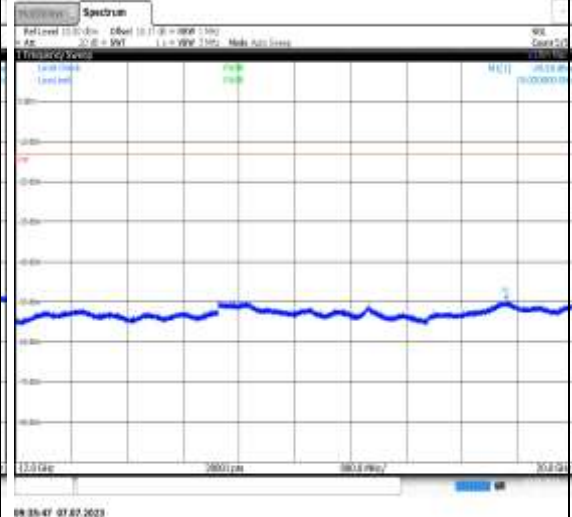


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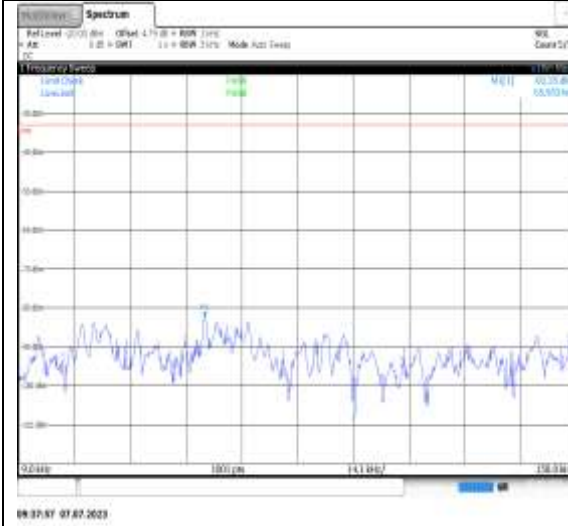
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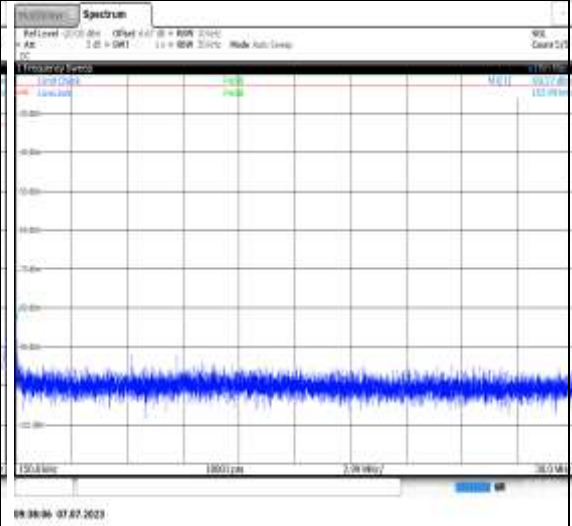
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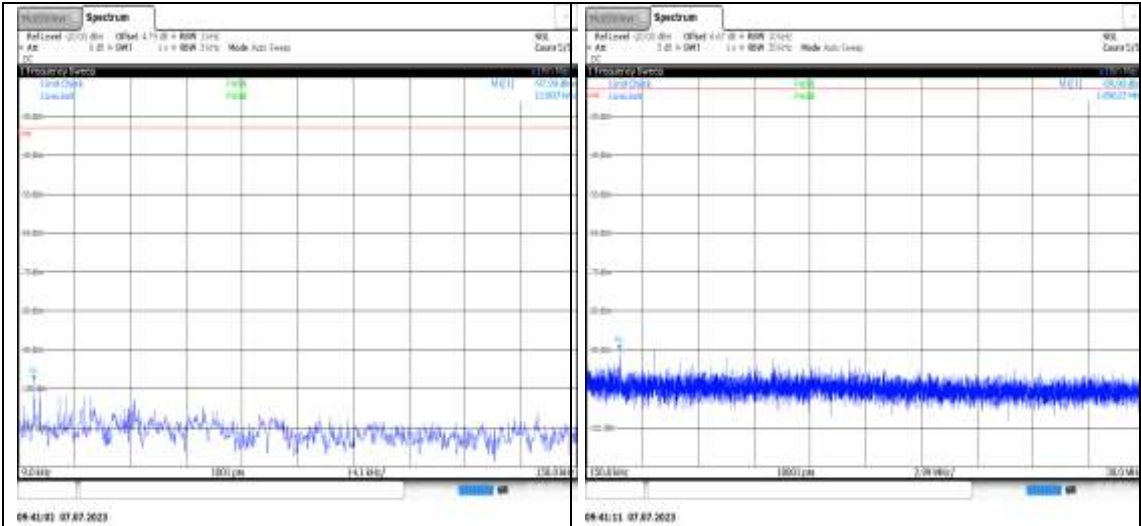


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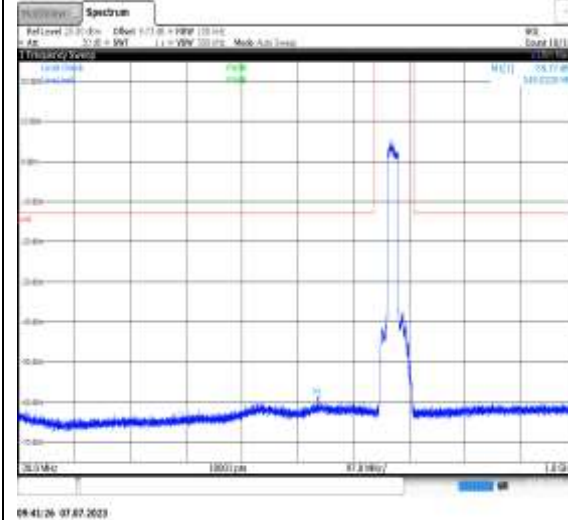
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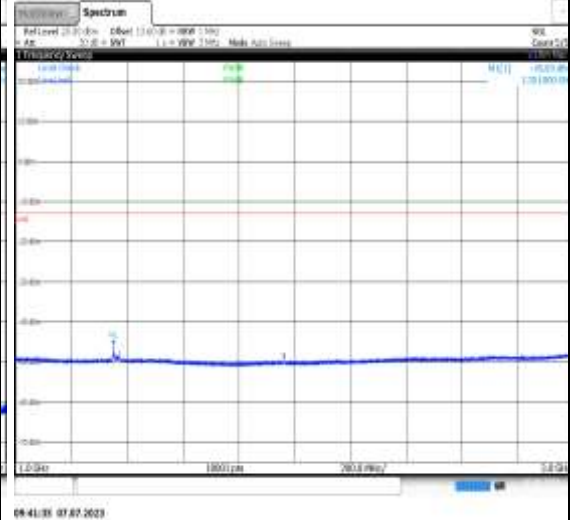


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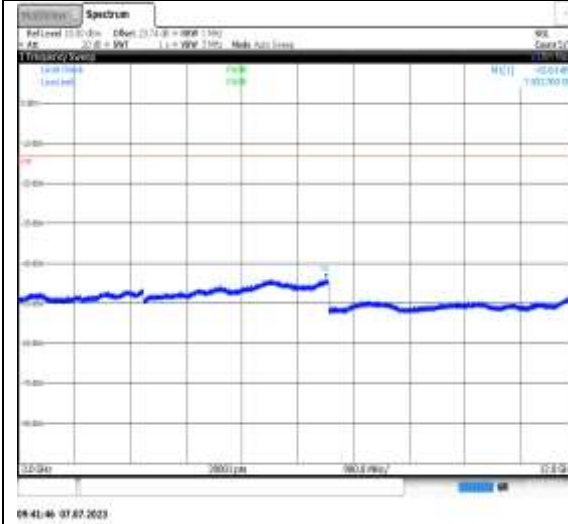
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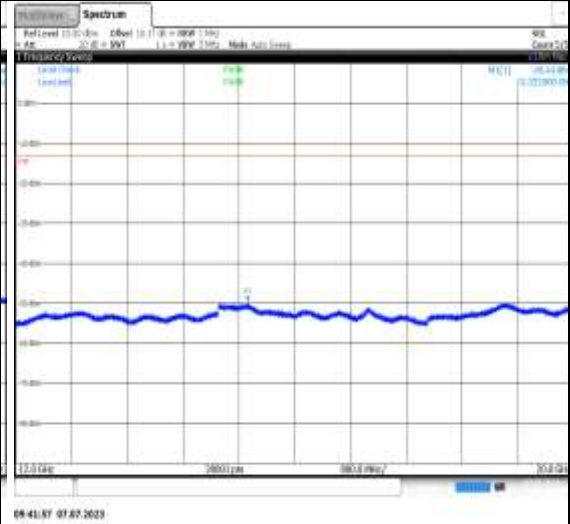
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1-DC_2A_n71A-PC3-15-5+20-M+5mhz-3-NS_01-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-1000-3000-1351.00--45.83--13-PASS

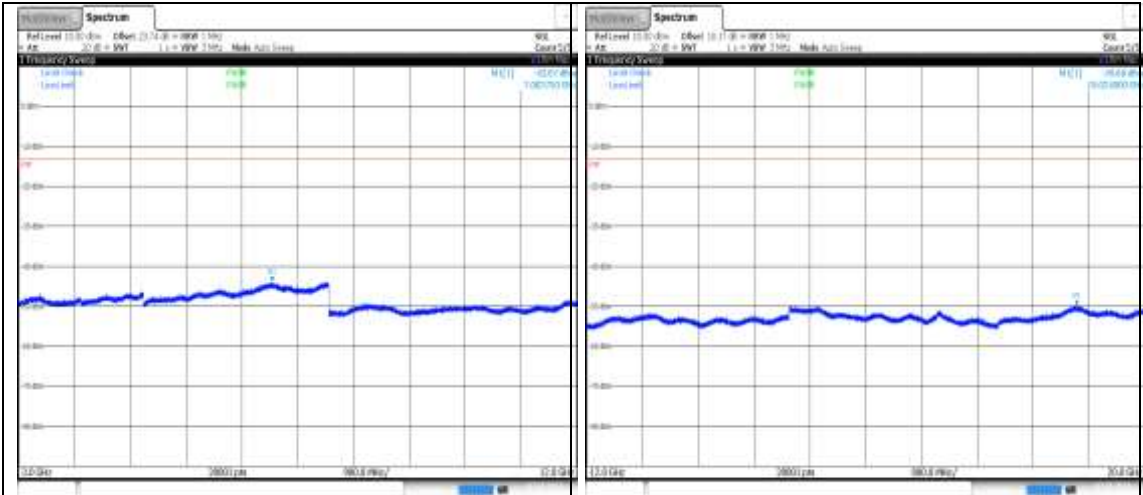


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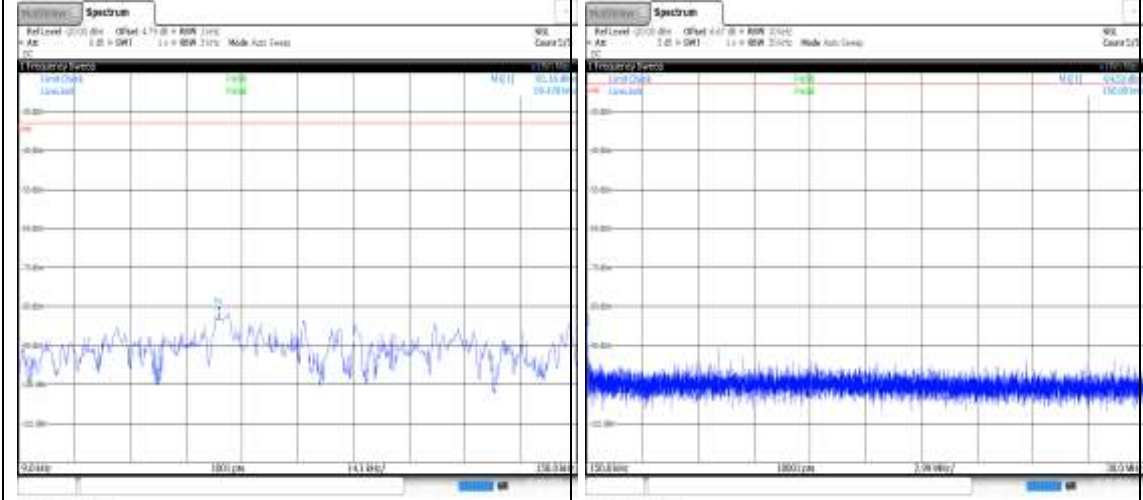
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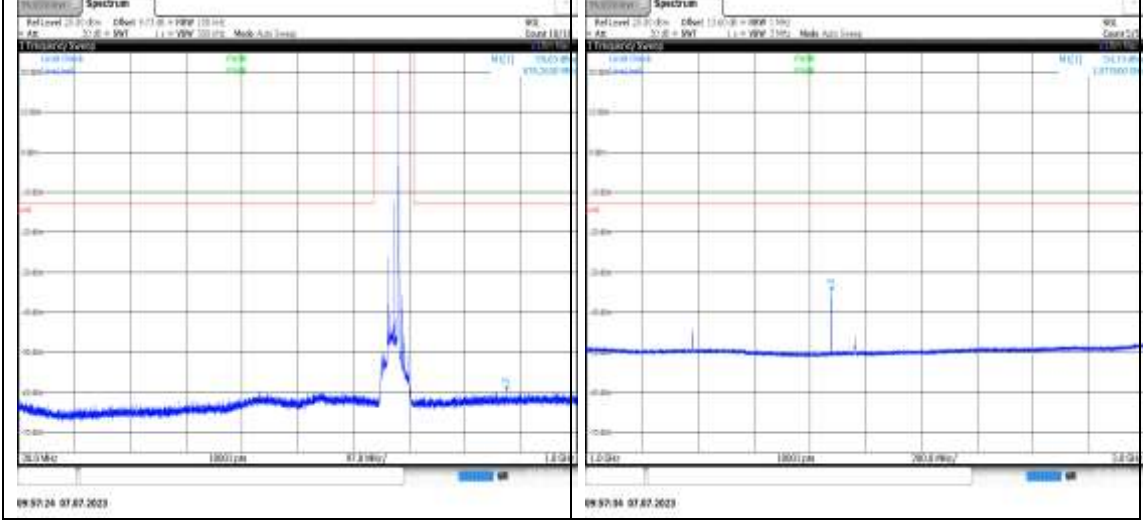
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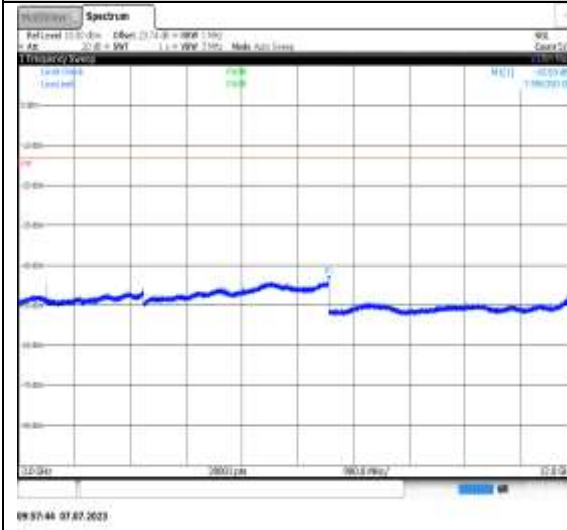
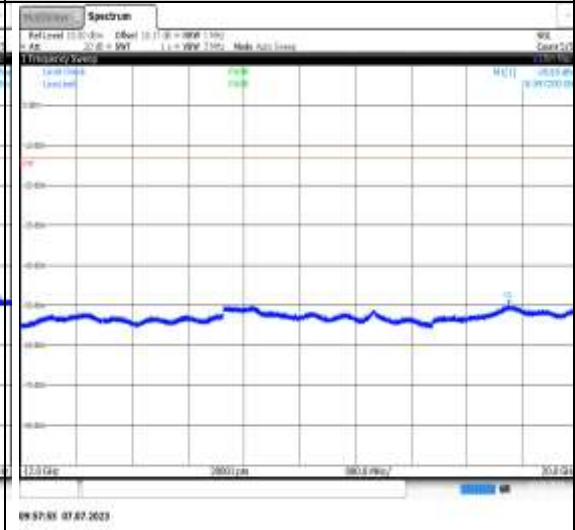
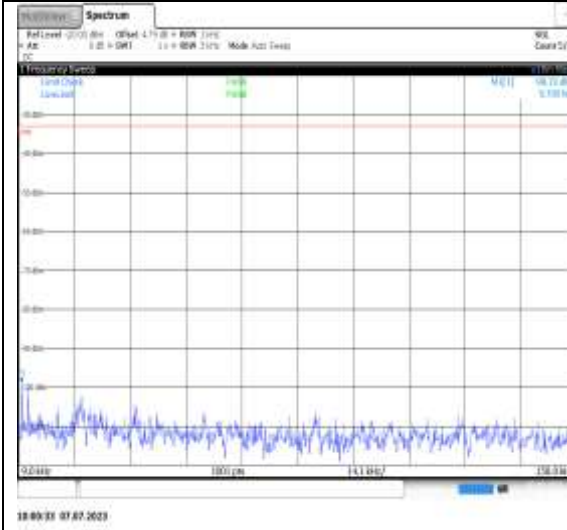
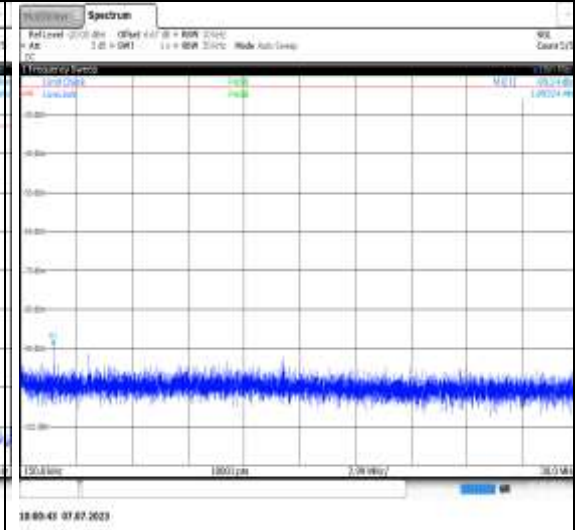
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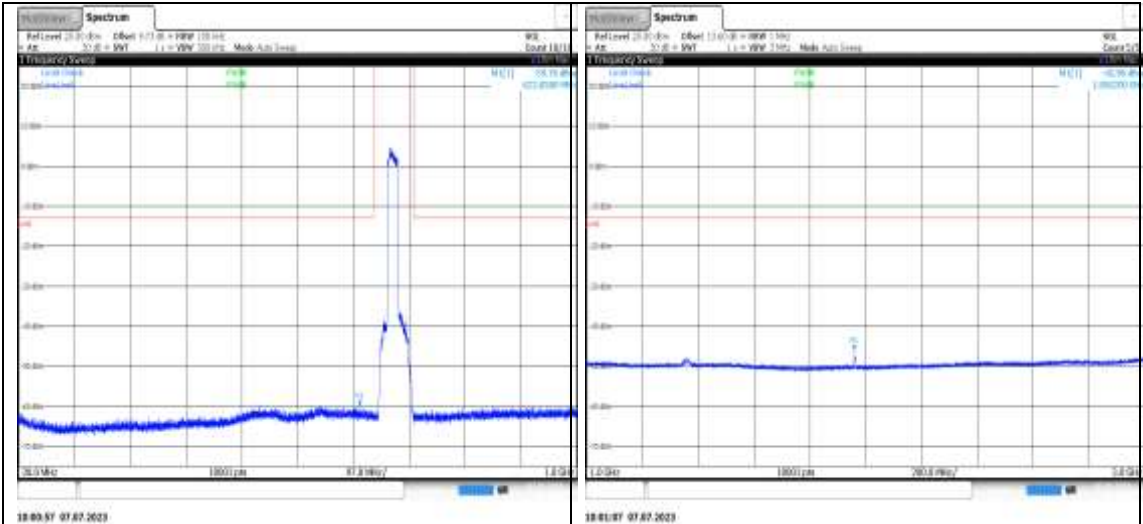
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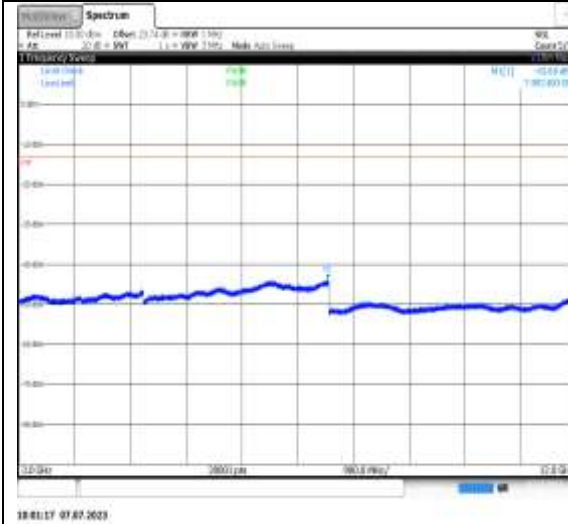
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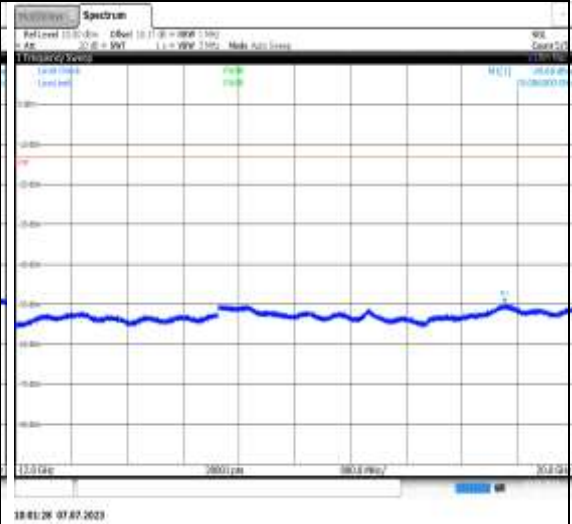


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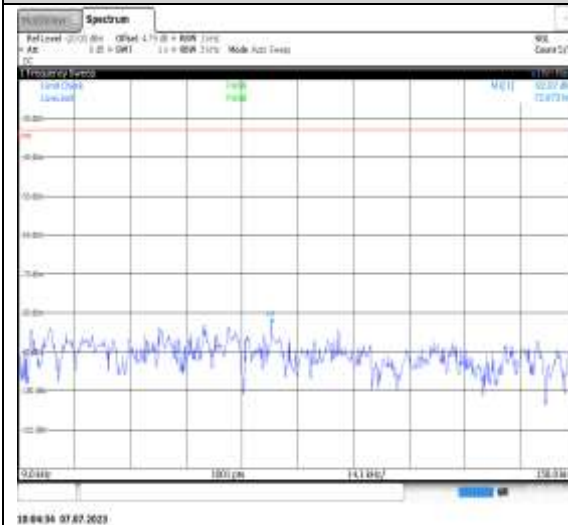
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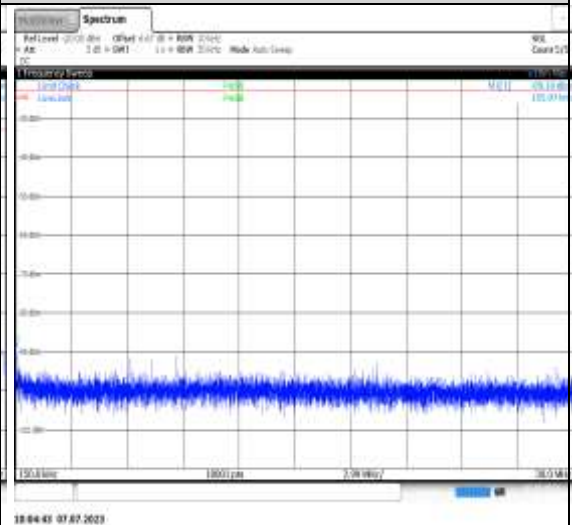
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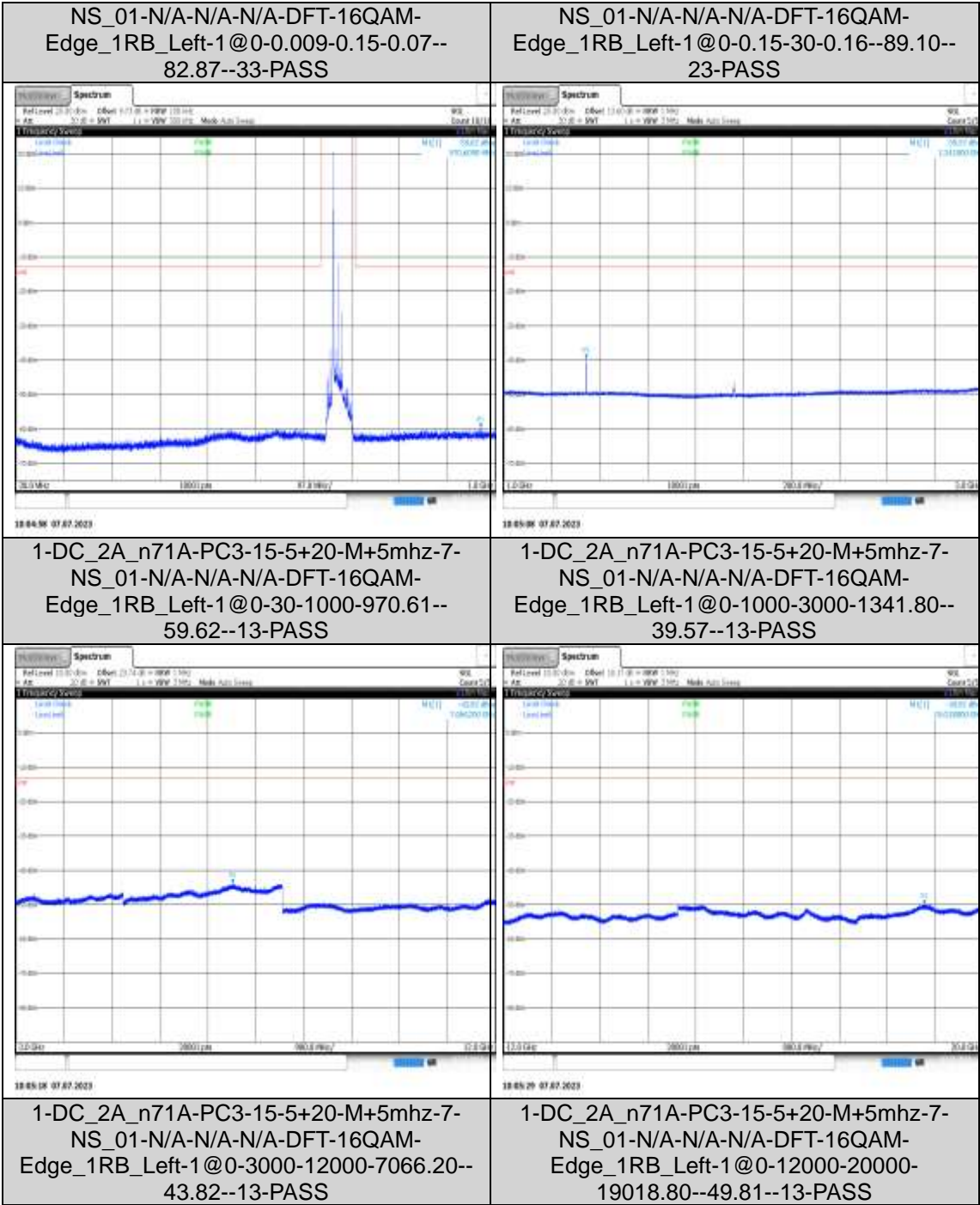
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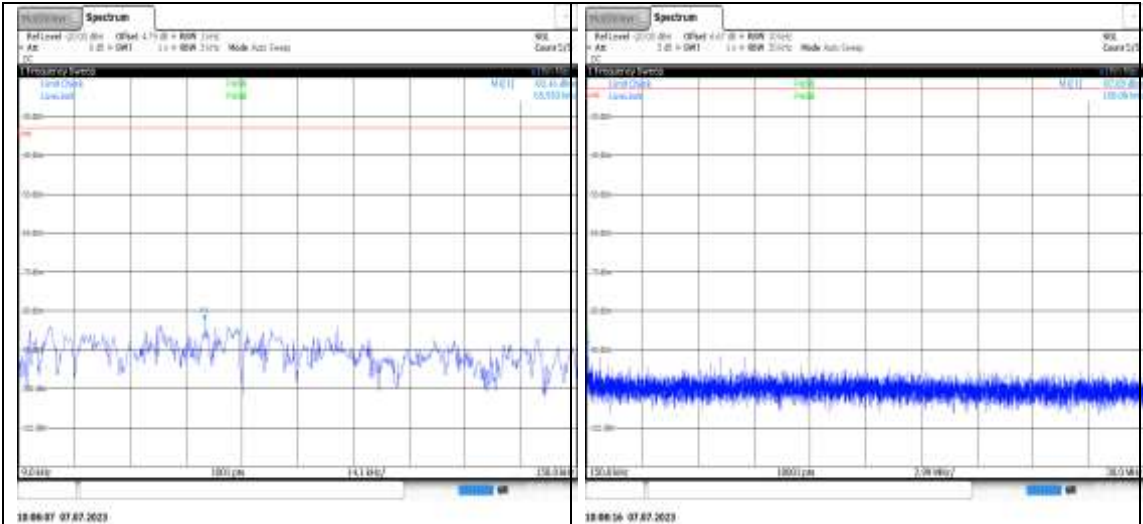


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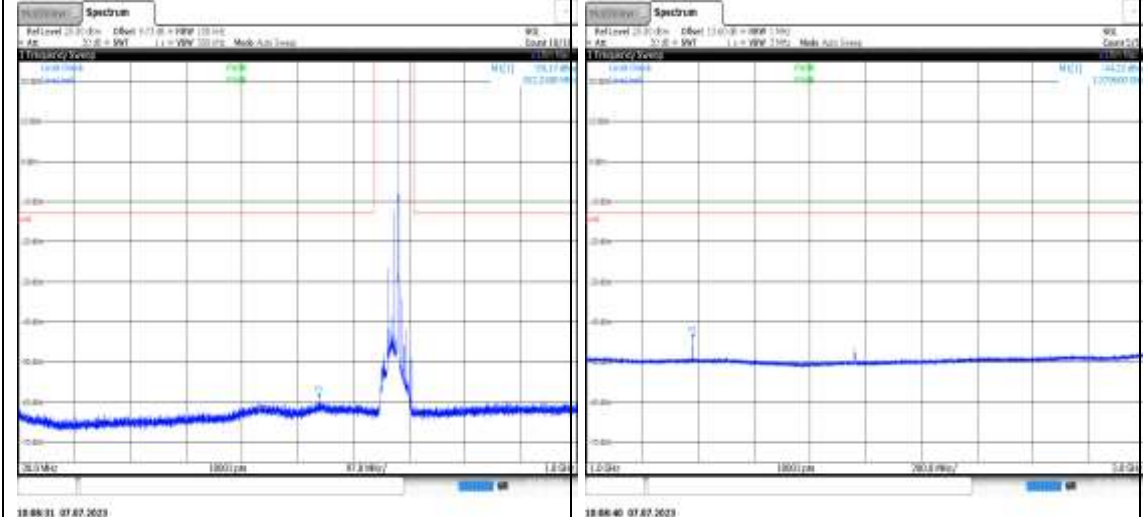


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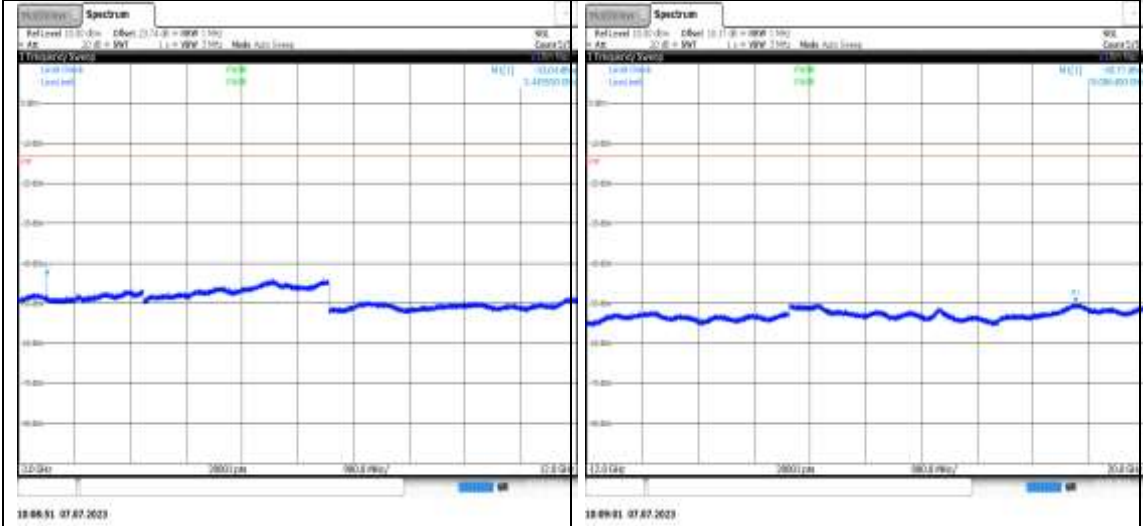




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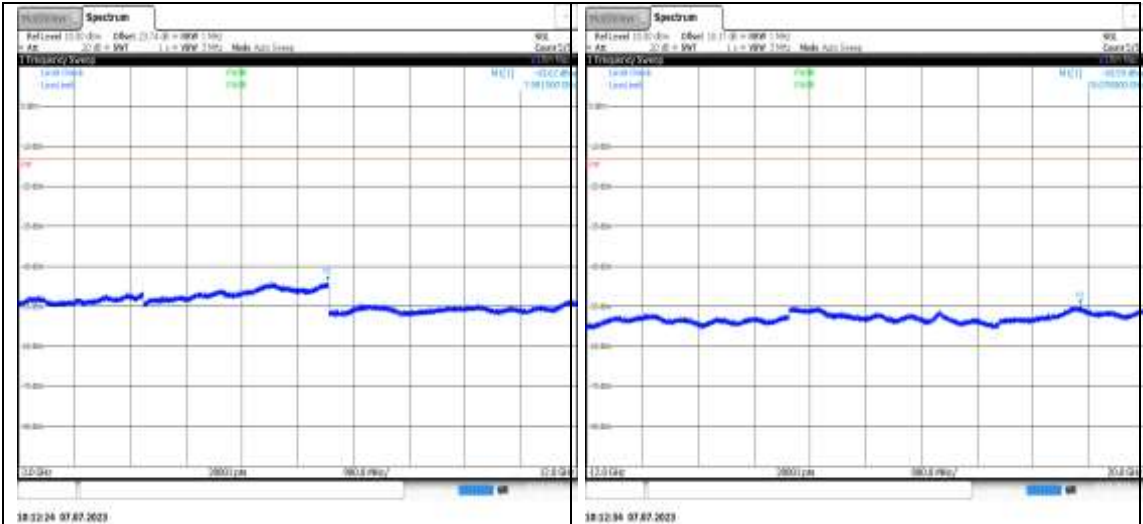


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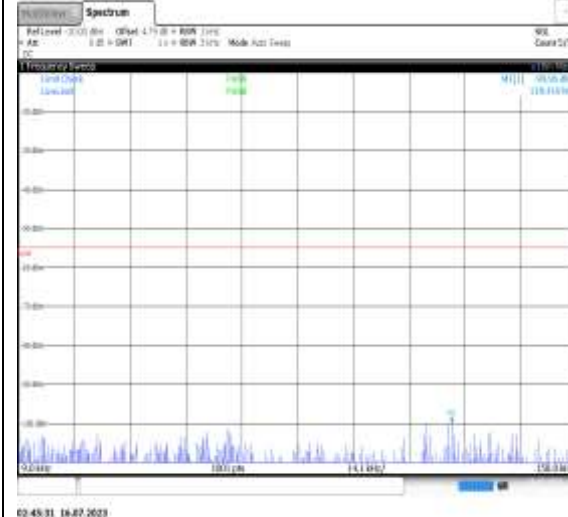
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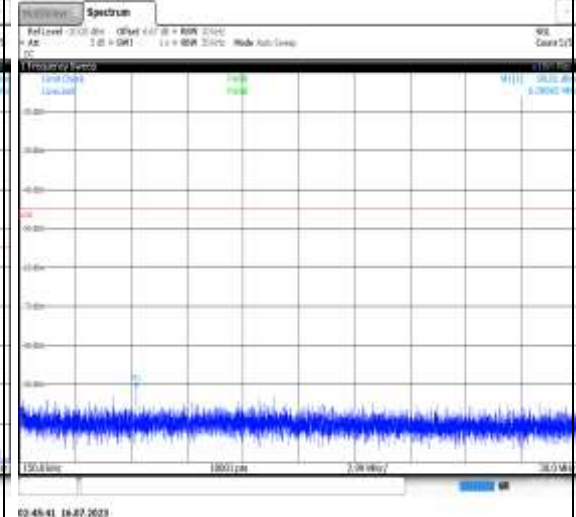


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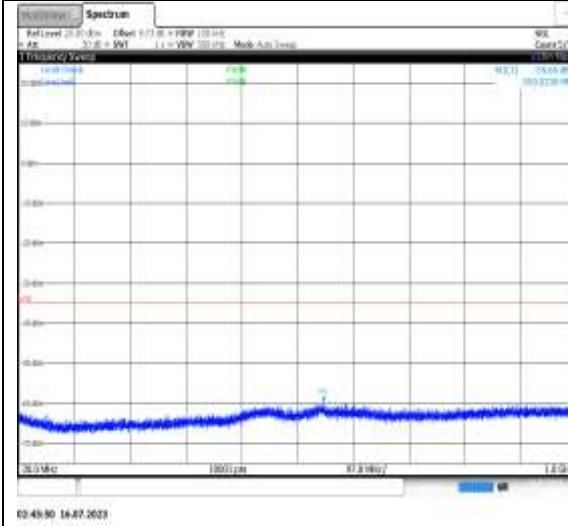
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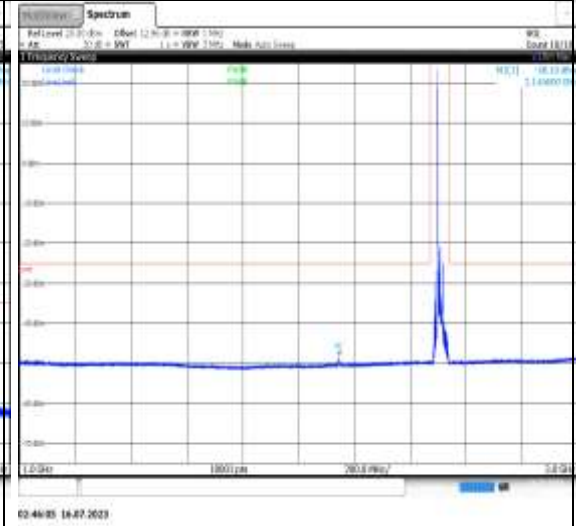
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1-DC_66A_n41A-PC3-30-5+20-M+L-1-NS_01-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_Left-1@0-0.15-30-0.19--90.81--45-PASS

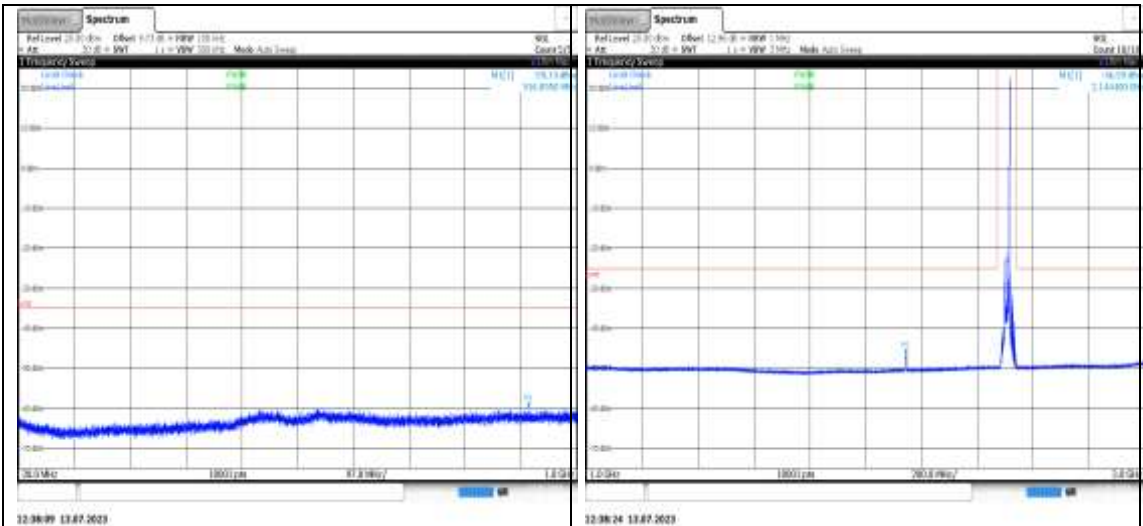


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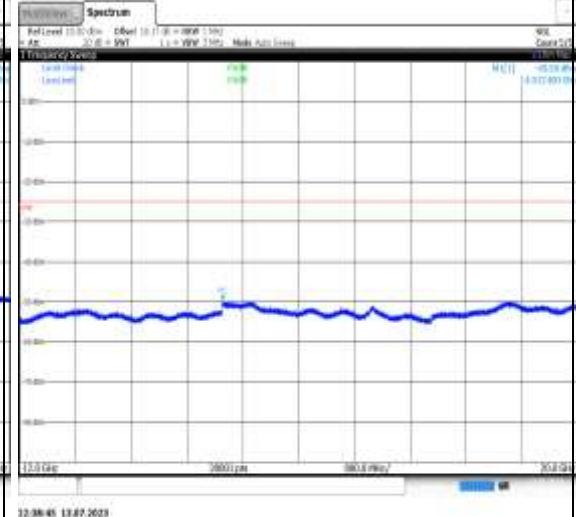
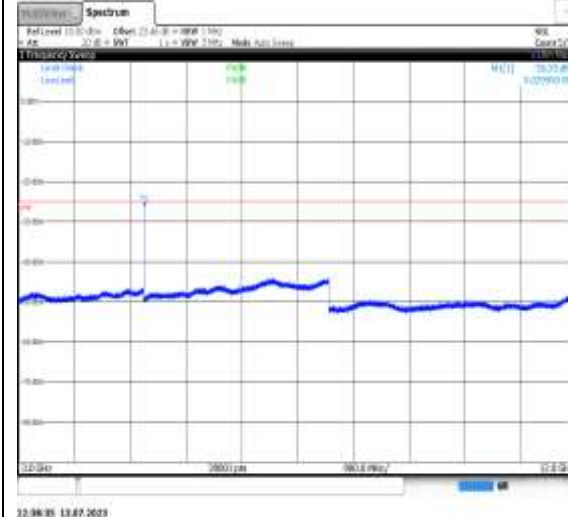
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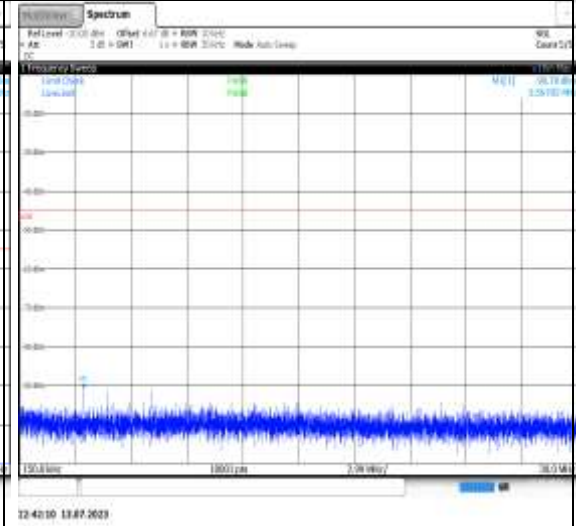
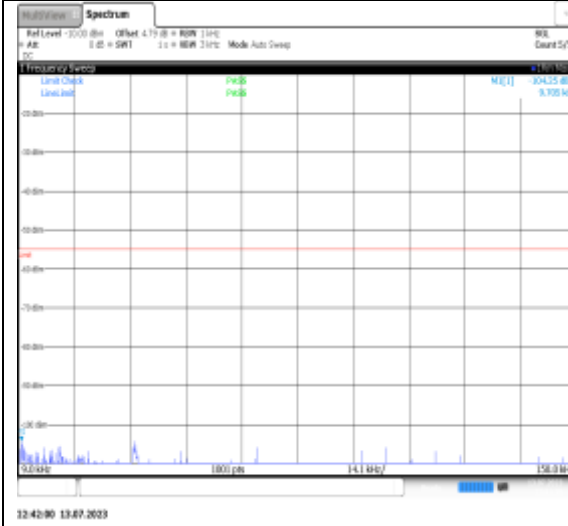
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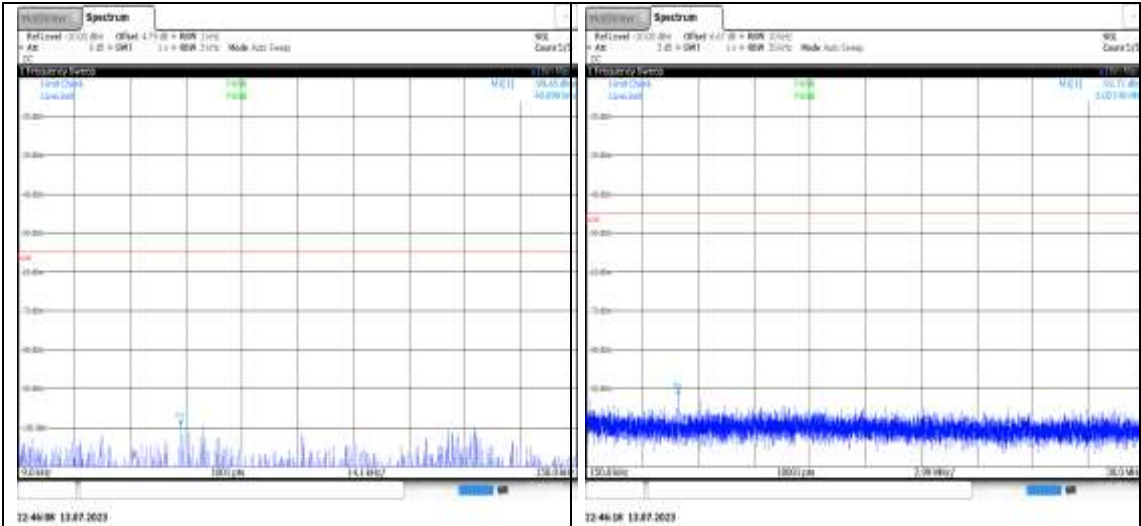
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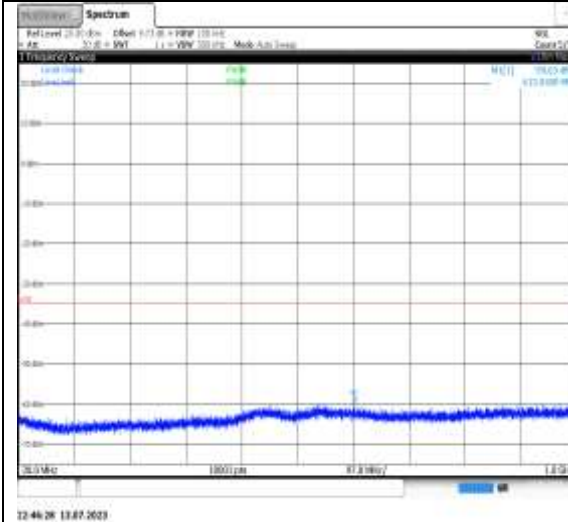
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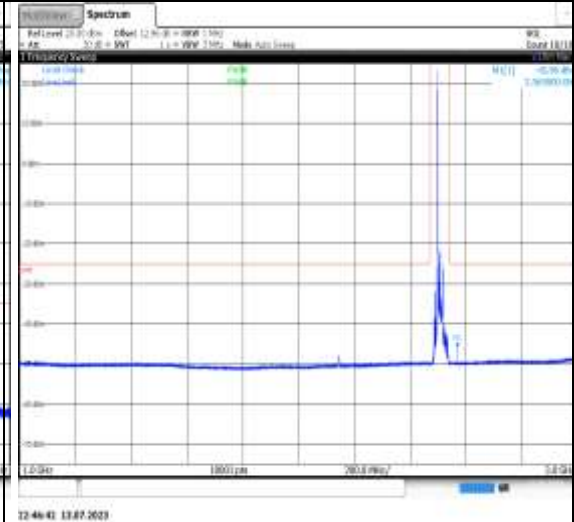


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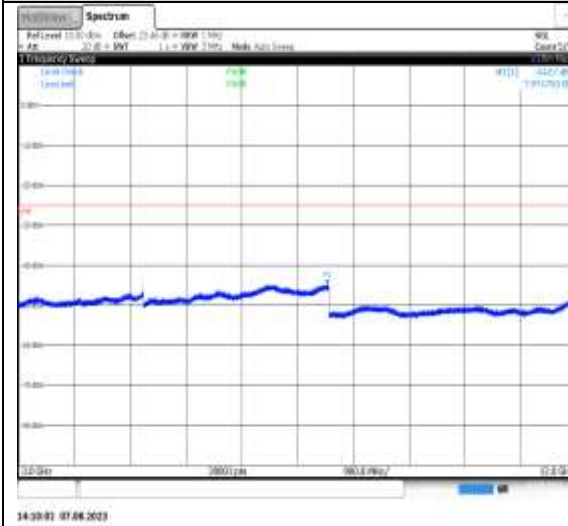
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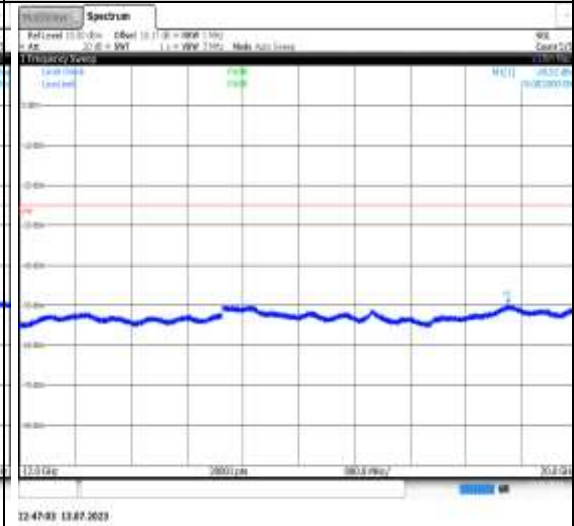
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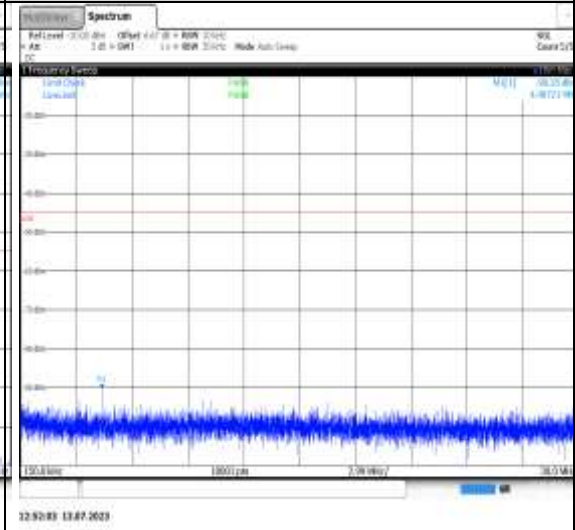
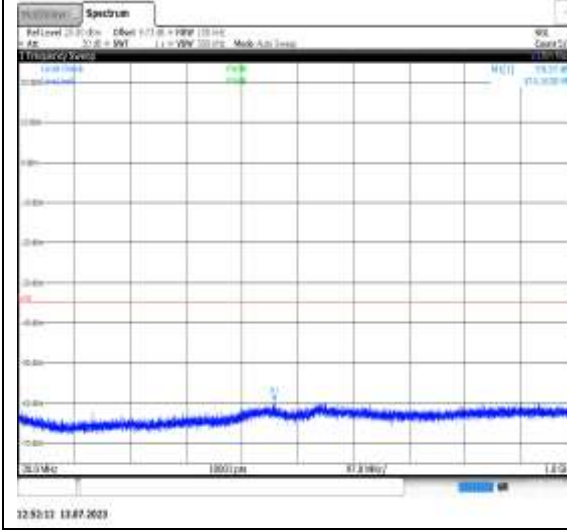
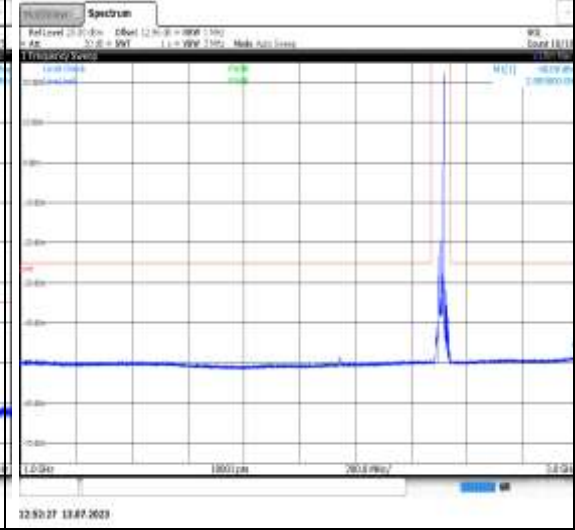
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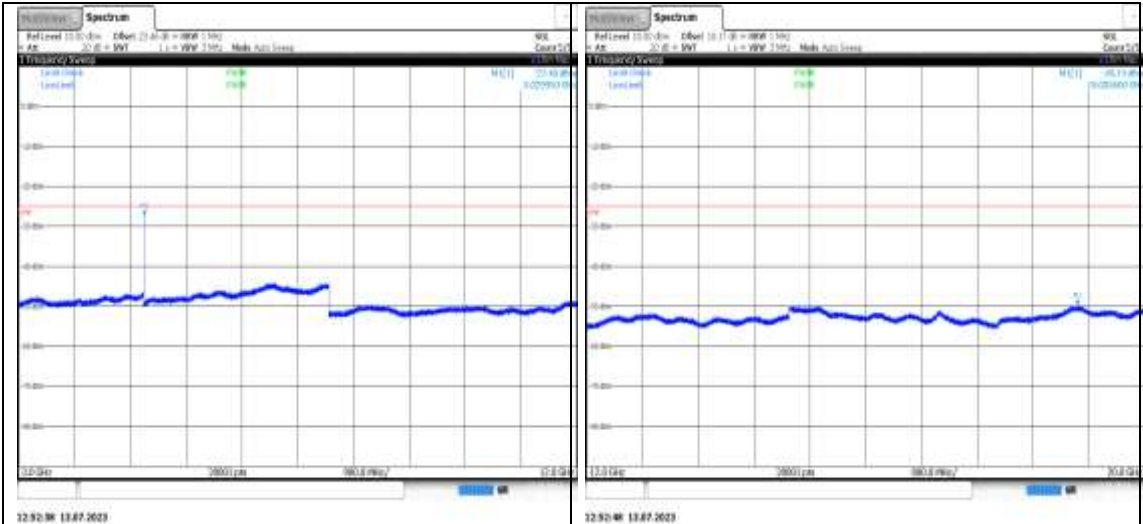


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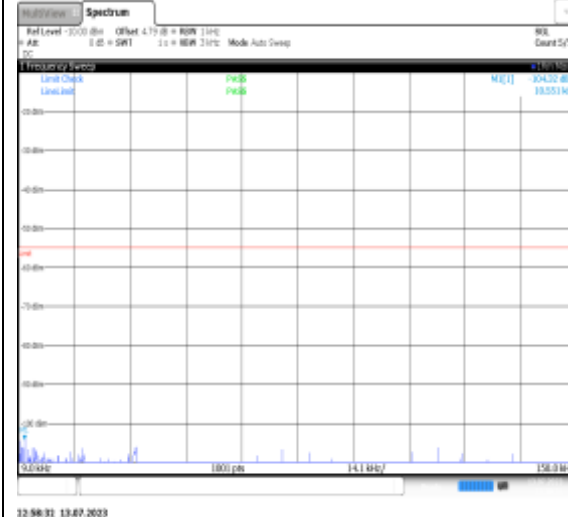
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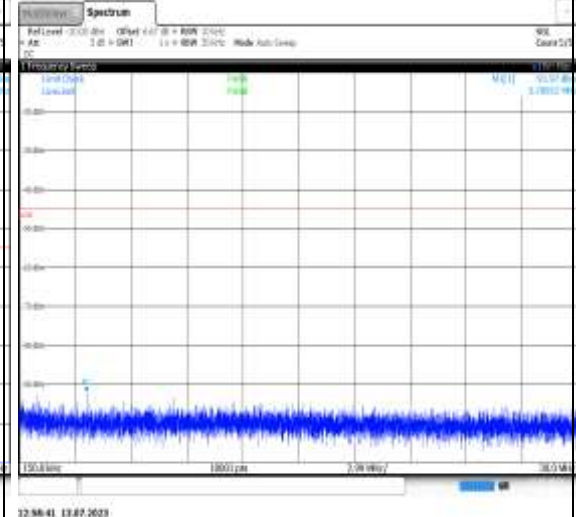


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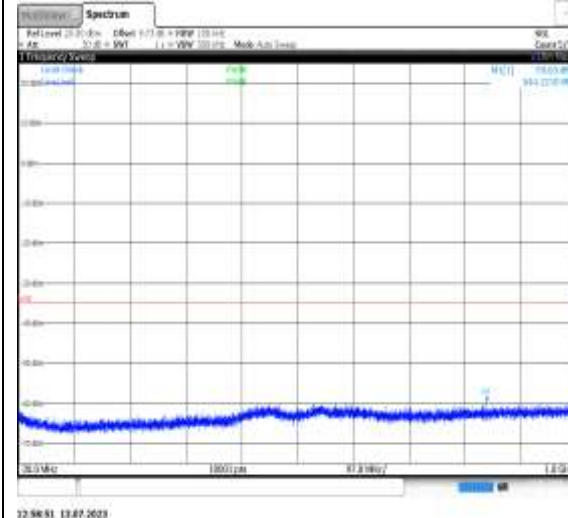
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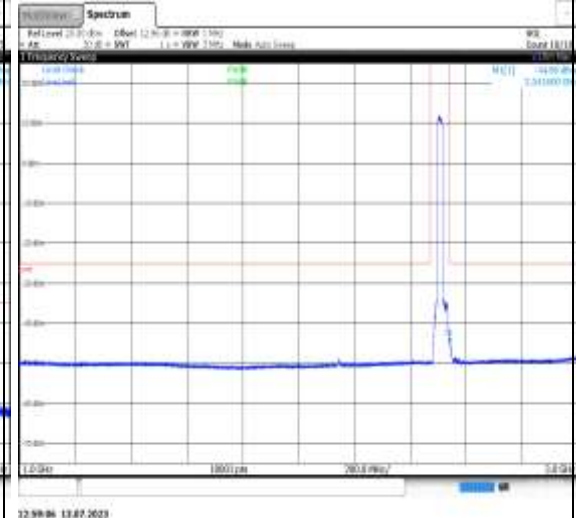
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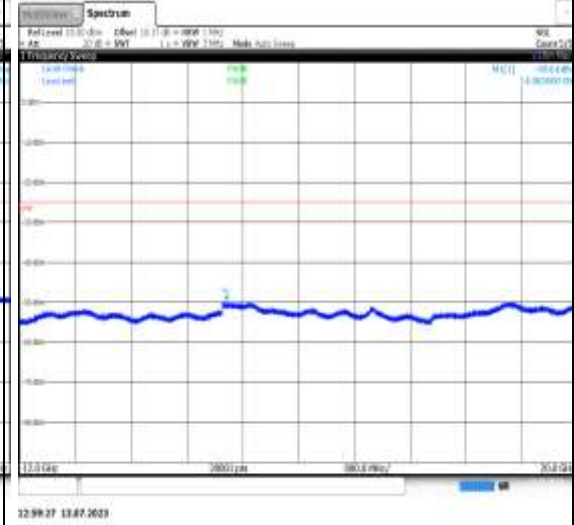
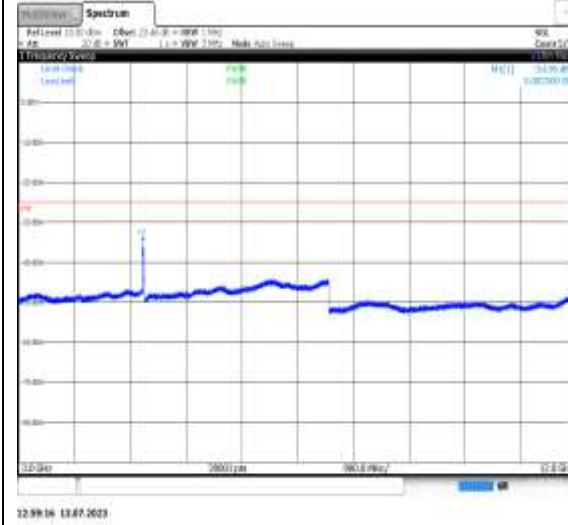


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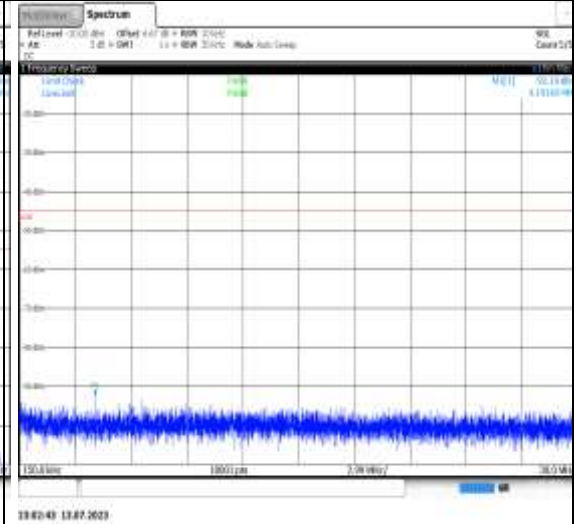
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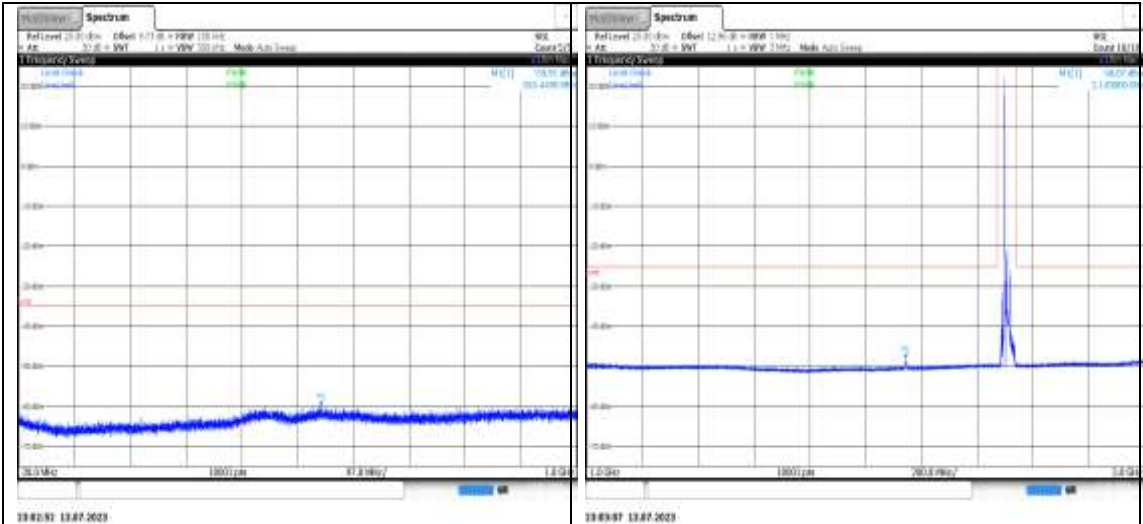
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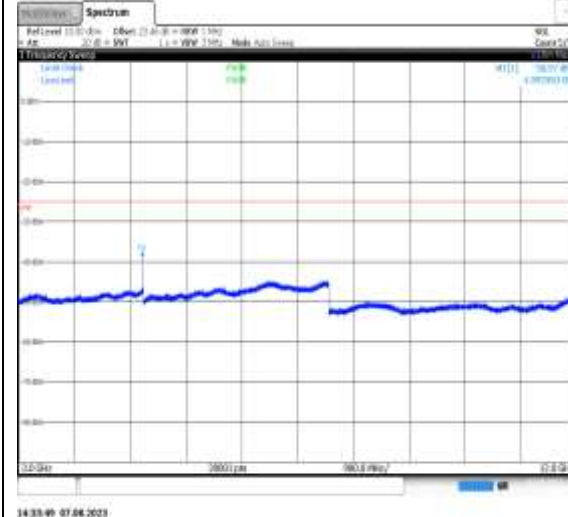
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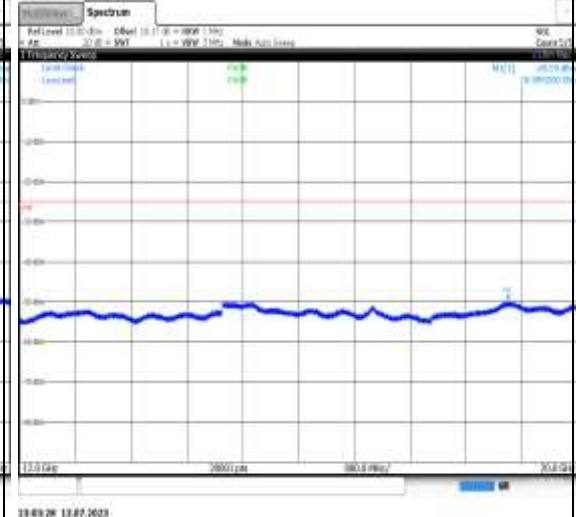


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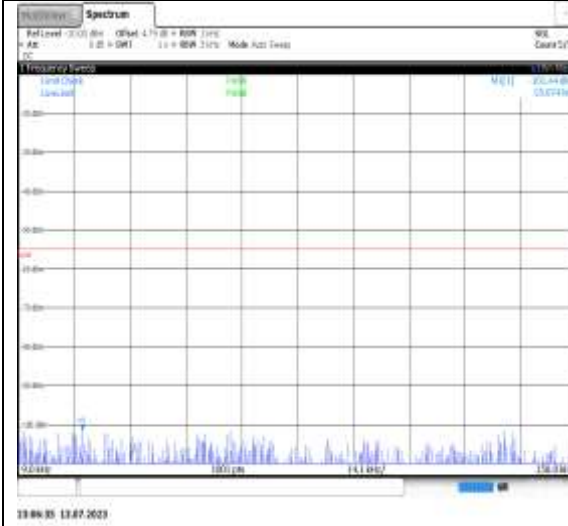
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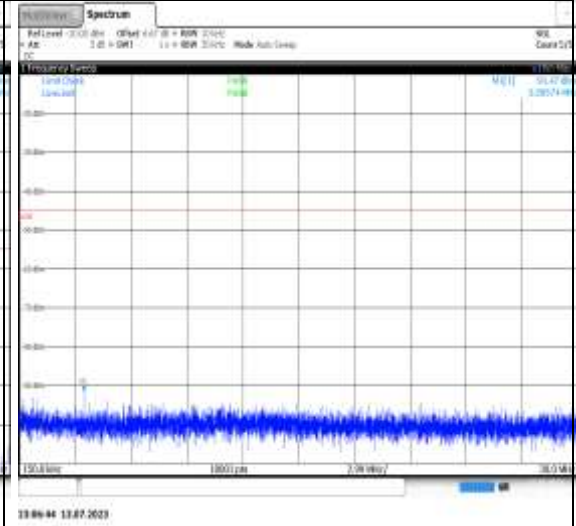
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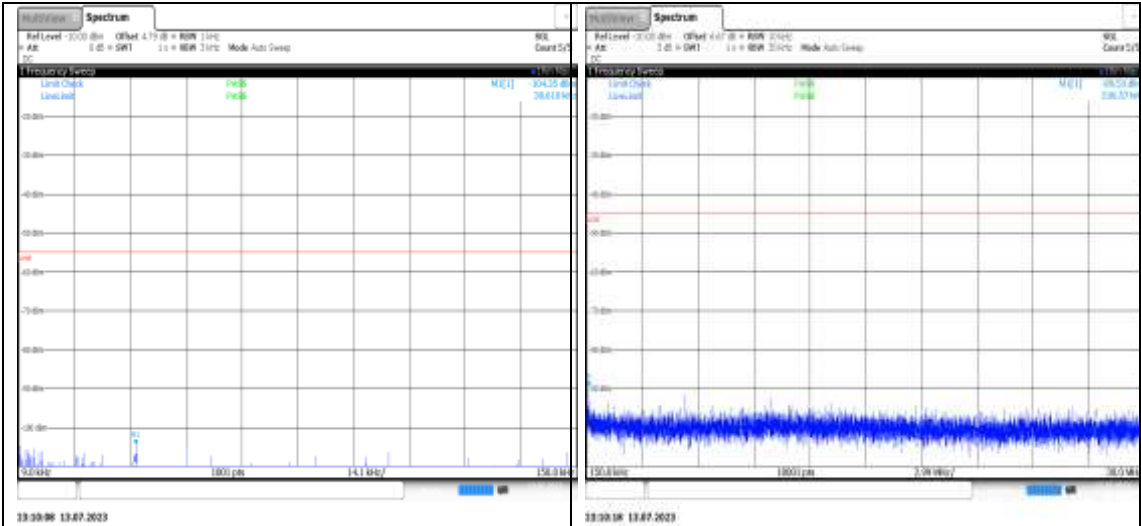


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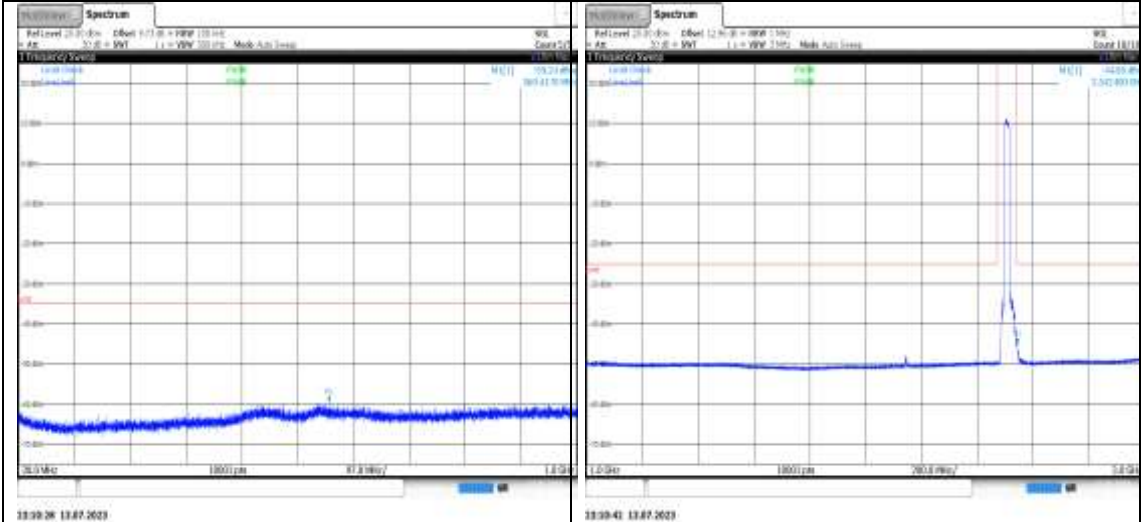
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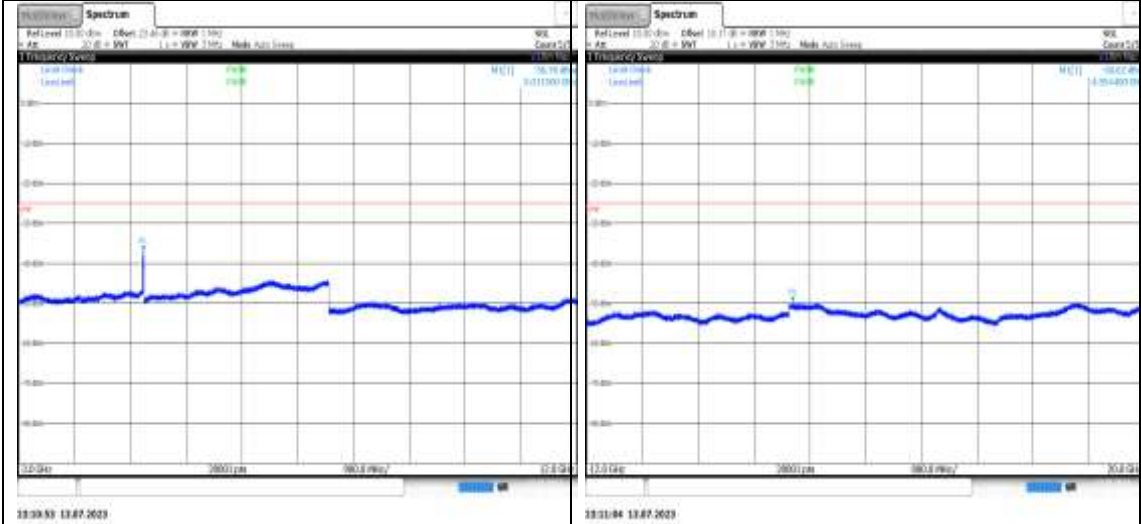
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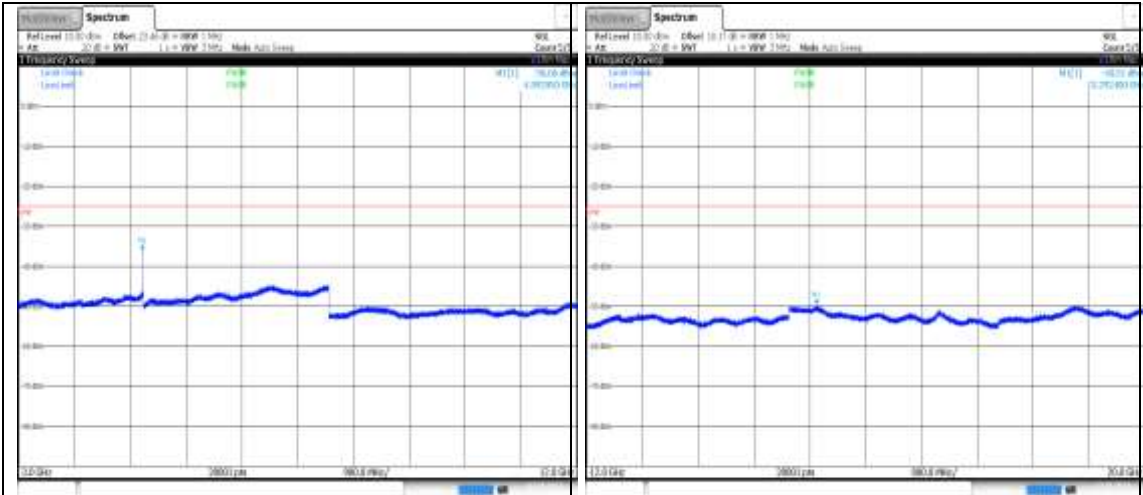
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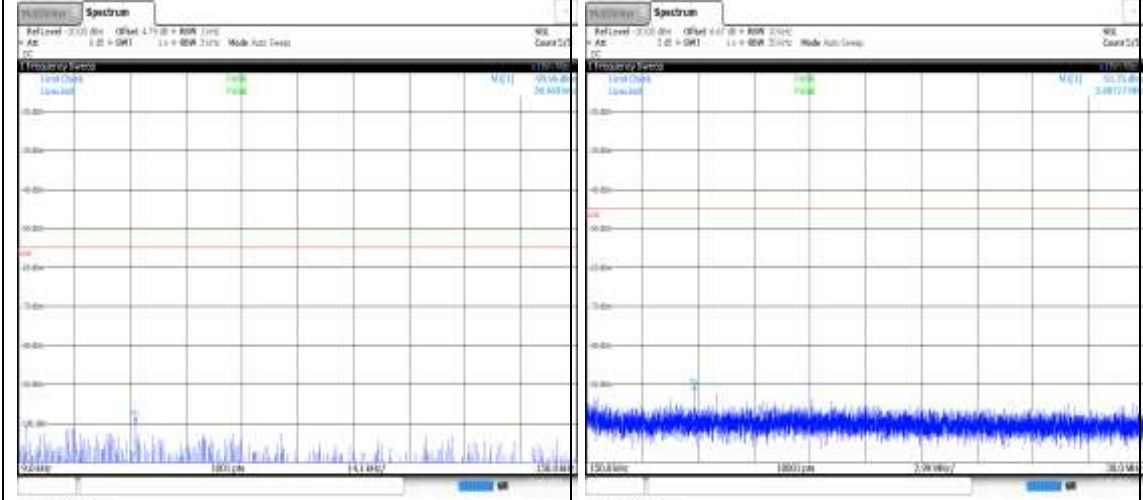
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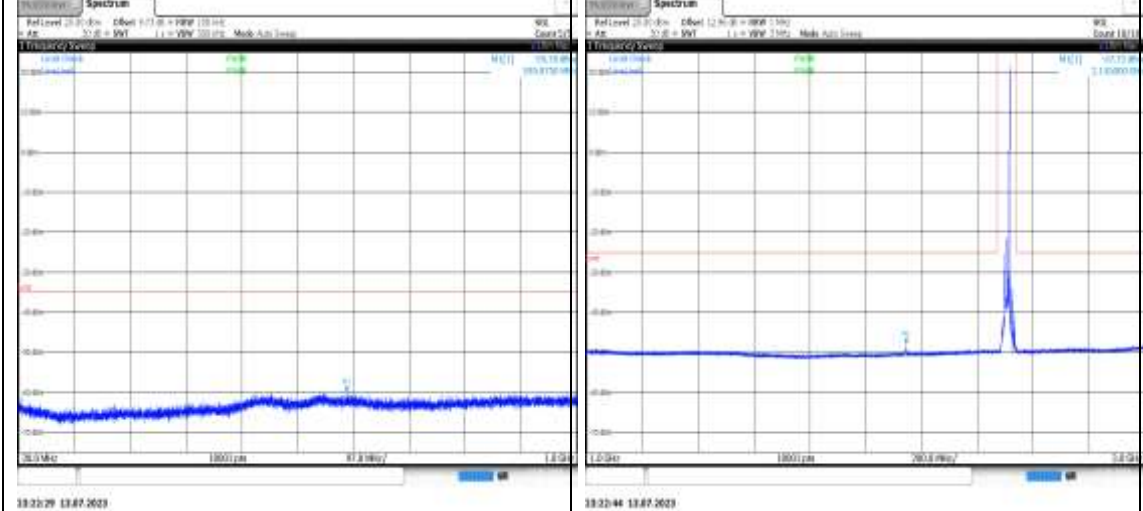
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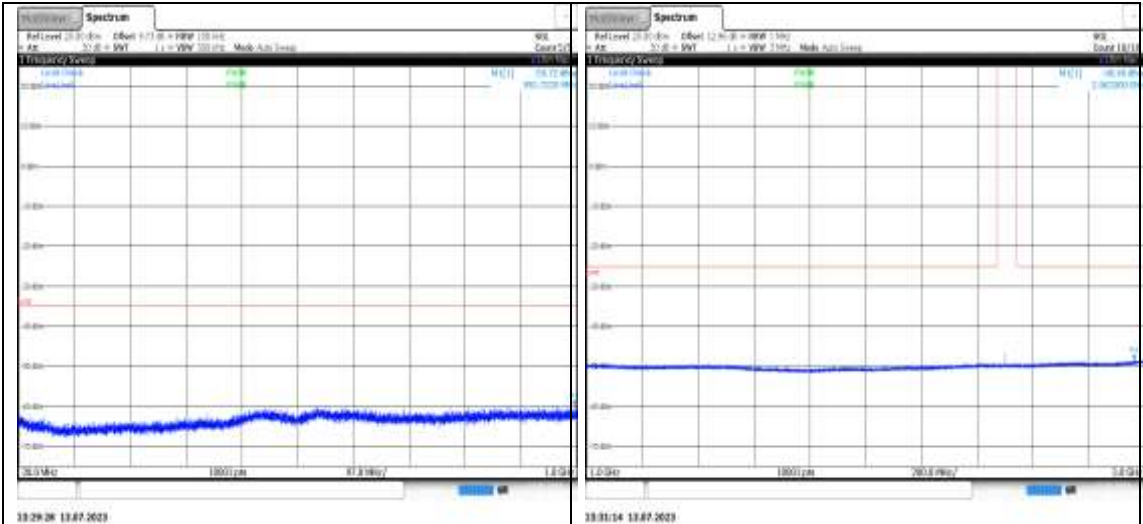
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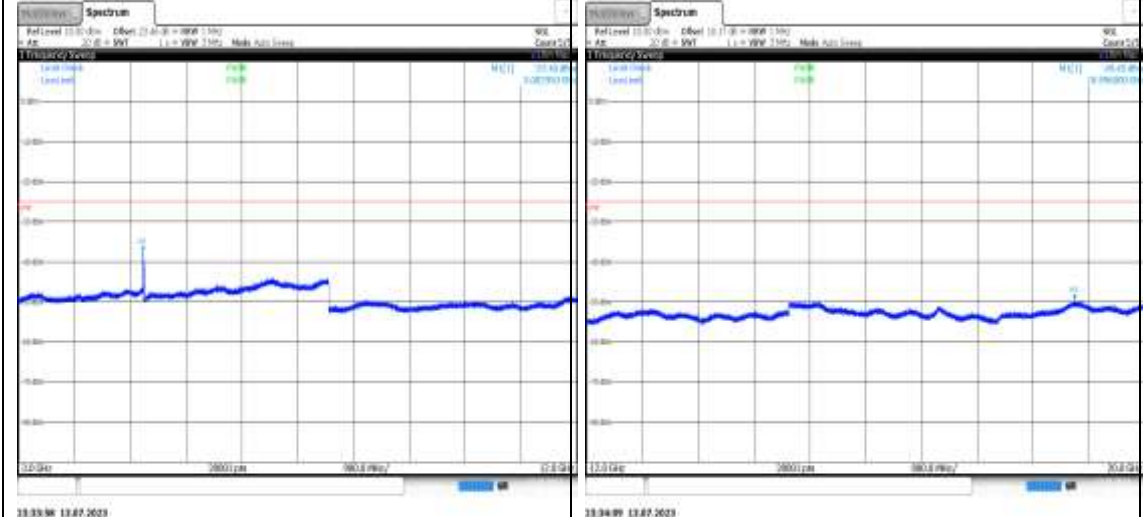
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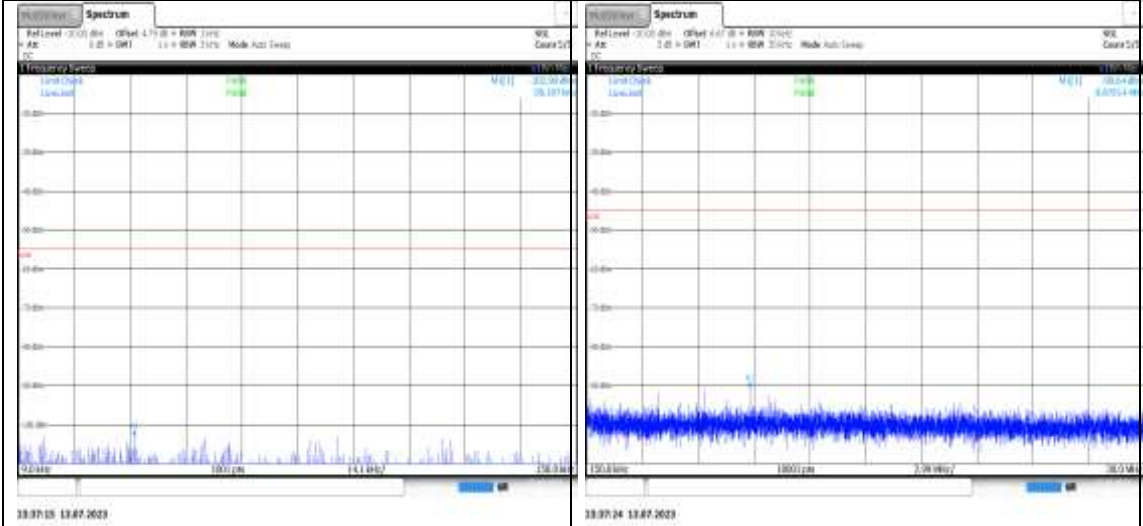
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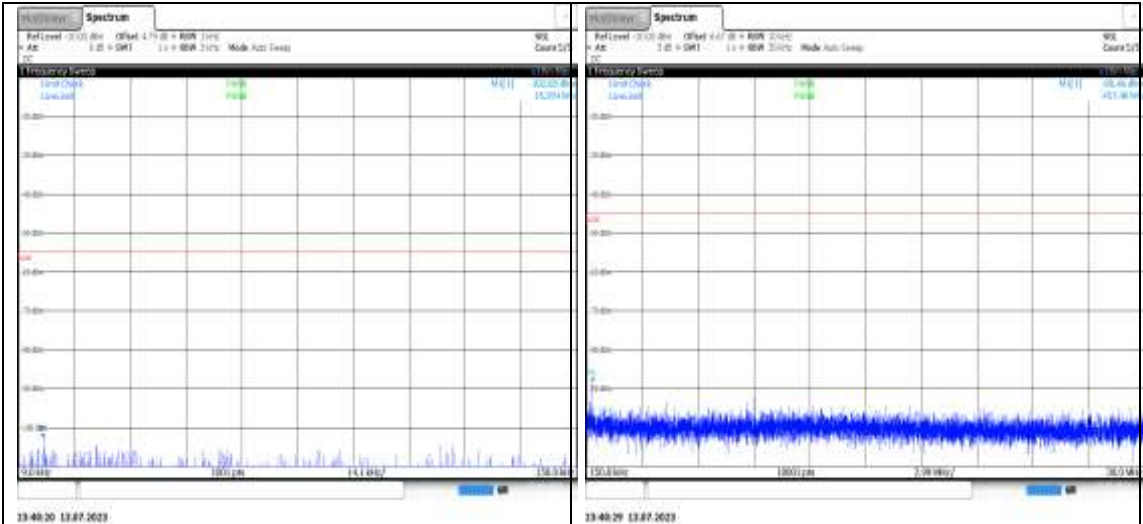
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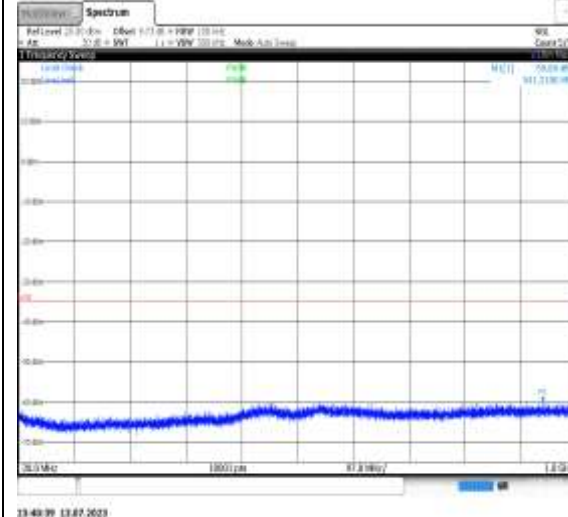
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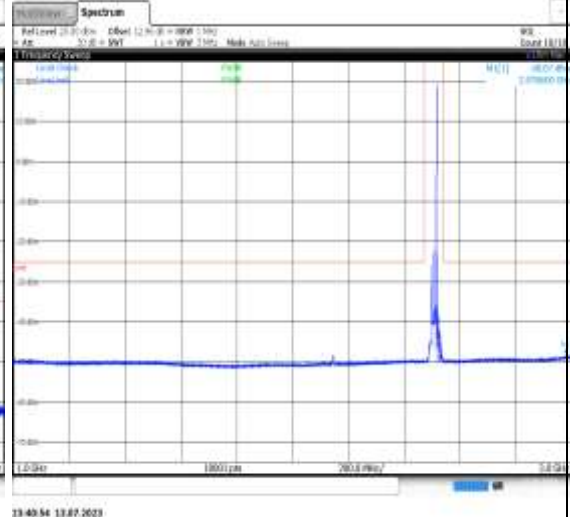


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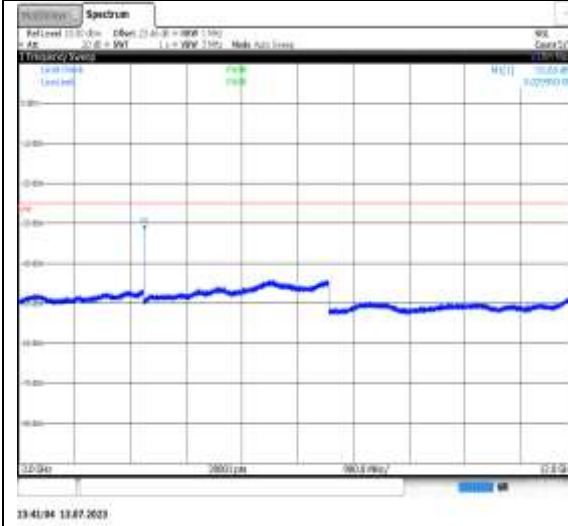
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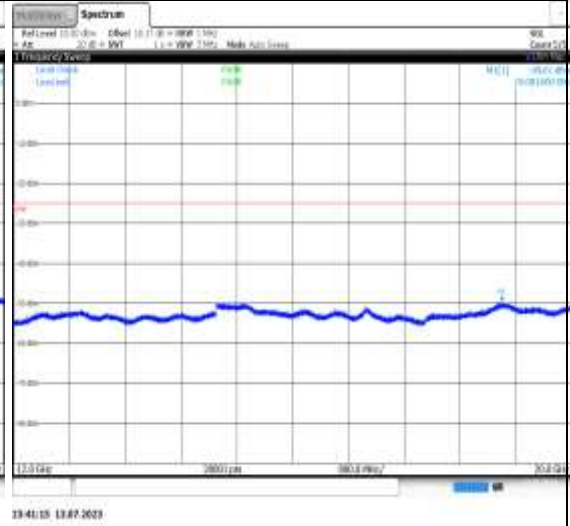
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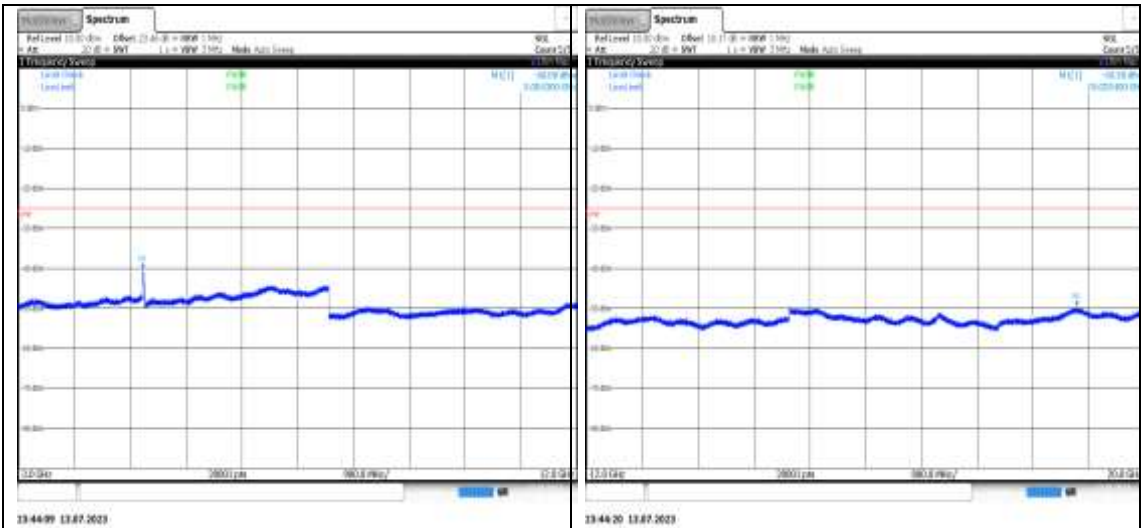


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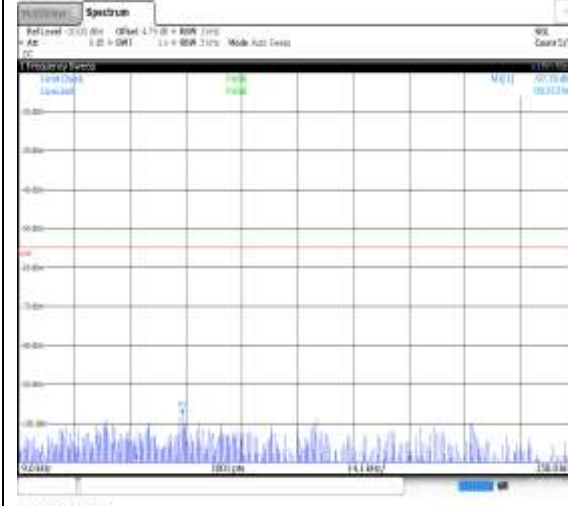
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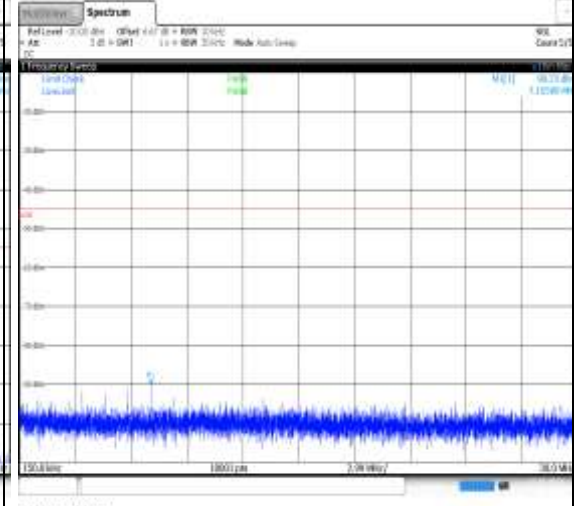


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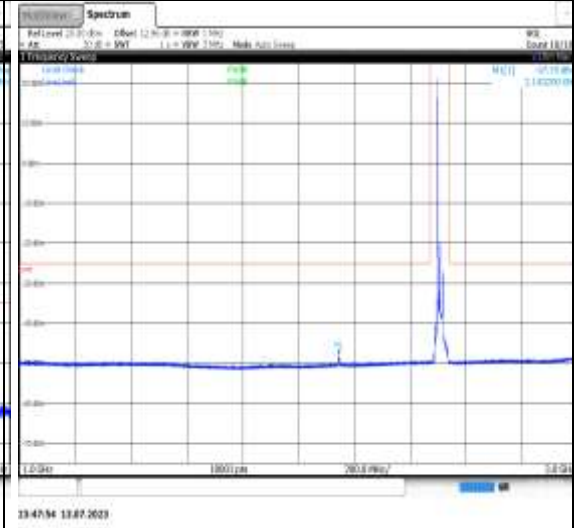
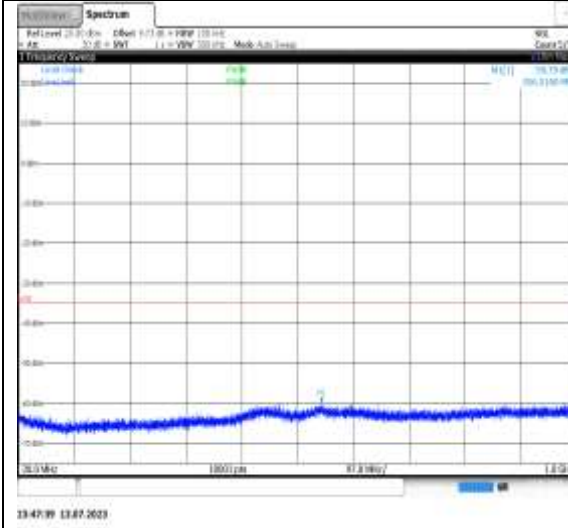
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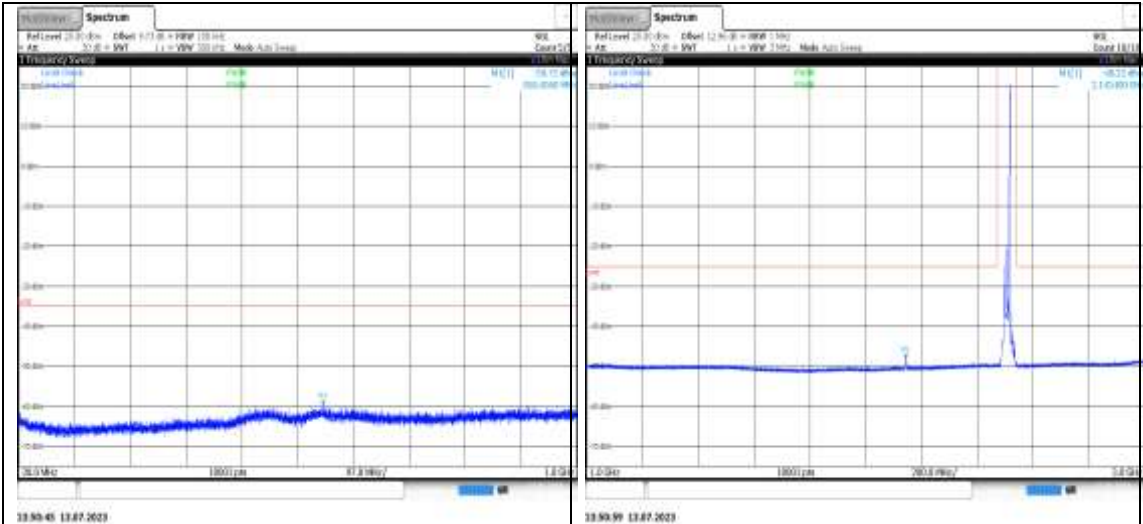
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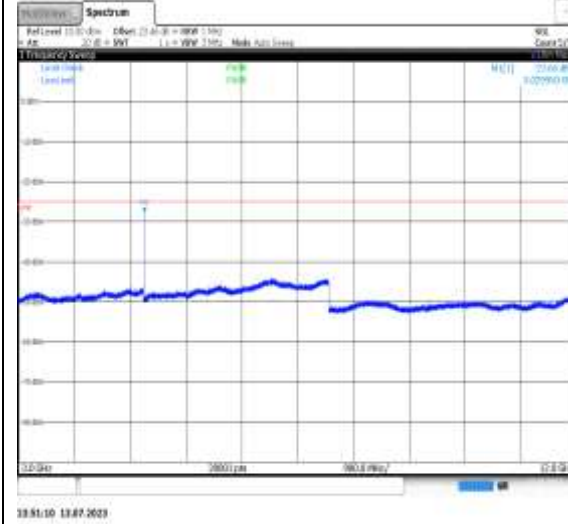


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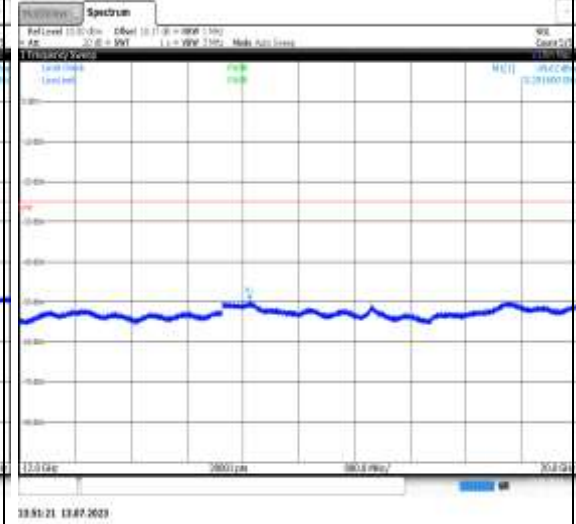


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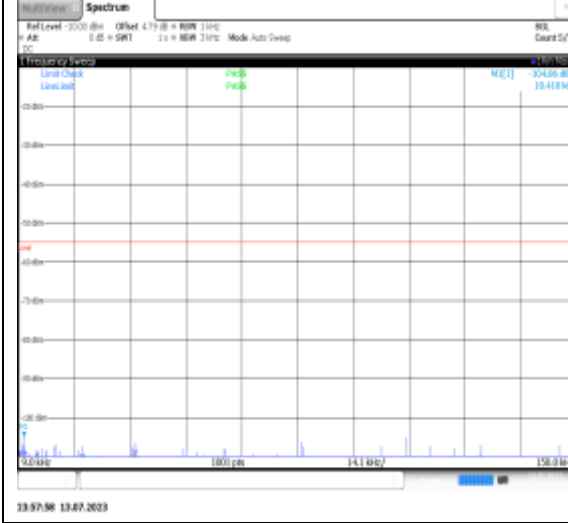
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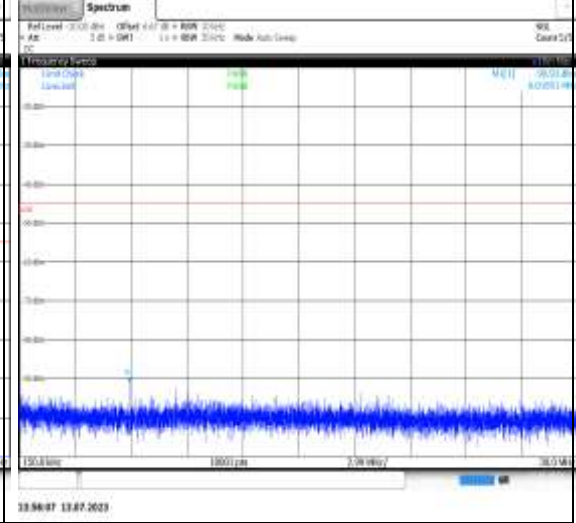
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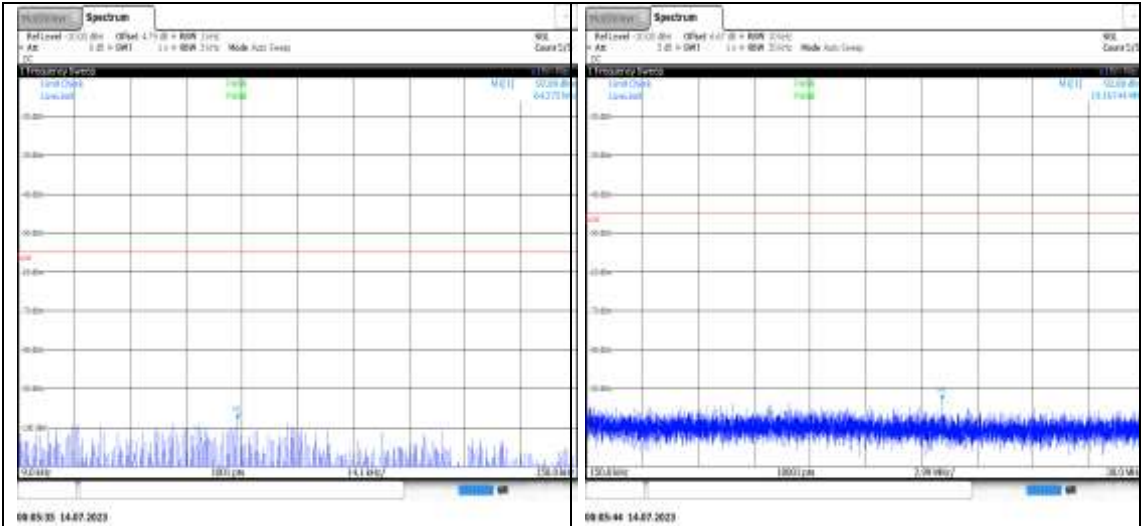


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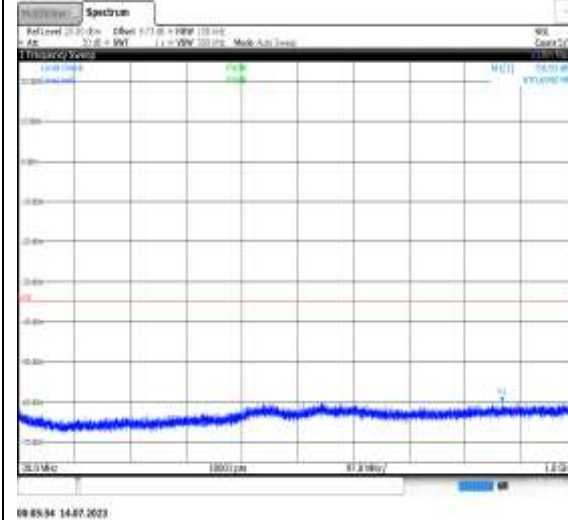
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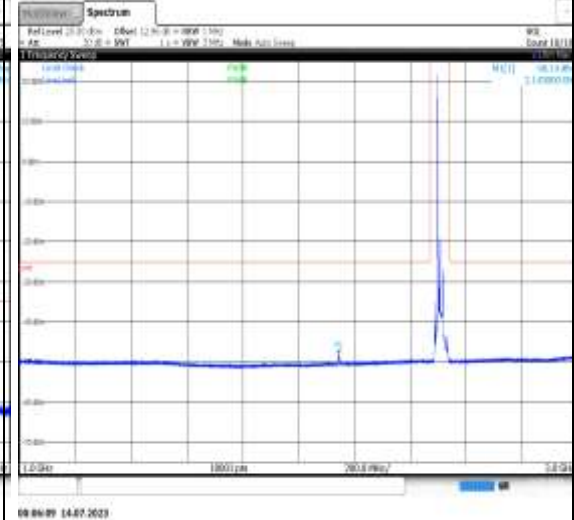


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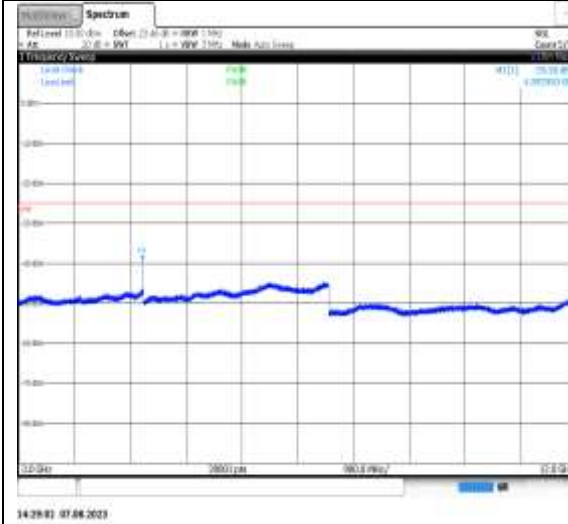
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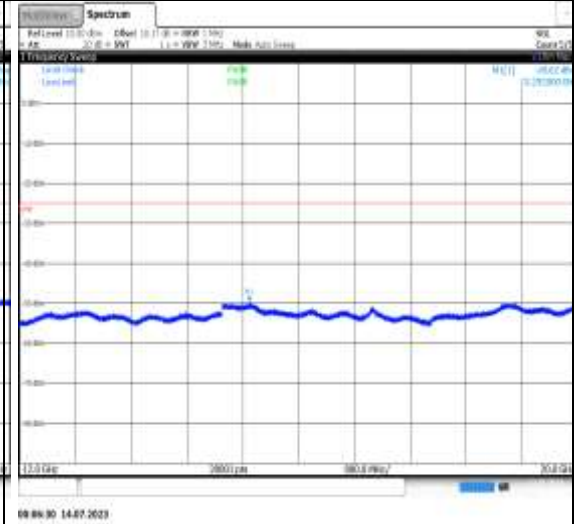
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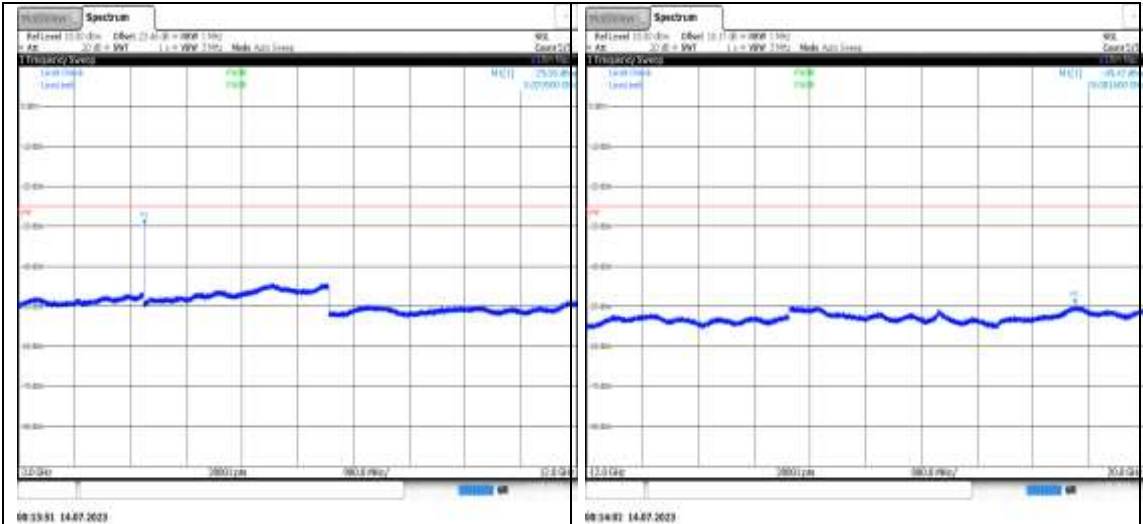


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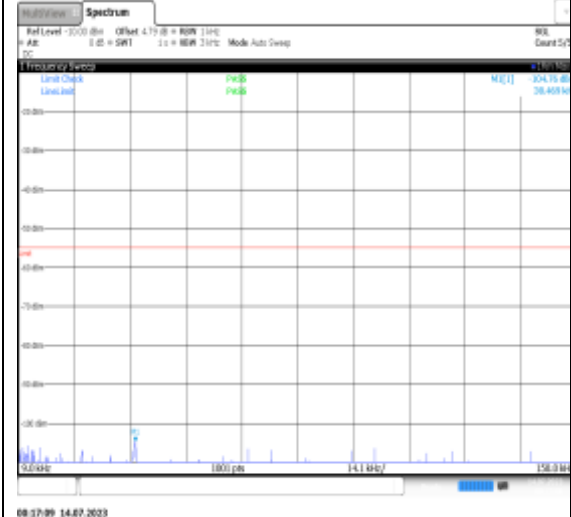
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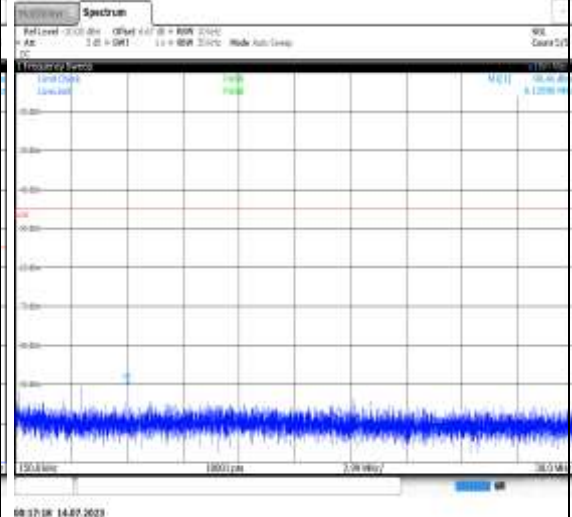


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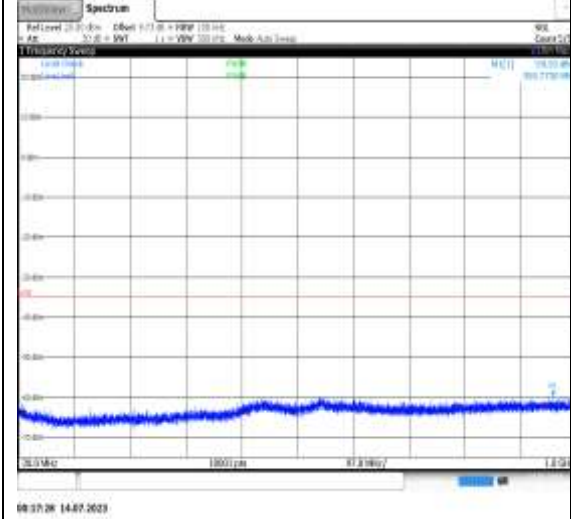
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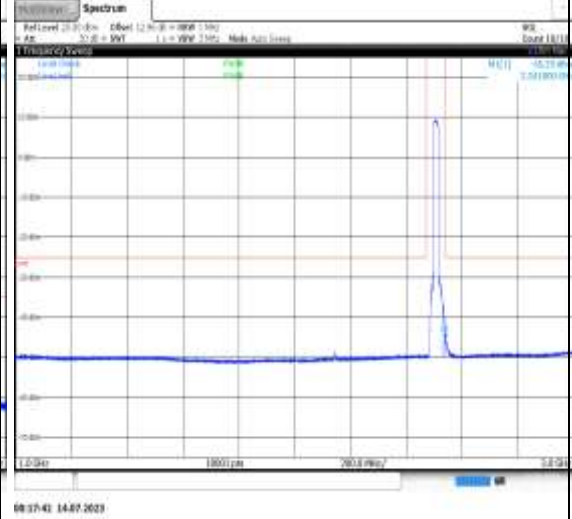
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1-DC_66A_n41A-PC3-30-5+20-M+L-21-NS_01-N/A-N/A-N/A-CP-16QAM-Outer_Full-51@0-0.15-30-6.13--90.46--45-PASS

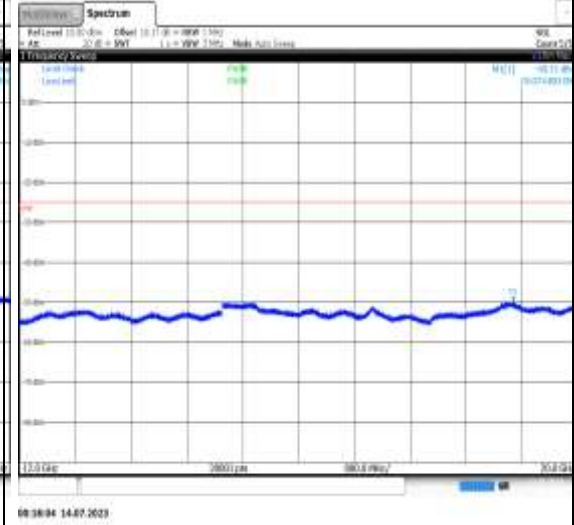
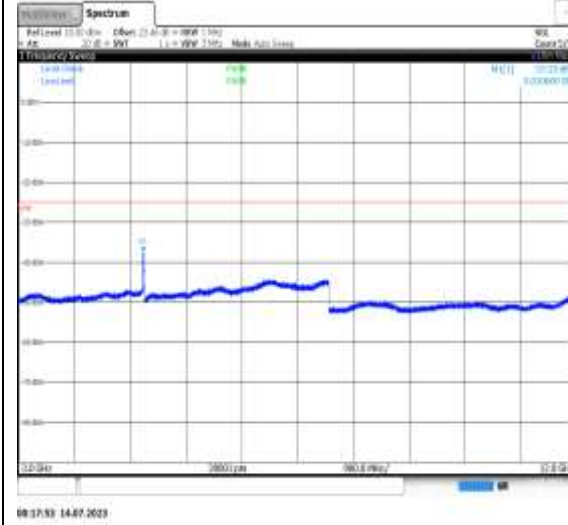


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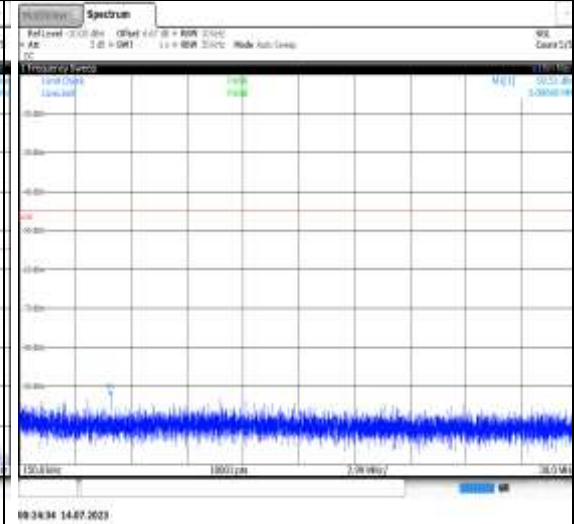
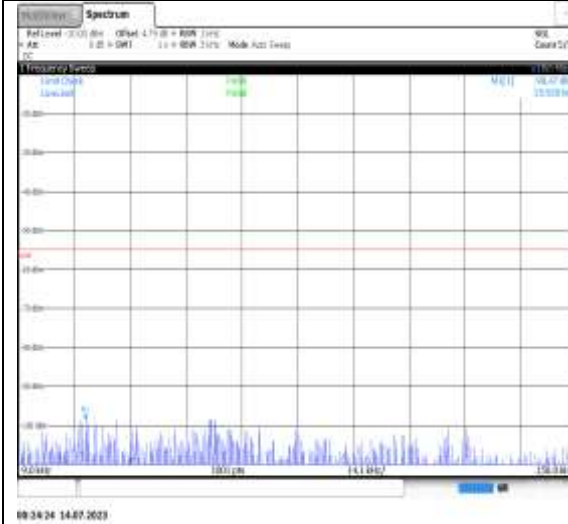
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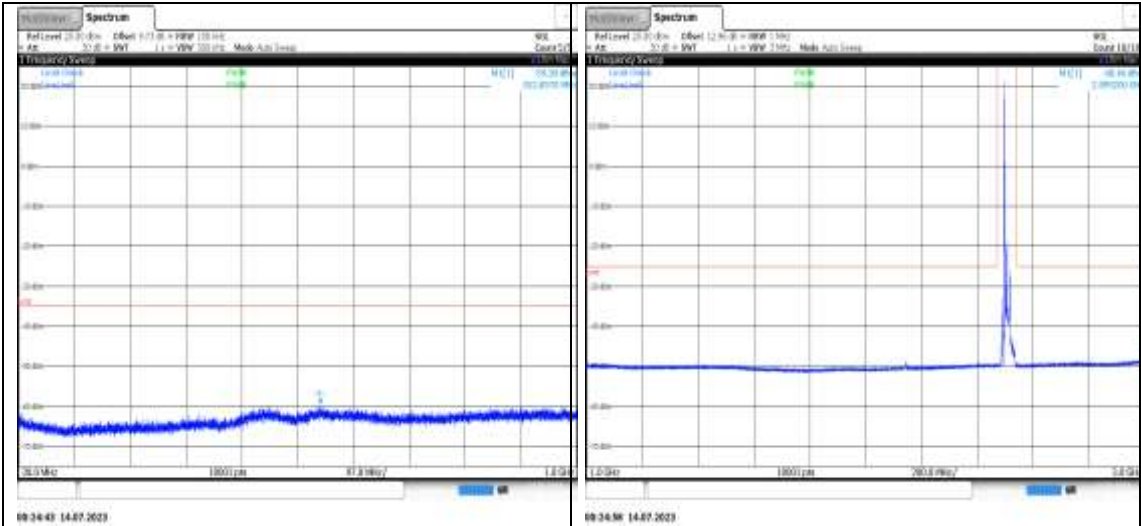
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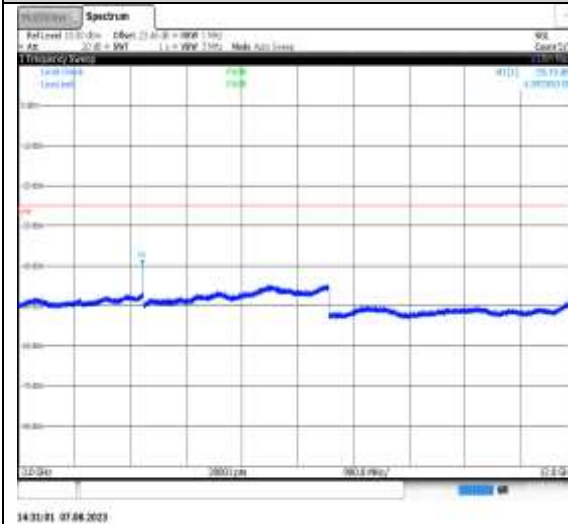
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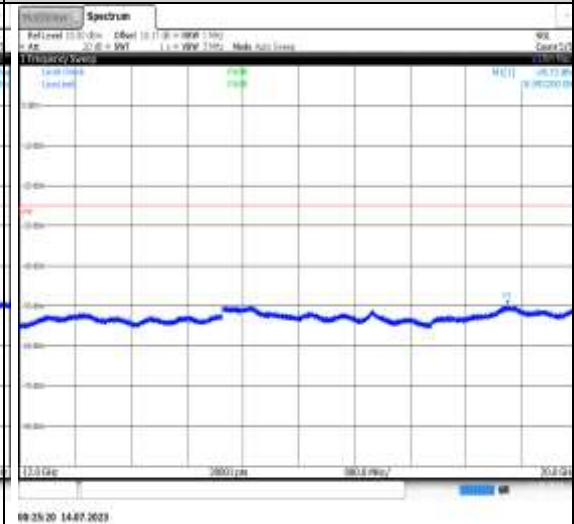


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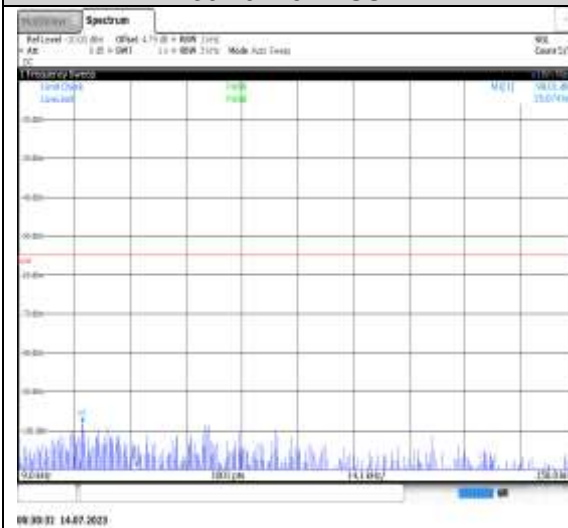
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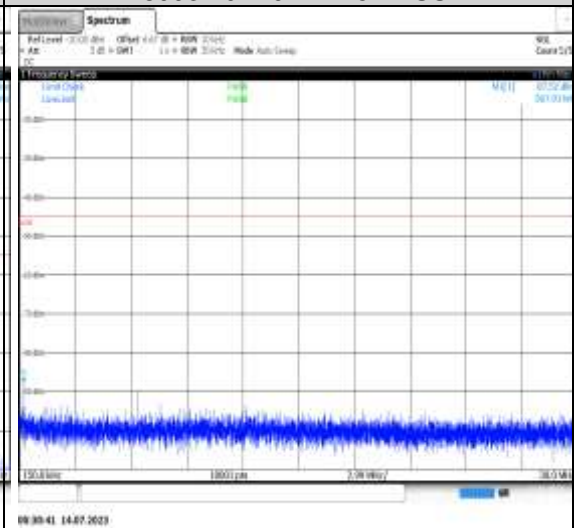
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1-DC_66A_n41A-PC3-30-5+20-M+L-22-NS_01-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Left-1@0-12000-20000-18993.20--49.72--25-PASS

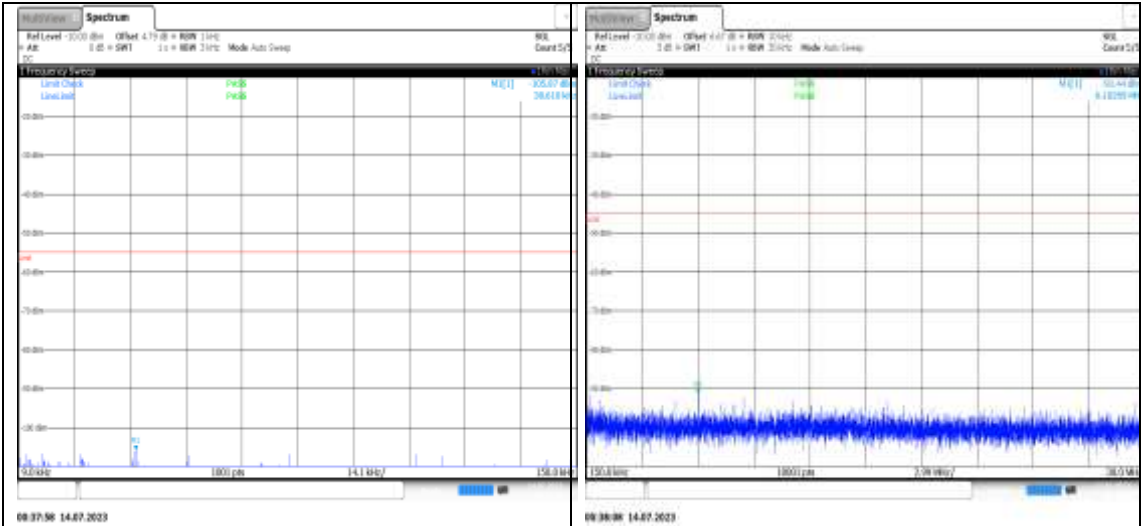


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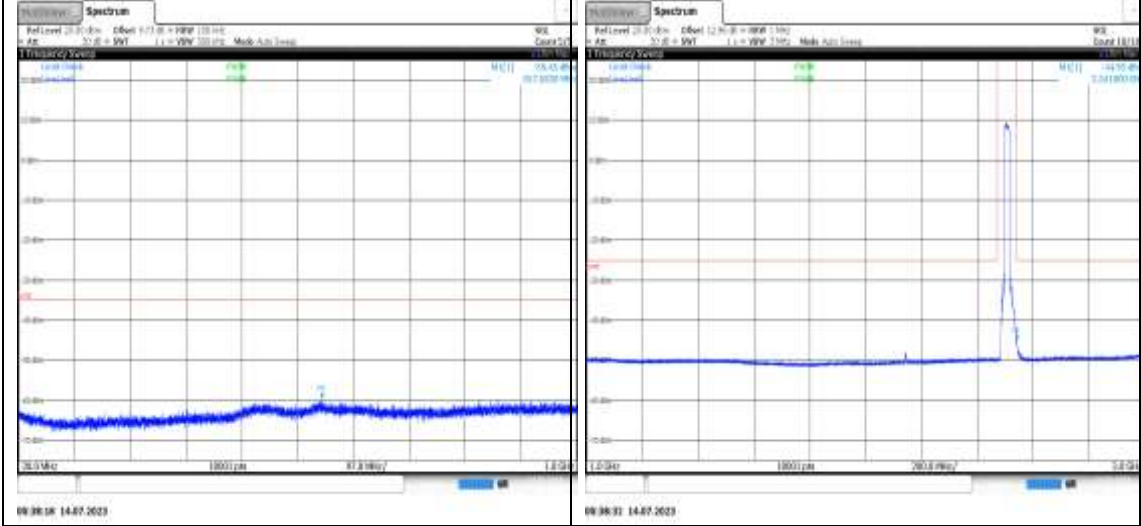


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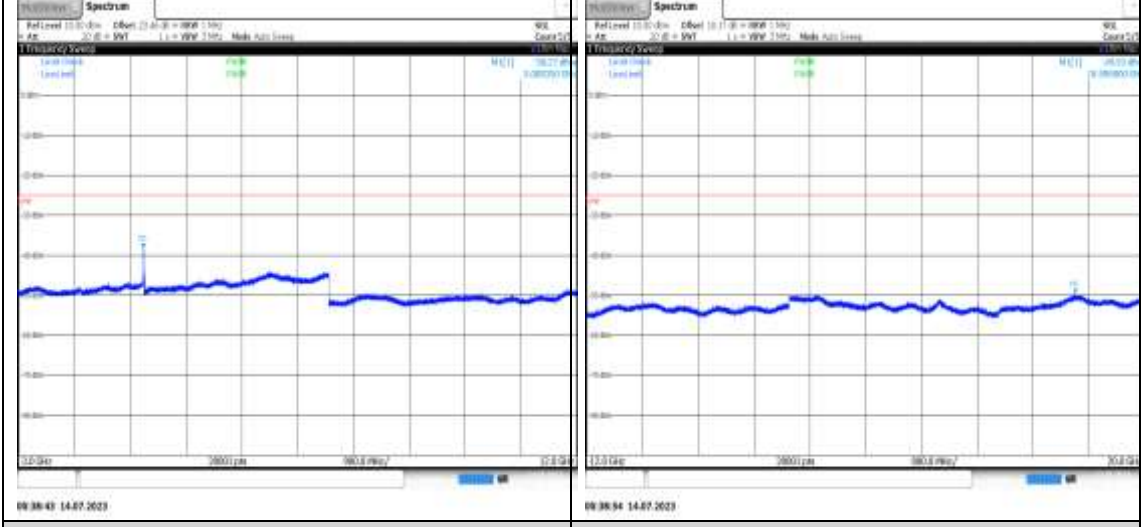
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1-DC_66A_n41A-PC3-30-5+20-M+L-24-NS_01-N/A-N/A-N/A-CP-64QAM-Outer_Full-51@0-0.009-0.15-0.04--105.87--55-PASS

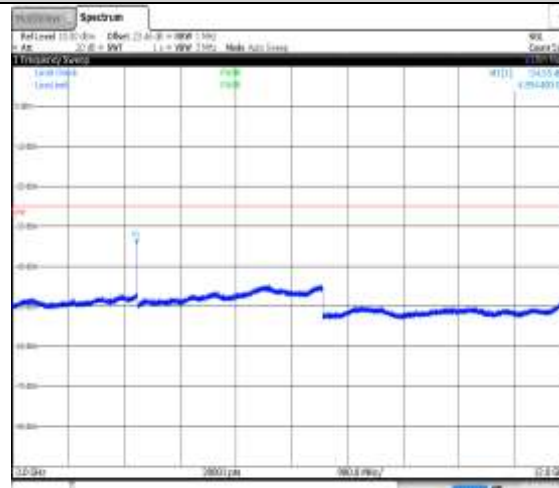


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1-DC_66A_n41A-PC3-30-5+20-M+L-24-NS_01-N/A-N/A-N/A-CP-64QAM-Outer_Full-

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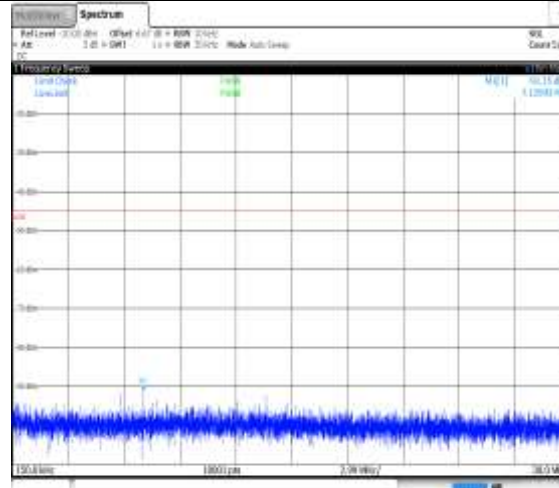


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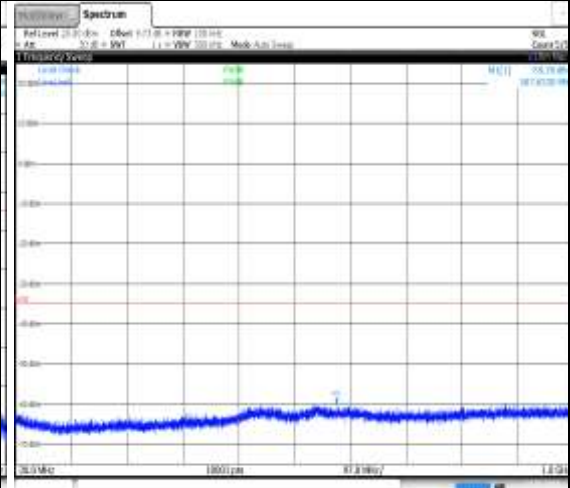
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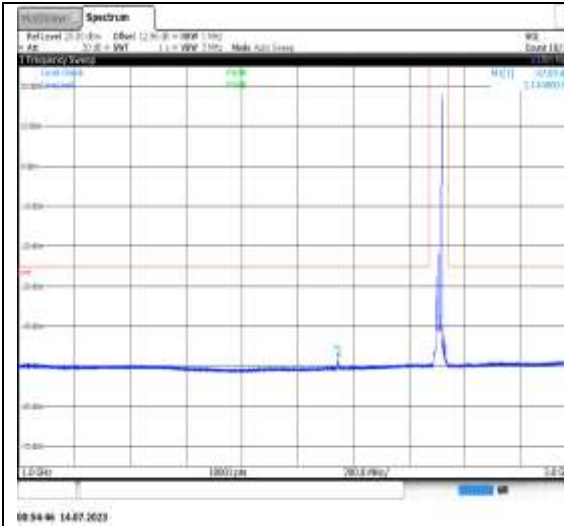
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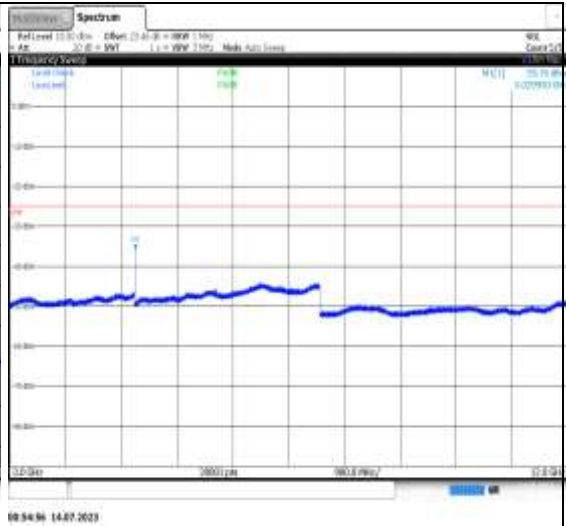
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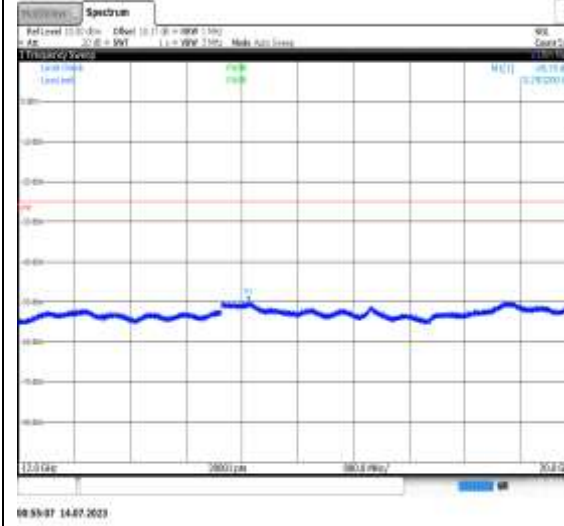
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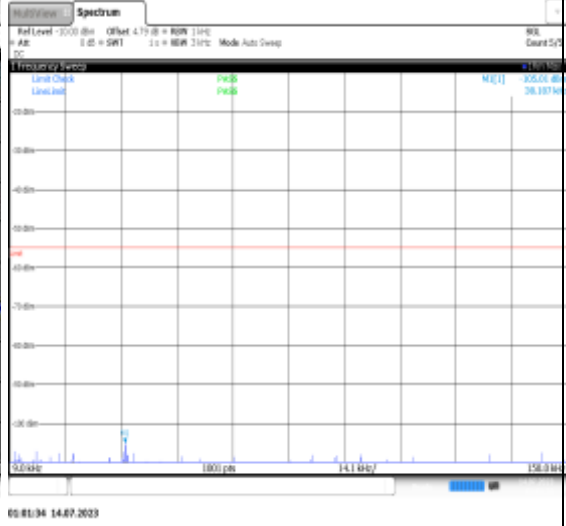
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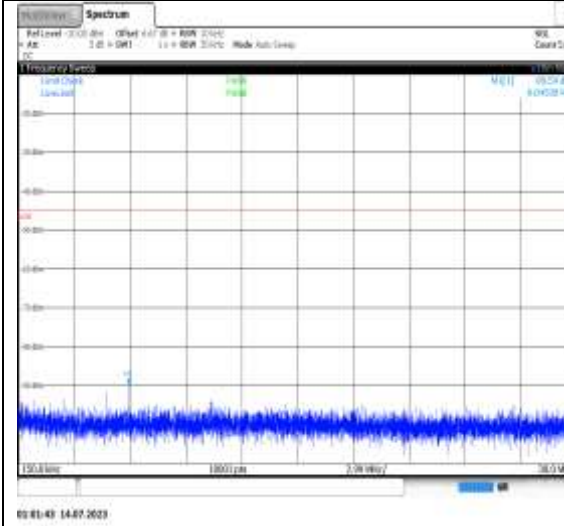
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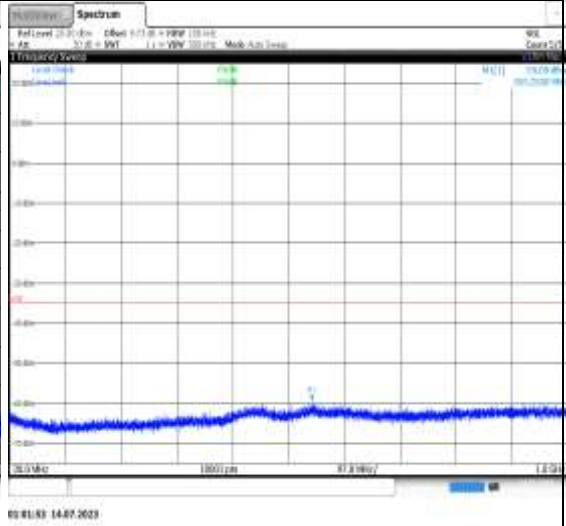
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1-DC_66A_n41A-PC3-30-5+20-M+L-27-NS_01-N/A-N/A-N/A-CP-256QAM-Outer_Full-51@0-0.009-0.15-0.04--105.01--55-PASS

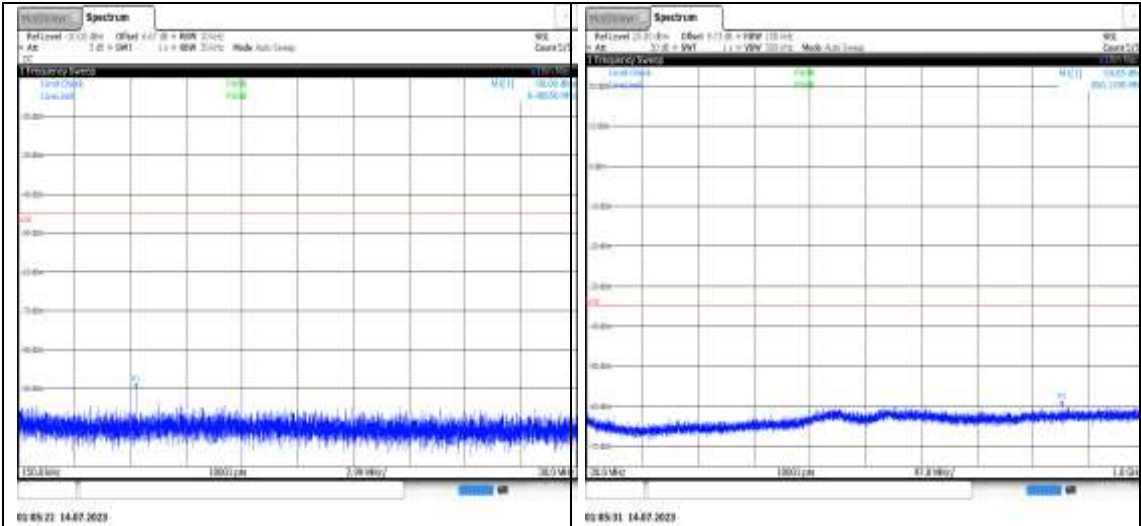


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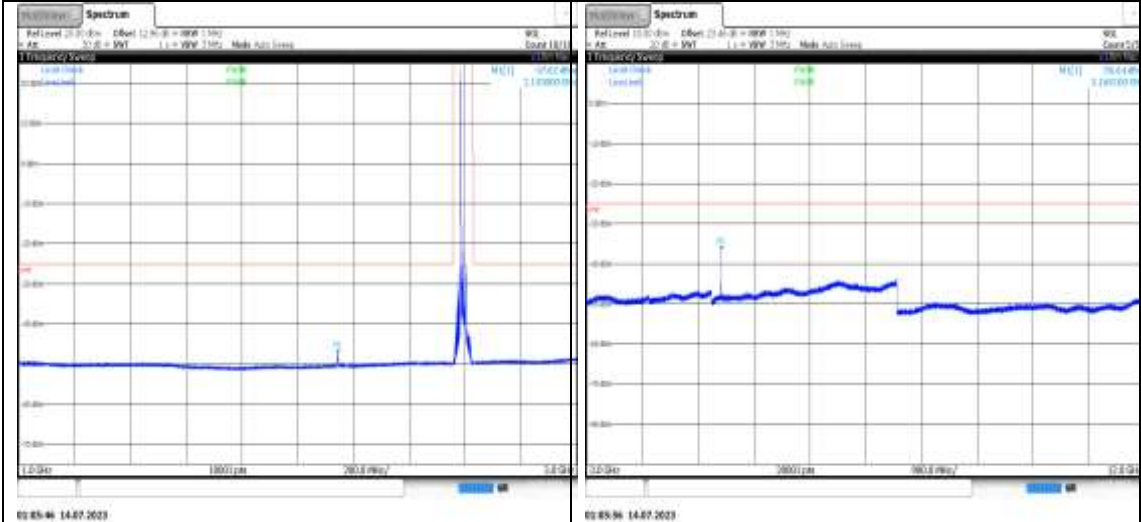
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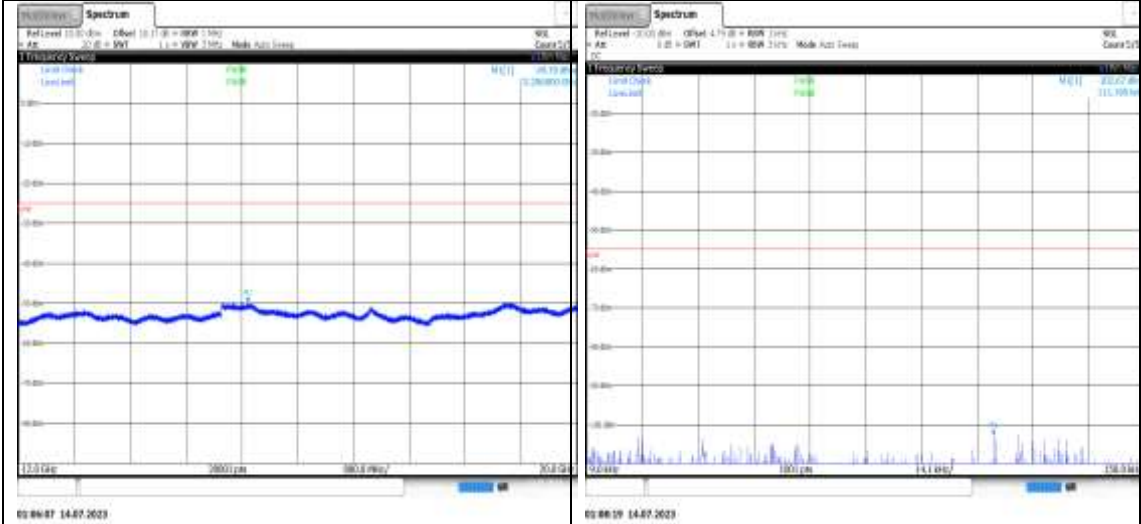
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1-DC_66A_n41A-PC3-30-5+20-M+M-1-NS_01-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_Left-1@0-30-1000-856.15--59.85--35-PASS



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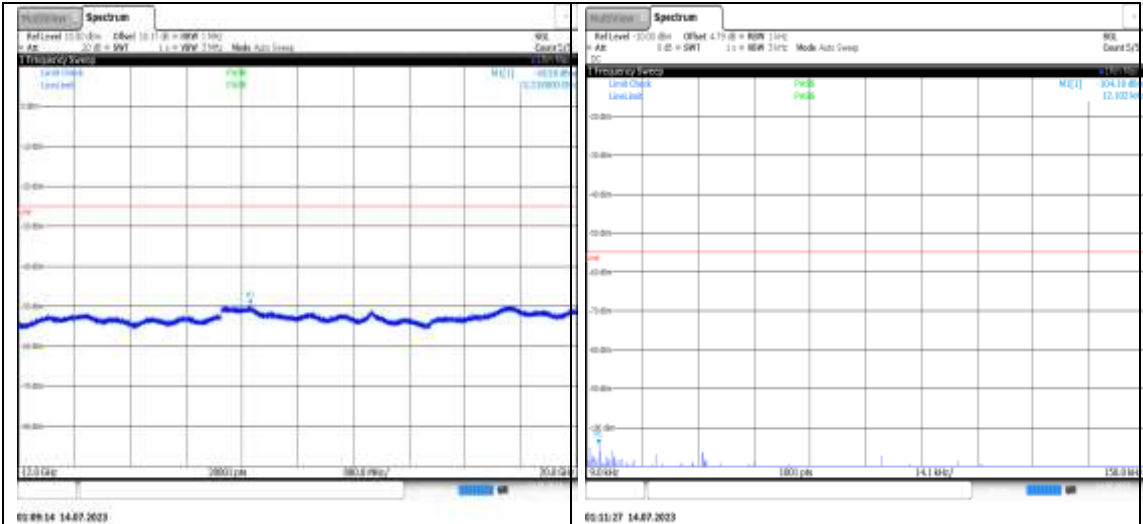
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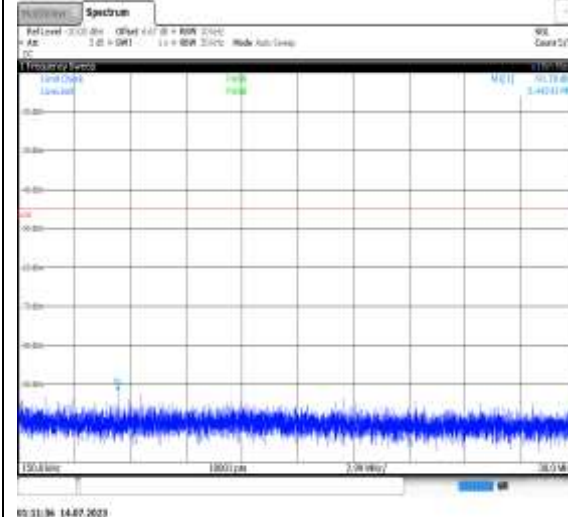
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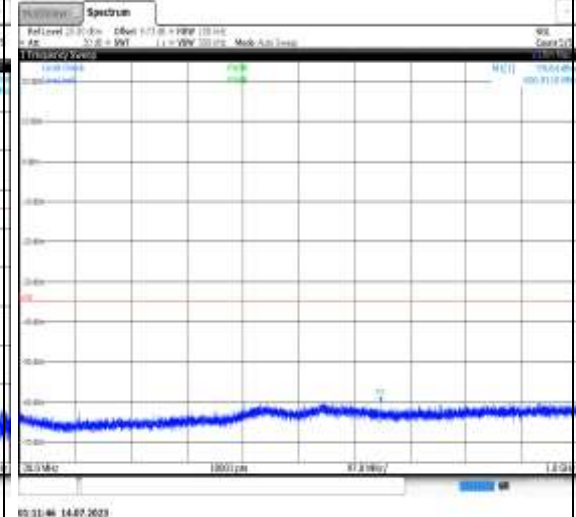


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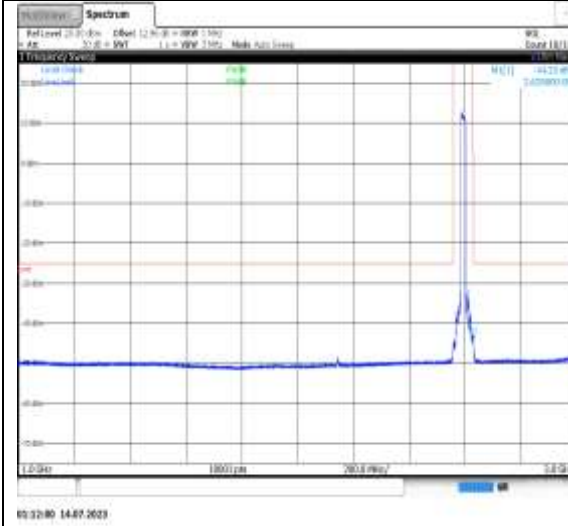
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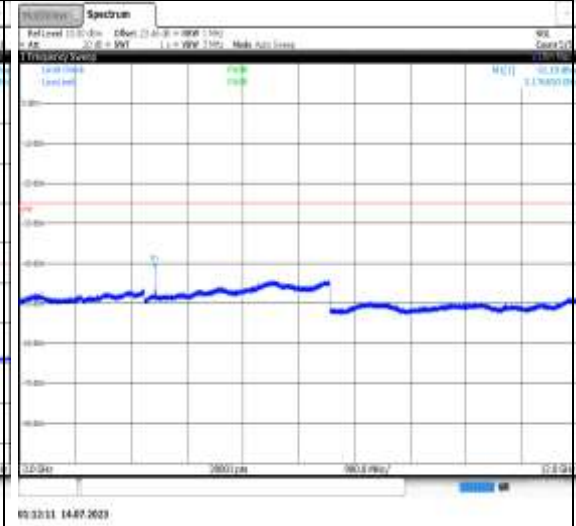
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1-DC_66A_n41A-PC3-30-5+20-M+M-3-NS_01-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-50@0-30-1000-656.91--59.84--35-PASS

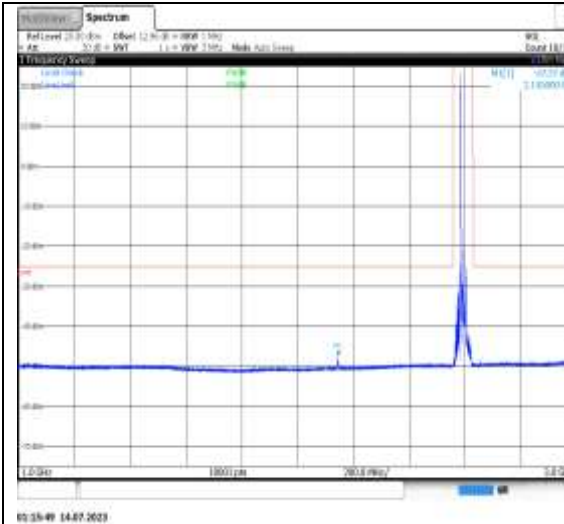


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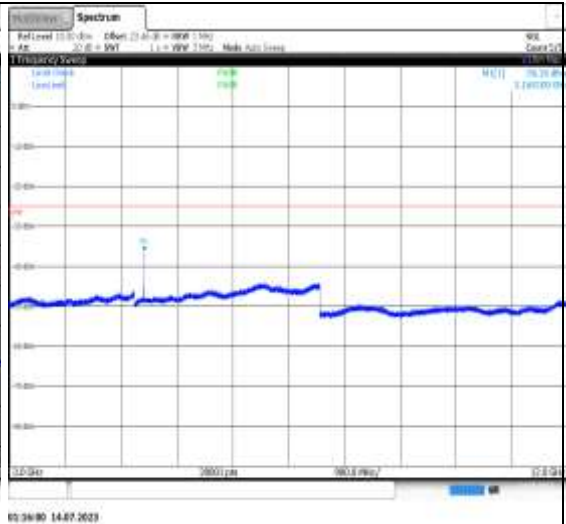


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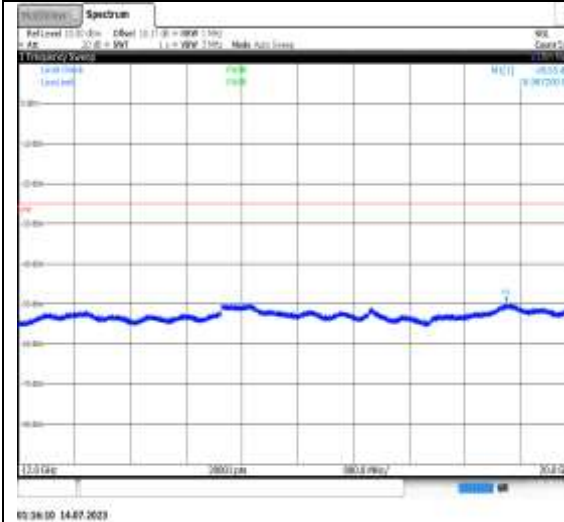
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1-DC_66A_n41A-PC3-30-5+20-M+M-4-NS_01-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Left-1@0-1000-3000-2145.00--47.37--25-PASS



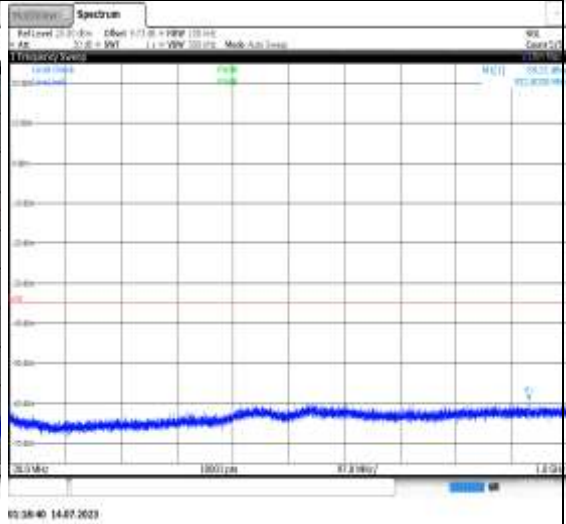
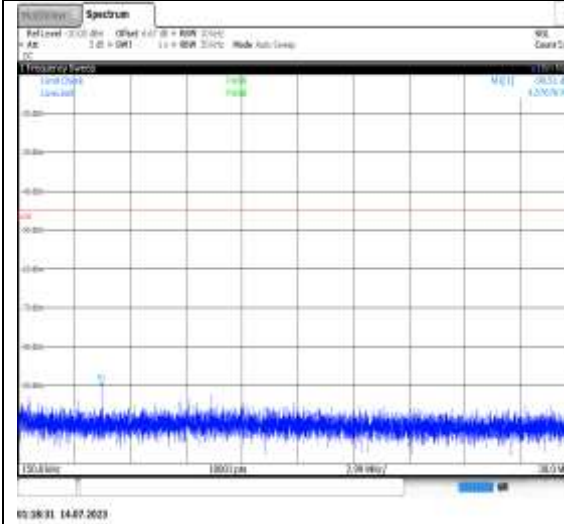
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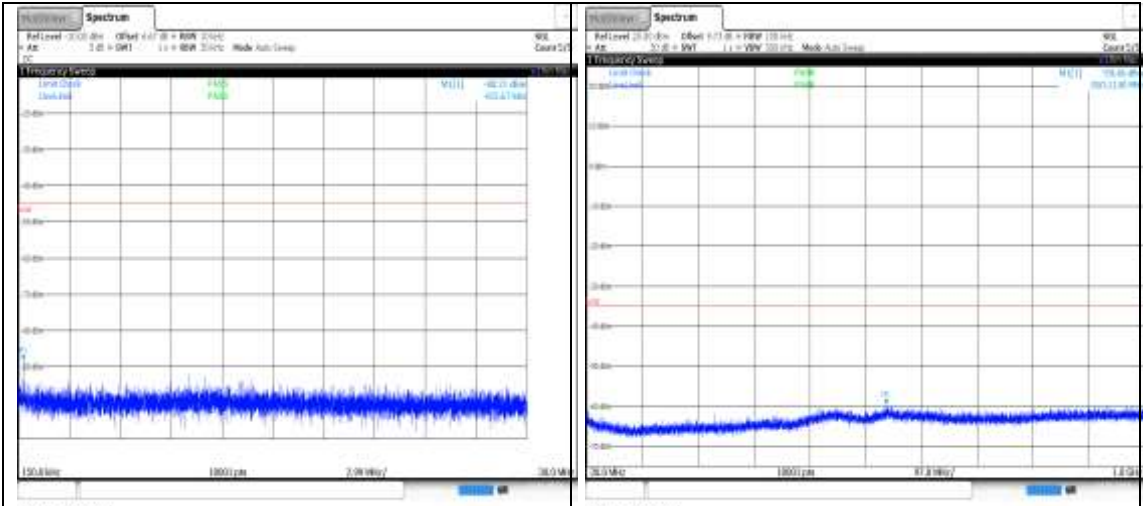
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1-DC_66A_n41A-PC3-30-5+20-M+M-5-NS_01-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1@50-0.009-0.15-0.12--101.04--55-PASS

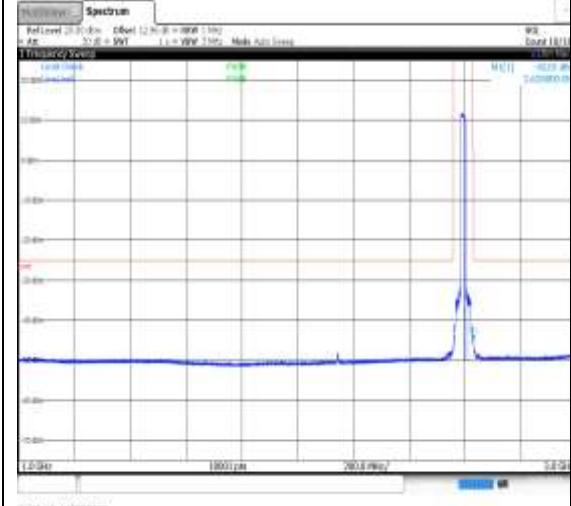


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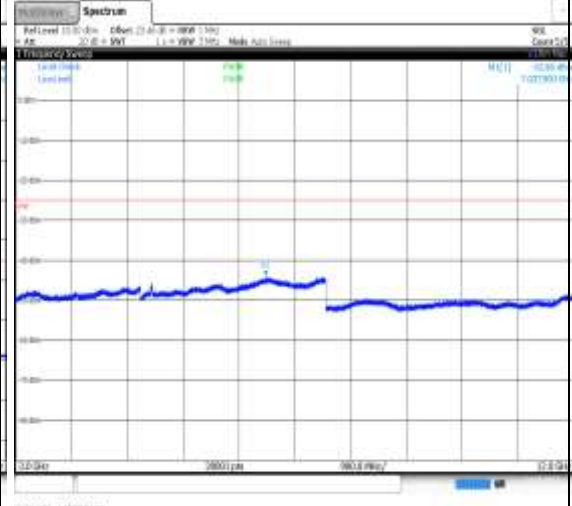


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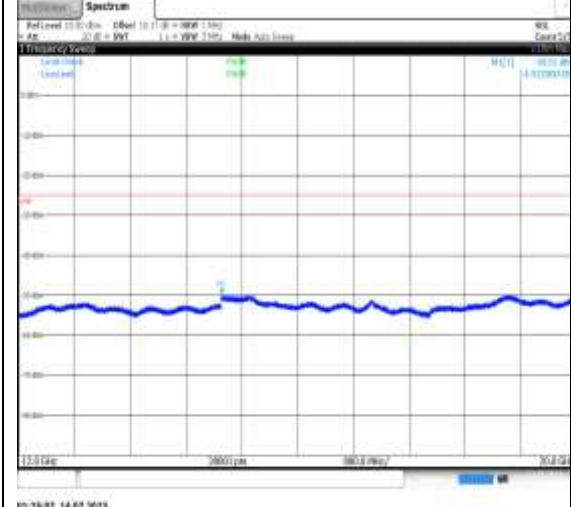
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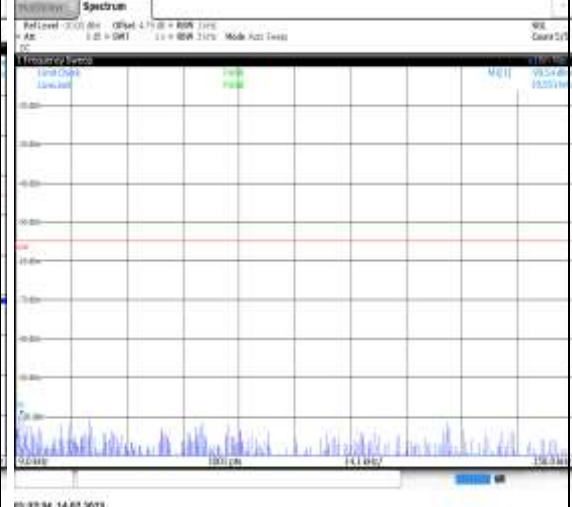
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1-DC_66A_n41A-PC3-30-5+20-M+M-6-NS_01-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50@0-3000-12000-7032.90--43.86--25-PASS

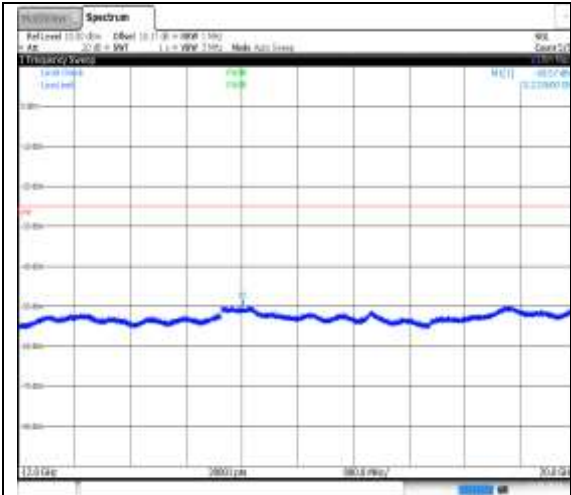


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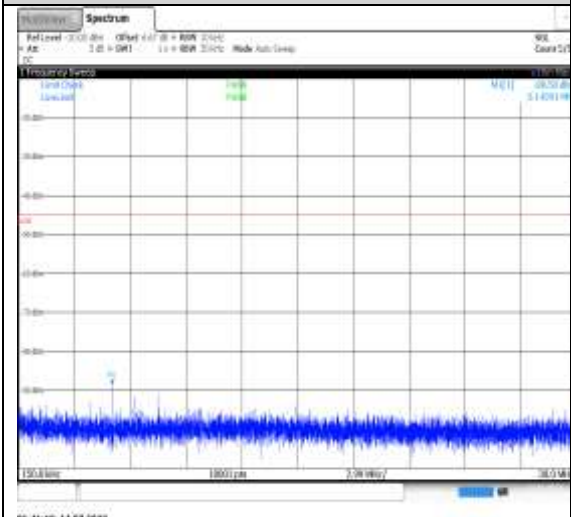
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<p>1-DC_66A_n41A-PC3-30-5+20-M+M-7-NS_01-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Left-1@0-0.15-30-6.22--93.21--45-PASS</p>	<p>1-DC_66A_n41A-PC3-30-5+20-M+M-7-NS_01-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Left-1@0-30-1000-999.32--59.48--35-PASS</p>
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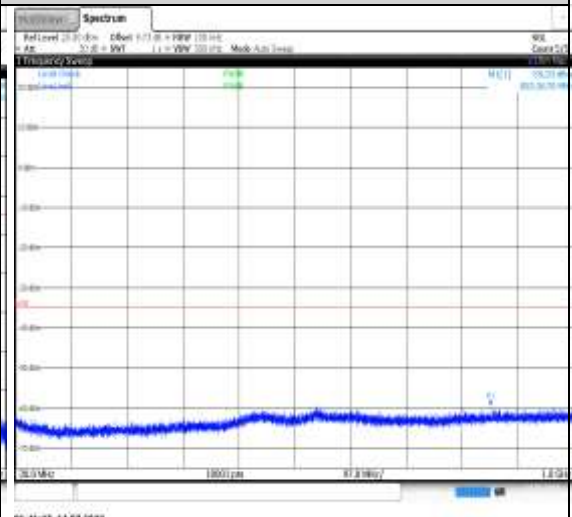
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1-DC_66A_n41A-PC3-30-5+20-M+M-8-NS_01-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Right-1@50-0.009-0.15-0.05--102.00--55-PASS



1-DC_66A_n41A-PC3-30-5+20-M+M-8-NS_01-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Right-1@50-0.15-30-5.14--88.50--45-PASS



1-DC_66A_n41A-PC3-30-5+20-M+M-8-NS_01-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Right-1@50-30-1000-855.57--59.33--35-PASS

