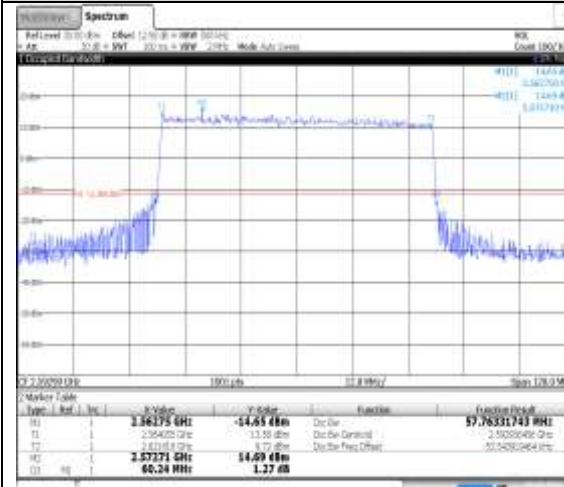
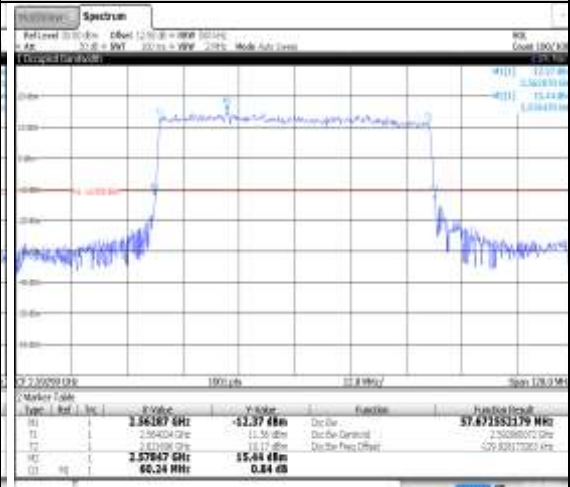


1-DC_66A_n41A-PC3-30-5+60-M+L-7-N/A-
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Ant1-57.713-60.240≤60-PASS



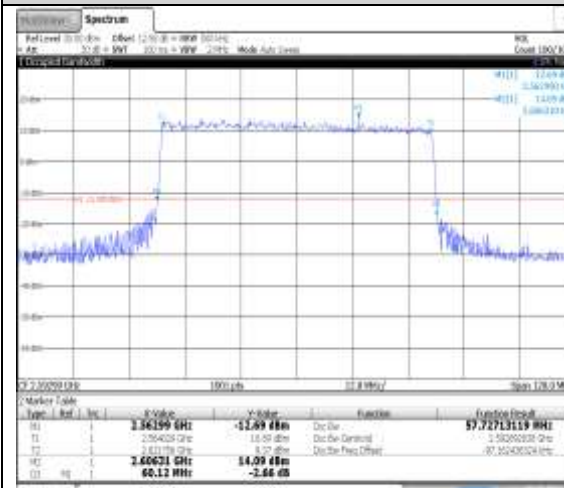
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1-DC_66A_n41A-PC3-30-5+60-M+L-8-N/A-
N/A-N/A-CP-256QAM-Outer_Full-162@0-
Ant1-57.642-60.000≤60-PASS



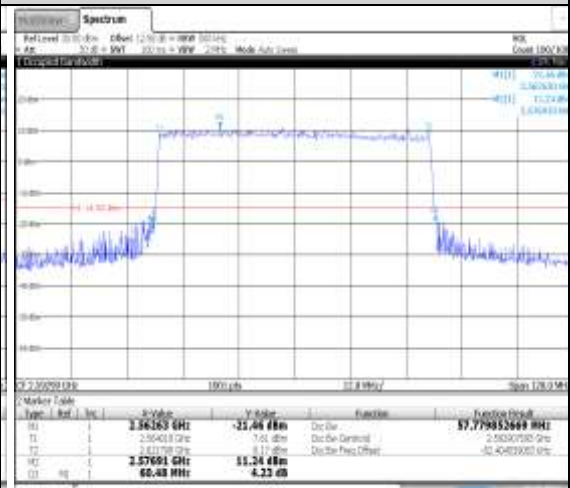
02:58:44 09.07.2023

1-DC_66A_n41A-PC3-30-5+60-M+M-1-N/A-
N/A-N/A-DFT-QPSK-Outer_Full-162@0-
Ant1-57.763-60.240≤60-PASS



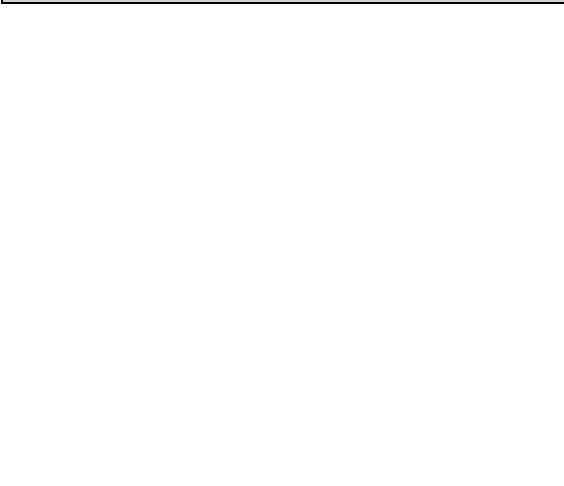
02:02:56 09.07.2023

1-DC_66A_n41A-PC3-30-5+60-M+M-2-N/A-
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Ant1-57.673-60.240≤60-PASS



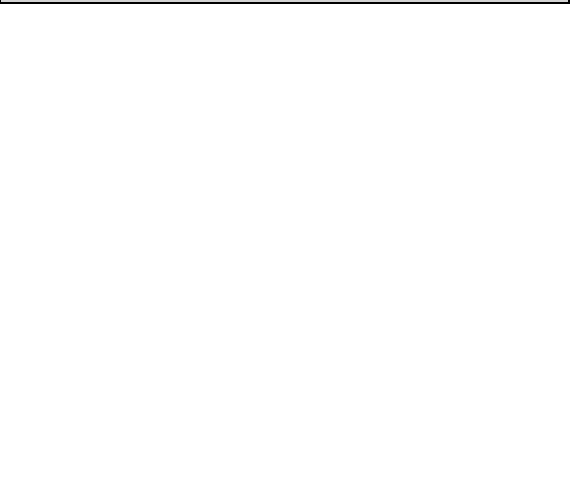
02:04:51 09.07.2023

1-DC_66A_n41A-PC3-30-5+60-M+M-3-N/A-
N/A-N/A-DFT-16QAM-Outer_Full-162@0-
Ant1-57.727-60.120≤60-PASS

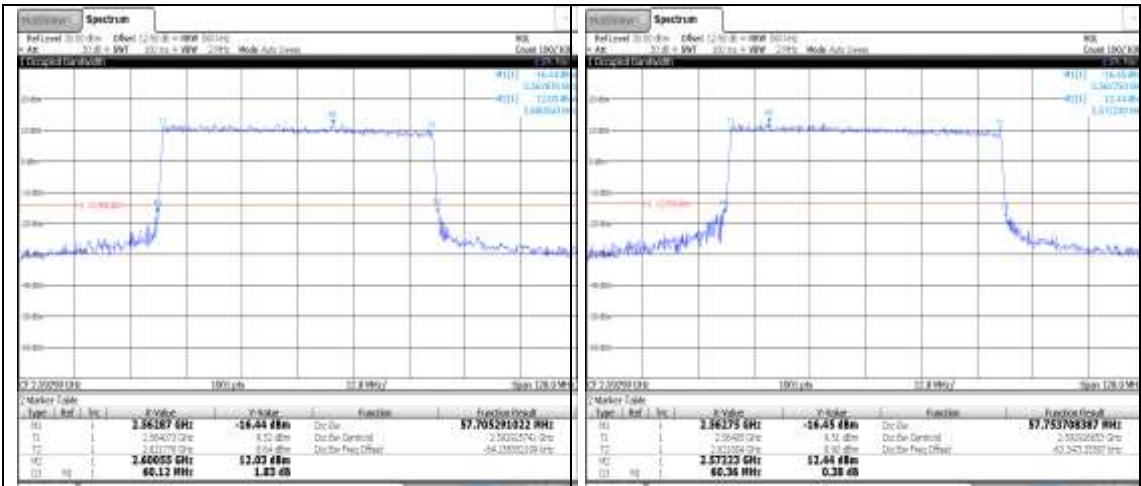


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1-DC_66A_n41A-PC3-30-5+60-M+M-4-N/A-
N/A-N/A-DFT-64QAM-Outer_Full-162@0-
Ant1-57.78-60.480≤60-PASS

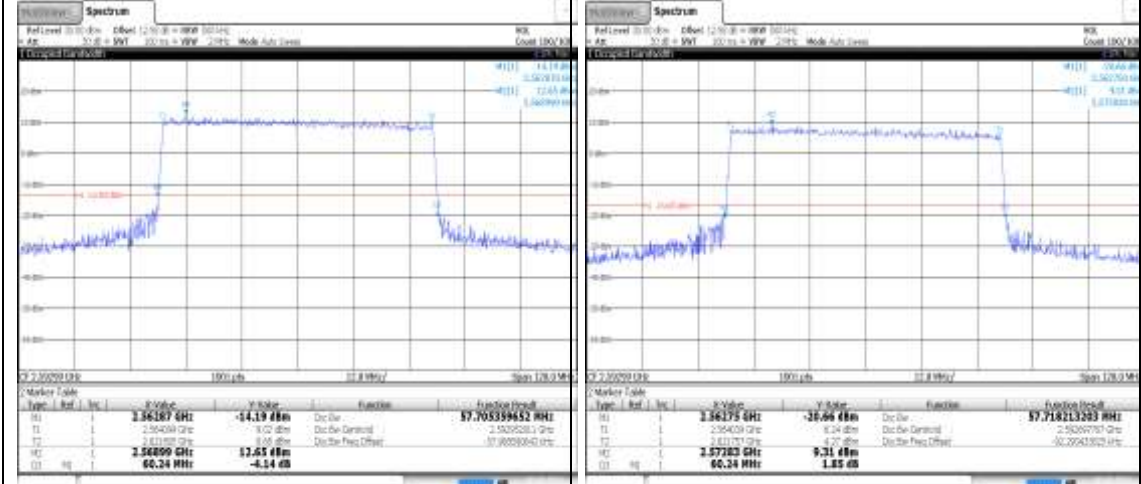


02:04:51 09.07.2023



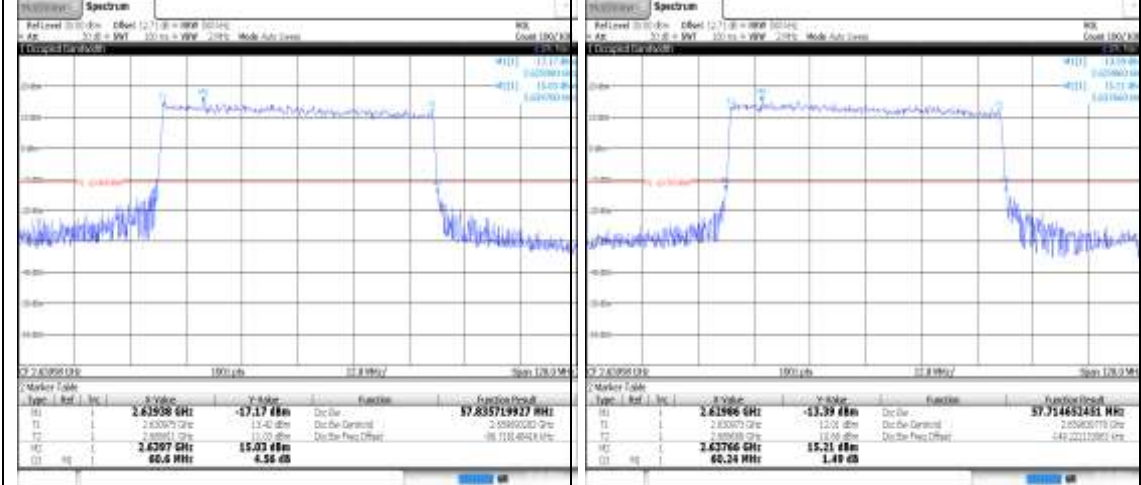
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1-DC_66A_n41A-PC3-30-5+60-M+M-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-162@0-Ant1-57.754-60.360-≤60-PASS



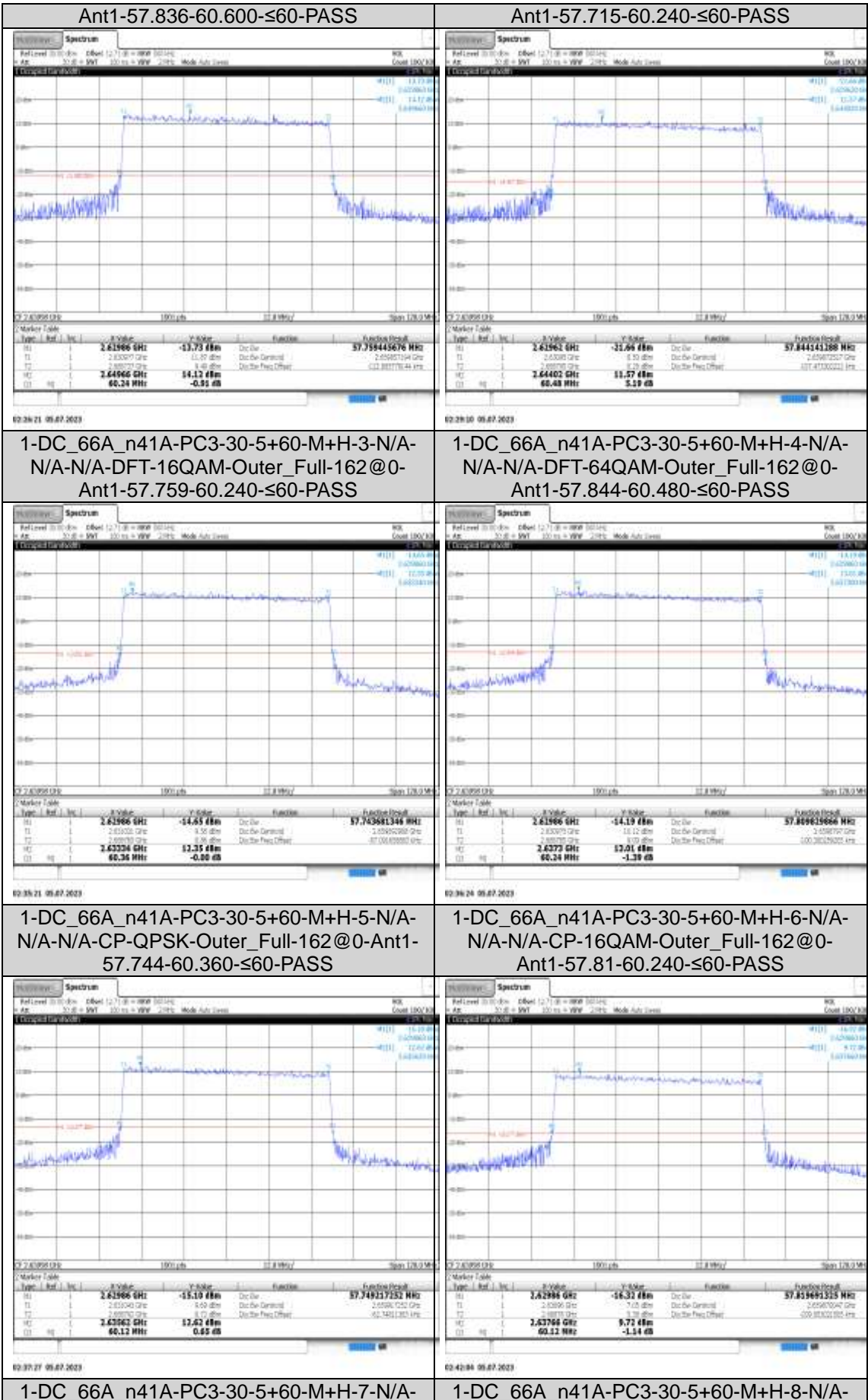
1-DC_66A_n41A-PC3-30-5+60-M+M-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-162@0-Ant1-57.705-60.240-≤60-PASS

1-DC_66A_n41A-PC3-30-5+60-M+M-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-162@0-Ant1-57.718-60.240-≤60-PASS

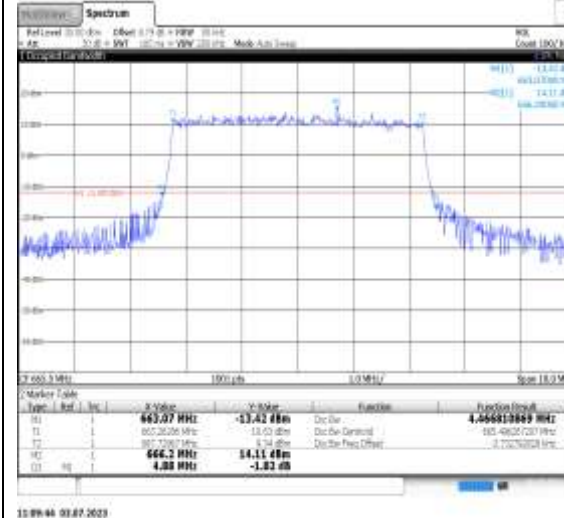


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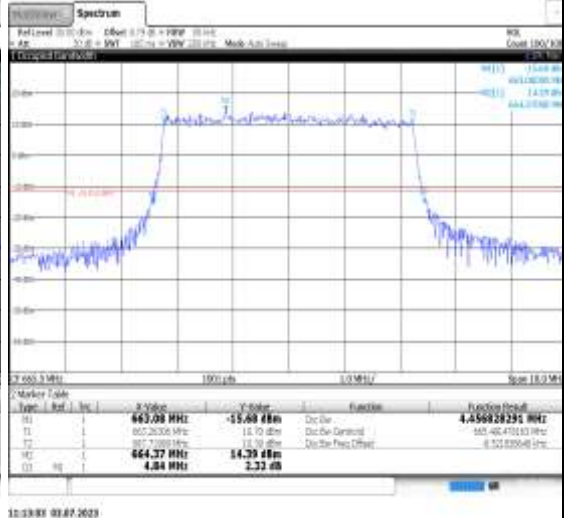
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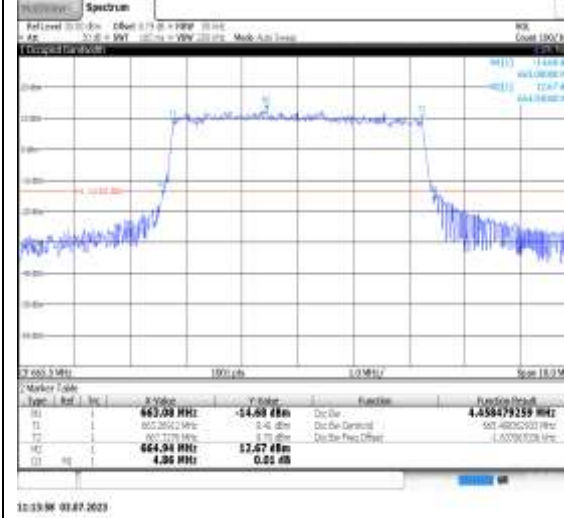
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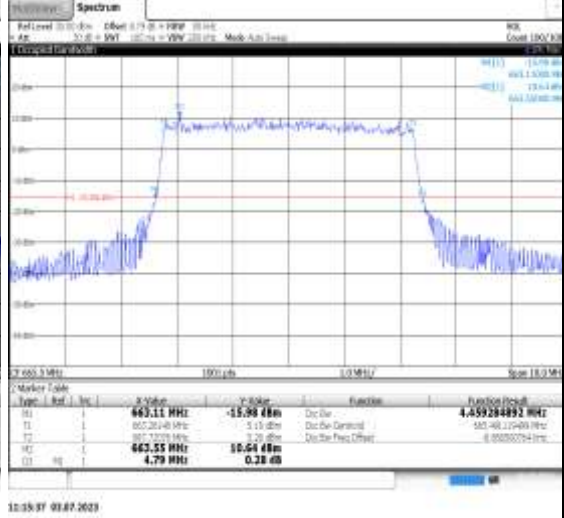
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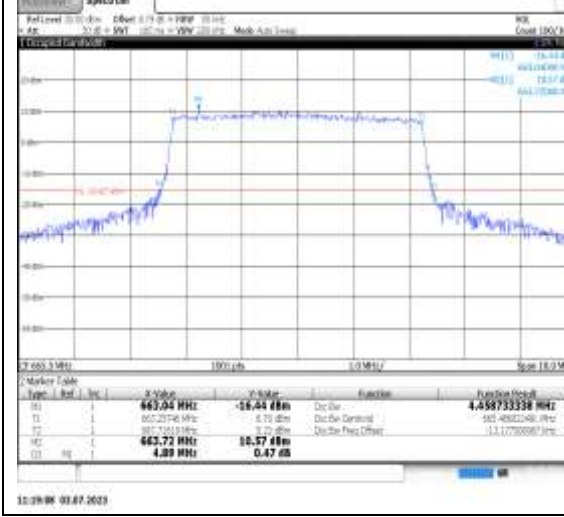
1-DC_66A_n71A-PC3-15-5+5-M+L-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-25@0-Ant1-4.467-4.880-≤5-PASS



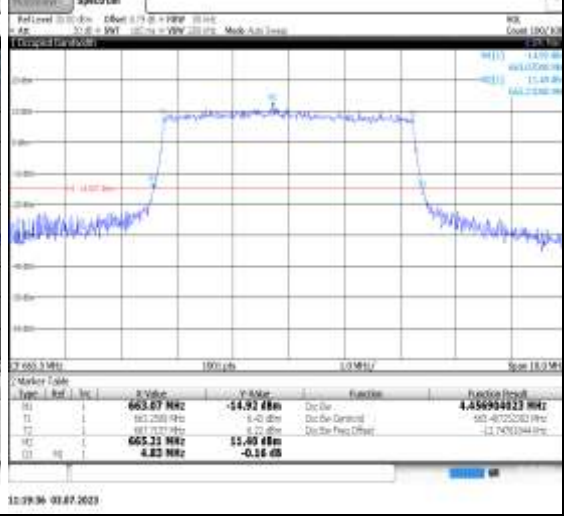
1-DC_66A_n71A-PC3-15-5+5-M+L-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-25@0-Ant1-4.457-4.840-≤5-PASS



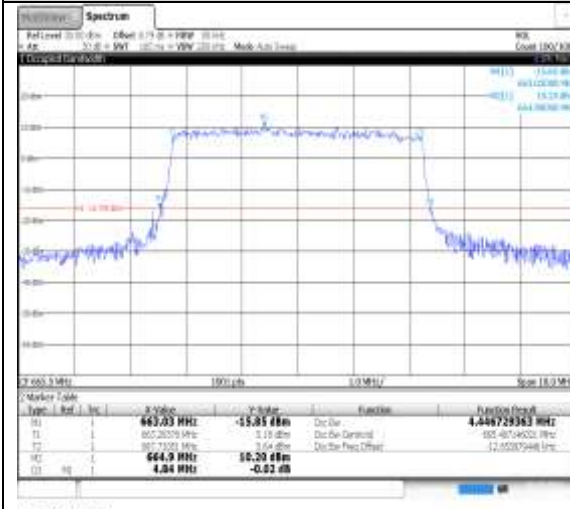
1-DC_66A_n71A-PC3-15-5+5-M+L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-25@0-Ant1-4.458-4.860-≤5-PASS



1-DC_66A_n71A-PC3-15-5+5-M+L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-25@0-Ant1-4.459-4.790-≤5-PASS

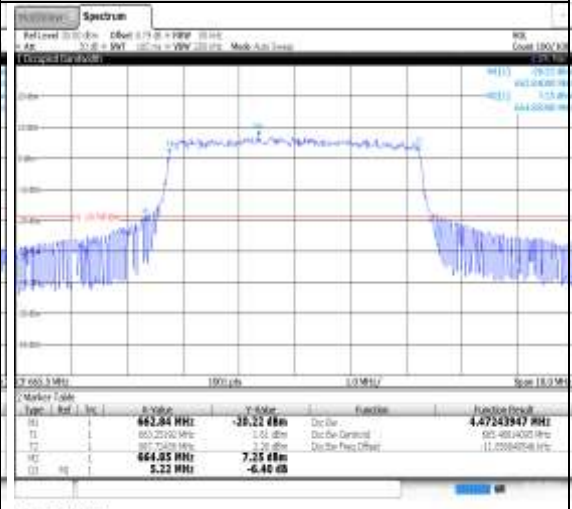


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4.459-4.890-≤5-PASS



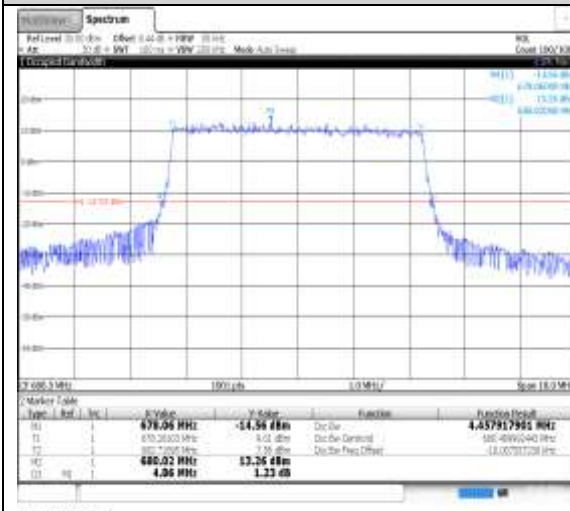
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1-DC_66A_n71A-PC3-15-5+5-M+L-6-N/A-
N/A-N/A-CP-16QAM-Outer_Full-25@0-Ant1-
4.457-4.830-≤5-PASS



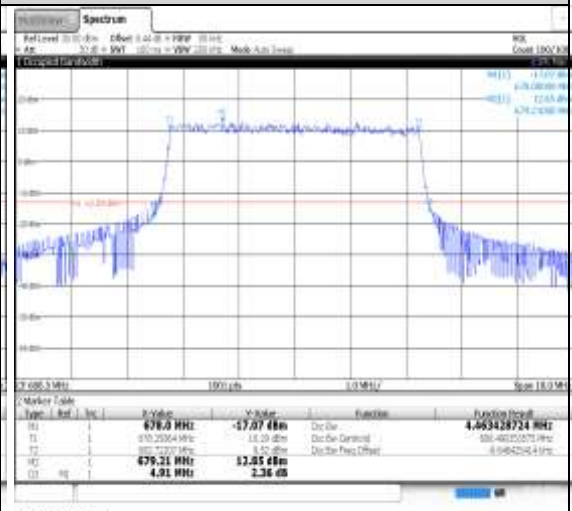
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1-DC_66A_n71A-PC3-15-5+5-M+L-7-N/A-
N/A-N/A-CP-64QAM-Outer_Full-25@0-Ant1-
4.447-4.840-≤5-PASS



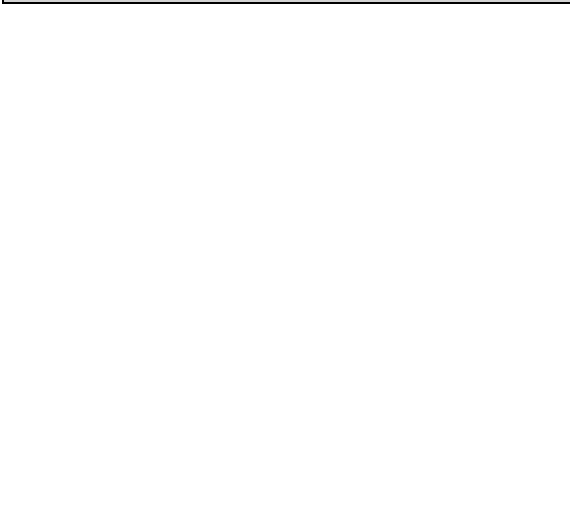
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1-DC_66A_n71A-PC3-15-5+5-M+L-8-N/A-
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Ant1-4.472-5.220-≤5-PASS



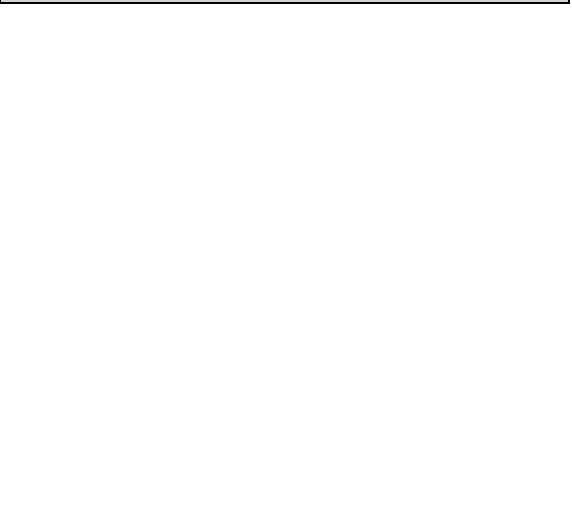
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1-DC_66A_n71A-PC3-15-5+5-M+M-1-N/A-
N/A-N/A-DFT-QPSK-Outer_Full-25@0-Ant1-
4.458-4.860-≤5-PASS

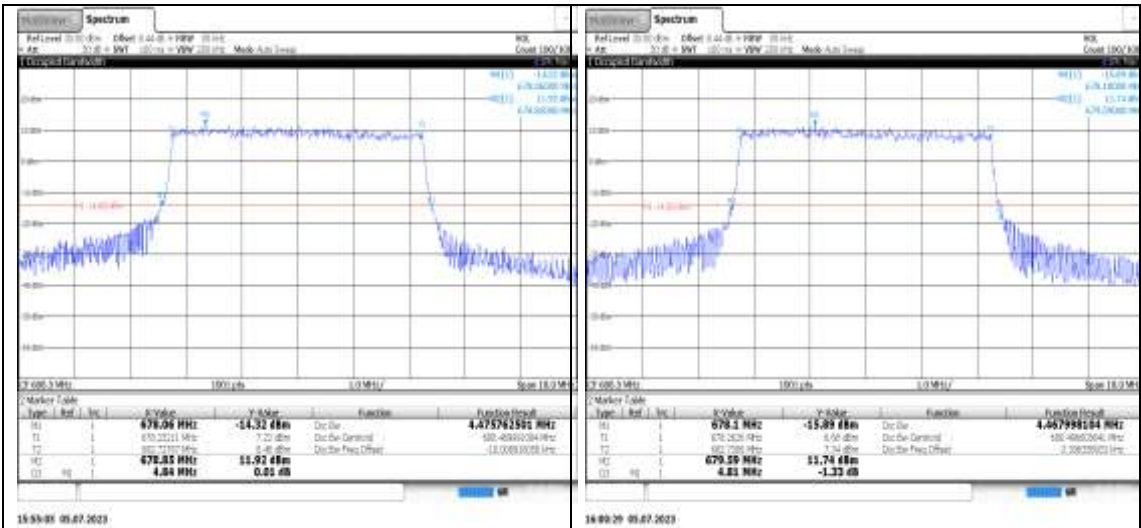


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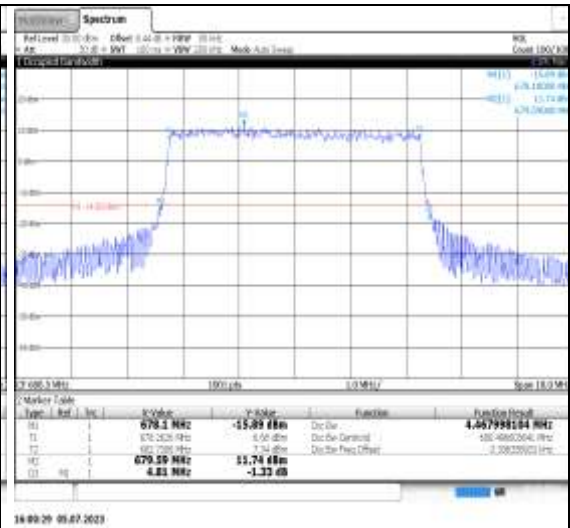
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N/A-N/A-DFT-PI2BPSK-Outer_Full-25@0-
Ant1-4.463-4.910-≤5-PASS



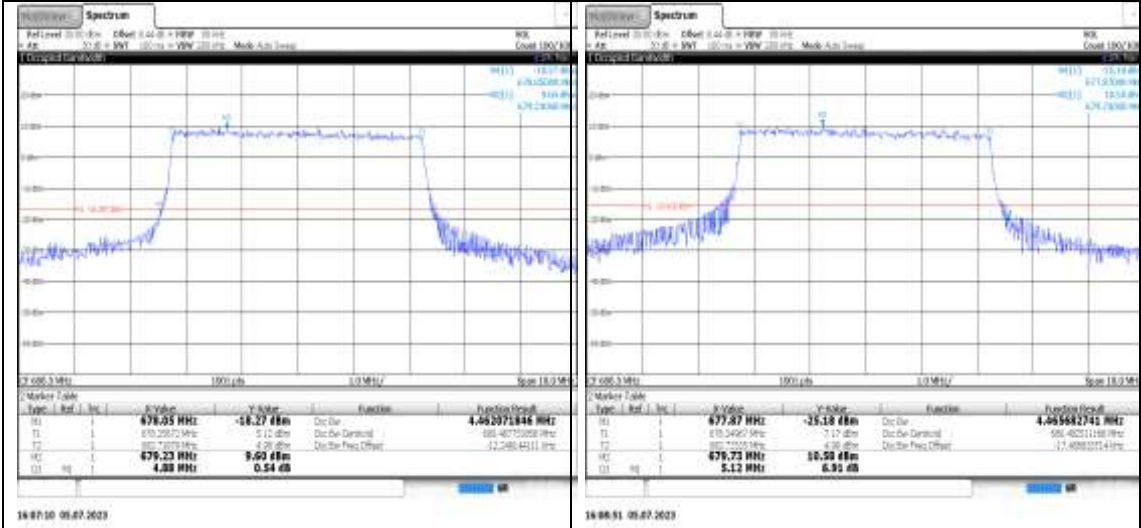
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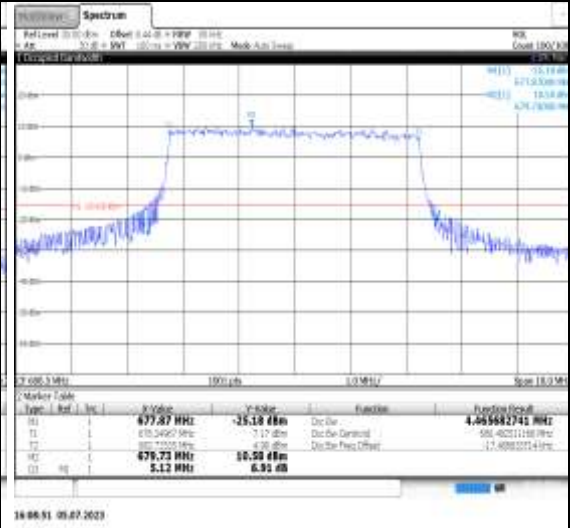
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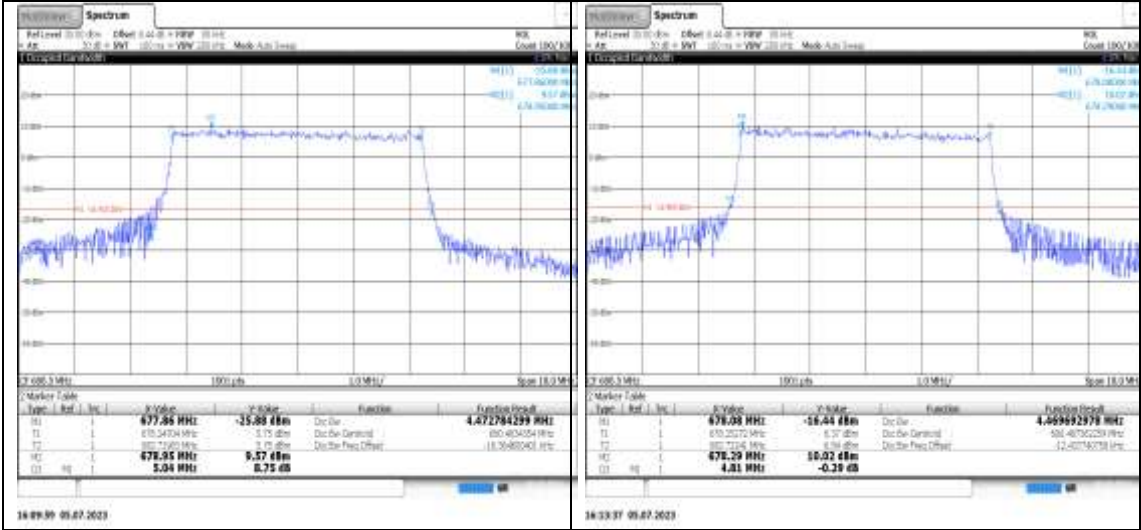
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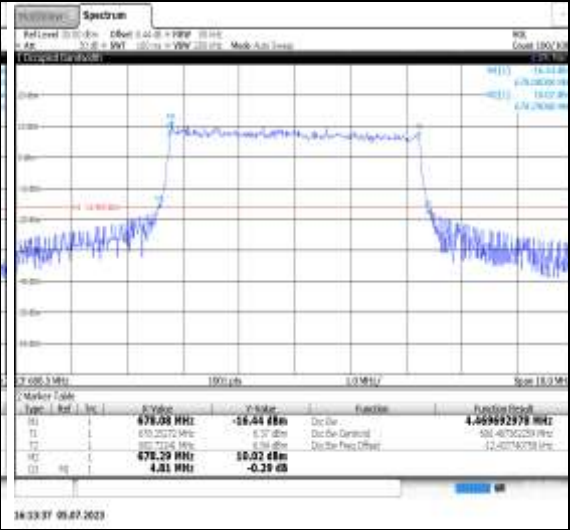
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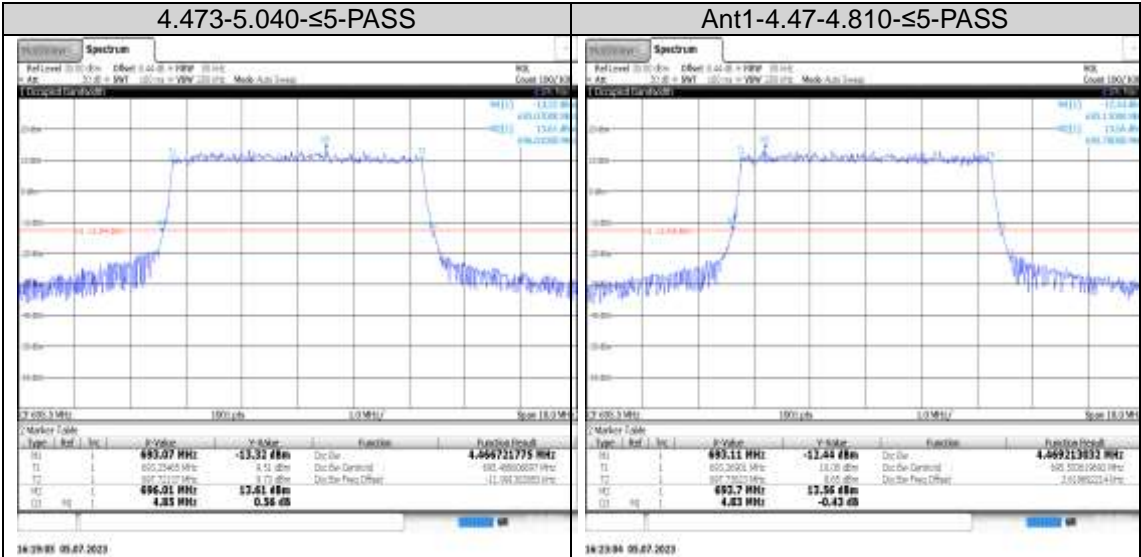
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1-DC_66A_n71A-PC3-15-5+5-M+M-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-25@0-Ant1-

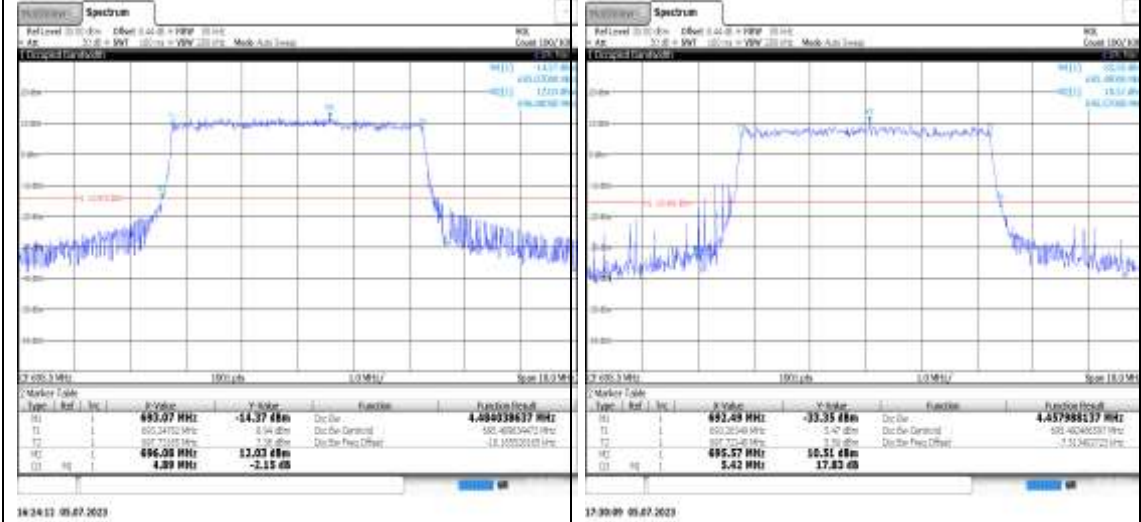


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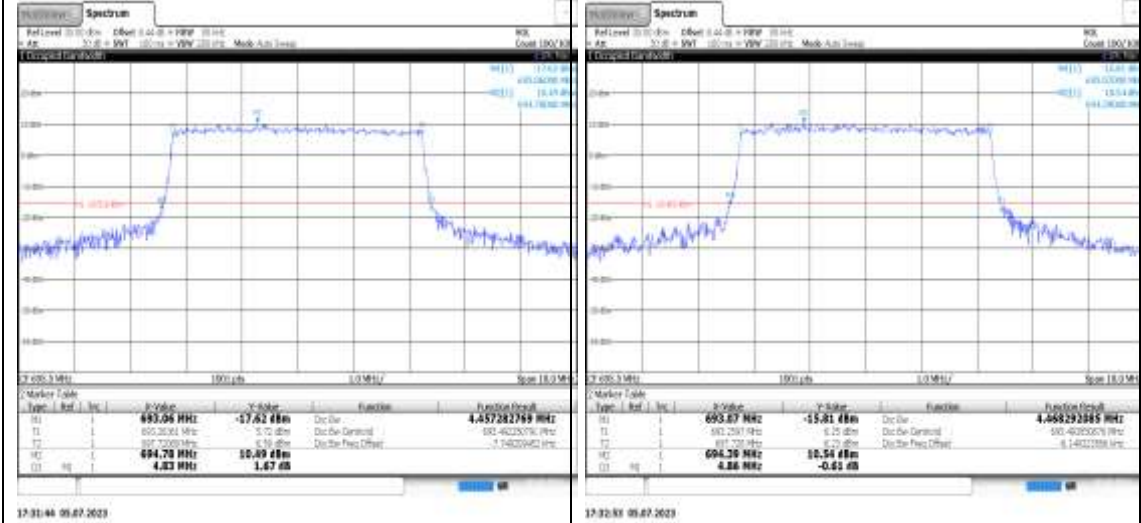
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1-DC_66A_n71A-PC3-15-5+5-M+H-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-25@0-Ant1-4.469-4.830-≤5-PASS



1-DC_66A_n71A-PC3-15-5+5-M+H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-25@0-Ant1-4.484-4.890-≤5-PASS

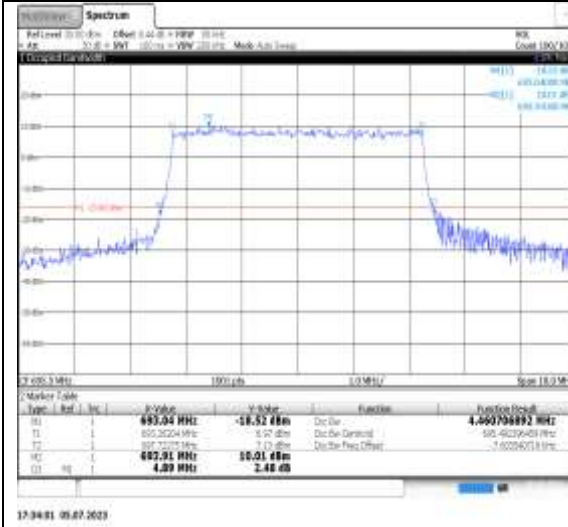
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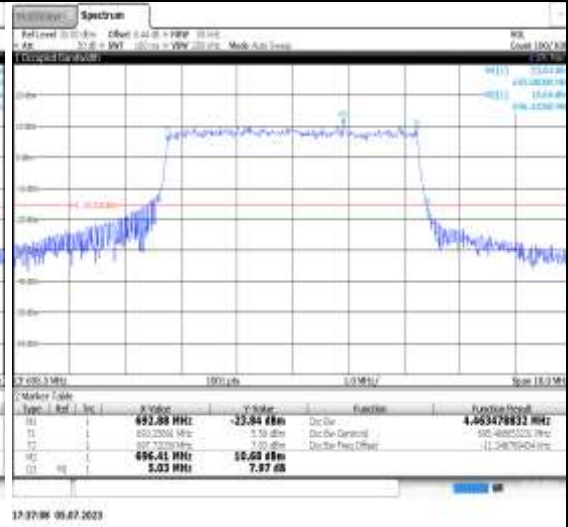
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1-DC_66A_n71A-PC3-15-5+5-M+H-6-N/A-

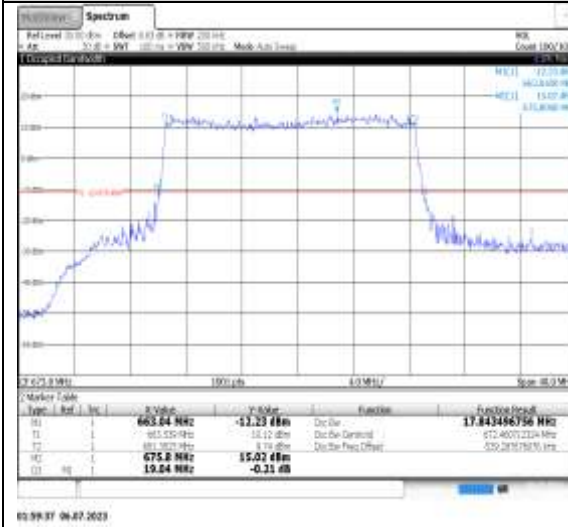
N/A-N/A-CP-QPSK-Outer_Full-25@0-Ant1-4.457-4.830-≤5-PASS



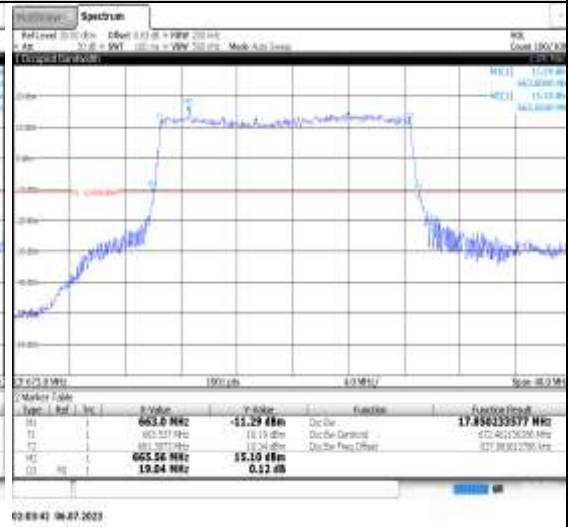
N/A-N/A-CP-16QAM-Outer_Full-25@0-Ant1-4.468-4.860-≤5-PASS



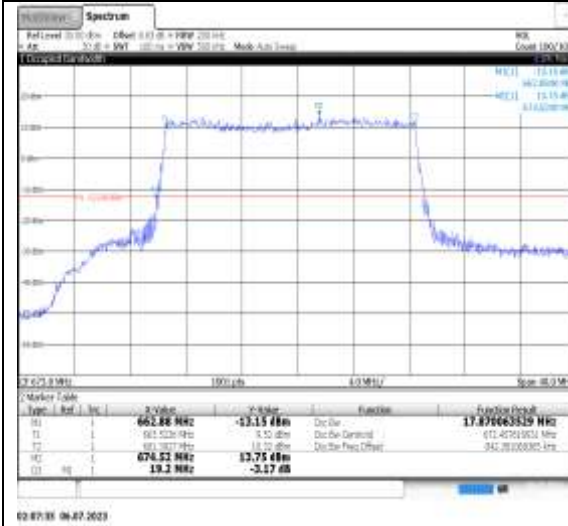
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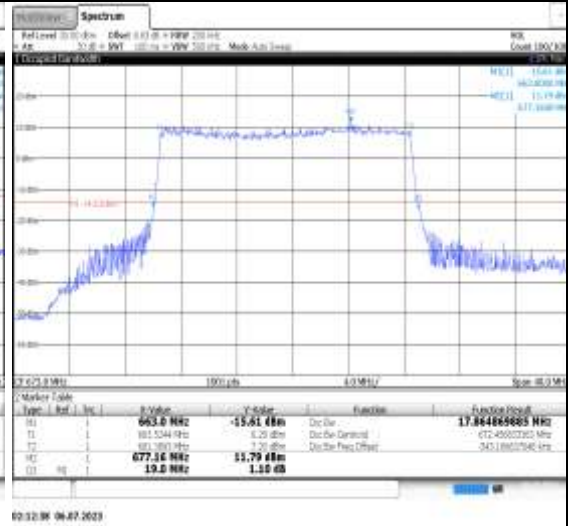
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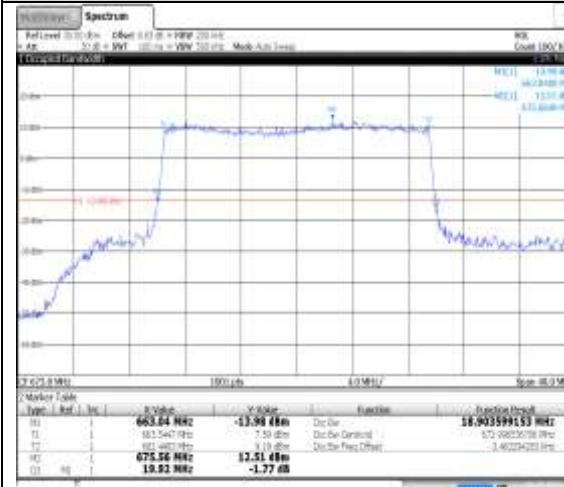
1-DC_66A_n71A-PC3-15-5+20-M+L-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100@0-Ant1-17.843-19.040-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+L-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-Ant1-17.85-19.040-≤20-PASS



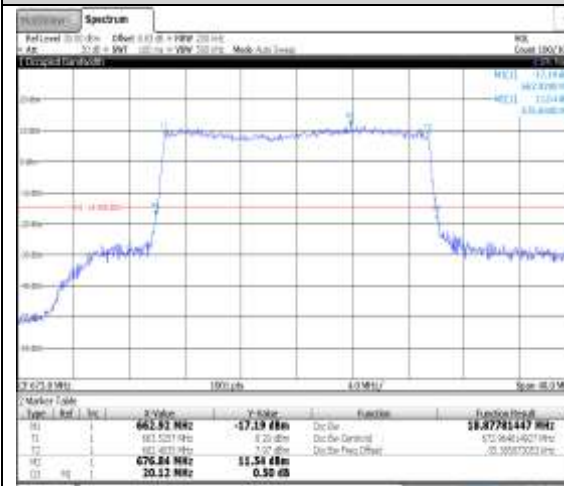
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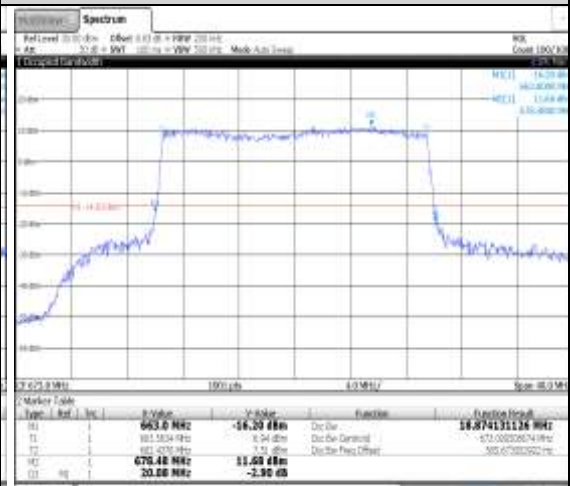
1-DC_66A_n71A-PC3-15-5+20-M+L-4-N/A-
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Ant1-17.865-19.000-≤20-PASS



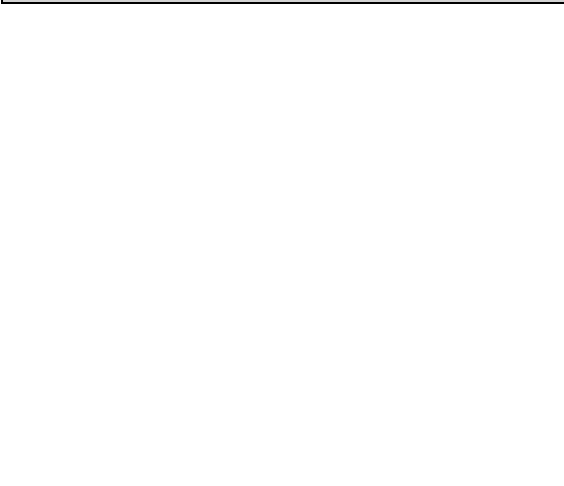
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18.904-19.920-≤20-PASS



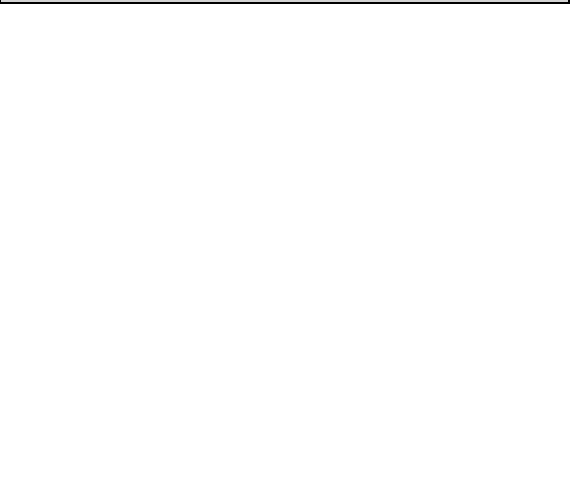
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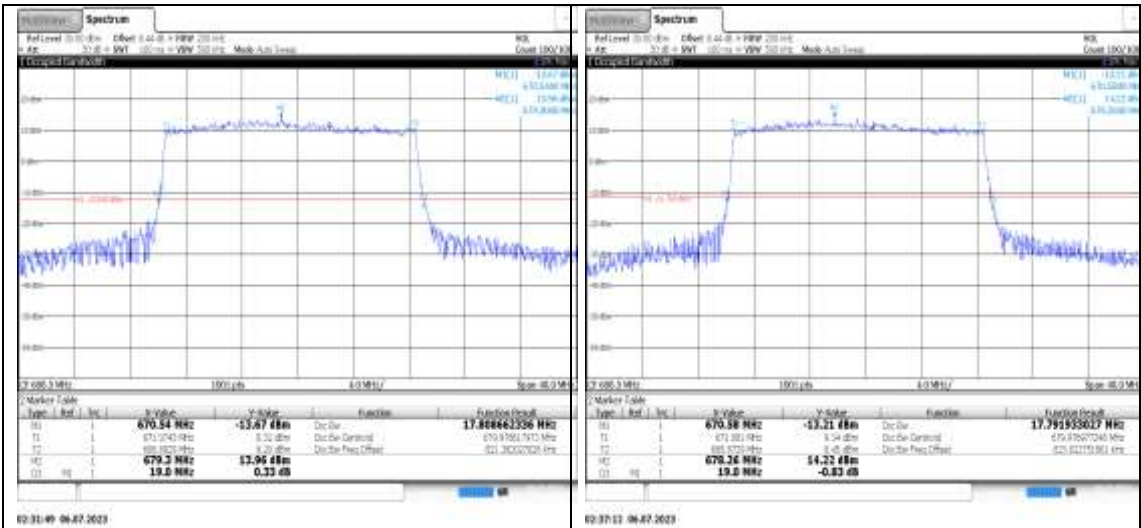


1-DC_66A_n71A-PC3-15-5+20-M+L-7-N/A-
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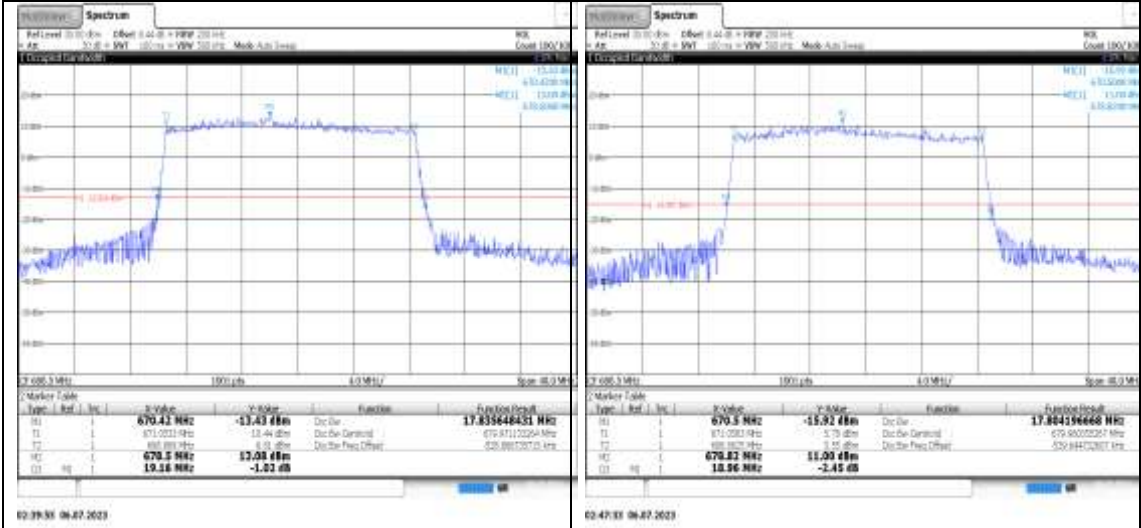
1-DC_66A_n71A-PC3-15-5+20-M+L-8-N/A-
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Ant1-18.874-20.080-≤20-PASS





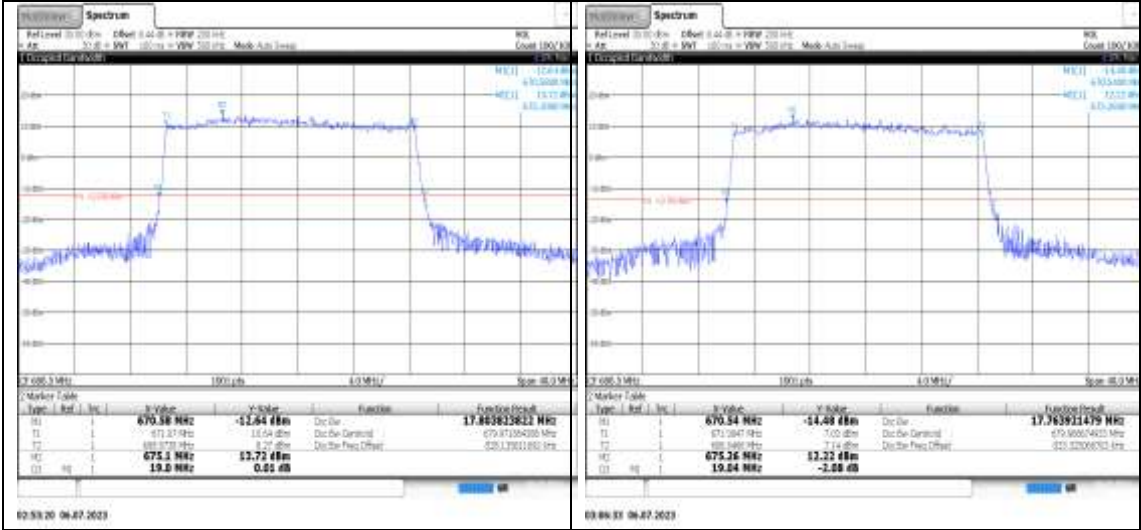
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1-DC_66A_n71A-PC3-15-5+20-M+M-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-Ant1-17.792-19.000-≤20-PASS



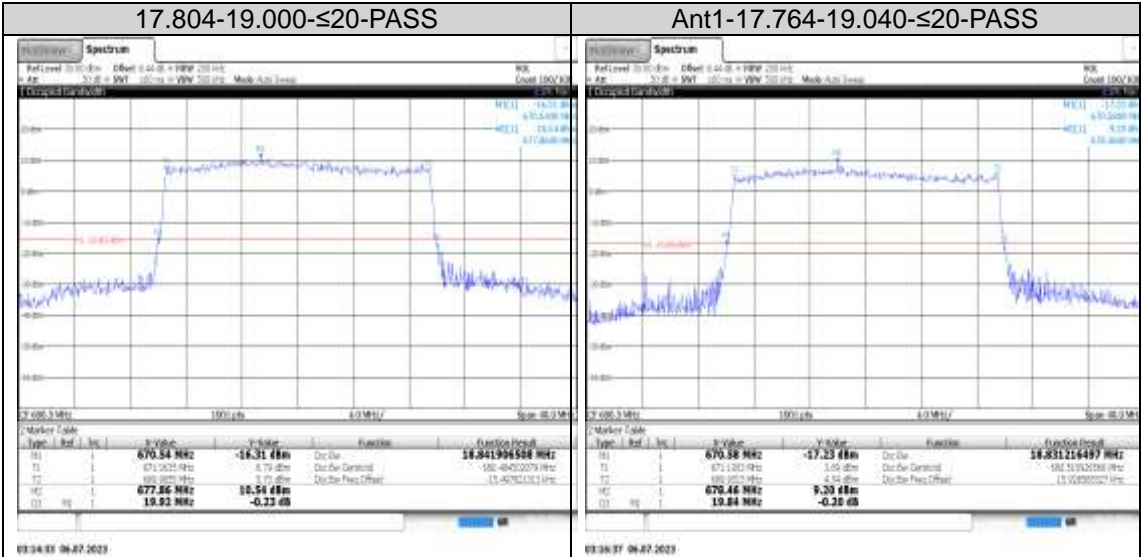
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1-DC_66A_n71A-PC3-15-5+20-M+M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-100@0-Ant1-17.804-18.960-≤20-PASS



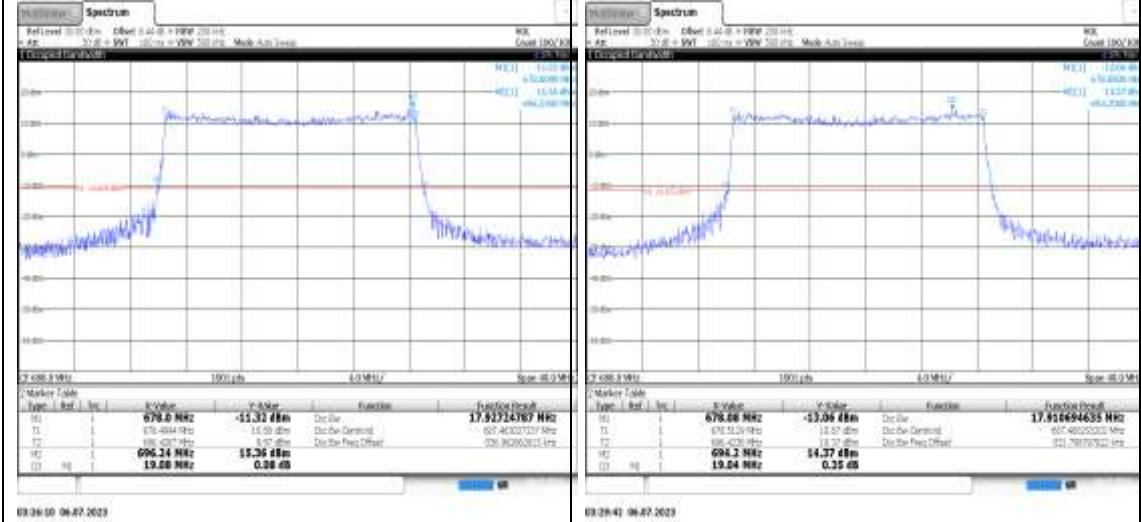
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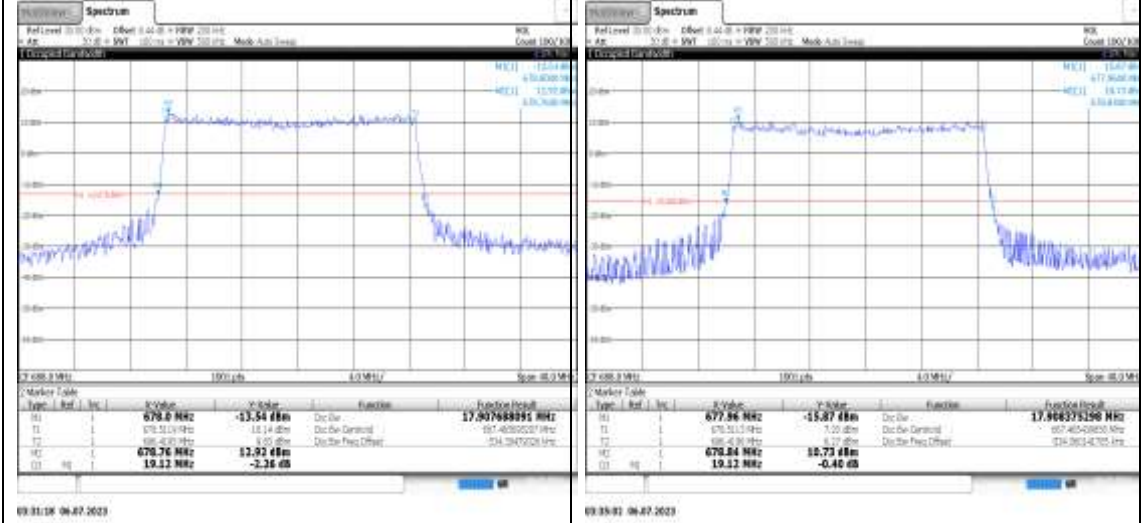
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1-DC_66A_n71A-PC3-15-5+20-M+M-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-106@0-Ant1-18.831-19.840-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+H-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100@0-Ant1-17.927-19.080-≤20-PASS

1-DC_66A_n71A-PC3-15-5+20-M+H-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-Ant1-17.911-19.040-≤20-PASS



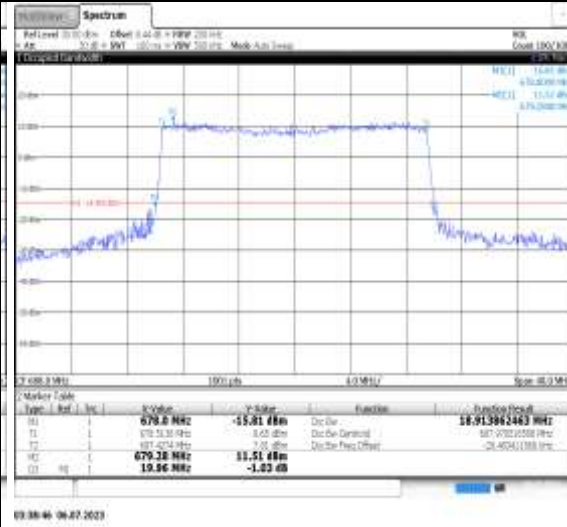
1-DC_66A_n71A-PC3-15-5+20-M+H-3-N/A-

1-DC_66A_n71A-PC3-15-5+20-M+H-4-N/A-

N/A-N/A-DFT-16QAM-Outer_Full-100@0-Ant1-17.908-19.120-≤20-PASS



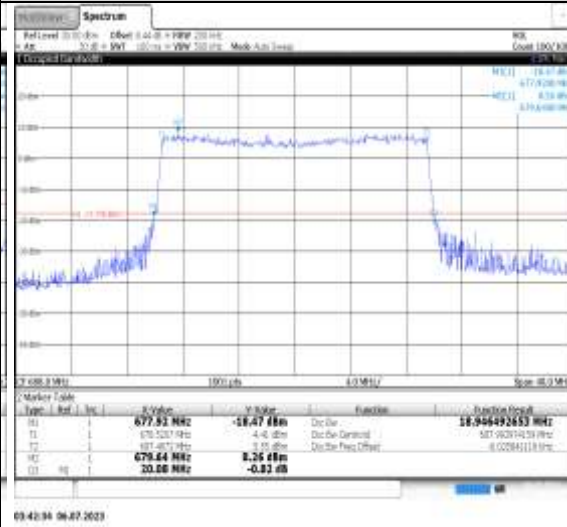
N/A-N/A-DFT-64QAM-Outer_Full-100@0-Ant1-17.908-19.120-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+H-5-N/A-N/A-N/A-CP-QPSK-Outer_Full-106@0-Ant1-18.944-20.040-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+H-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-106@0-Ant1-18.914-19.960-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+H-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-106@0-Ant1-18.936-20.080-≤20-PASS



1-DC_66A_n71A-PC3-15-5+20-M+H-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-106@0-Ant1-18.946-20.080-≤20-PASS



Appendix D: Band Edge for NSA

Test Result

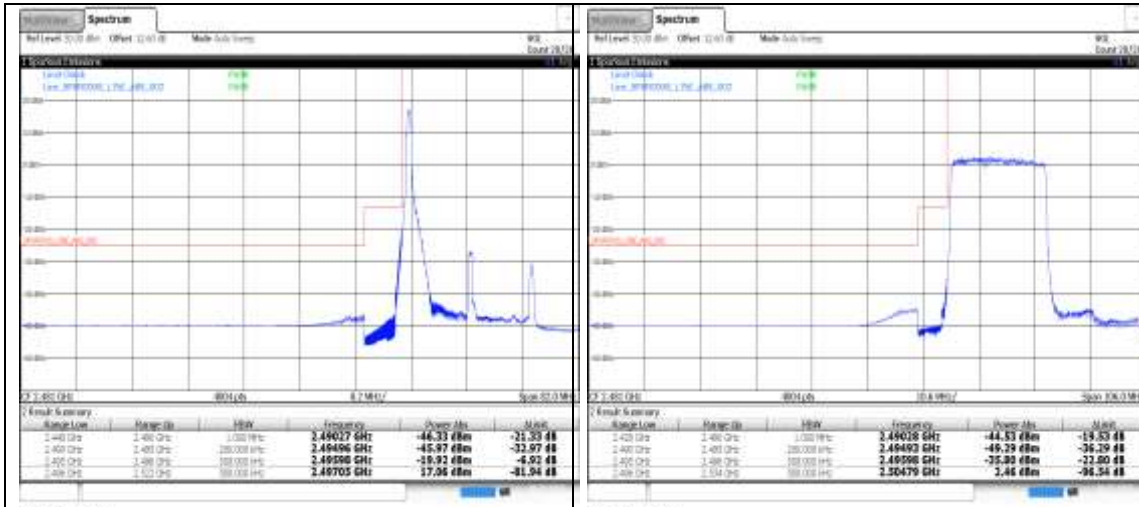
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n41A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+20	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+20	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	see graph	PASS

DC_2A_n41A	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n41A	30	5+100	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n41A	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n41A	30	5+100	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n71A	15	5+5	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n71A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n71A	15	5+5	CP-256QAM	M+H	Outer_Full	see graph	PASS

DC_66A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n41A	30	5+100	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n41A	30	5+100	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n41A	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n41A	30	5+100	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+5	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	CP-64QAM	M+H	Outer_Full	see graph	PASS

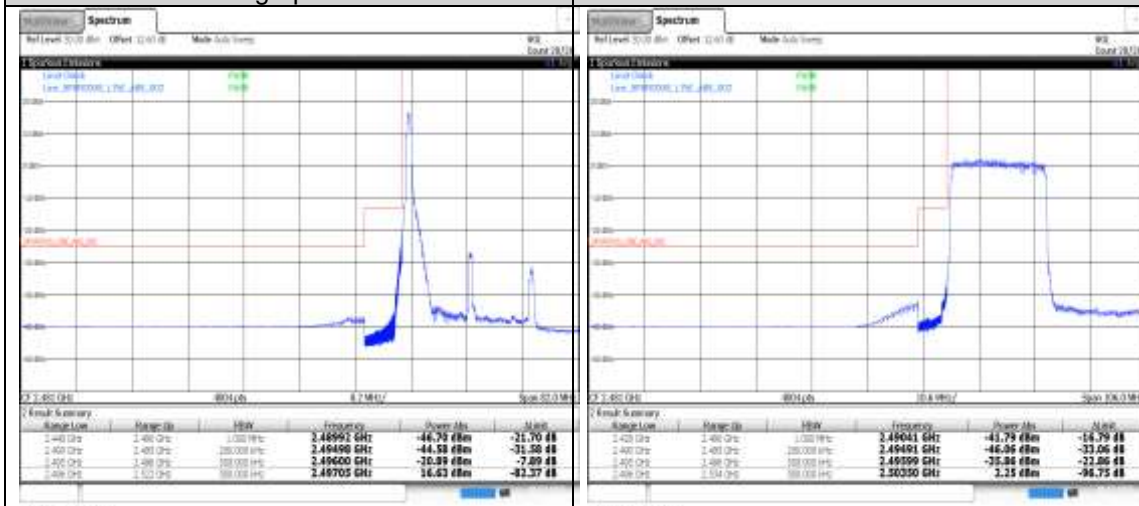
DC_66A_n71A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+5	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_66A_n71A	15	5+20	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_66A_n71A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_66A_n71A	15	5+20	CP-256QAM	M+H	Outer_Full	see graph	PASS

Test Graphs



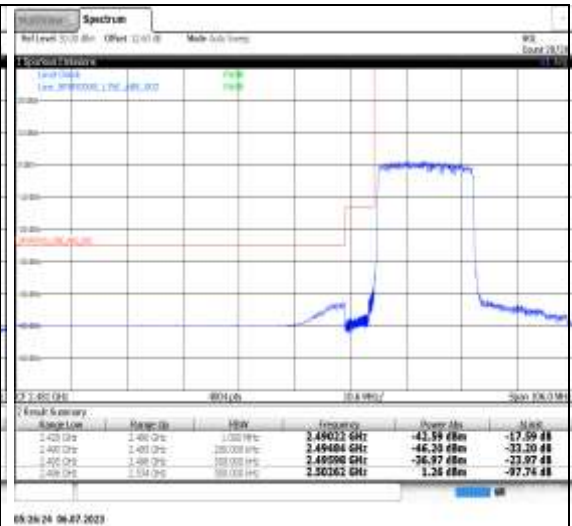
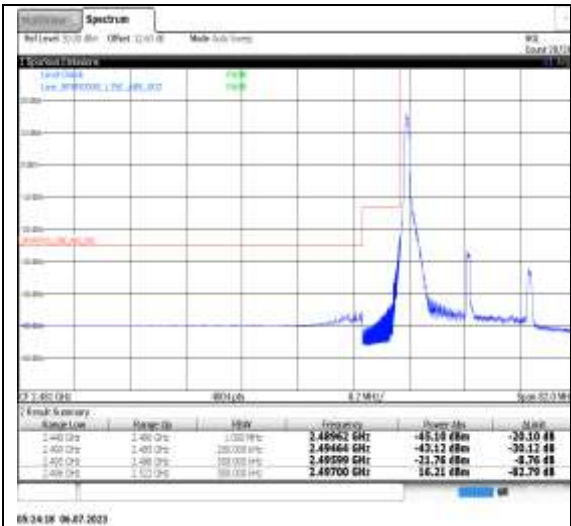
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1-DC_2A_n41A-PC3-30-5+20-M+L-3-QPSK-Outer_Full-Outer_Full@N/A-DFT-PI2BPSK-Outer_Full-50@0-1--PASS-see graph



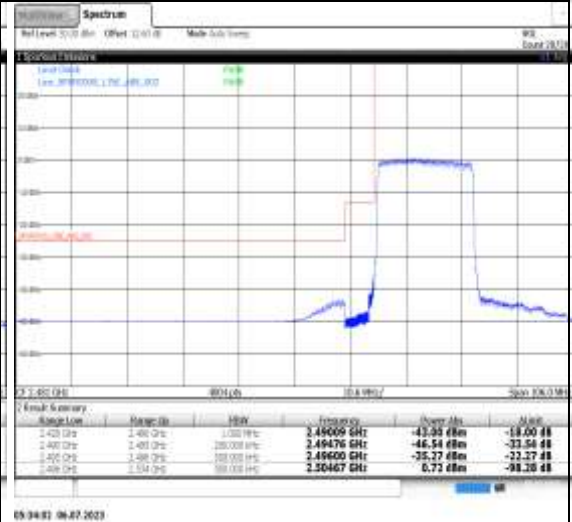
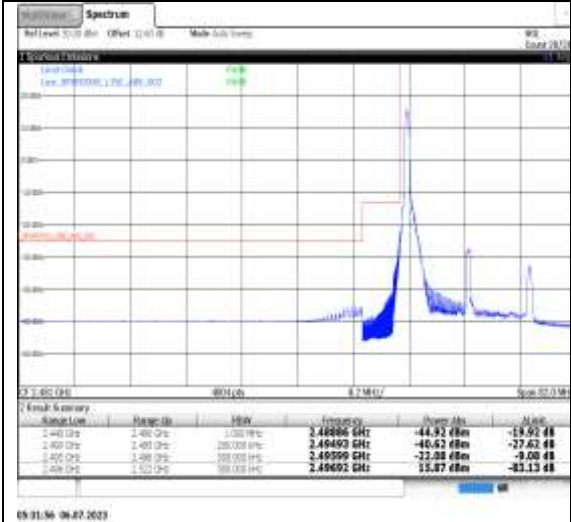
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1-DC_2A_n41A-PC3-30-5+20-M+L-6-QPSK-Outer_Full-Outer_Full@N/A-CP-QPSK-Outer_Full-51@0-1--PASS-see graph



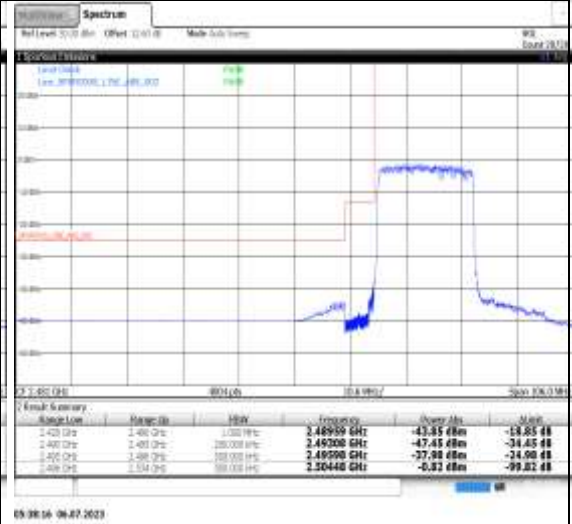
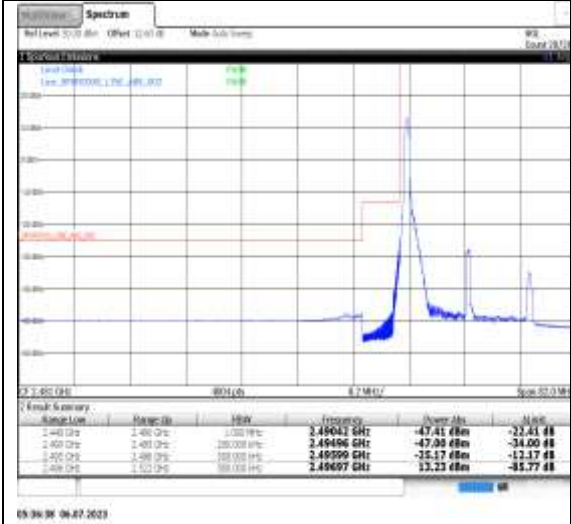
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1-DC_2A_n41A-PC3-30-5+20-M+L-9-QPSK-Outer_Full-Outer_Full@N/A-DFT-16QAM-Outer_Full-50@0-1--PASS-see graph



1-DC_2A_n41A-PC3-30-5+20-M+L-10-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph

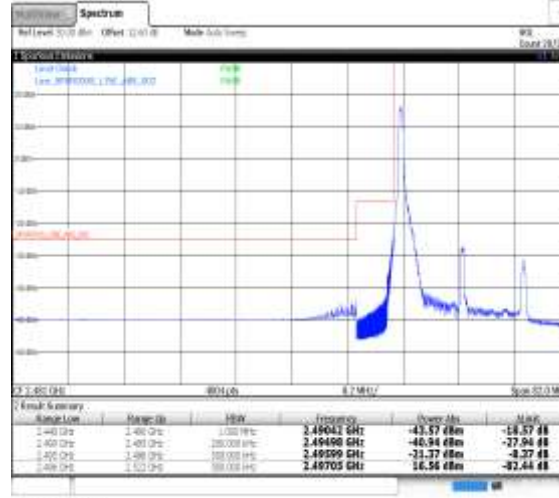
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1-DC_2A_n41A-PC3-30-5+20-M+L-10-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph

1-DC_2A_n41A-PC3-30-5+20-M+L-12-QPSK-Outer_Full-Outer_Full@N/A-DFT-64QAM-Outer_Full-50@0-1--PASS-see graph

1-DC_2A_n41A-PC3-30-5+20-M+L-13-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph



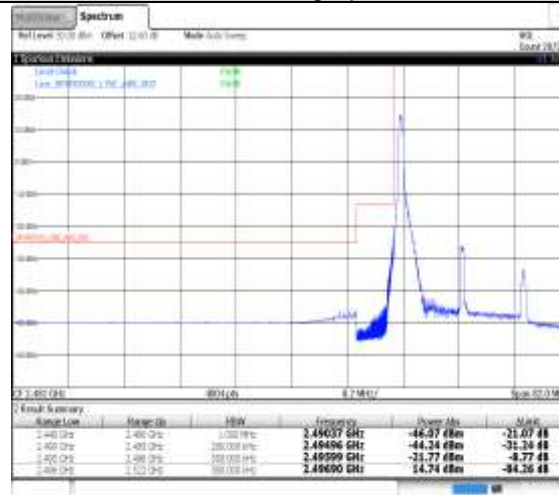
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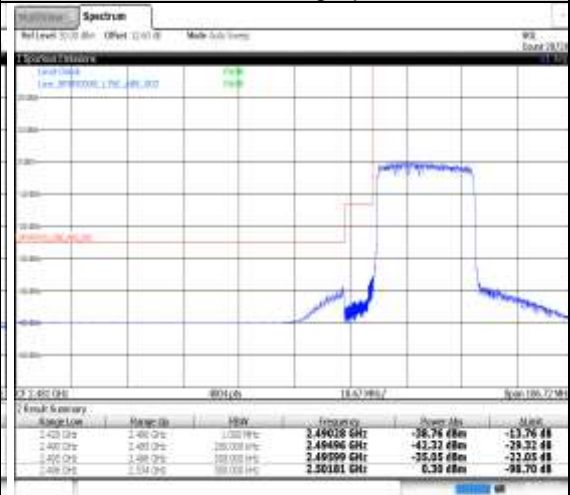
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06:56:54 06.27.2023

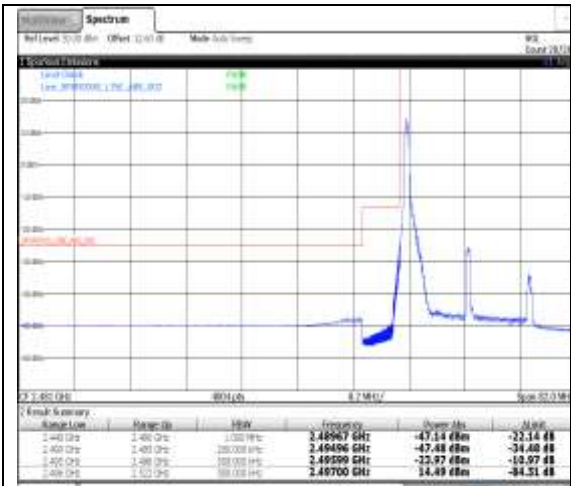
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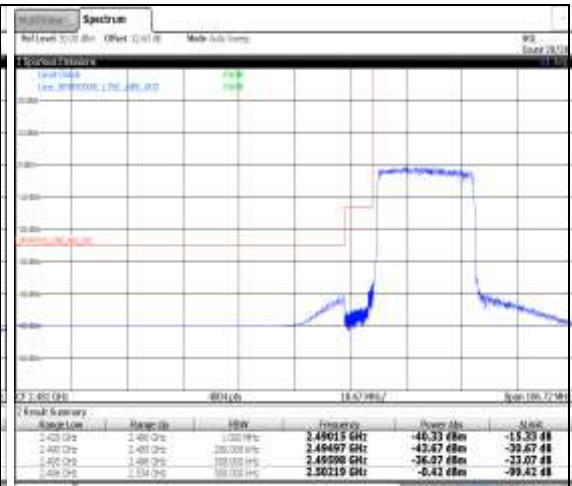
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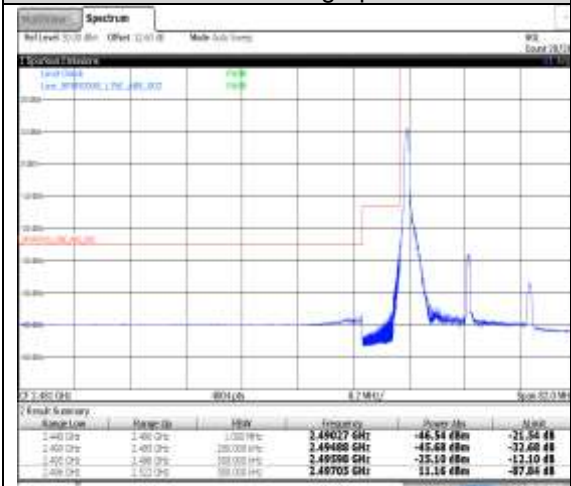
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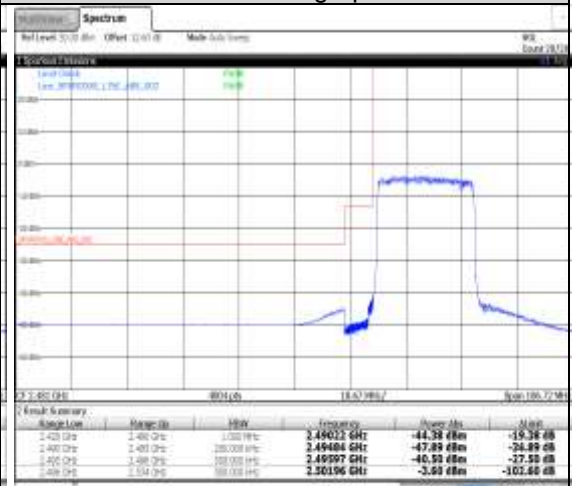
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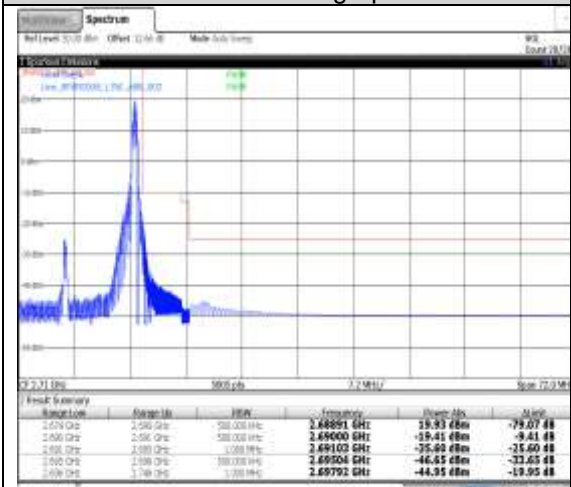
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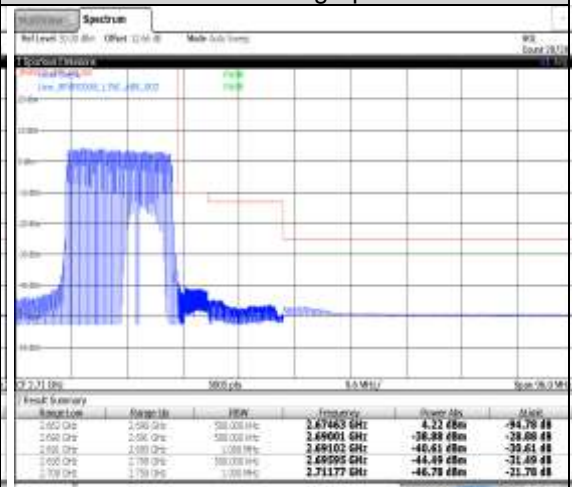
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1-DC_2A_n41A-PC3-30-5+20-M+L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-51@0-1--PASS-see graph

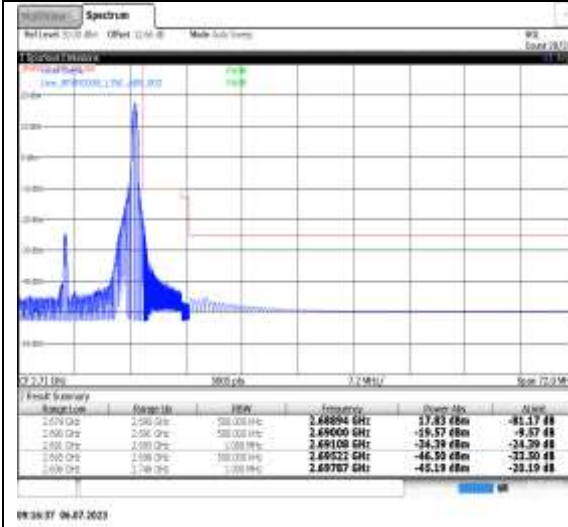


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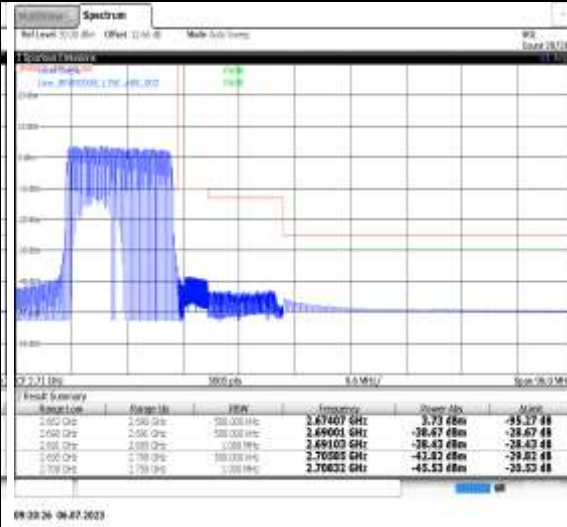


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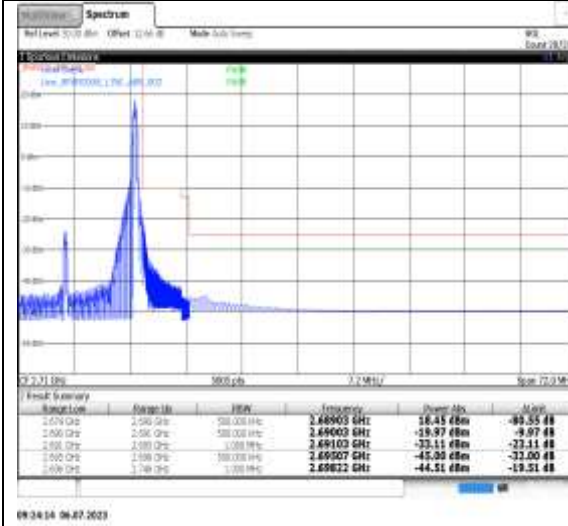
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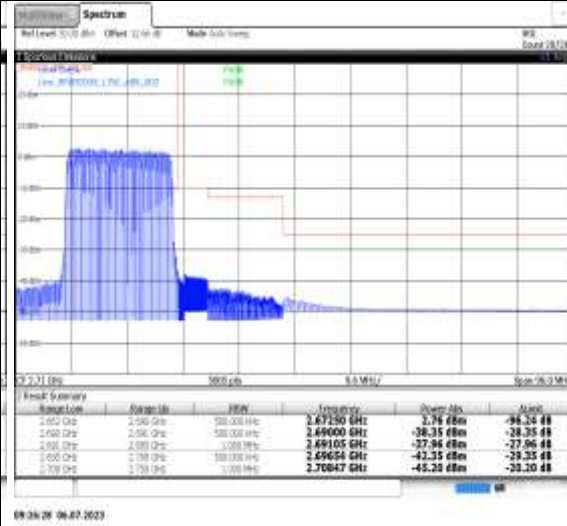
Outer_Full-50@0-1--PASS-see graph



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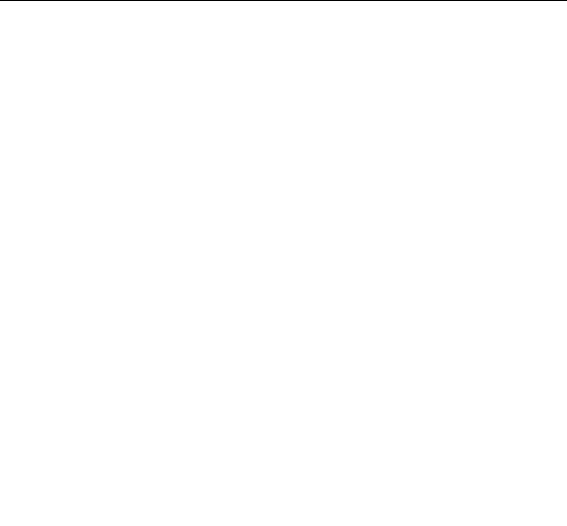
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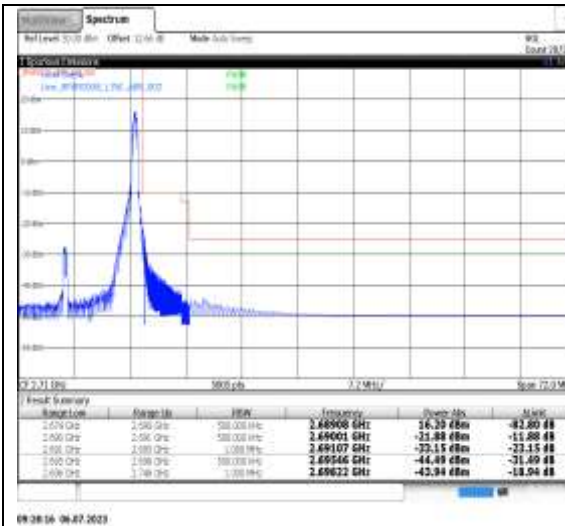


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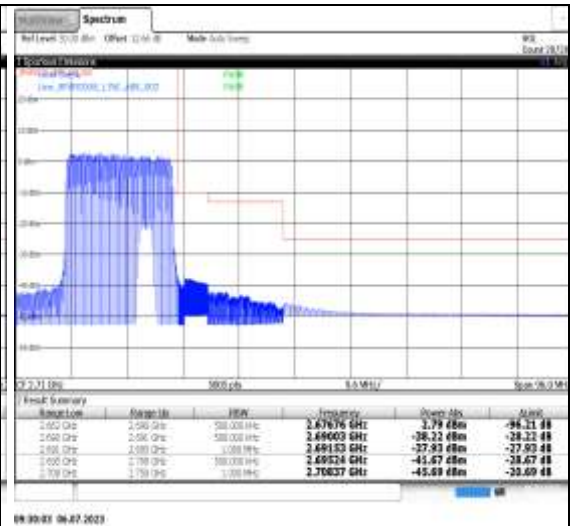


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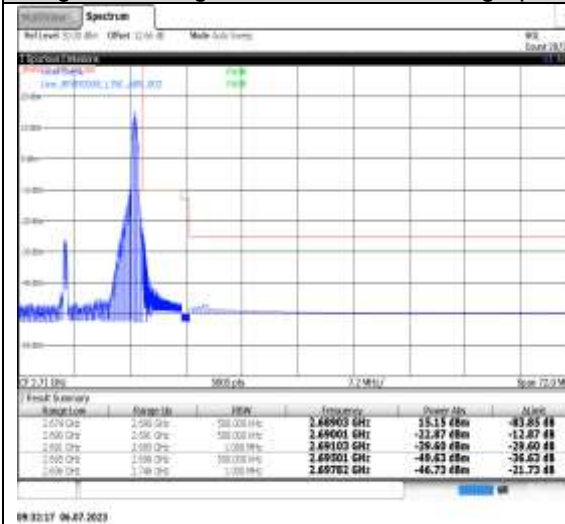




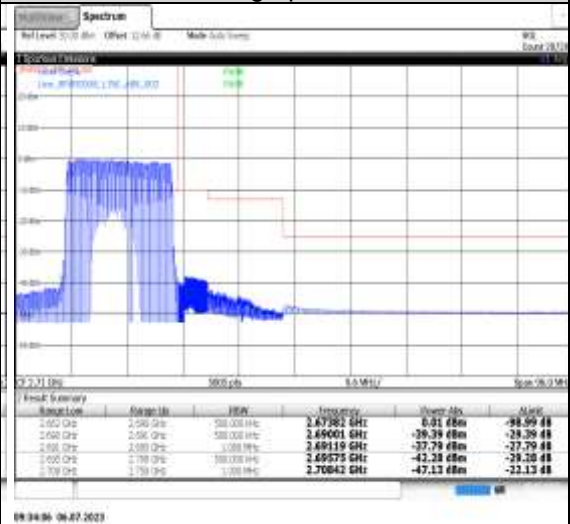
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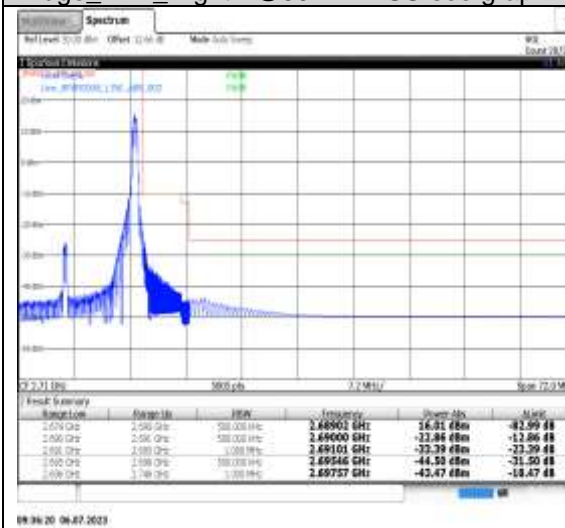
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1-DC_2A_n41A-PC3-30-5+20-M+H-14-QPSK-Edge_1RB_Right-Edge_1_Right@N/A-DFT-256QAM-Edge_1RB_Right-1@50-1--PASS-see graph



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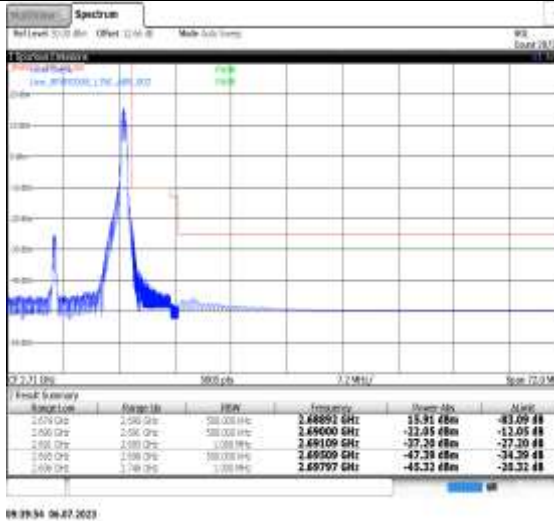


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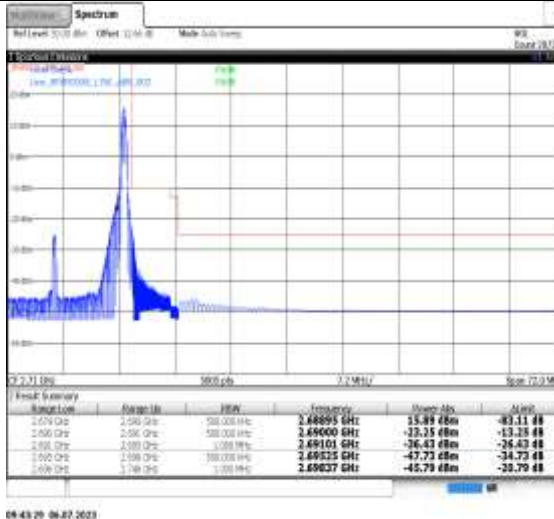
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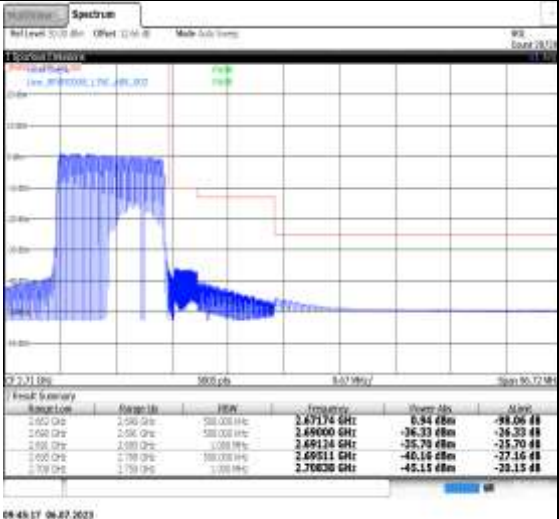
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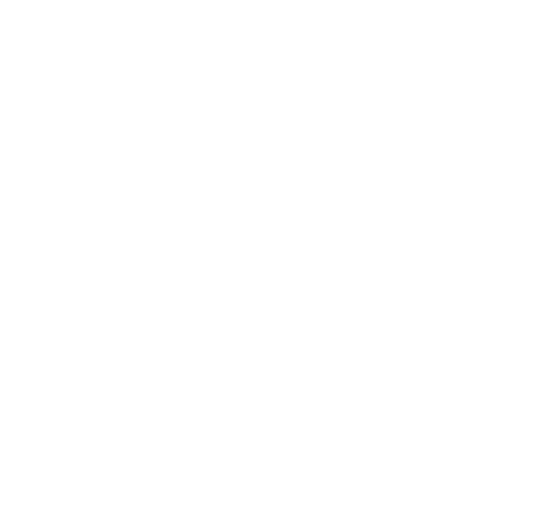
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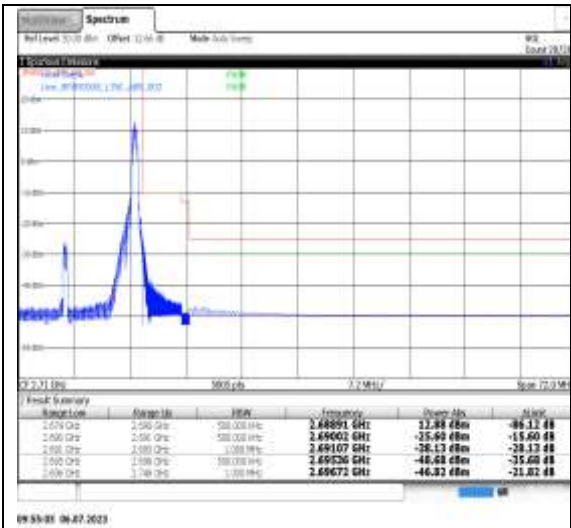


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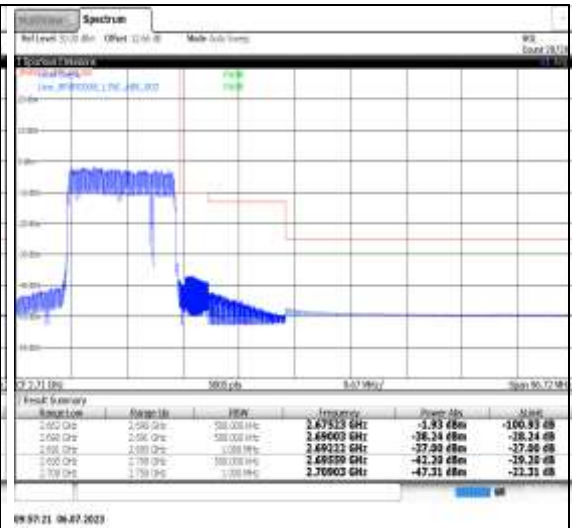
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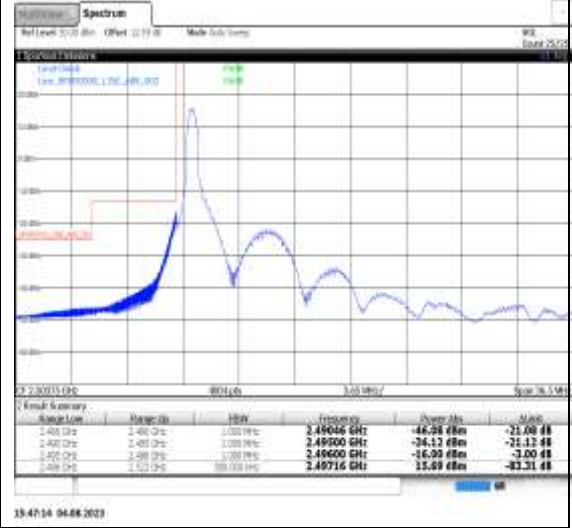
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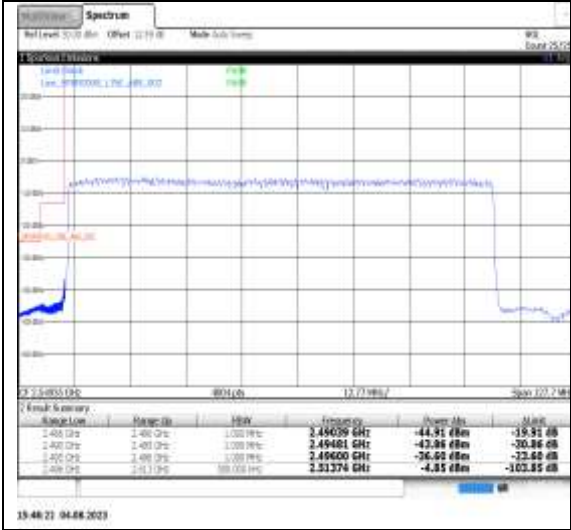
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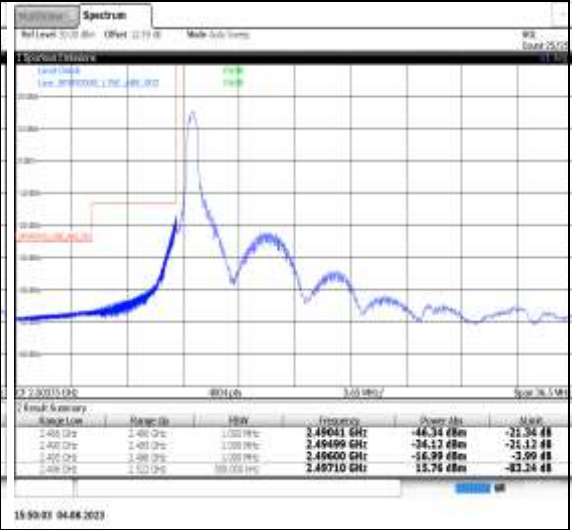
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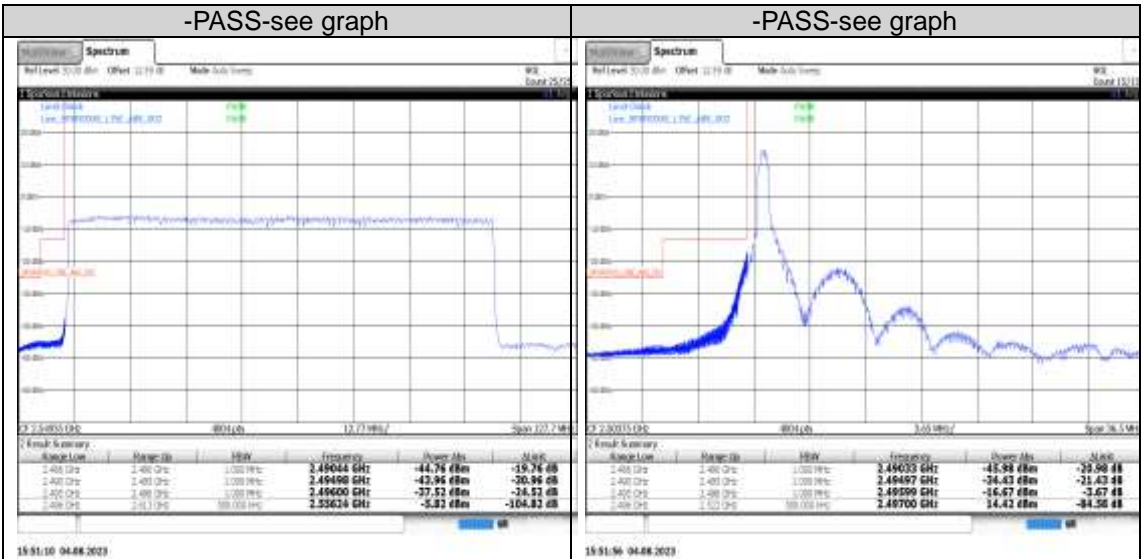
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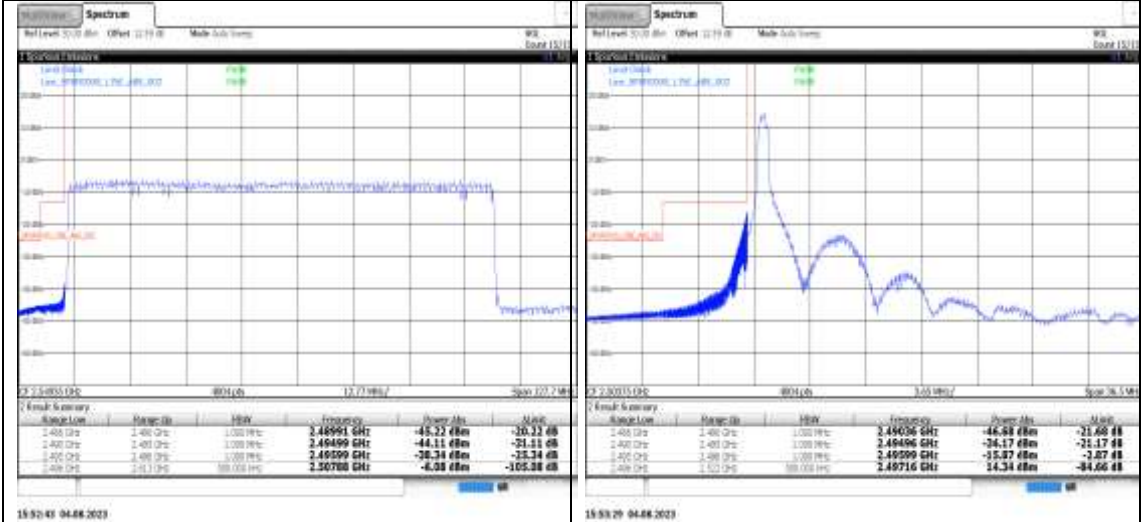
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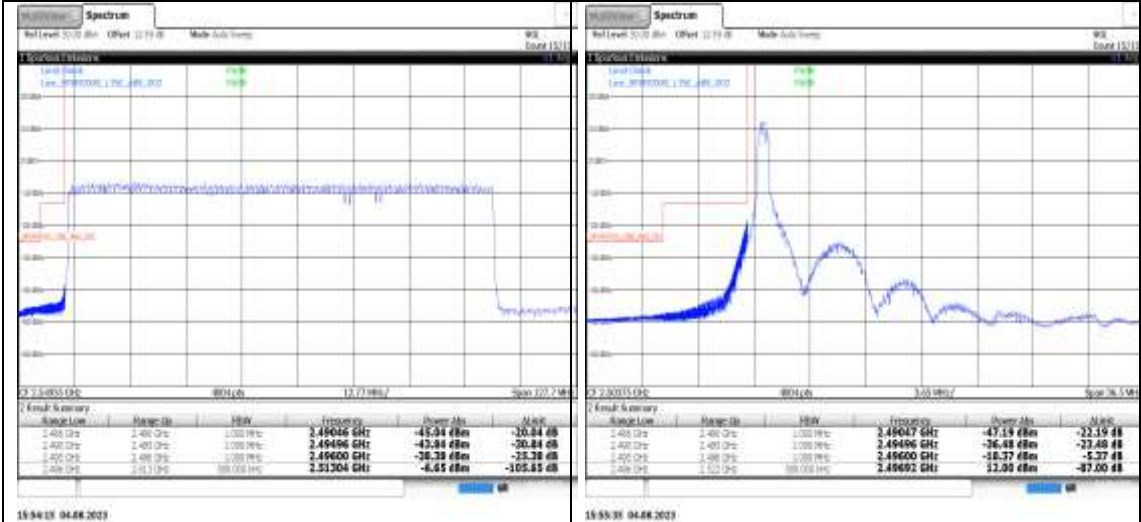
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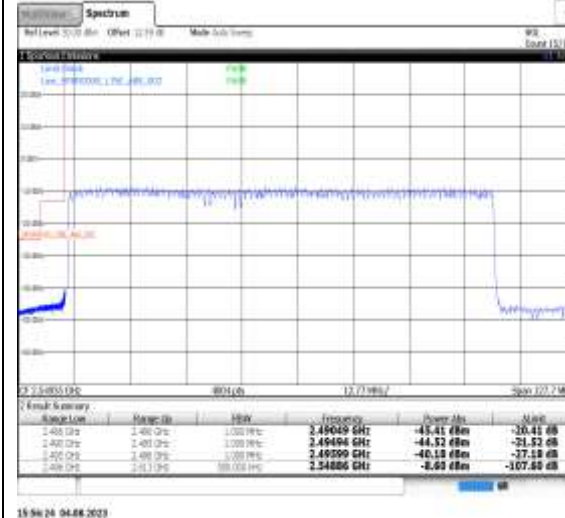


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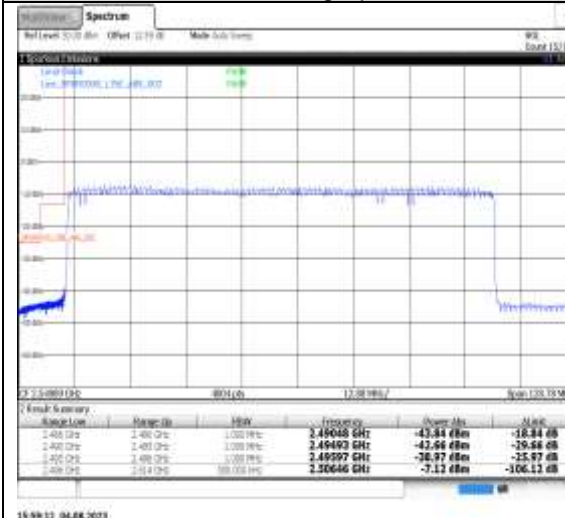


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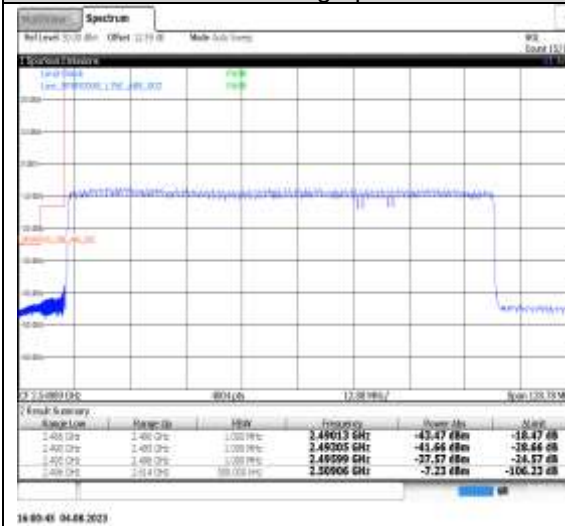
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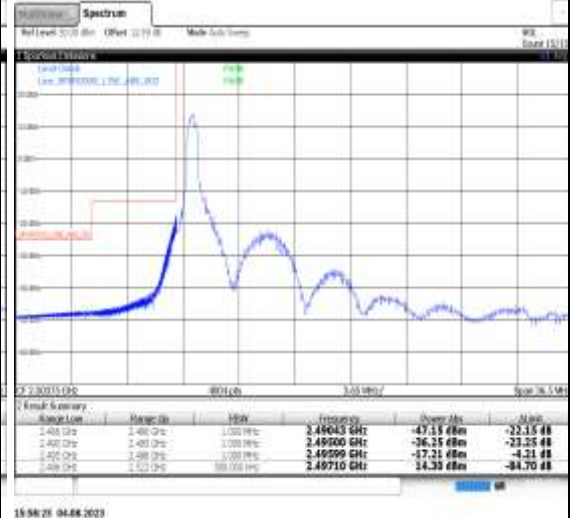
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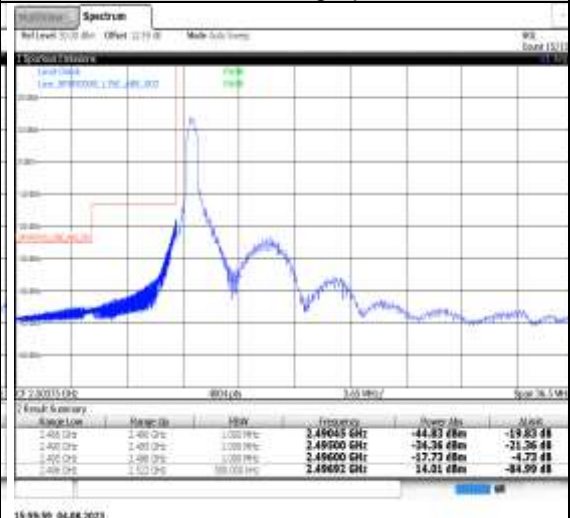
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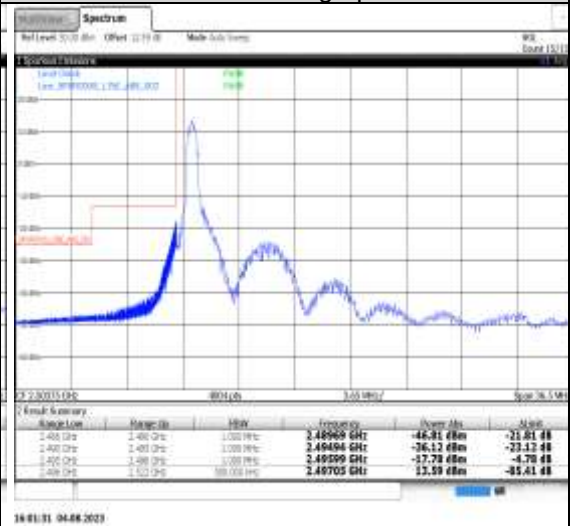
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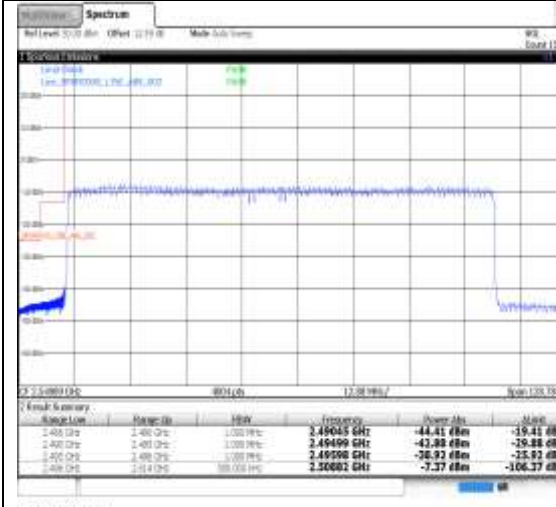
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1-DC_2A_n41A-PC3-30-5+100-M+L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Left-1@0-1--PASS-see graph

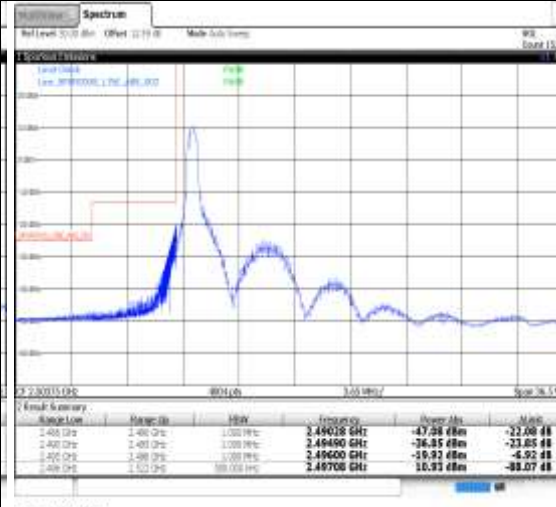


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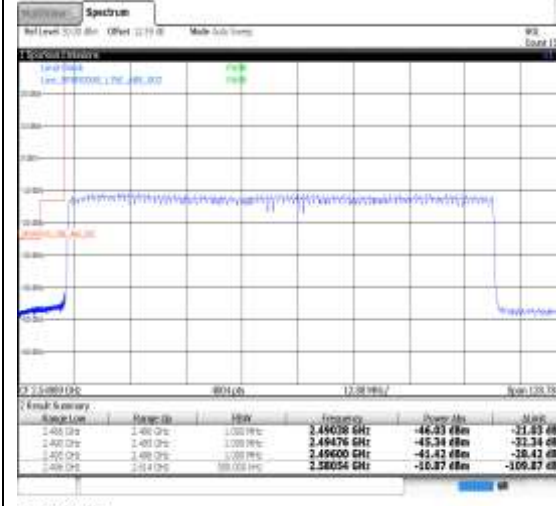
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18 03 17 04.08.2023

1-DC_2A_n41A-PC3-30-5+100-M+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-273@0-1--PASS-see graph



18 04 24 04.08.2023

1-DC_2A_n41A-PC3-30-5+100-M+L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph



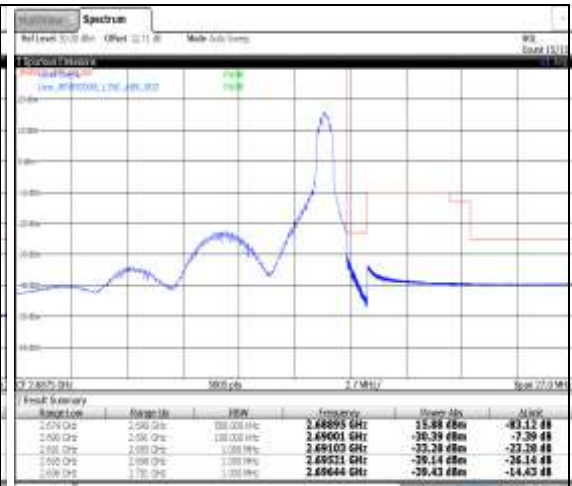
18 04 06 07.08.2023

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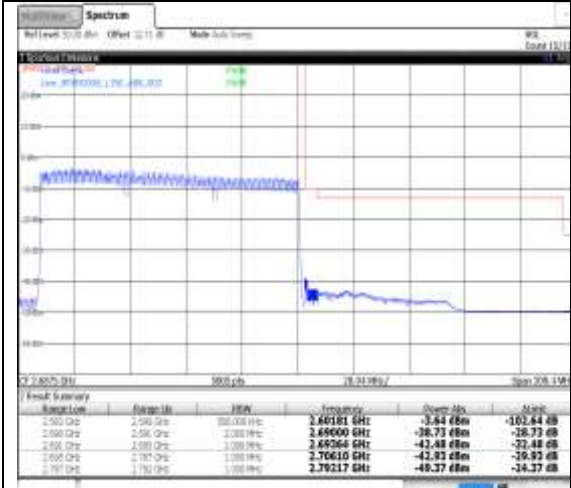
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1-DC_2A_n41A-PC3-30-5+100-M+H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outter_Full-270@0-1-PASS-see graph



1-DC_2A_n41A-PC3-30-5+100-M+H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1@272-1--PASS-see graph



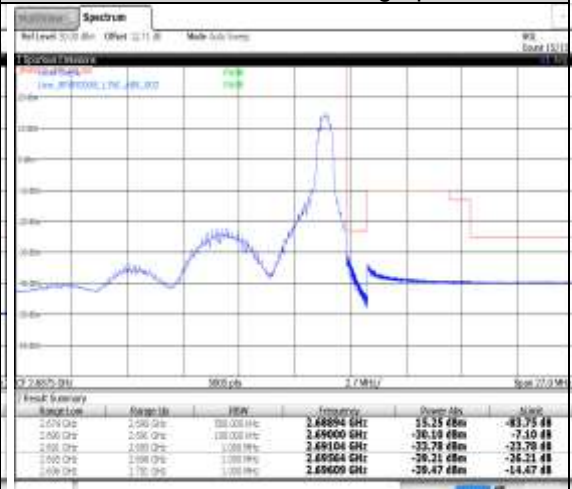
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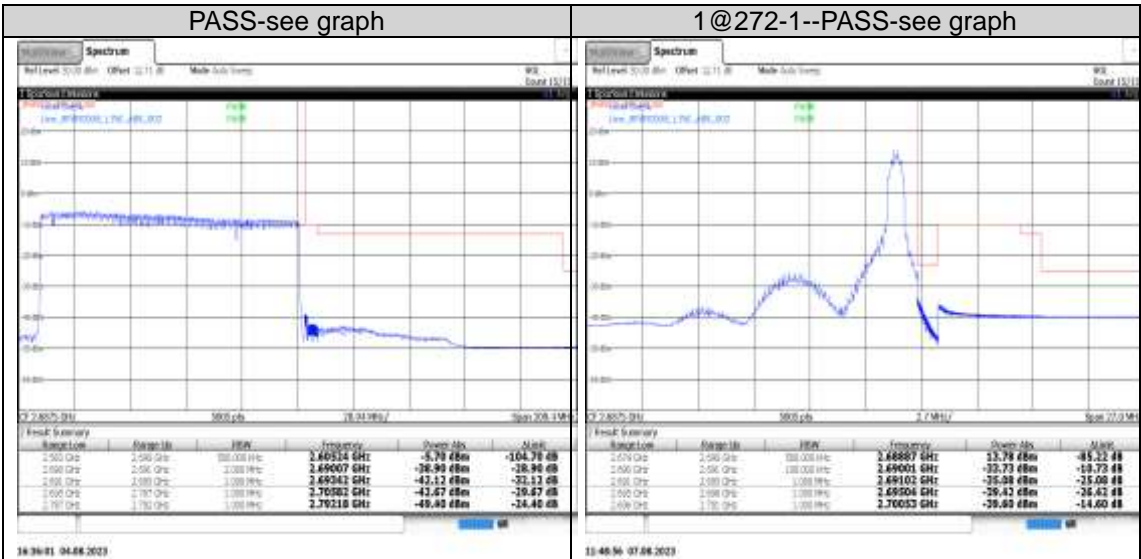
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1-DC_2A_n41A-PC3-30-5+100-M+H-9-N/A-N/A-N/A-DFT-16QAM-Outter_Full-270@0-1--PASS-see graph

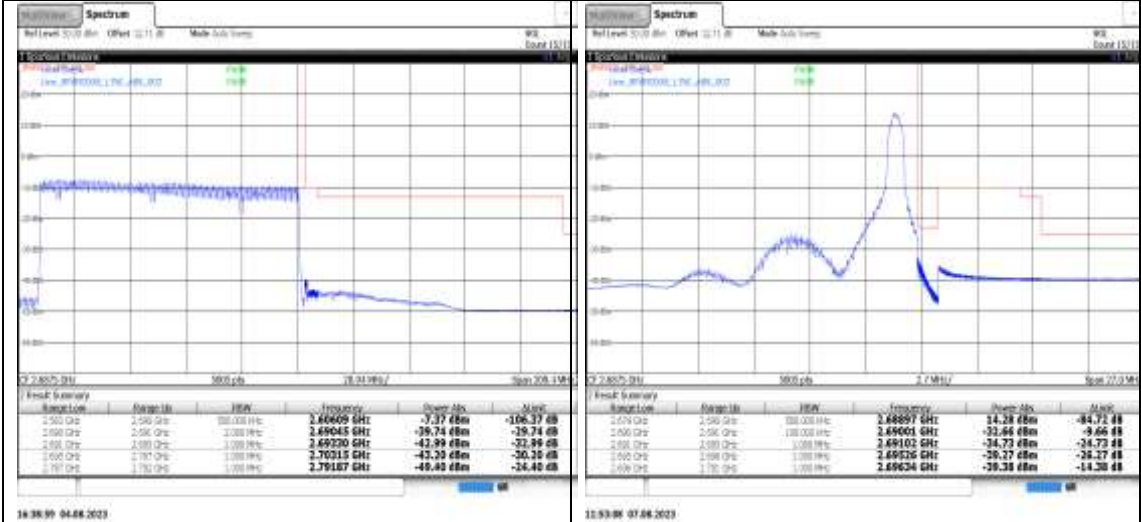


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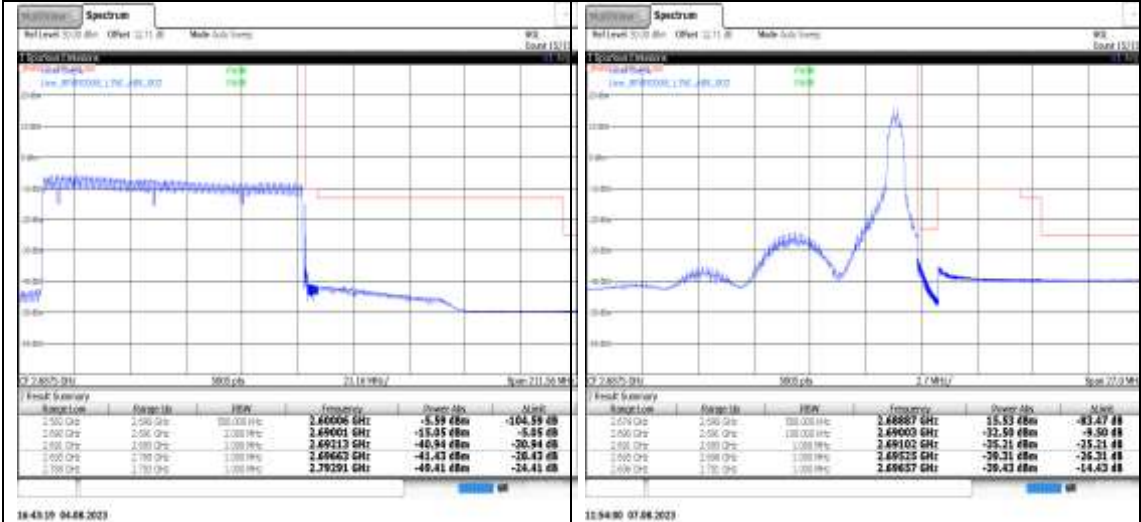
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1-DC_2A_n41A-PC3-30-5+100-M+H-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB_Right-1@272-1--PASS-see graph



1-DC_2A_n41A-PC3-30-5+100-M+H-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-270@0-1--PASS-see graph

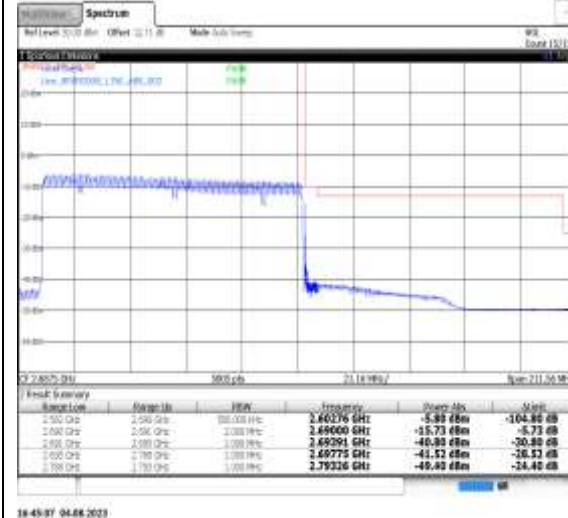
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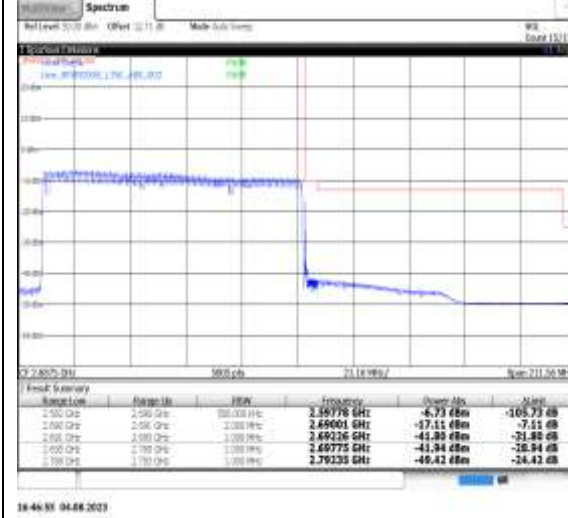
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1-DC_2A_n41A-PC3-30-5+100-M+H-20-N/A-

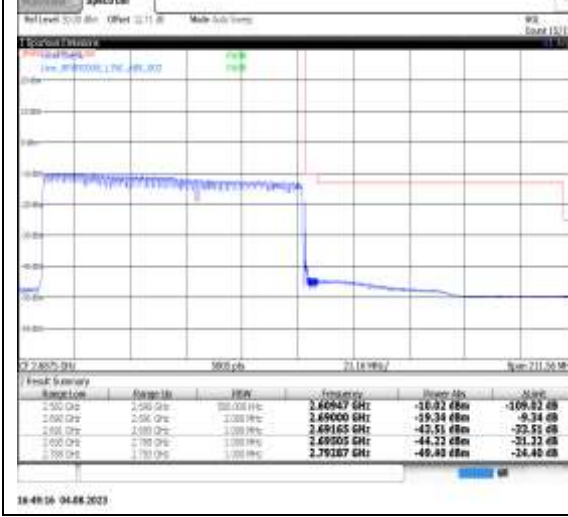
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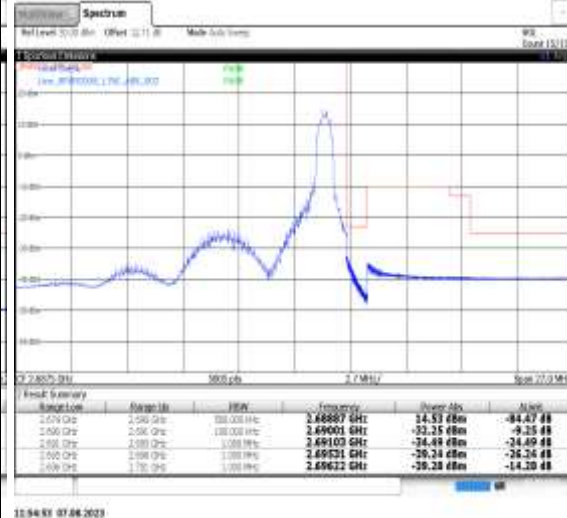
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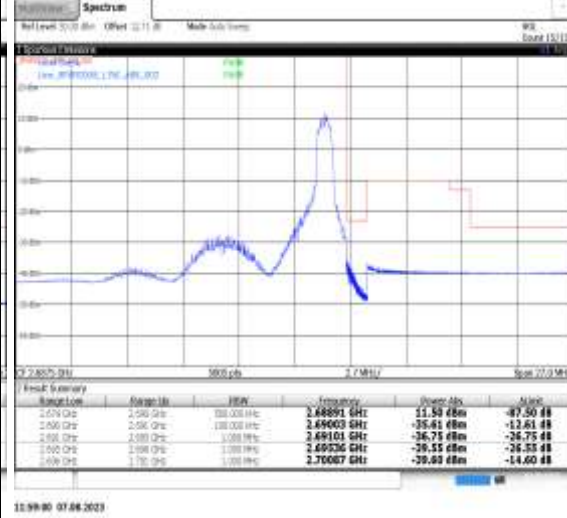
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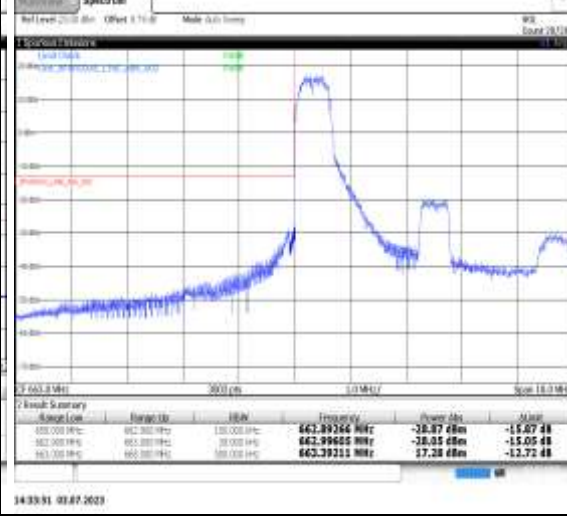
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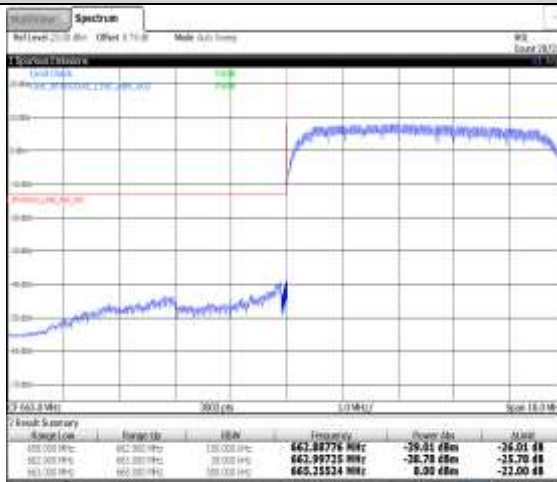
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1-DC_2A_n41A-PC3-30-5+100-M+H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@272-1--PASS-see graph

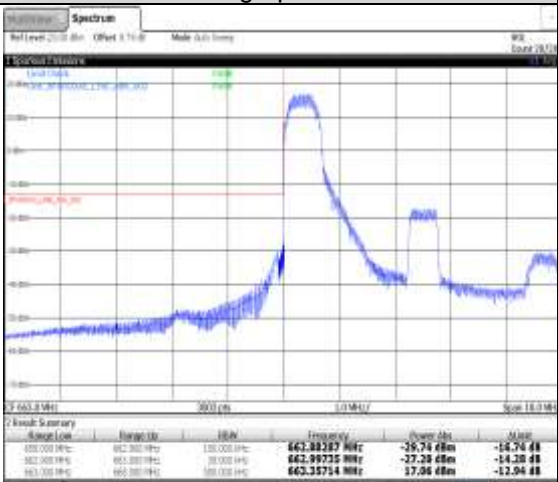


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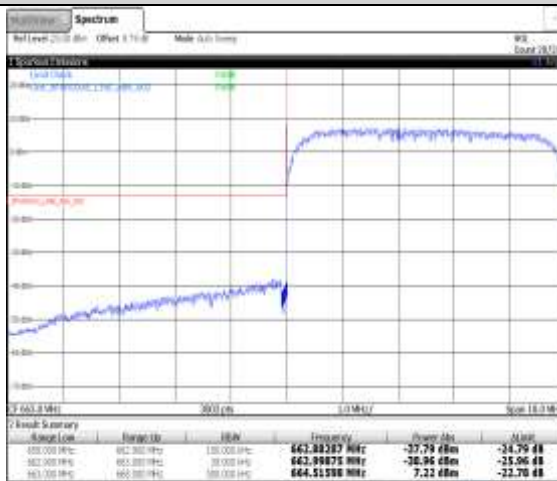
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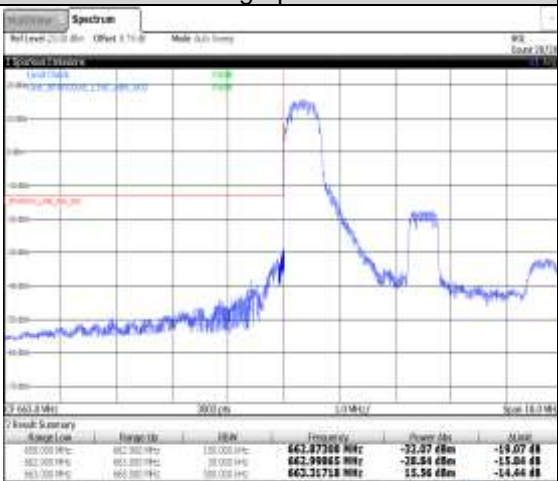
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1-DC_2A_n71A-PC3-15-5+5-M+L-3-QPSK-Outer_Full-Outer_Full@N/A-DFT-PI2BPSK-Outer_Full-25@0-1--PASS-see graph



1441:07 03.07.2023

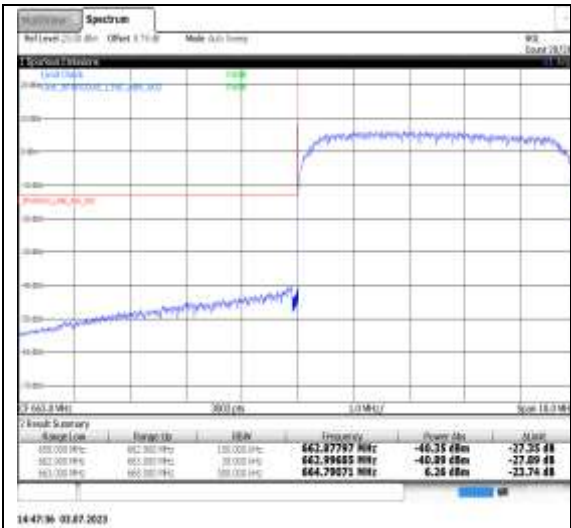
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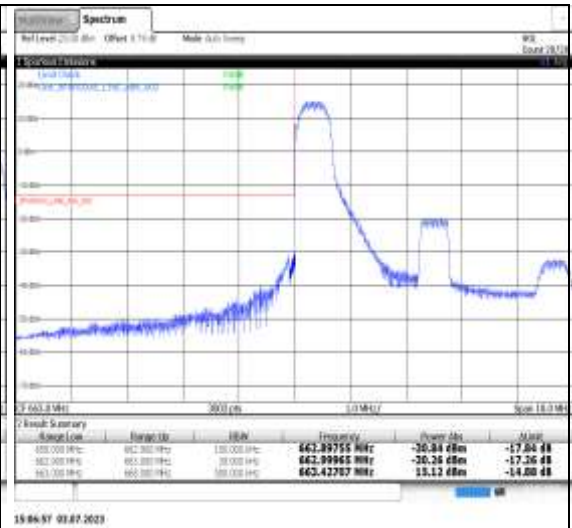
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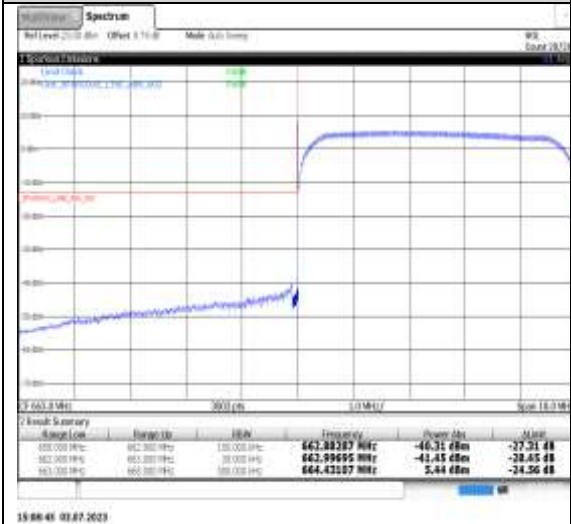
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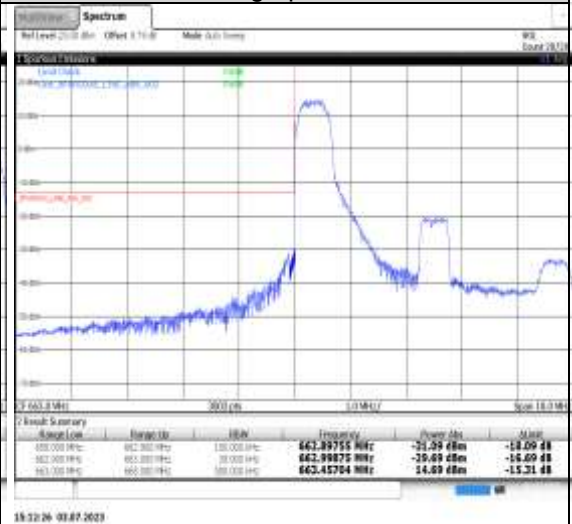
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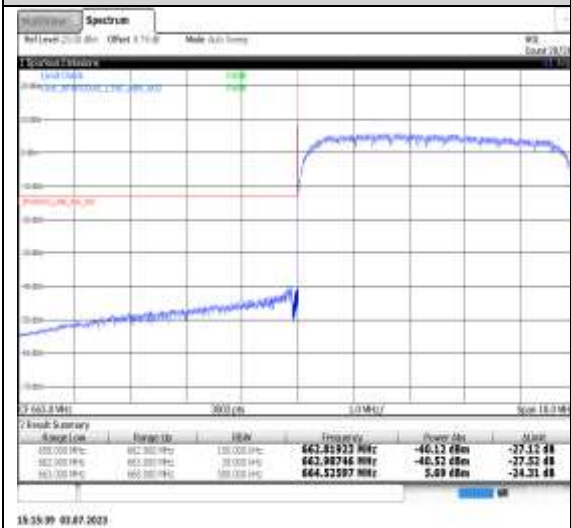
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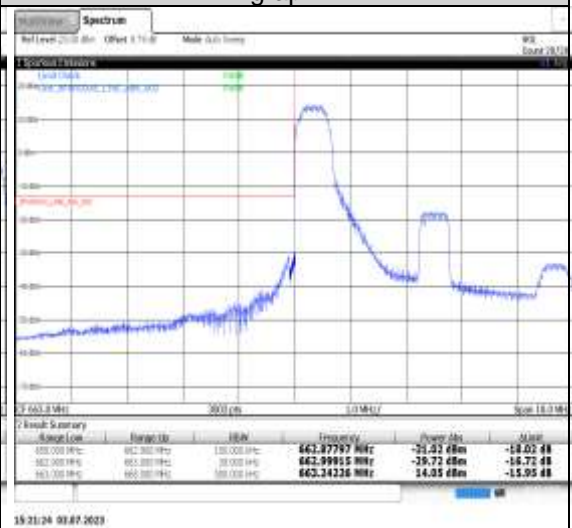
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1-DC_2A_n71A-PC3-15-5+5-M+L-13-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

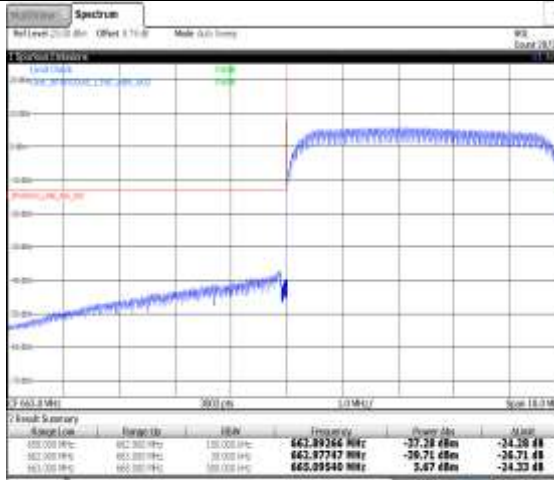


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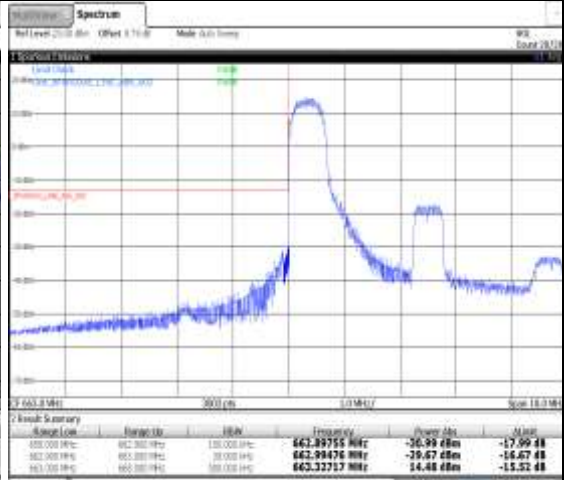
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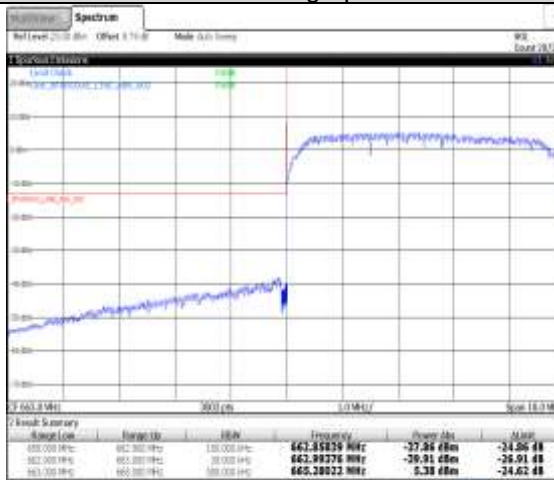
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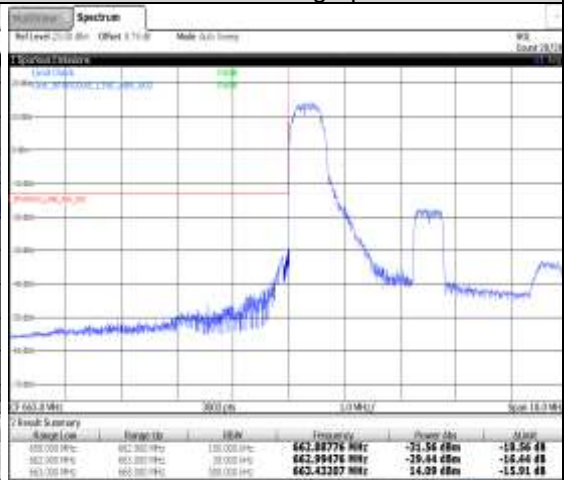
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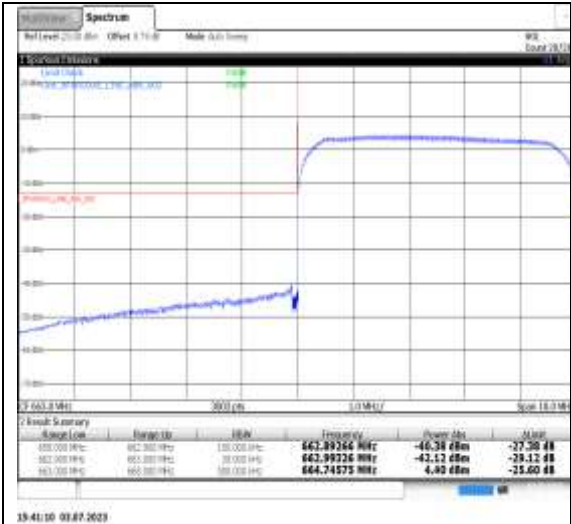
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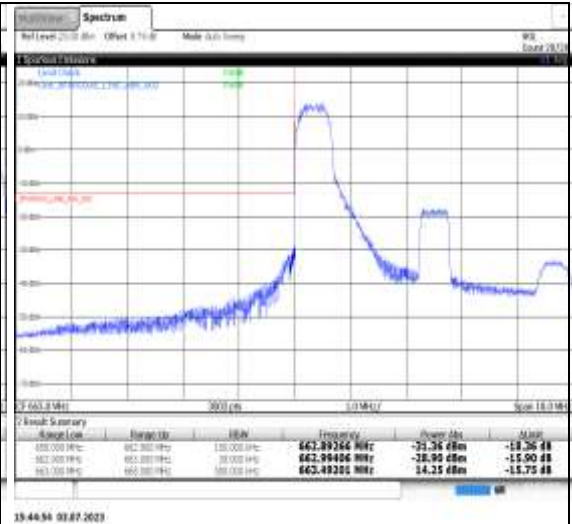
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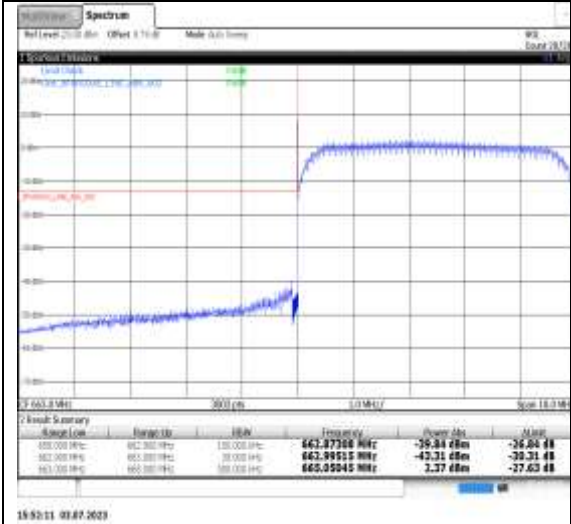
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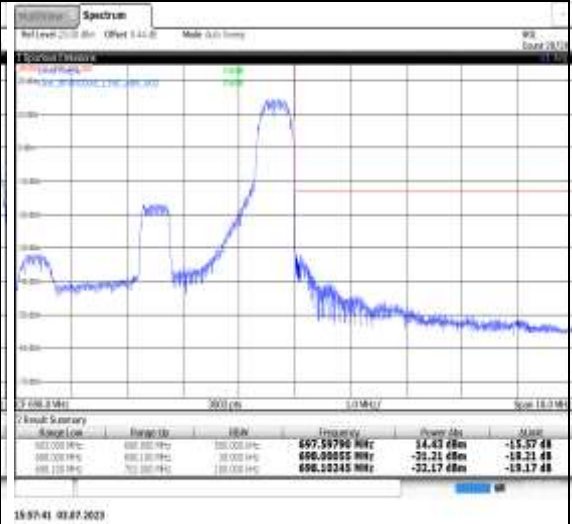
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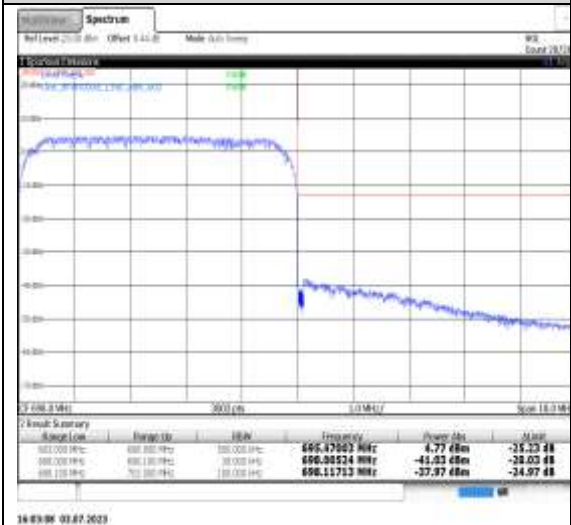
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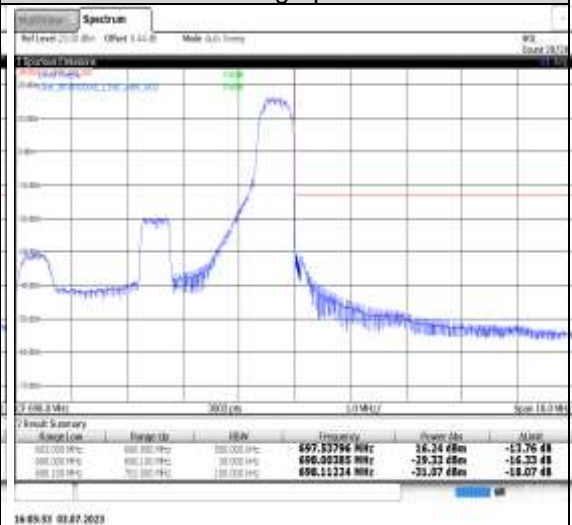
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1-DC_2A_n71A-PC3-15-5+5-M+H-2-QPSK-Edge_1RB_Right-Edge_1_Right@N/A-DFT-PI2BPSK-Edge_1RB_Right-1@24-1--PASS-see graph

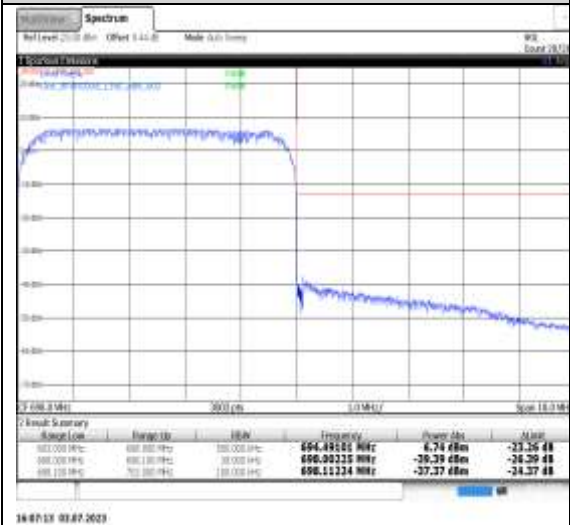


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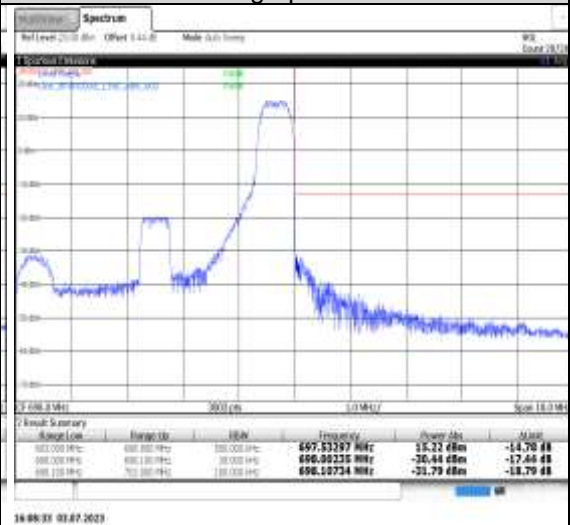
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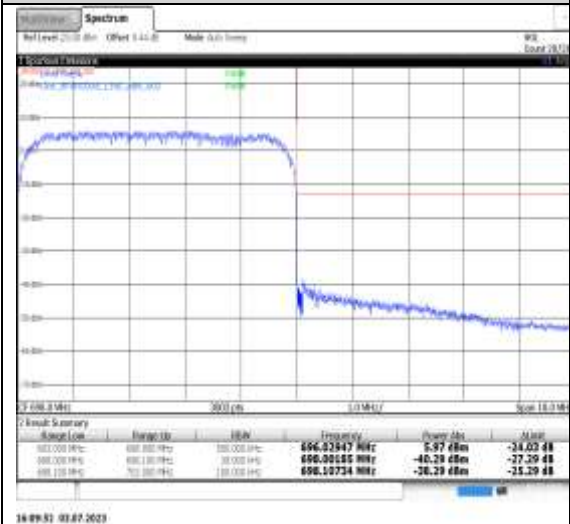
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Edge_1RB_Right-Edge_1_Right@N/A-CP-QPSK-Edge_1RB_Right-1@24-1--PASS-see graph



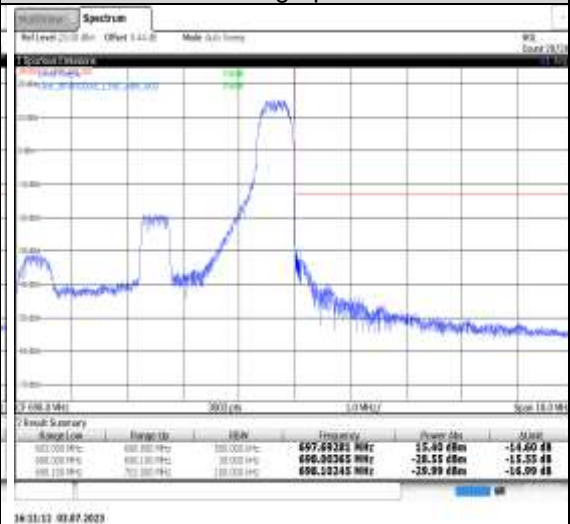
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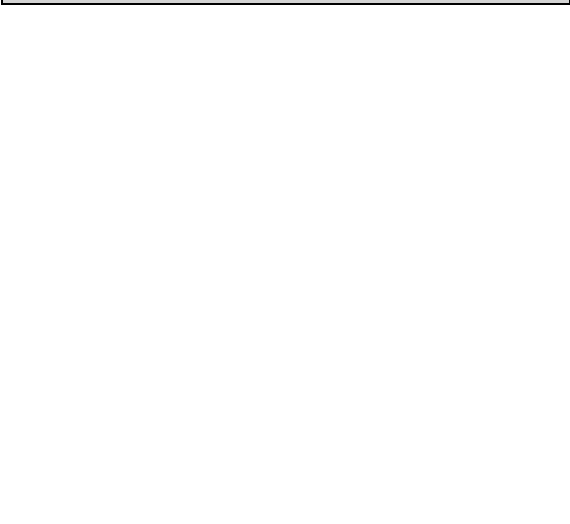
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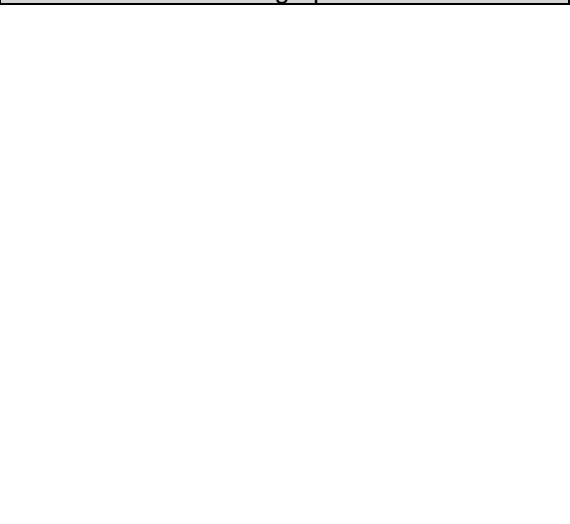
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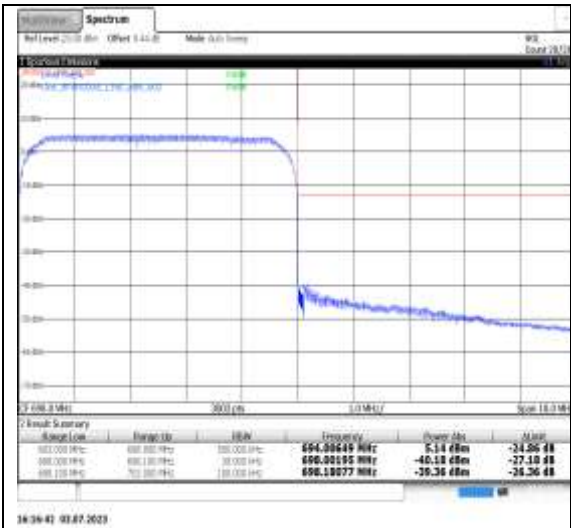


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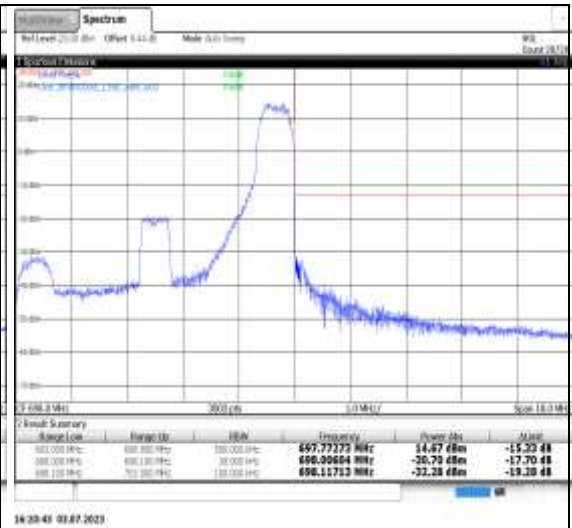
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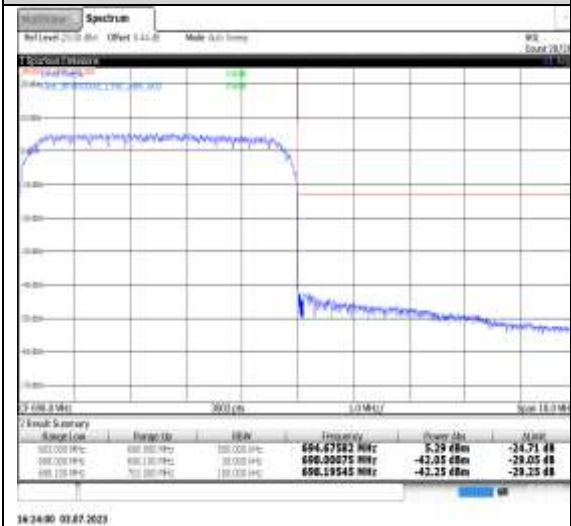
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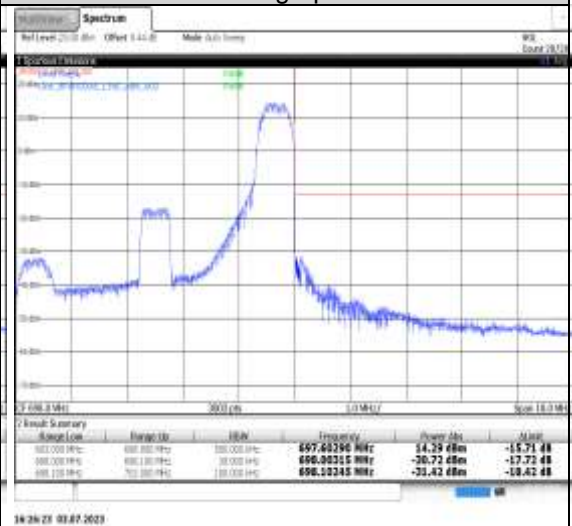
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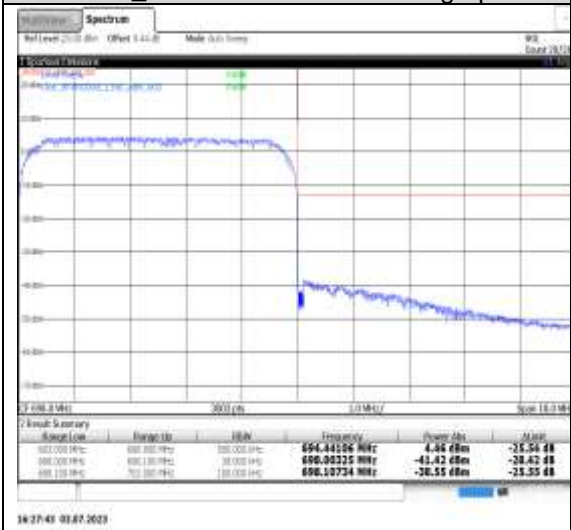
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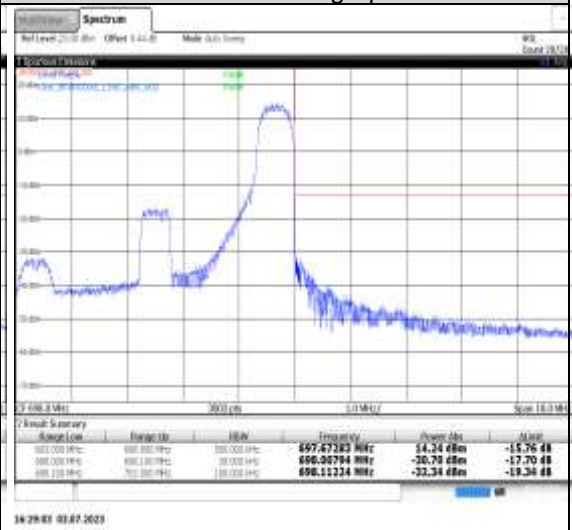
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1-DC_2A_n71A-PC3-15-5+5-M+H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Right-1@24-1--PASS-see graph

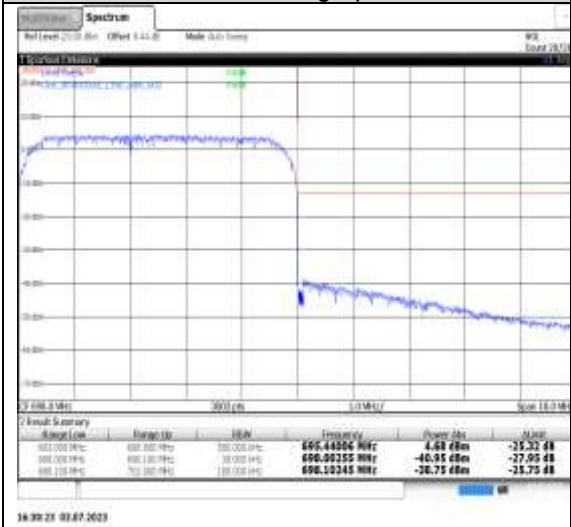


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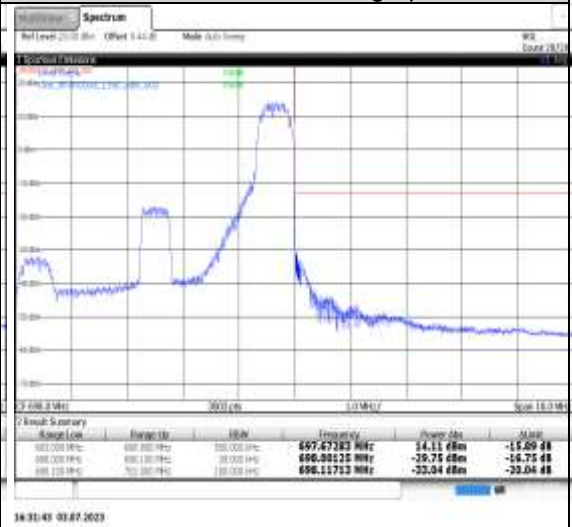


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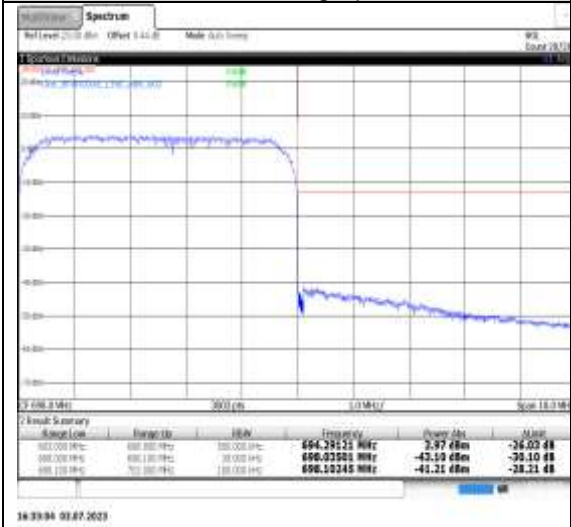
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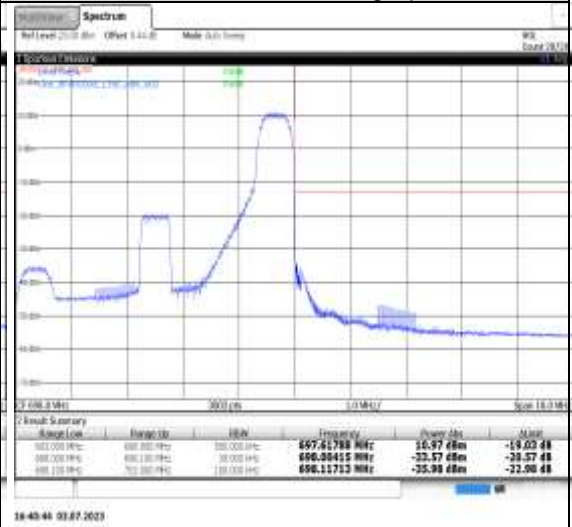
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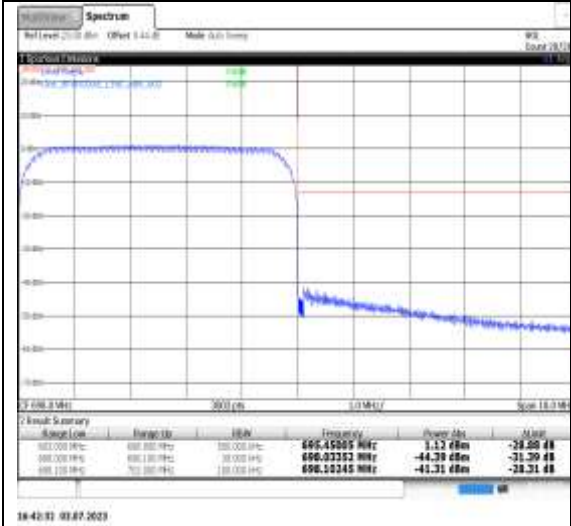
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1-DC_2A_n71A-PC3-15-5+5-M+H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Right-1@24-1--PASS-see graph



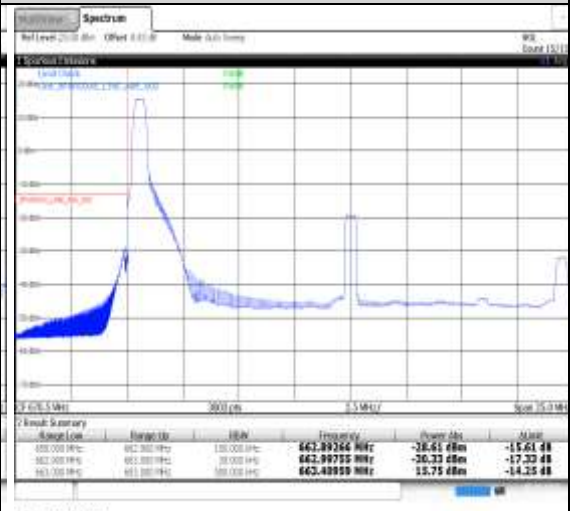
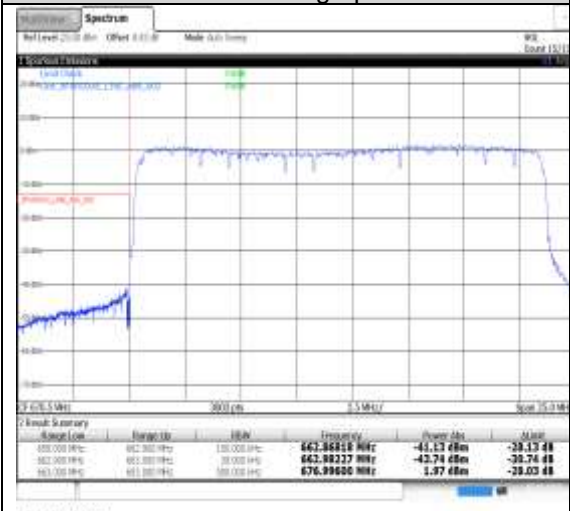
1-DC_2A_n71A-PC3-15-5+5-M+H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-25@0-1--PASS-see graph



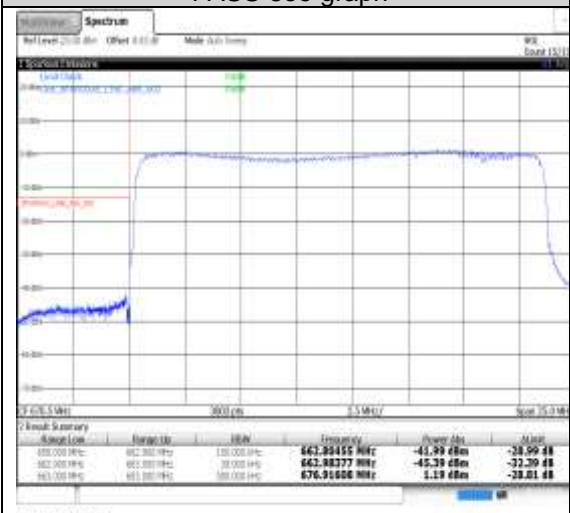
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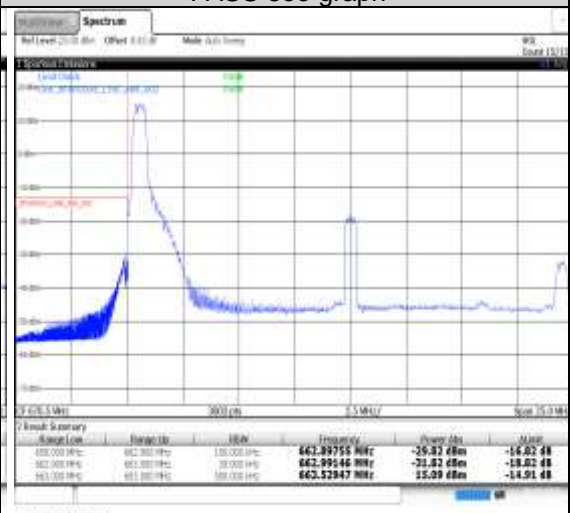
1-DC_2A_n71A-PC3-15-5+5-M+H-27-N/A-
N/A-N/A-CP-256QAM-Outer_Full-25@0-1--
PASS-see graph



1-DC_2A_n71A-PC3-15-5+20-M+L-3-N/A-
N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-1-
-PASS-see graph

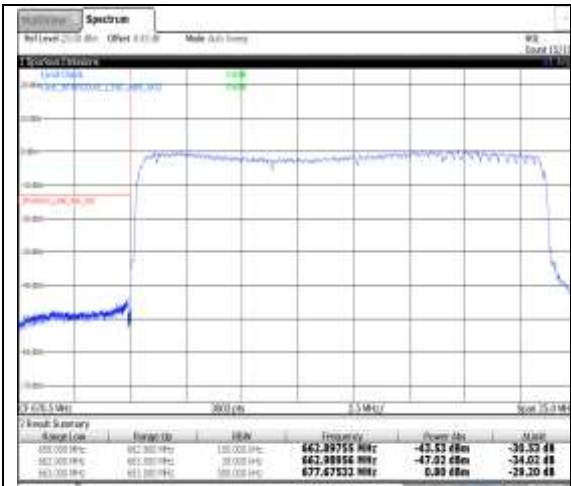


1-DC_2A_n71A-PC3-15-5+20-M+L-4-N/A-
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-PASS-see graph

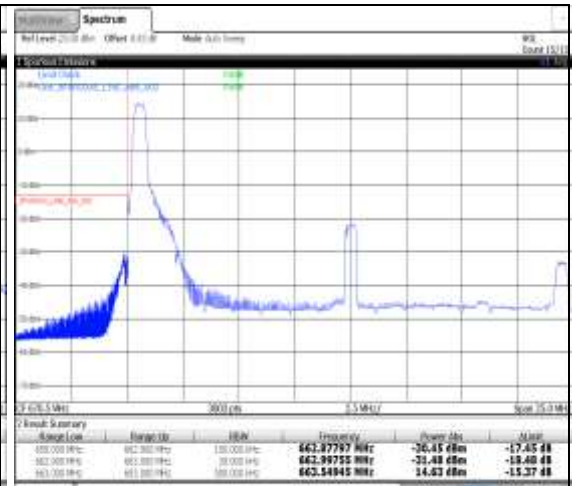


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PASS-see graph

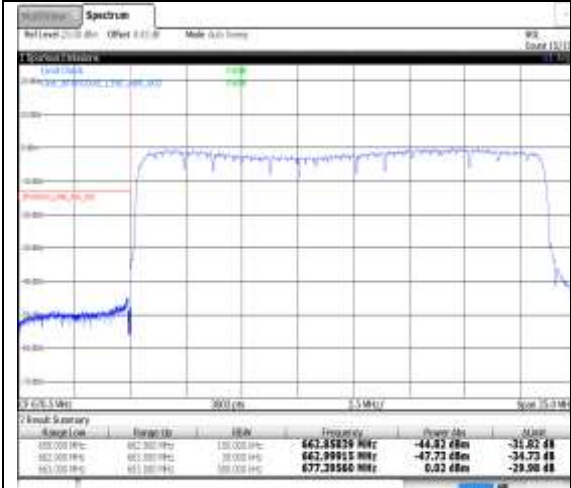
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1--PASS-see graph



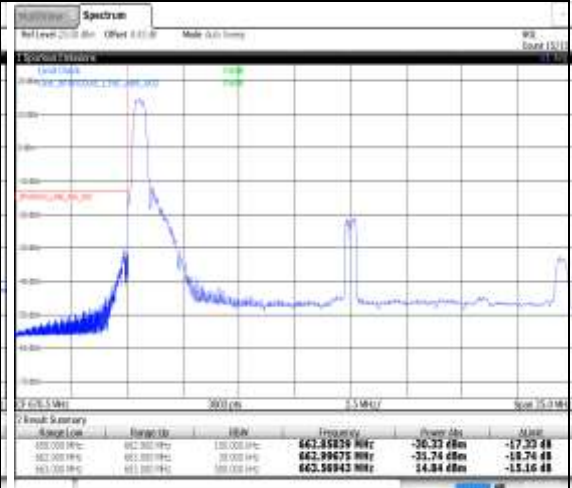
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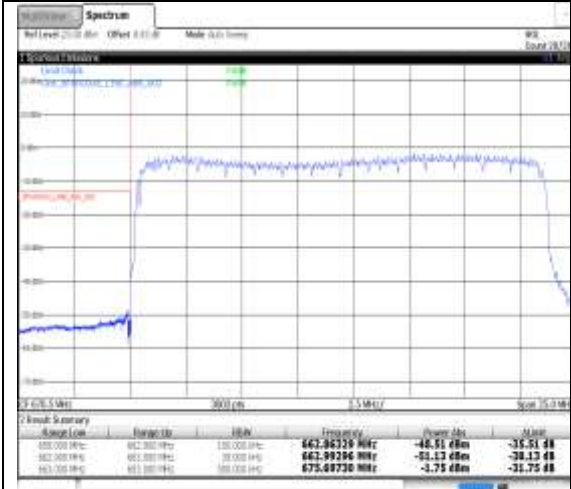
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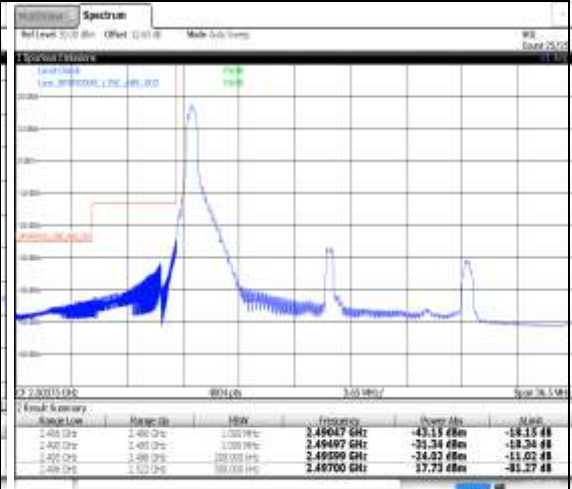
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1-DC_2A_n71A-PC3-15-5+20-M+L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

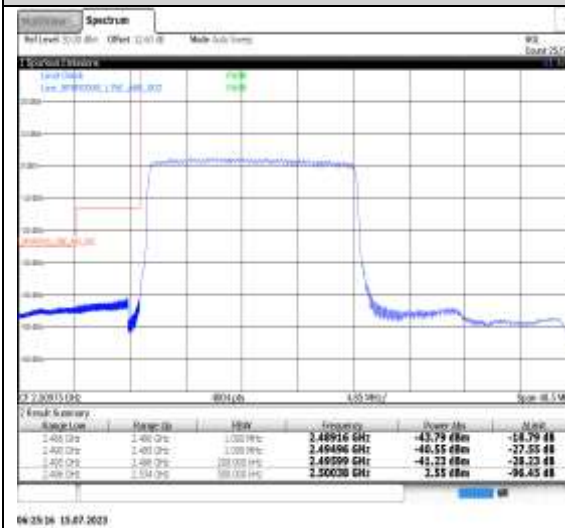


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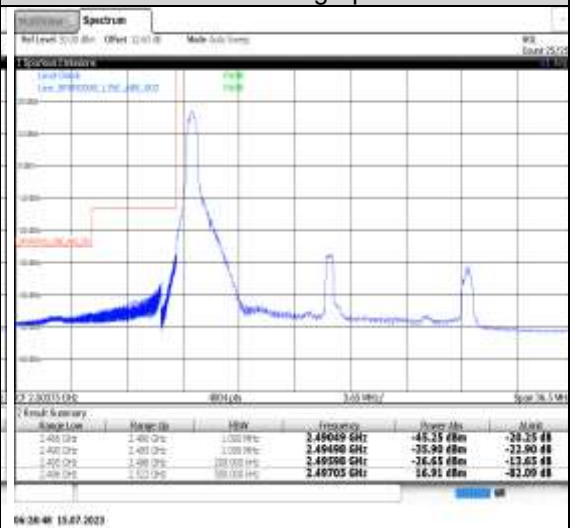


1-DC_66A_n41A-PC3-30-5+20-M+L-1-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-

-PASS-see graph

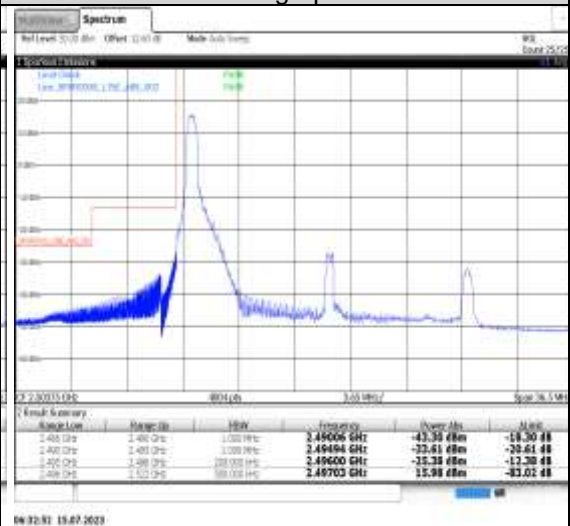
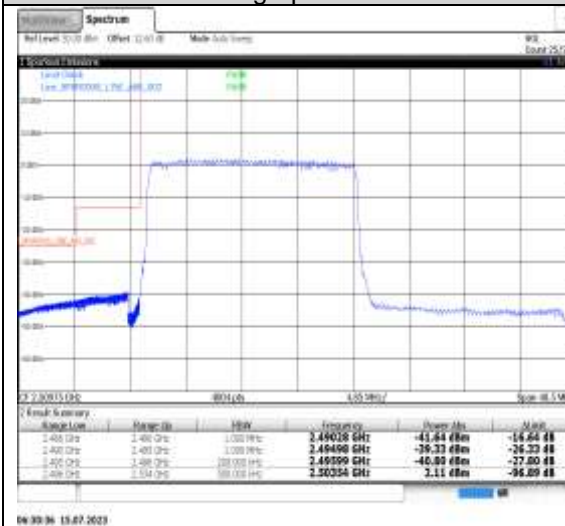


DFT-PI2BPSK-Edge_1RB_Left-1@0-1--PASS-see graph



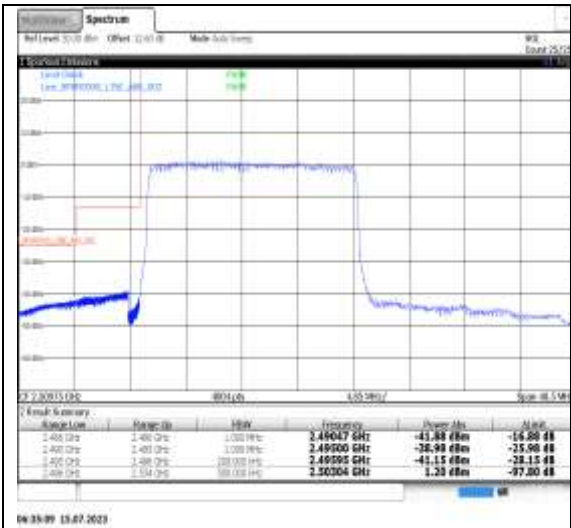
1-DC_66A_n41A-PC3-30-5+20-M+L-3-QPSK-Outer_Full-Outer_Full@N/A-DFT-PI2BPSK-Outer_Full-50@0-1--PASS-see graph

1-DC_66A_n41A-PC3-30-5+20-M+L-4-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-CP-QPSK-Edge_1RB_Left-1@0-1--PASS-see graph

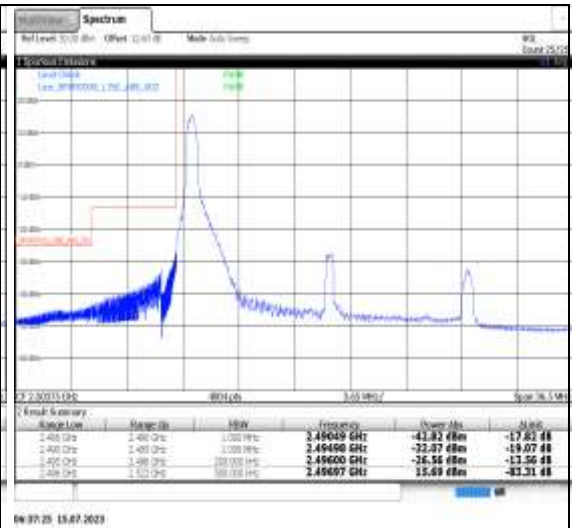


1-DC_66A_n41A-PC3-30-5+20-M+L-6-QPSK-Outer_Full-Outer_Full@N/A-CP-QPSK-Outer_Full-51@0-1--PASS-see graph

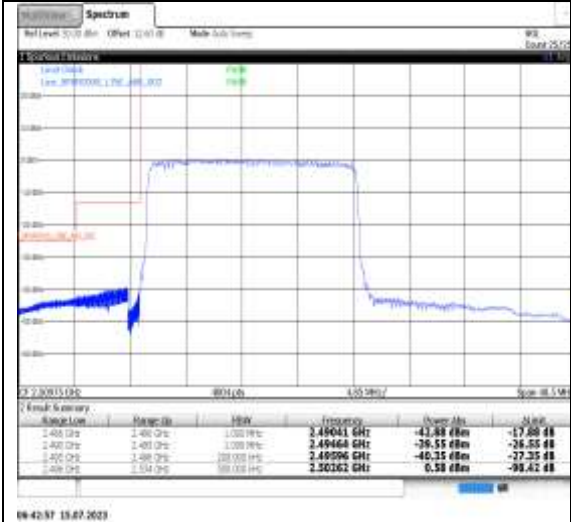
1-DC_66A_n41A-PC3-30-5+20-M+L-7-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-16QAM-Edge_1RB_Left-1@0-1--PASS-see graph



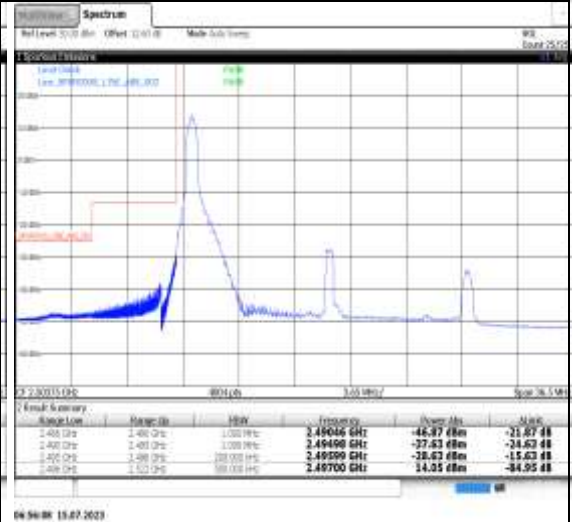
1-DC_66A_n41A-PC3-30-5+20-M+L-9-QPSK-Outer_Full-Outer_Full@N/A-DFT-16QAM-Outer_Full-50@0-1--PASS-see graph



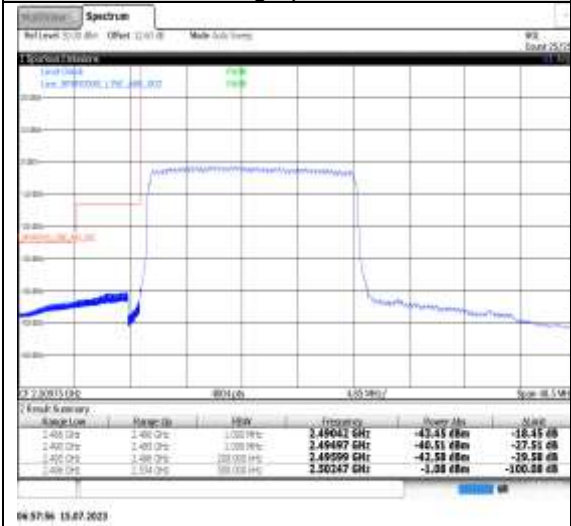
1-DC_66A_n41A-PC3-30-5+20-M+L-10-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph



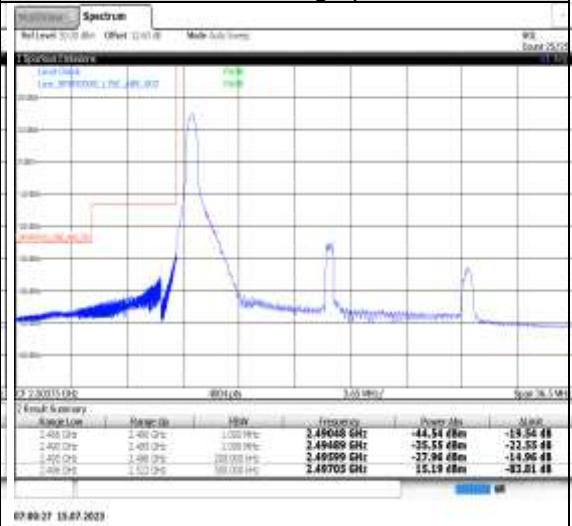
1-DC_66A_n41A-PC3-30-5+20-M+L-12-QPSK-Outer_Full-Outer_Full@N/A-DFT-64QAM-Outer_Full-50@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+20-M+L-13-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

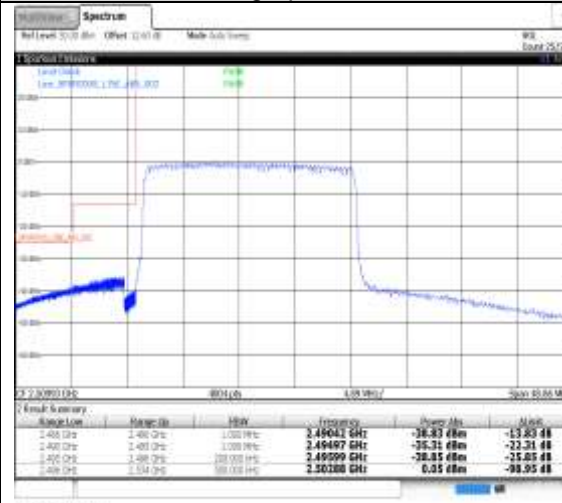


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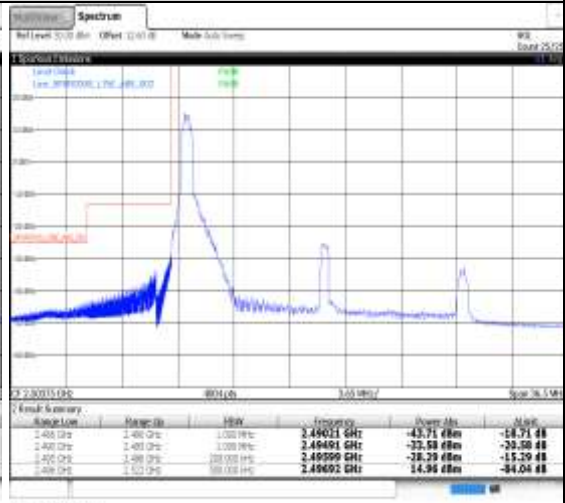


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1-DC_66A_n41A-PC3-30-5+20-M+L-15-QPSK-Outer_Full-Outer_Full@N/A-DFT-256QAM-Outer_Full-50@0-1--PASS-see graph



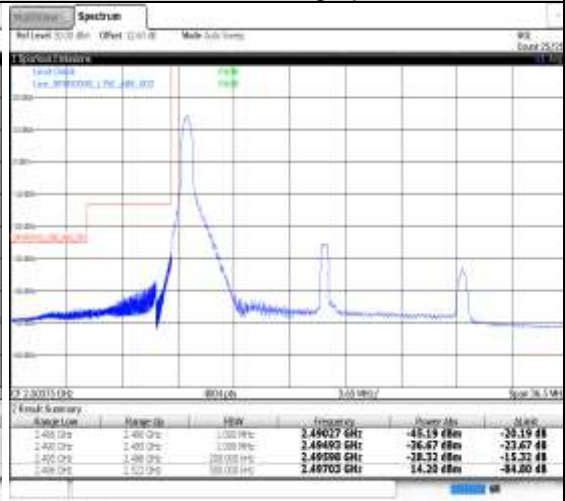
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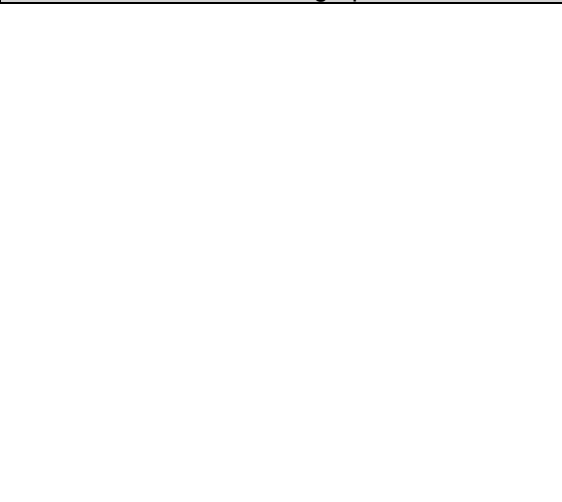
1-DC_66A_n41A-PC3-30-5+20-M+L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@0-1--PASS-see graph



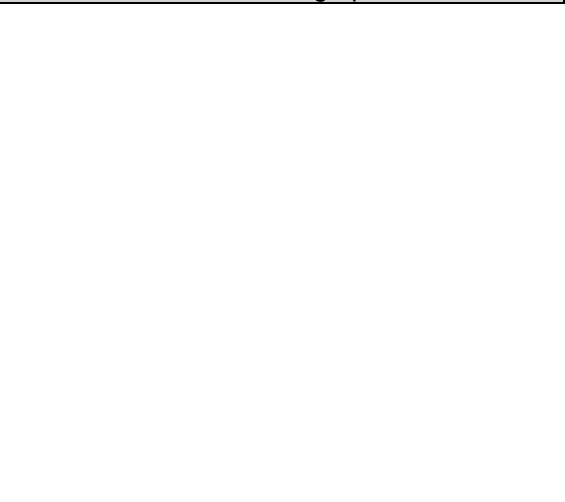
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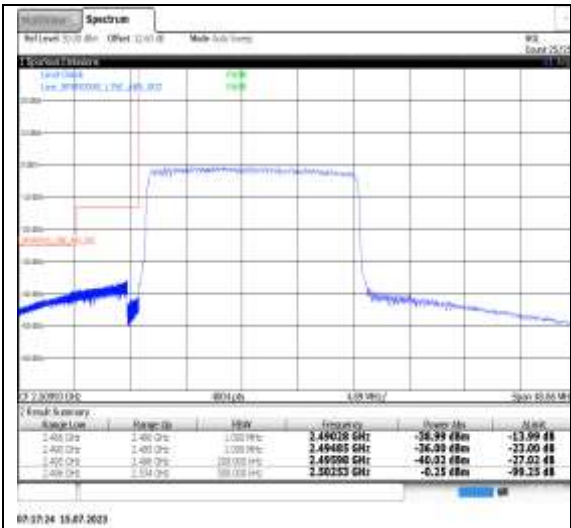


1-DC_66A_n41A-PC3-30-5+20-M+L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-51@0-1--PASS-see graph

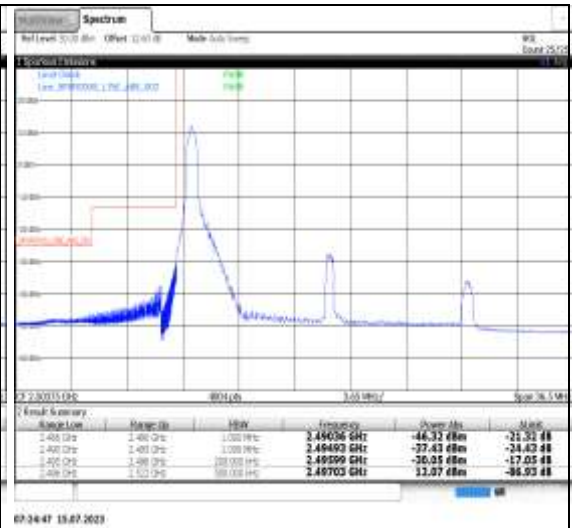


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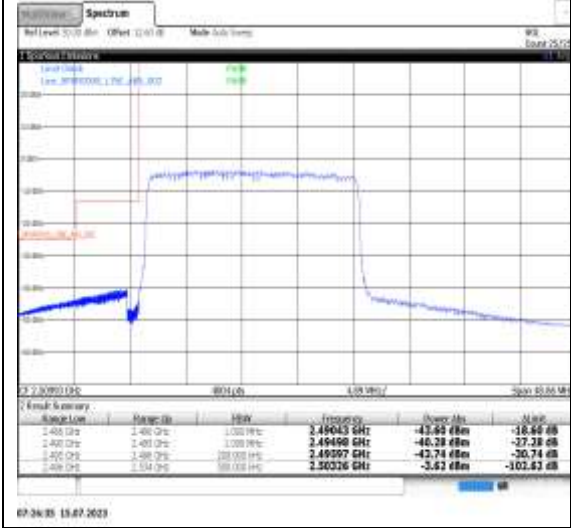




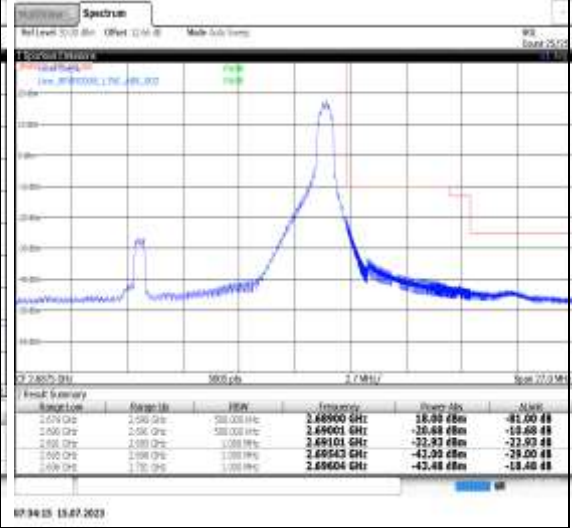
1-DC_66A_n41A-PC3-30-5+20-M+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-51@0-1--PASS-see graph



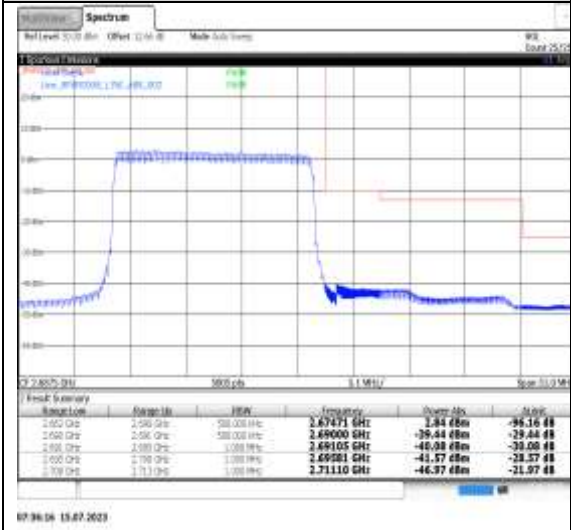
1-DC_66A_n41A-PC3-30-5+20-M+L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph



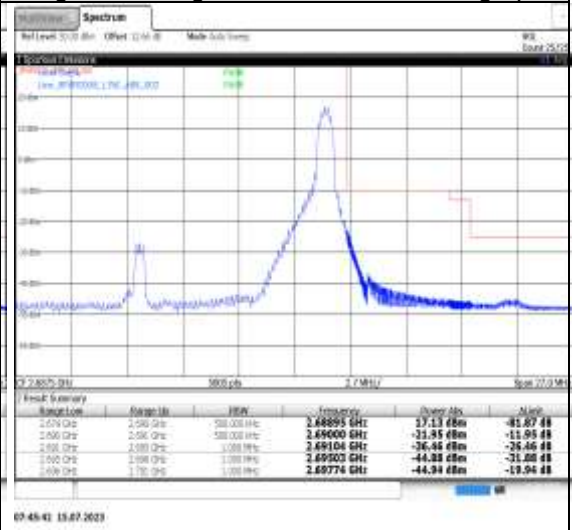
1-DC_66A_n41A-PC3-30-5+20-M+L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-51@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+20-M+H-2-QPSK-Edge_1RB_Right-Edge_1_Right@N/A-DFT-P12BPSK-Edge_1RB_Right-1@50-1--PASS-see graph

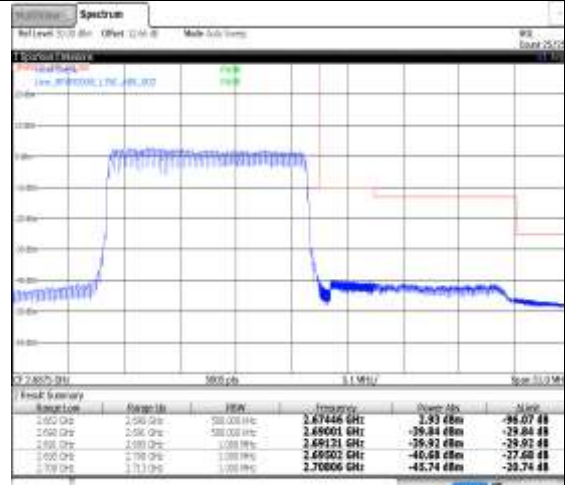


1-DC_66A_n41A-PC3-30-5+20-M+H-3-



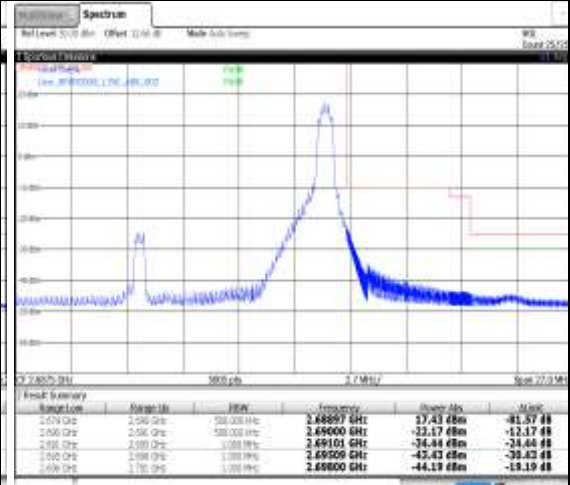
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graph



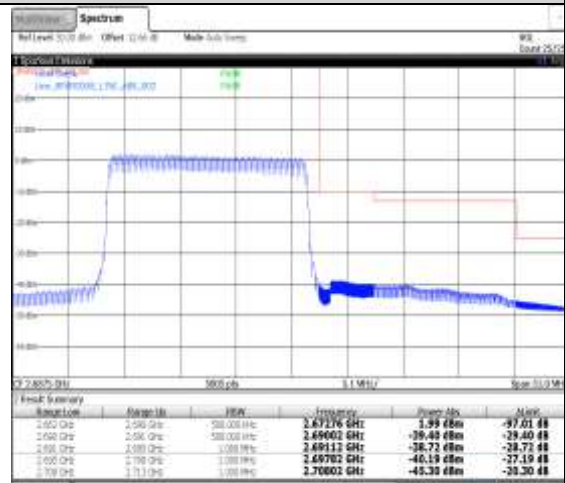
07:40:51 19.07.2023

QPSK-Edge_1RB_Right-
Edge_1_Right@N/A-CP-QPSK-
Edge_1RB_Right-1@50-1--PASS-see graph



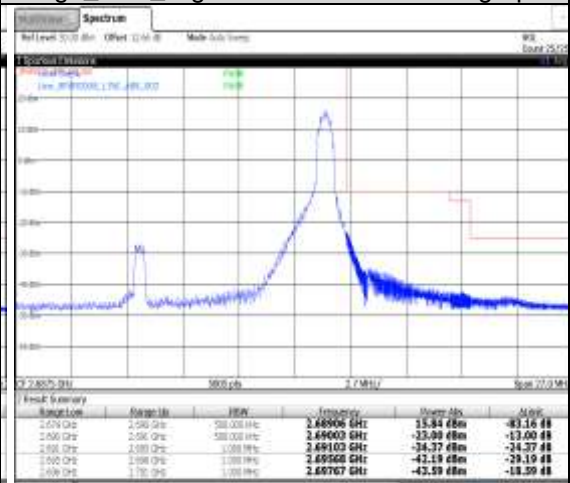
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07:50:52 19.07.2023

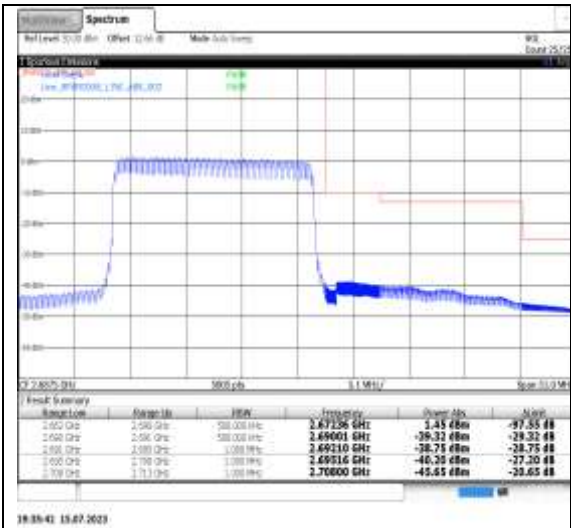
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Edge_1RB_Right-1@50-1--PASS-see graph



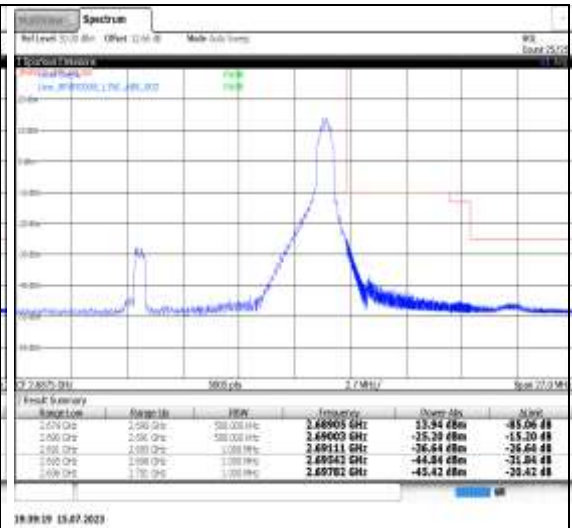
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graph

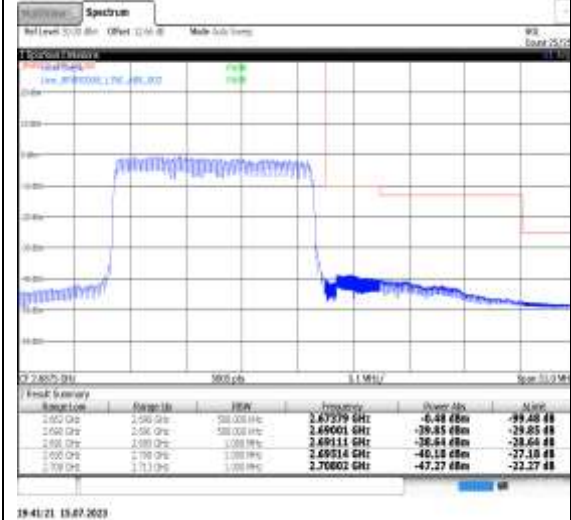
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Edge_1RB_Right-1@50-1--PASS-see graph



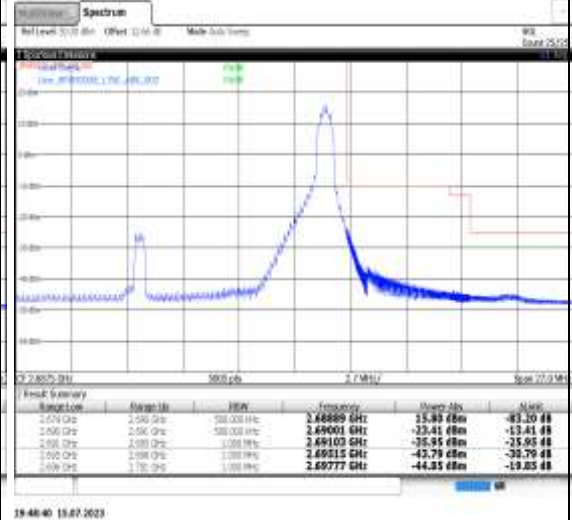
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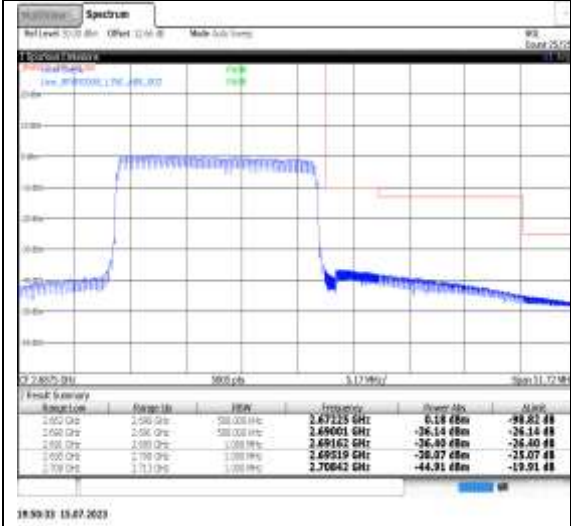
1-DC_66A_n41A-PC3-30-5+20-M+H-14-QPSK-Edge_1RB_Right-Edge_1_Right@N/A-DFT-256QAM-Edge_1RB_Right-1@50-1--PASS-see graph



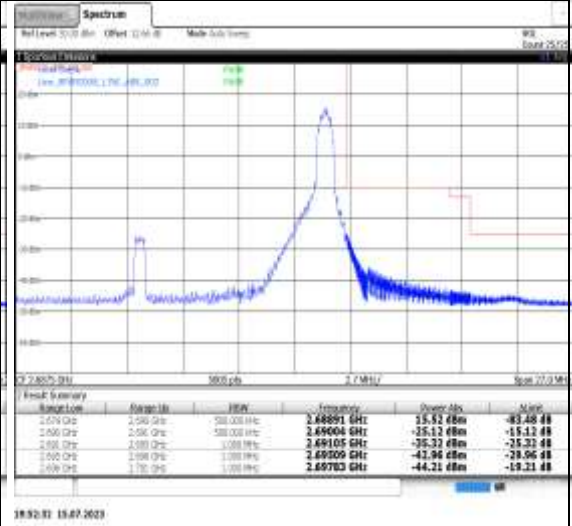
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1-DC_66A_n41A-PC3-30-5+20-M+H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Right-1@50-1--PASS-see graph

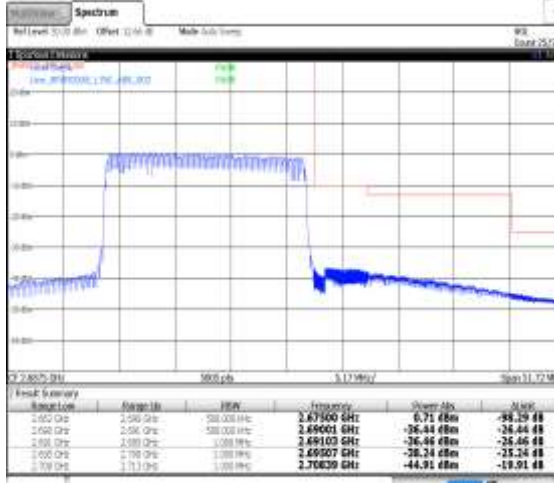


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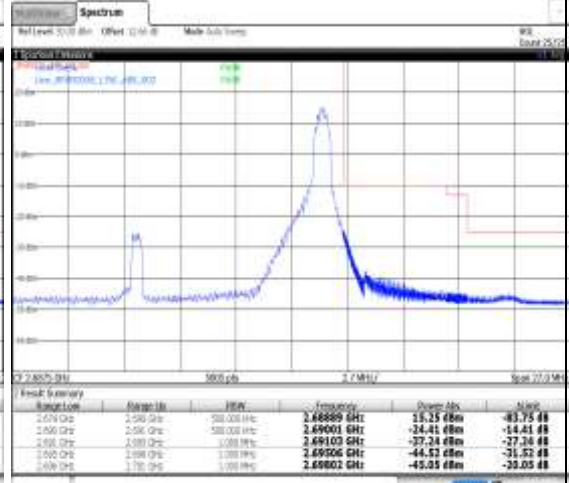
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1-DC_66A_n41A-PC3-30-5+20-M+H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@0-1--PASS-see graph



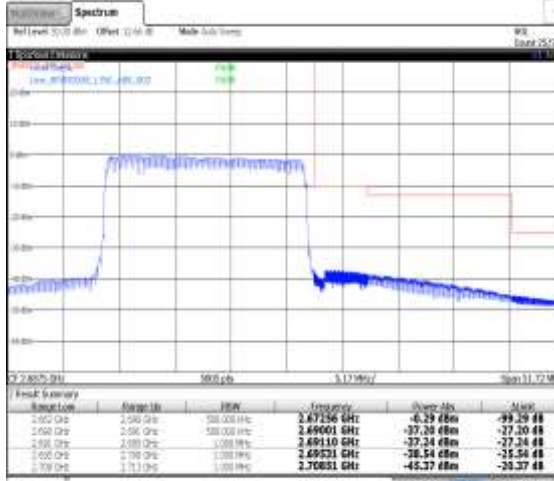
18:58:31 18.07.2023

1-DC_66A_n41A-PC3-30-5+20-M+H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Right-1@50-1--PASS-see graph



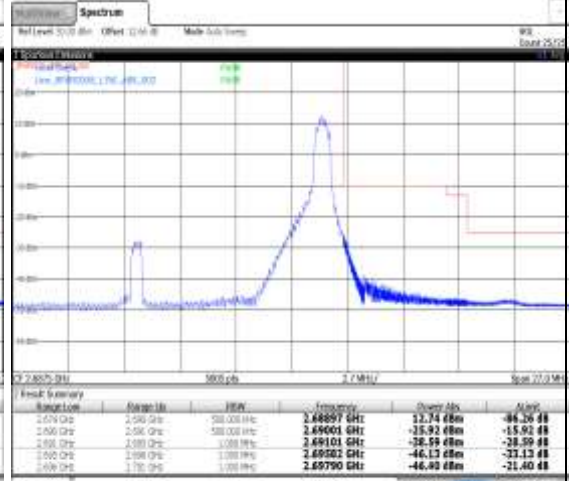
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1-DC_66A_n41A-PC3-30-5+20-M+H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-51@0-1--PASS-see graph



18:58:31 18.07.2023

1-DC_66A_n41A-PC3-30-5+20-M+H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Right-1@50-1--PASS-see graph



18:58:31 18.07.2023

1-DC_66A_n41A-PC3-30-5+20-M+H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-51@0-1--PASS-see graph

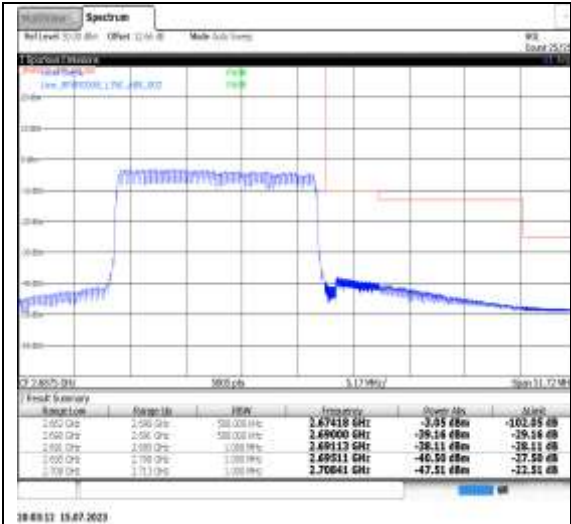


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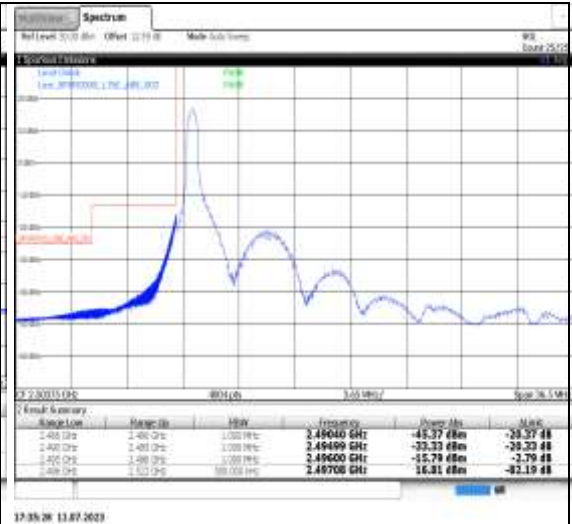
1-DC_66A_n41A-PC3-30-5+20-M+H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@50-1--PASS-see graph



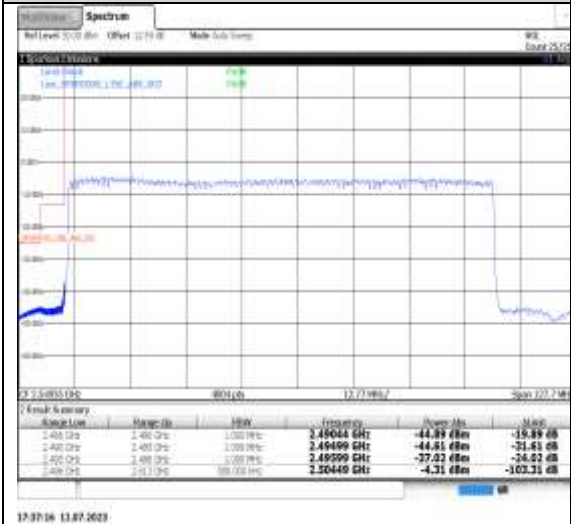
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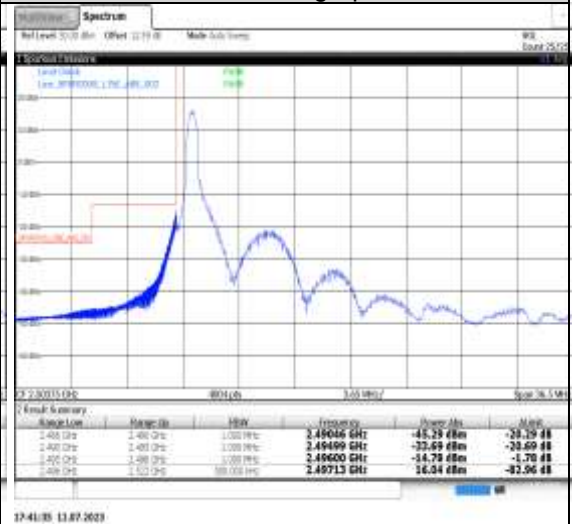
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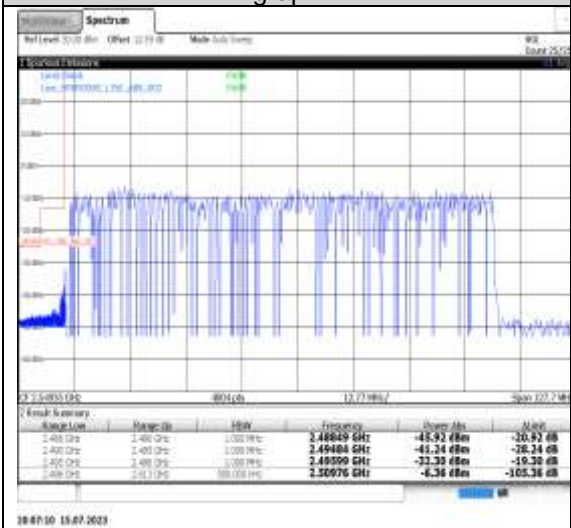
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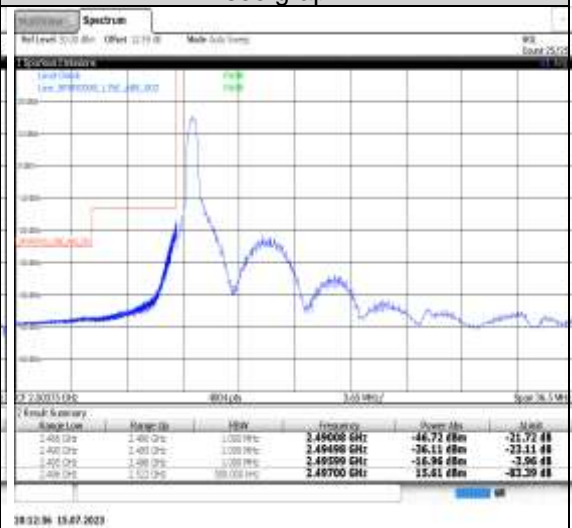
1-DC_66A_n41A-PC3-30-5+100-M+L-3-QPSK-Outer_Full-Outer_Full@N/A-DFT-PI2BPSK-Outer_Full-270@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+L-4-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-CP-QPSK-Edge_1RB_Left-1@0-1--PASS-see graph

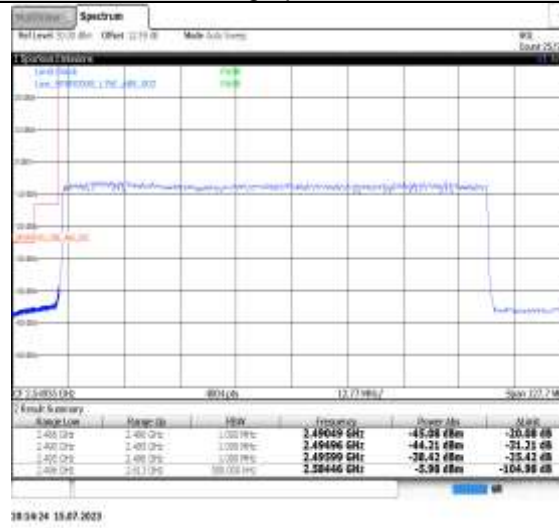


18:07:10 13.07.2023

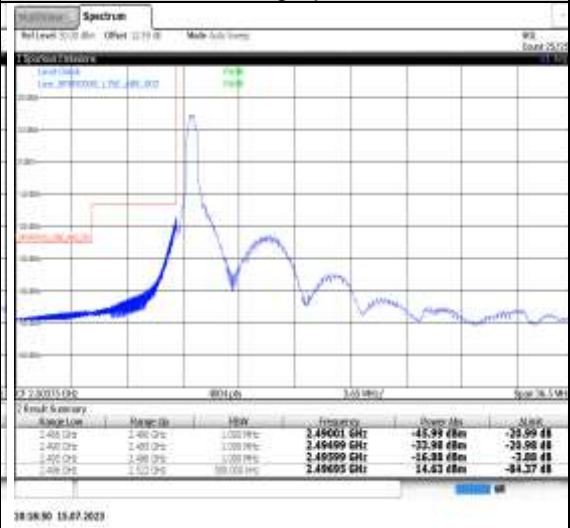


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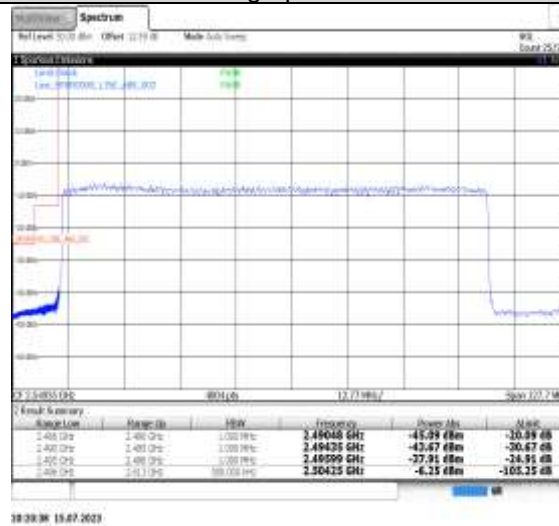
1-DC_66A_n41A-PC3-30-5+100-M+L-6-QPSK-Outer_Full-Outer_Full@N/A-CP-QPSK-Outer_Full-273@0-1--PASS-see graph



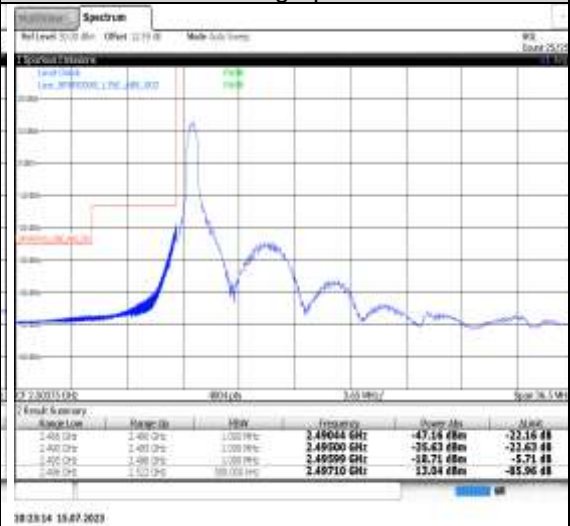
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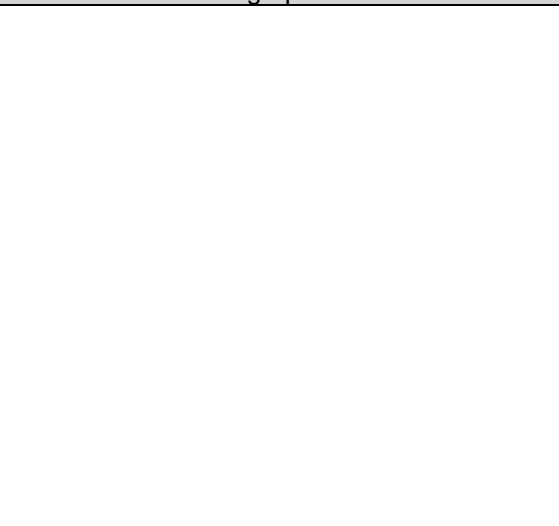
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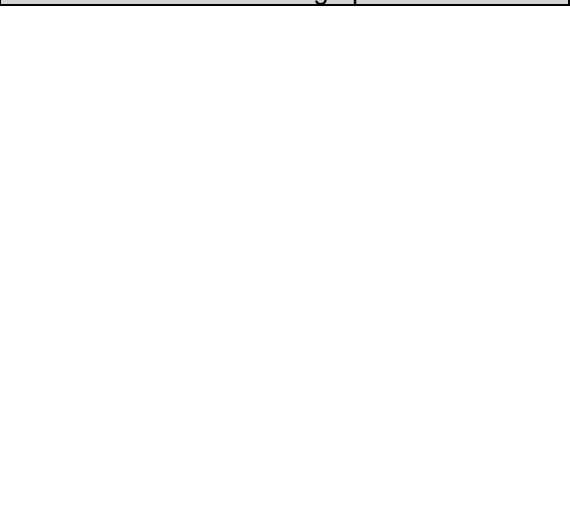
1-DC_66A_n41A-PC3-30-5+100-M+L-10-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph

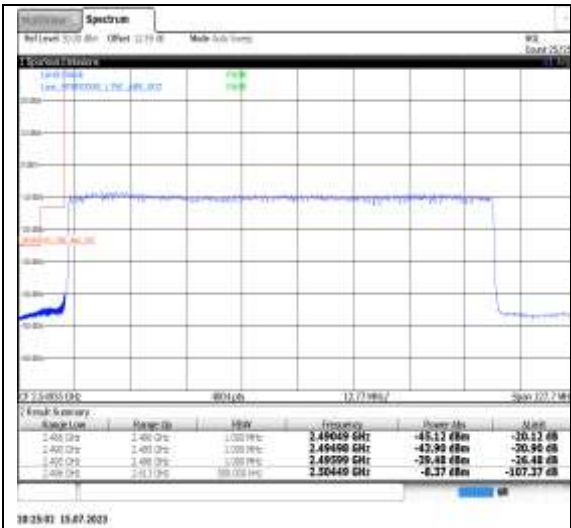


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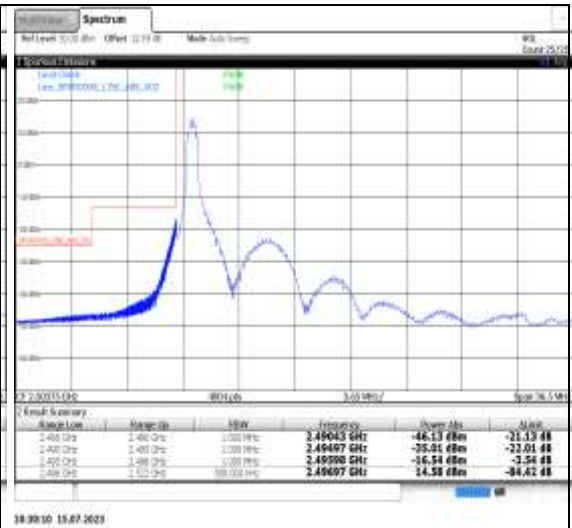


1-DC_66A_n41A-PC3-30-5+100-M+L-13-QPSK-Edge_1RB_Left-Edge_1_Left@N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

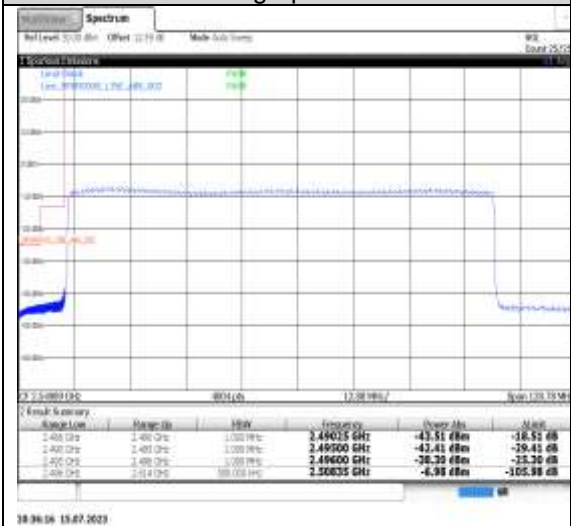




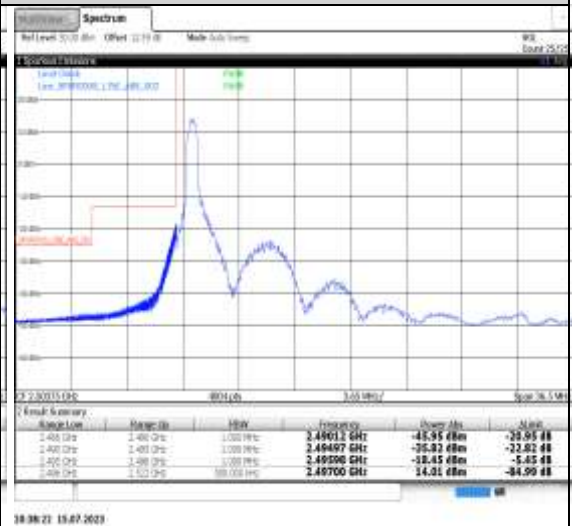
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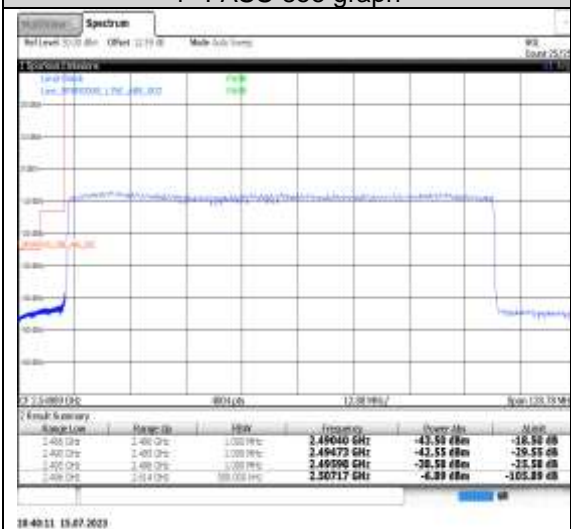
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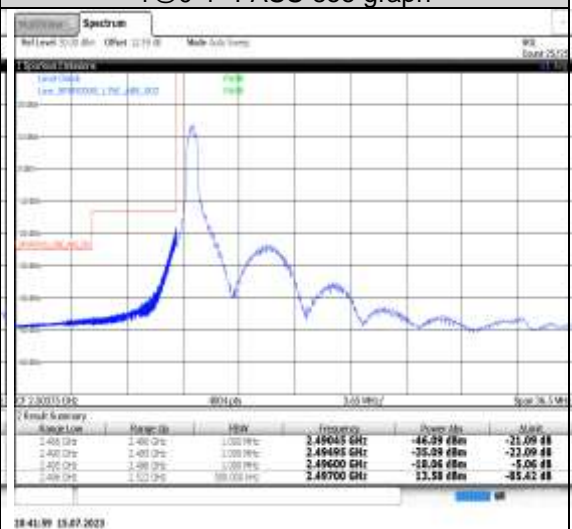
1-DC_66A_n41A-PC3-30-5+100-M+L-18-N/A-N/A-N/A-CP-QPSK-Outter_Full-273@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Left-1@0-1--PASS-see graph

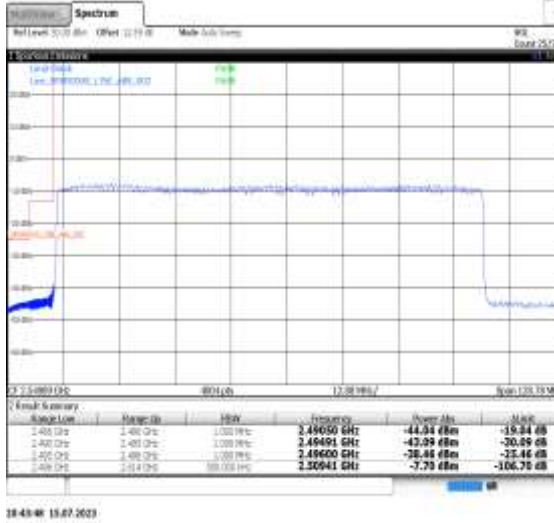


1-DC_66A_n41A-PC3-30-5+100-M+L-21-

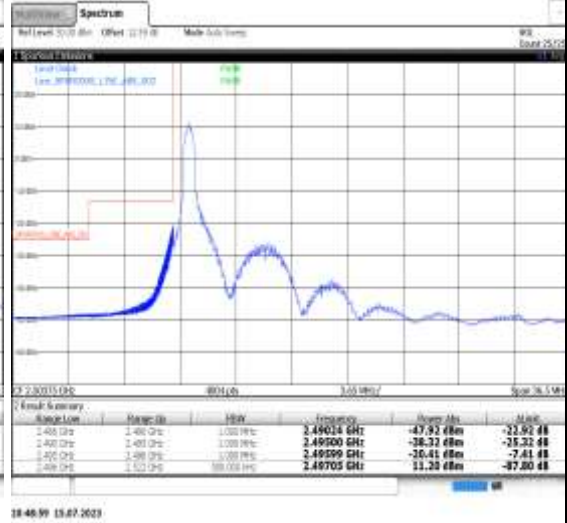


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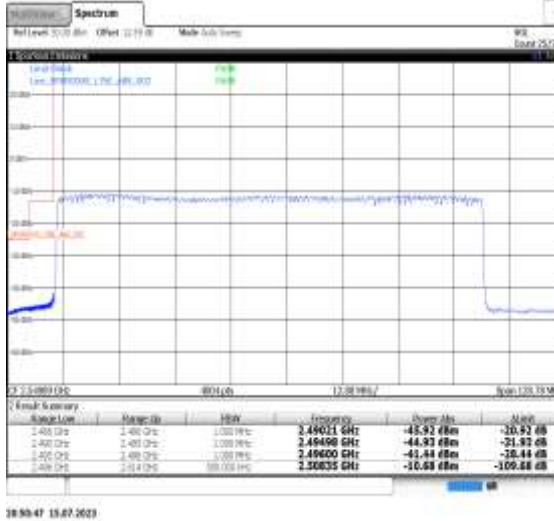
N/A-N/A-N/A-CP-16QAM-Outer_Full-273@0-1--PASS-see graph



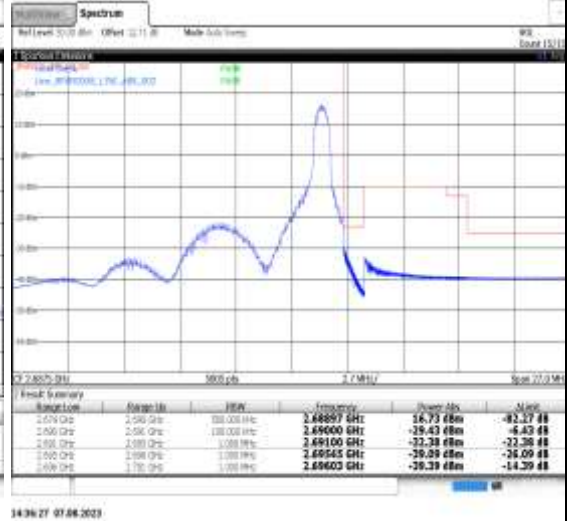
N/A-N/A-N/A-CP-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-273@0-1--PASS-see graph

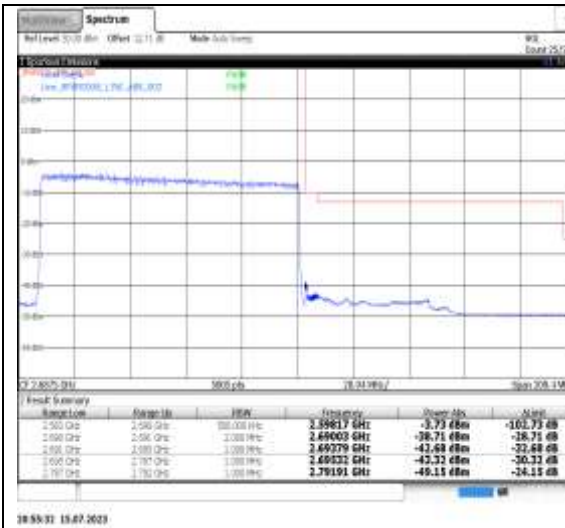


1-DC_66A_n41A-PC3-30-5+100-M+L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

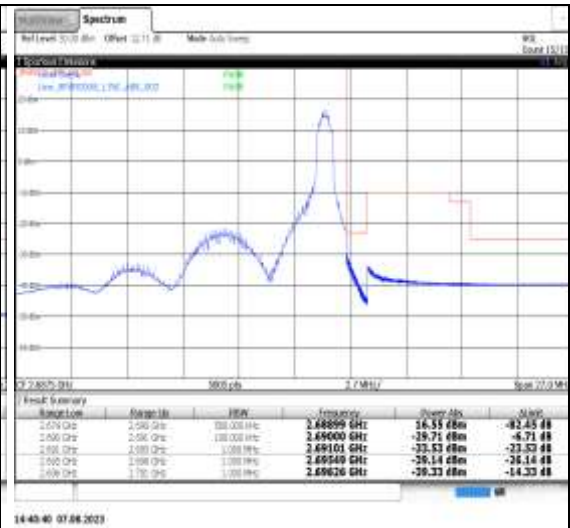


1-DC_66A_n41A-PC3-30-5+100-M+L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-273@0-1--PASS-see graph

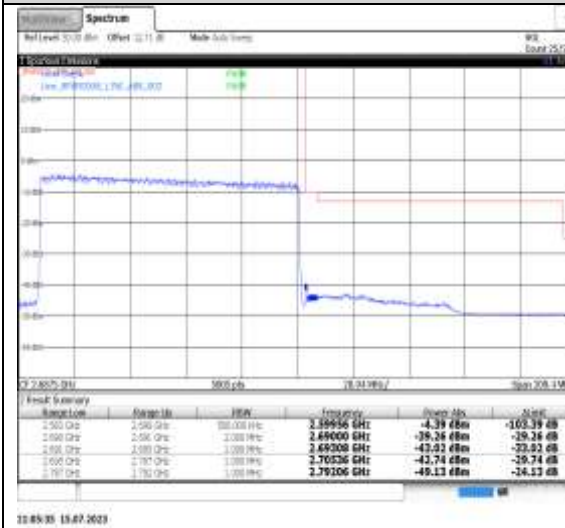
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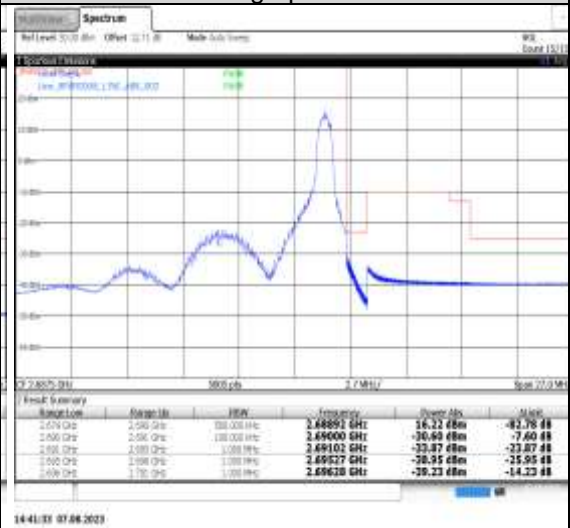
1-DC_66A_n41A-PC3-30-5+100-M+H-3-QPSK-Outer_Full-Outer_Full@N/A-DFT-PI2BPSK-Outer_Full-270@0-1--PASS-see graph



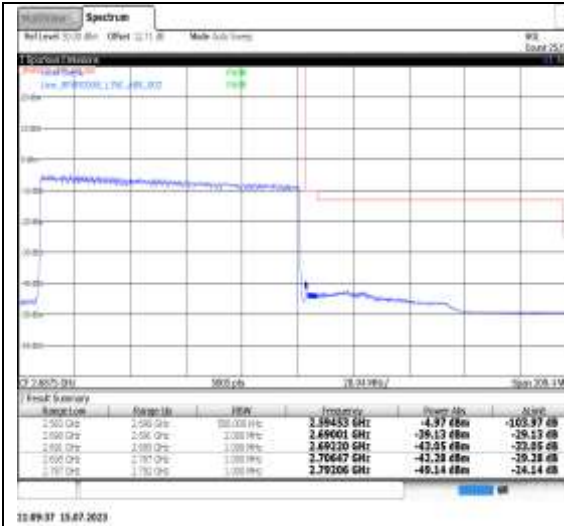
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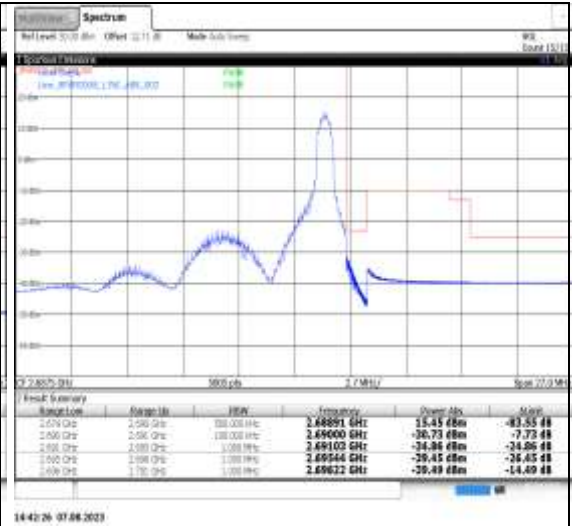
1-DC_66A_n41A-PC3-30-5+100-M+H-6-QPSK-Outer_Full-Outer_Full@N/A-CP-QPSK-Outer_Full-273@0-1--PASS-see graph



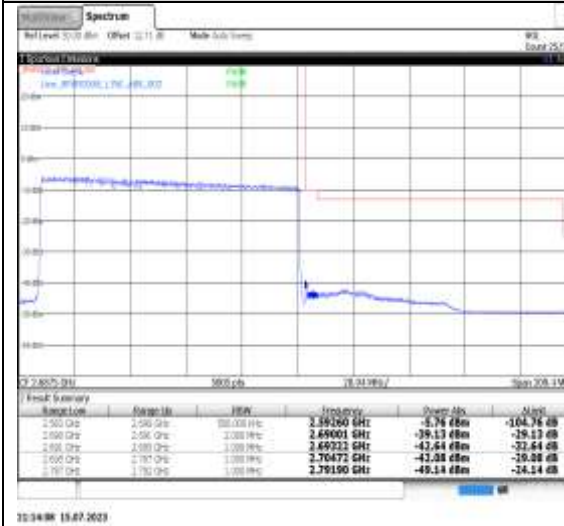
1-DC_66A_n41A-PC3-30-5+100-M+H-8-QPSK-Edge_1RB_Right-Edge_1_Right@N/A-DFT-16QAM-Edge_1RB_Right-1@272-1--PASS-see graph



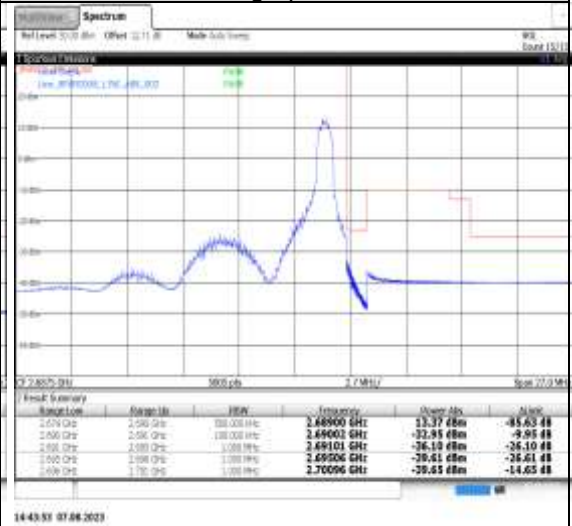
1-DC_66A_n41A-PC3-30-5+100-M+H-9-QPSK-Outer_Full-Outer_Full@N/A-DFT-16QAM-Outer_Full-270@0-1--PASS-see graph



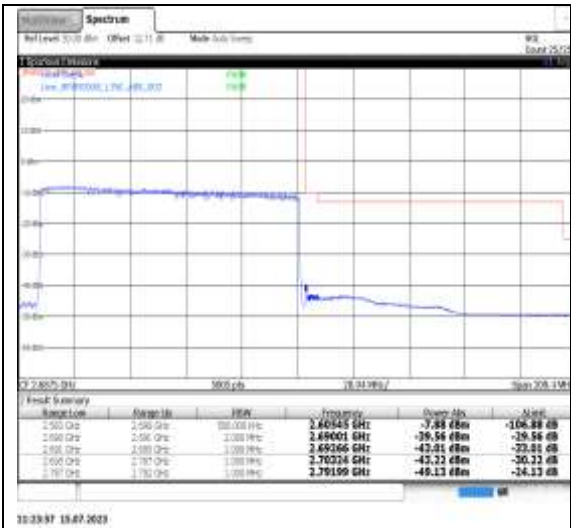
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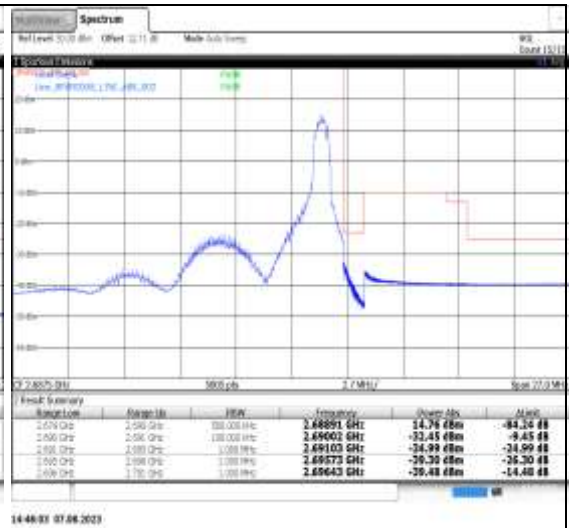
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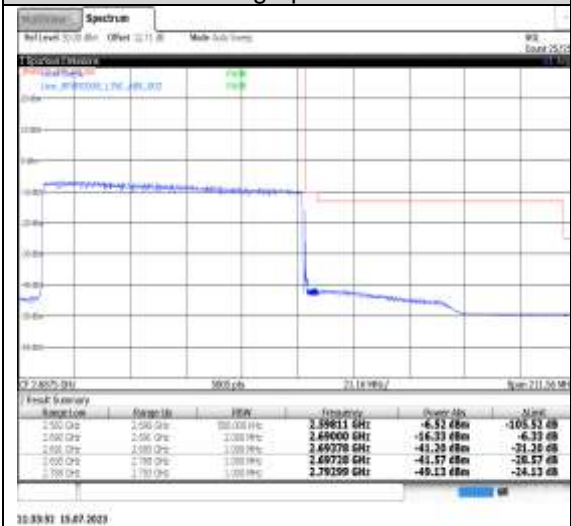
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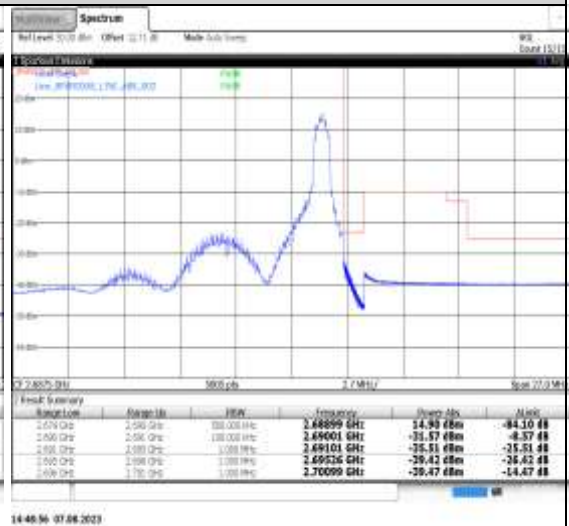
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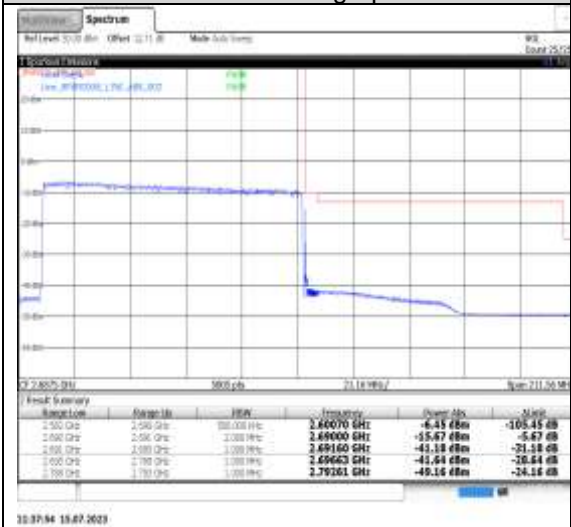
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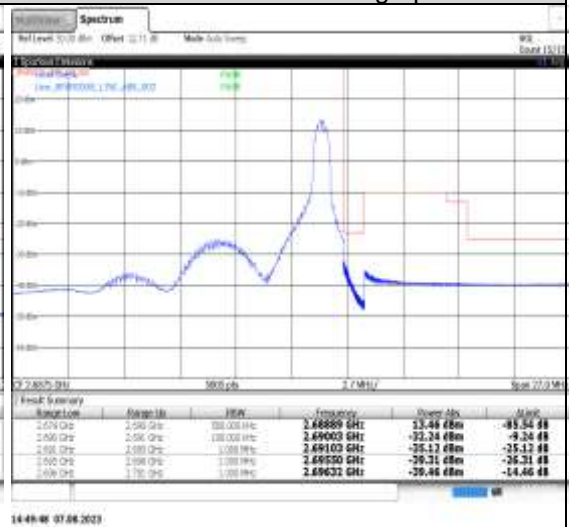
1-DC_66A_n41A-PC3-30-5+100-M+H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-273@0-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Right-1@272-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+H-21-

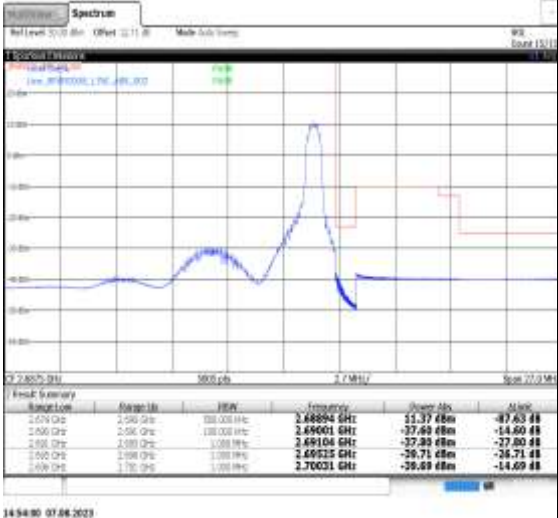


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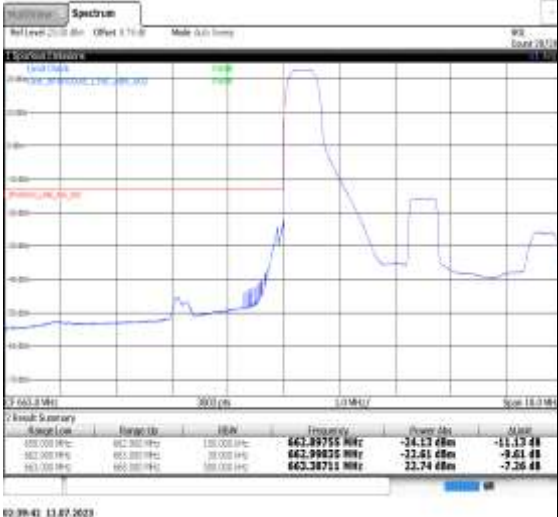
N/A-N/A-N/A-CP-64QAM-Edge_1RB_Right-1@272-1--PASS-see graph



1-DC_66A_n41A-PC3-30-5+100-M+H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-273@0-1--PASS-see graph



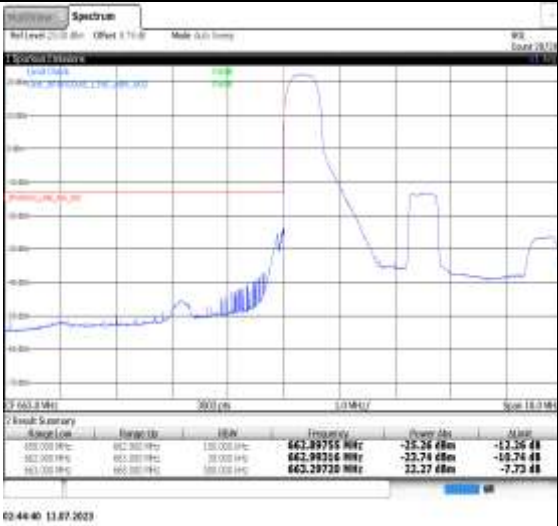
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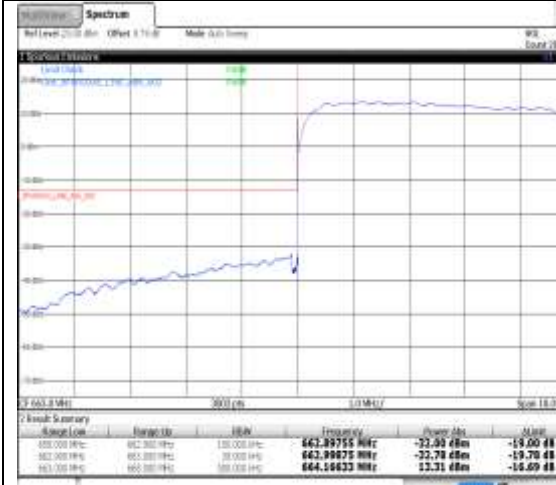
1-DC_66A_n41A-PC3-30-5+100-M+H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-273@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+5-M+L-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_Left-1@0-1--PASS-see graph

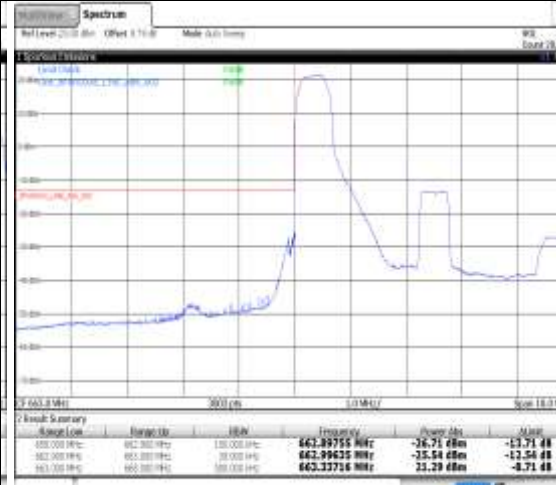


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PASS-see graph



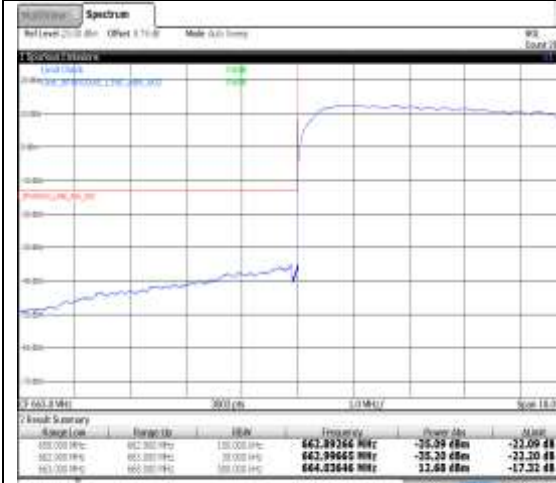
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-PASS-see graph



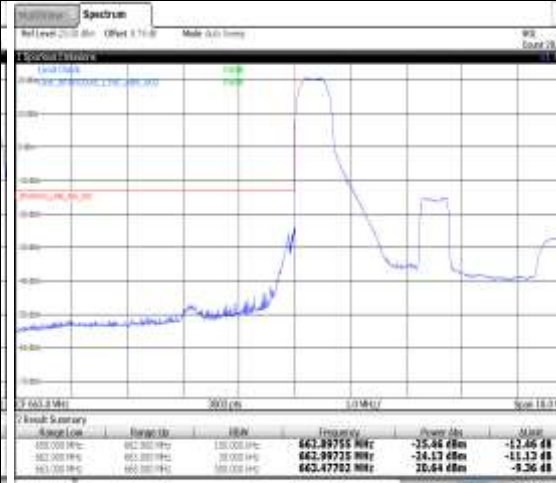
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1-DC_66A_n71A-PC3-15-5+5-M+L-6-N/A-
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PASS-see graph



02:50:53 13.87.2023

1-DC_66A_n71A-PC3-15-5+5-M+L-7-N/A-
N/A-N/A-DFT-16QAM-Edge_1RB_Left-1@0-
1--PASS-see graph



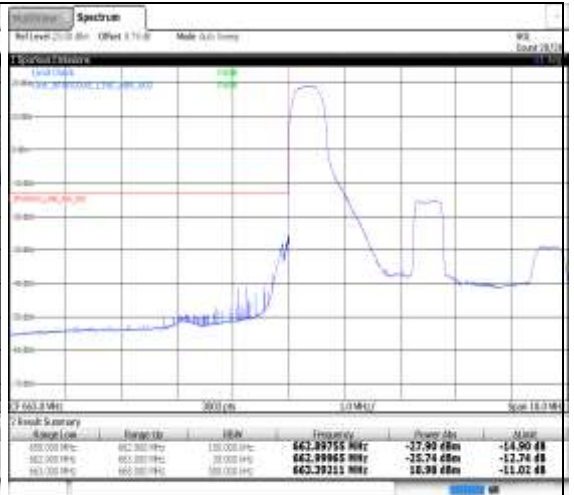
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1-DC_66A_n71A-PC3-15-5+5-M+L-9-N/A-
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PASS-see graph

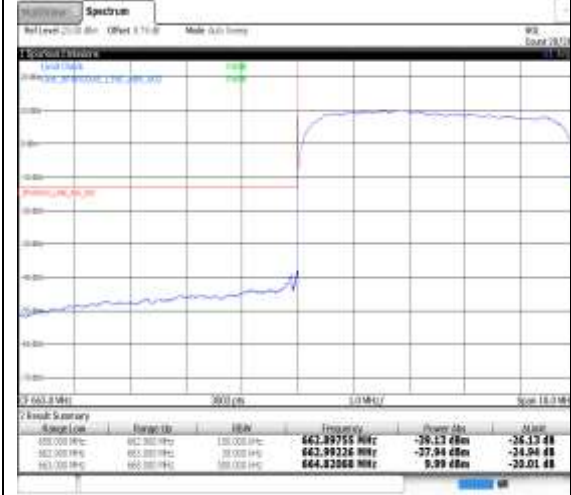
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1--PASS-see graph



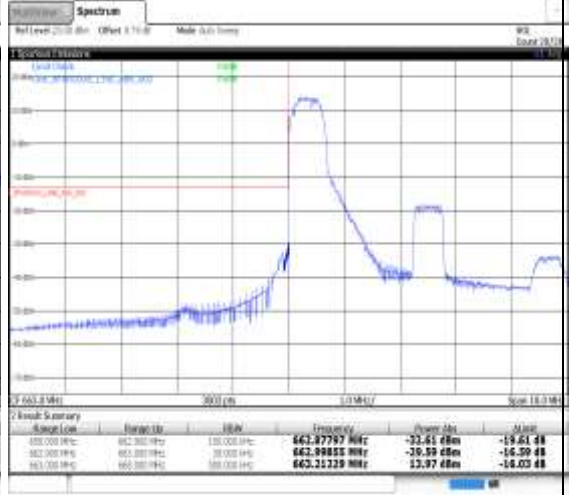
1-DC_66A_n71A-PC3-15-5+5-M+L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-25@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+5-M+L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph



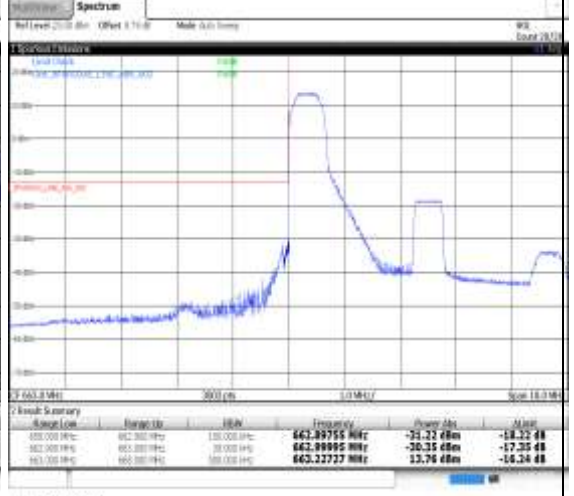
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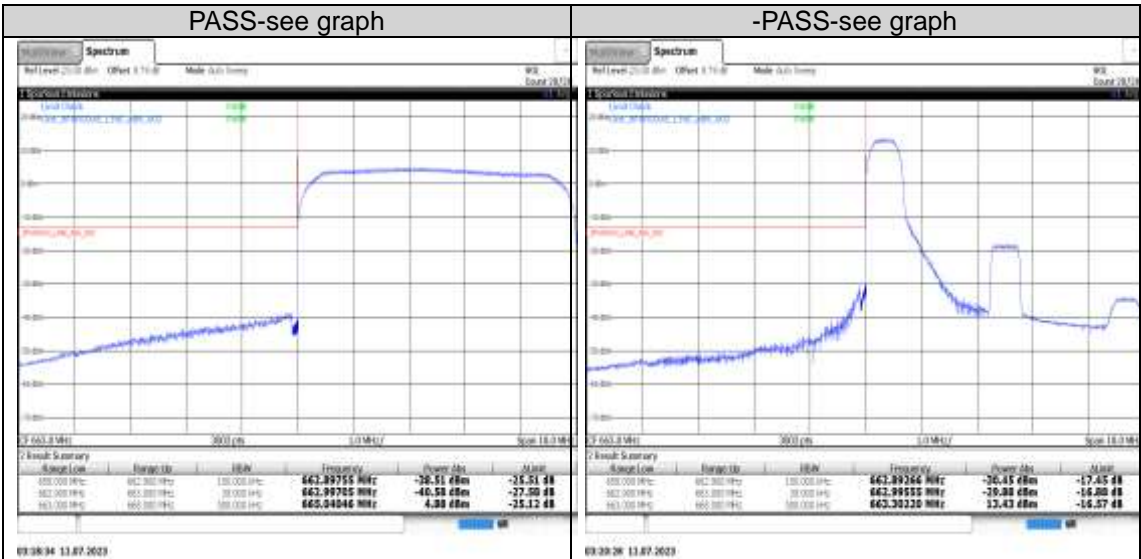
1-DC_66A_n71A-PC3-15-5+5-M+L-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Left-1@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+5-M+L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-25@0-1--PASS-see graph

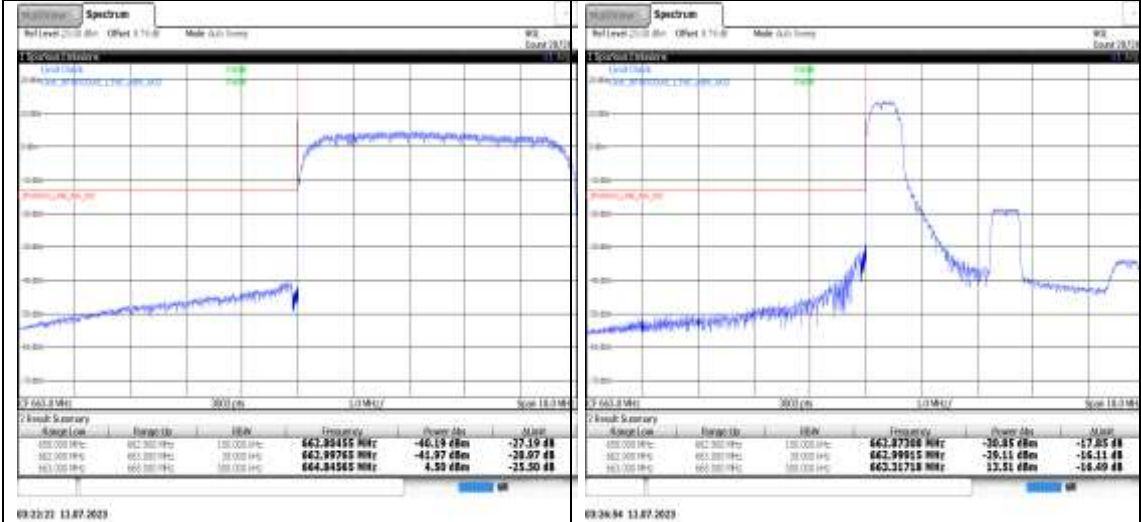


1-DC_66A_n71A-PC3-15-5+5-M+L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Left-1@0-1--PASS-see graph



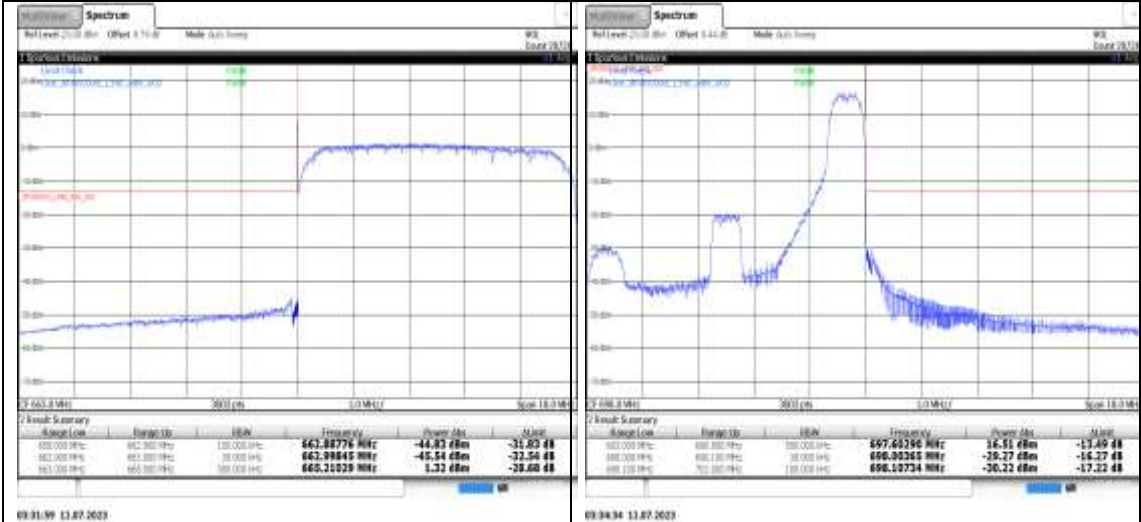
1-DC_66A_n71A-PC3-15-5+5-M+L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-25@0-1--PASS-see graph

1-DC_66A_n71A-PC3-15-5+5-M+L-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+5-M+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-25@0-1--PASS-see graph

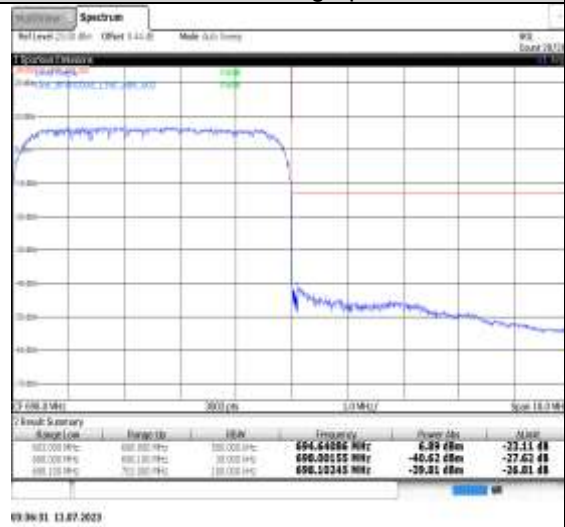
1-DC_66A_n71A-PC3-15-5+5-M+L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph



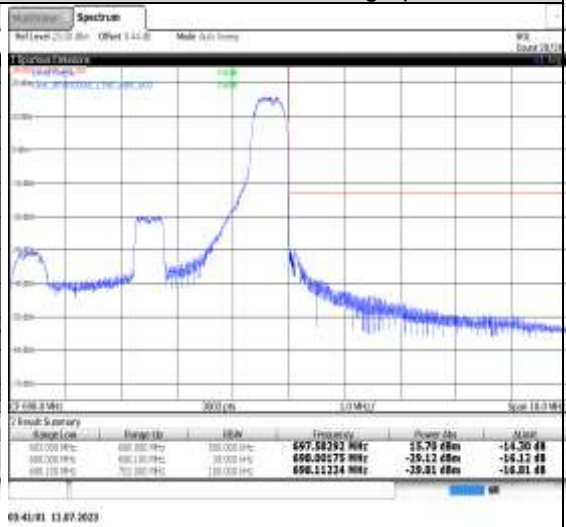
1-DC_66A_n71A-PC3-15-5+5-M+L-27-N/A-

1-DC_66A_n71A-PC3-15-5+5-M+H-2-N/A-

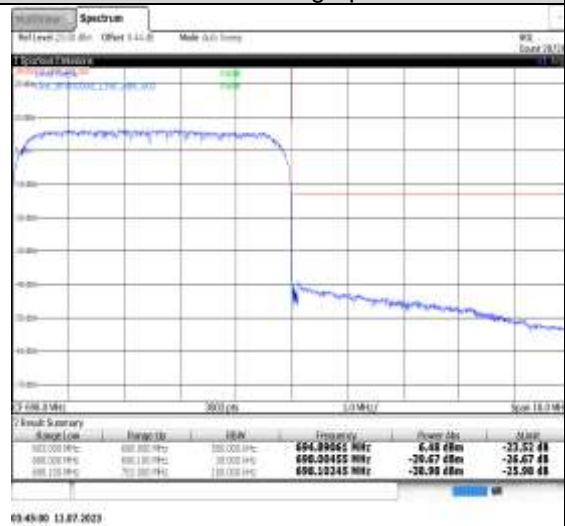
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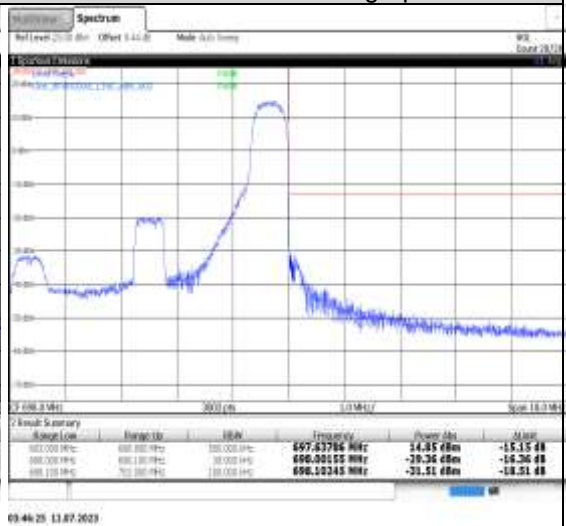
N/A-N/A-DFT-PI2BPSK-Edge_1RB_Right-1@24-1--PASS-see graph



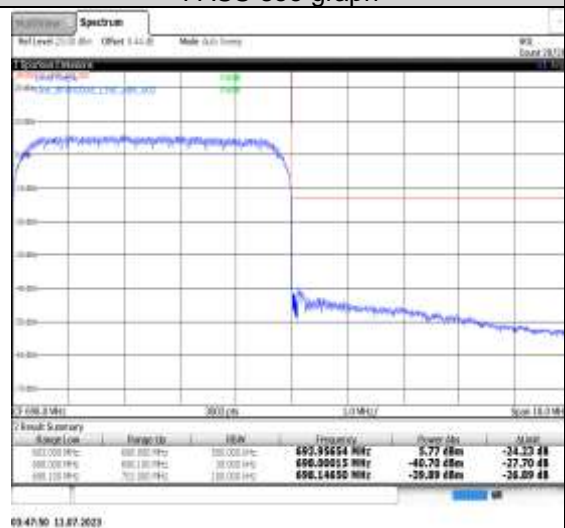
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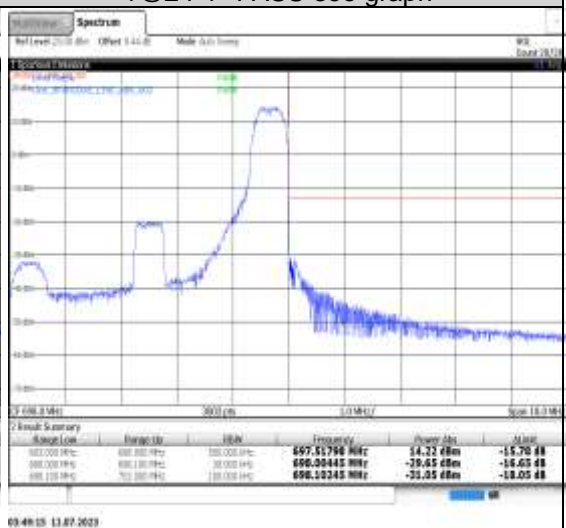
1-DC_66A_n71A-PC3-15-5+5-M+H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1@24-1--PASS-see graph

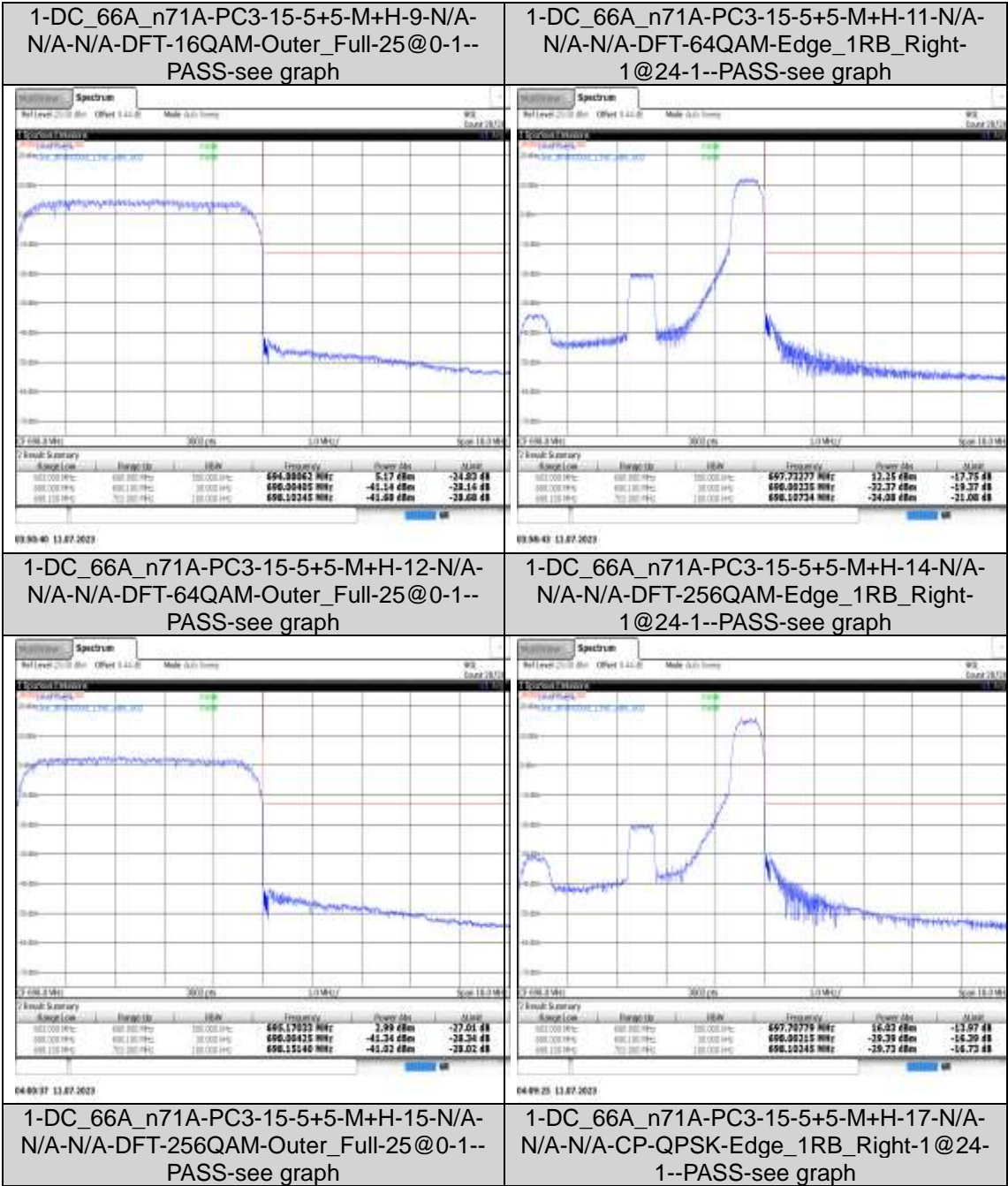


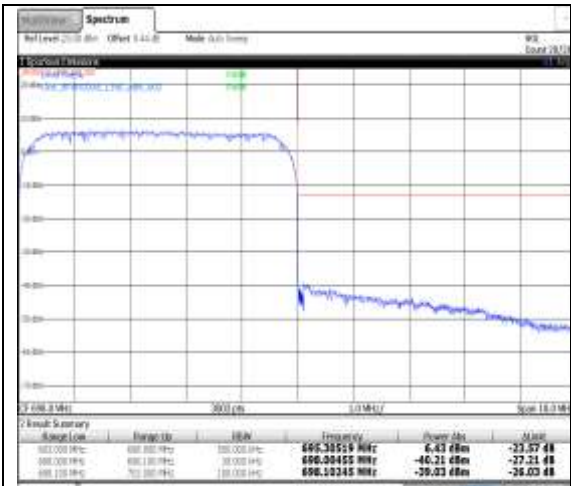
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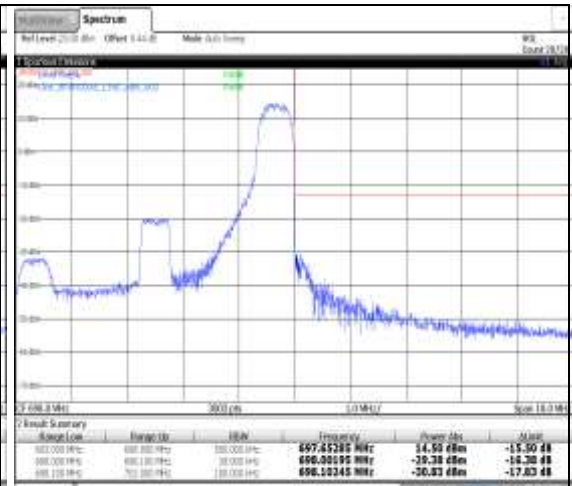
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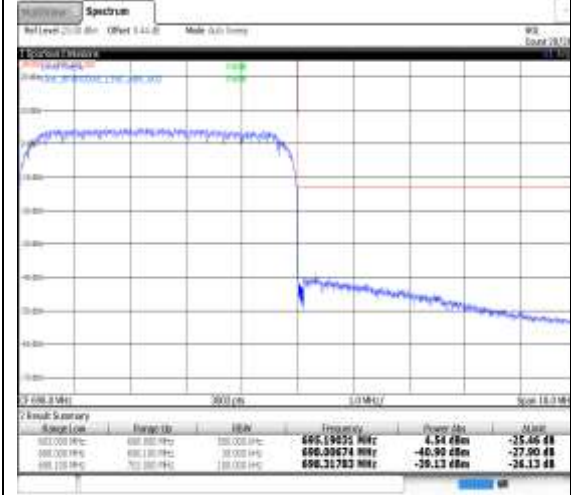




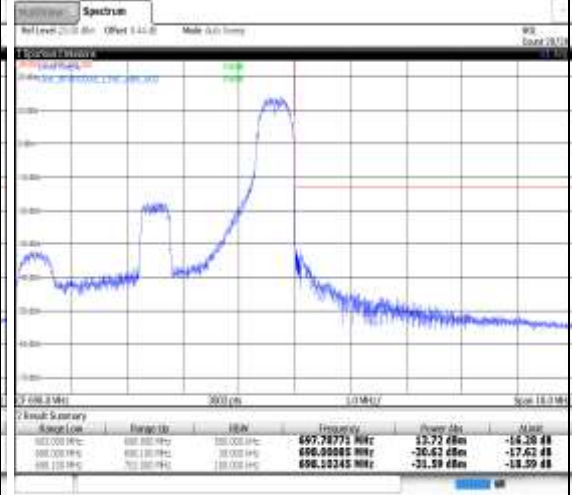
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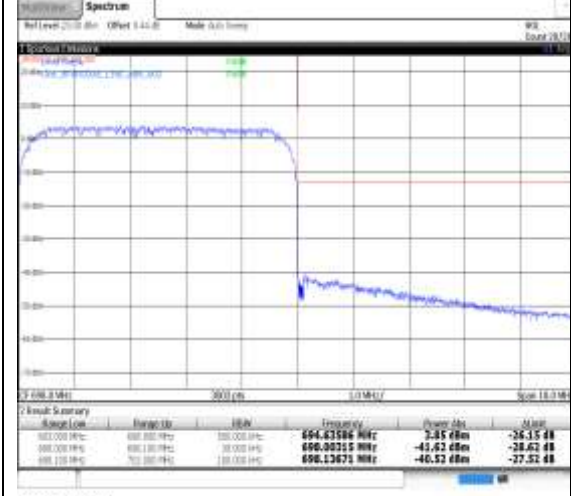
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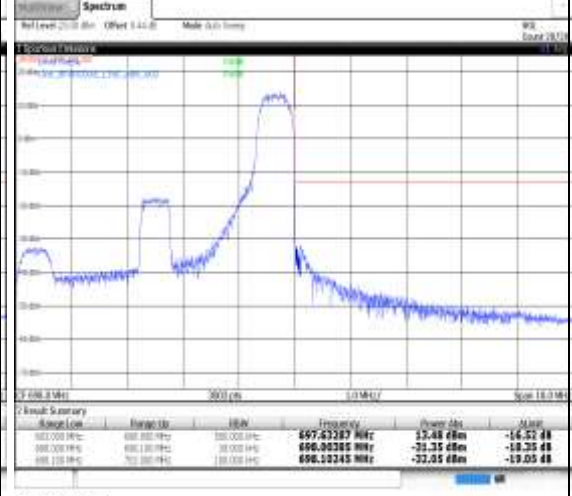
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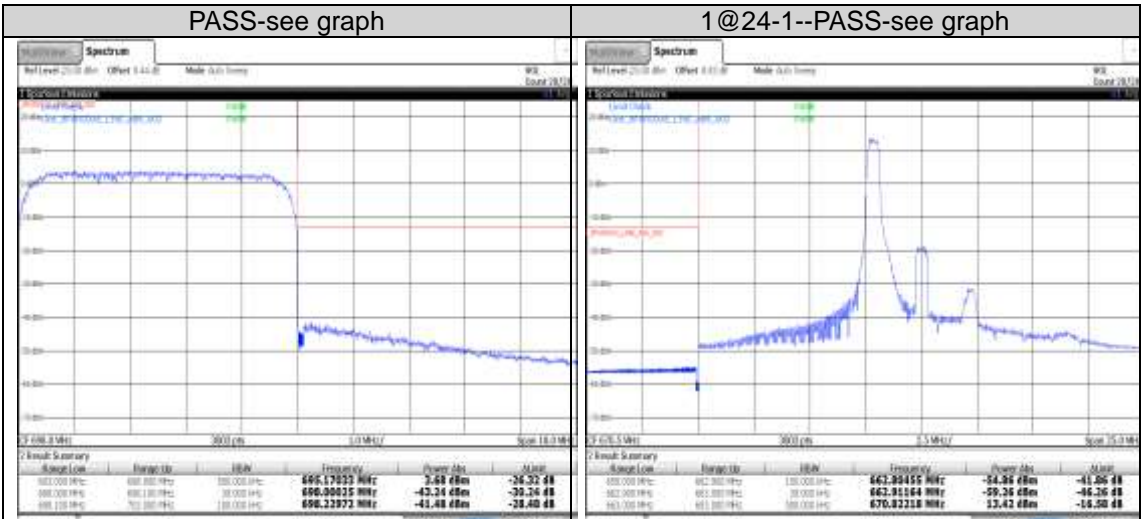
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04:34:25 13.07.2023
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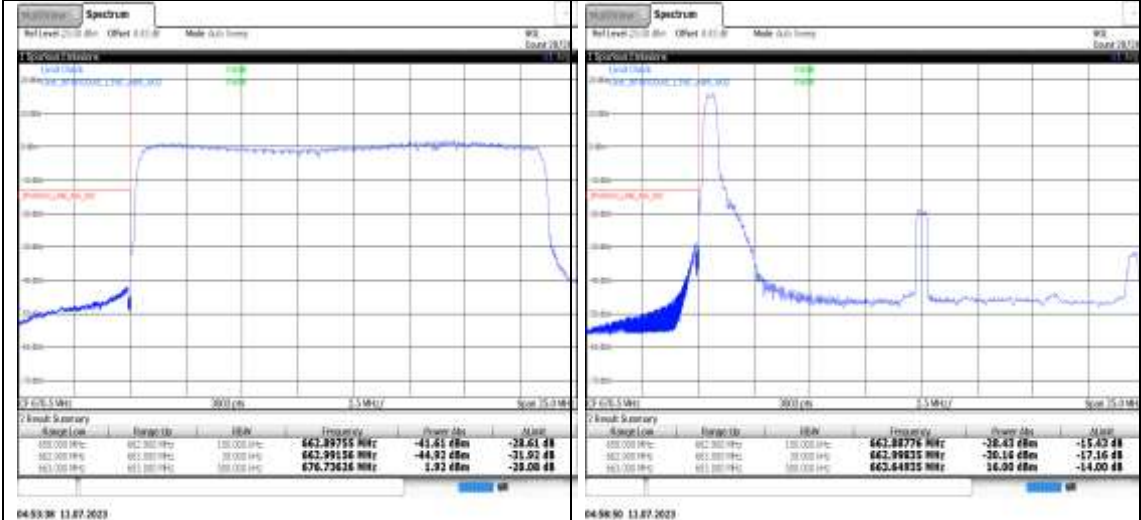


04:36:47 13.07.2023
 1-DC_66A_n71A-PC3-15-5+5-M+H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@24-1--PASS-see graph



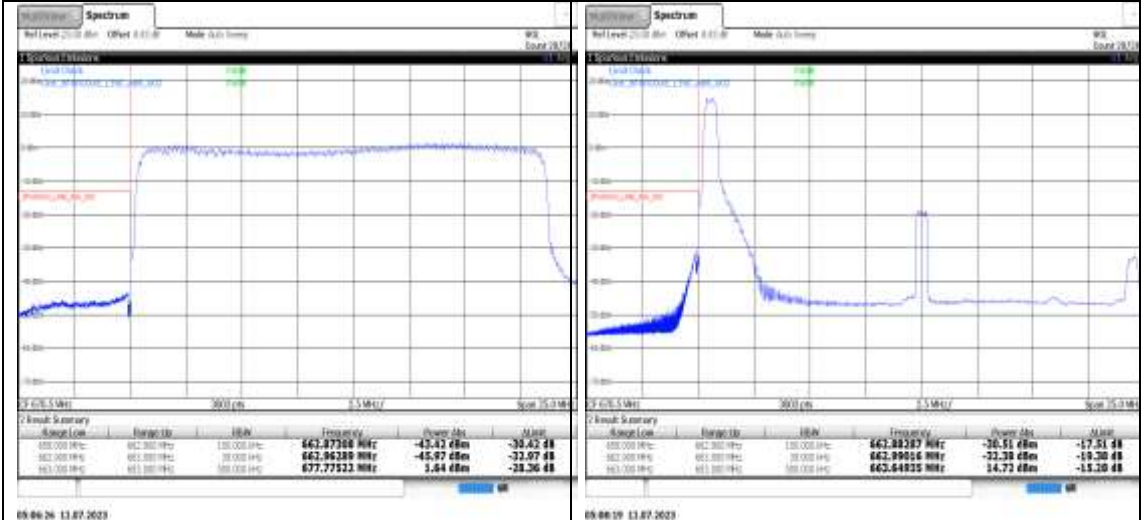
04:49:17 13.07.2023

1-DC_66A_n71A-PC3-15-5+5-M+H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-25@0-1--PASS-see graph



04:50:31 13.07.2023

1-DC_66A_n71A-PC3-15-5+20-M+L-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-100@0-1--PASS-see graph

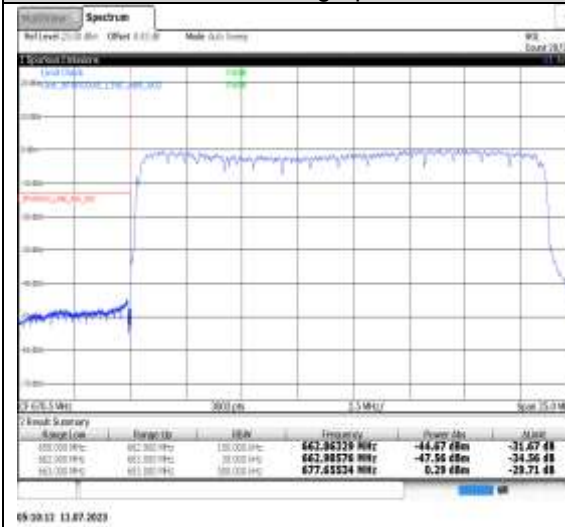


05:00:26 13.07.2023

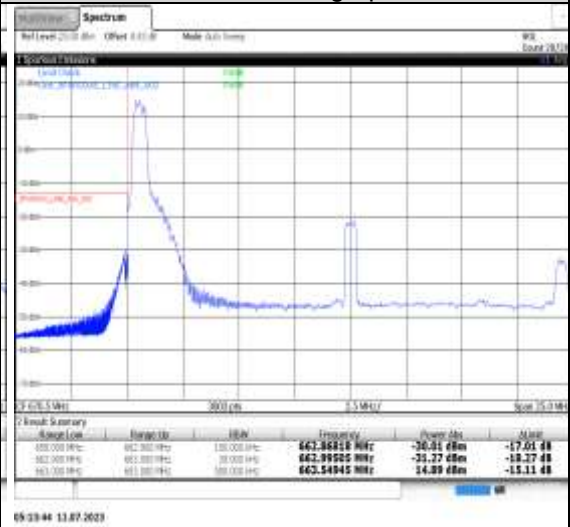
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1-DC_66A_n71A-PC3-15-5+20-M+L-7-N/A-

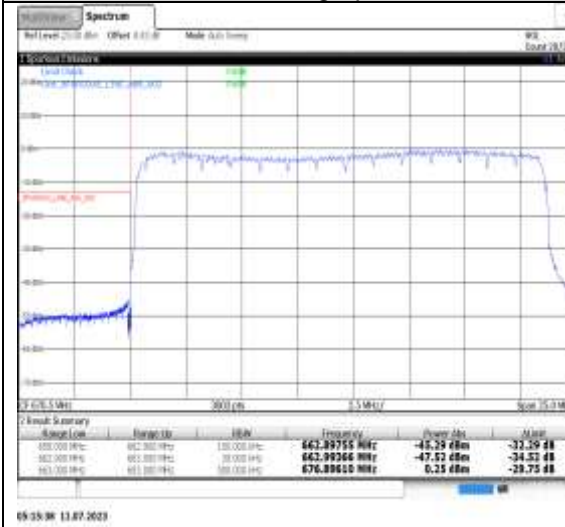
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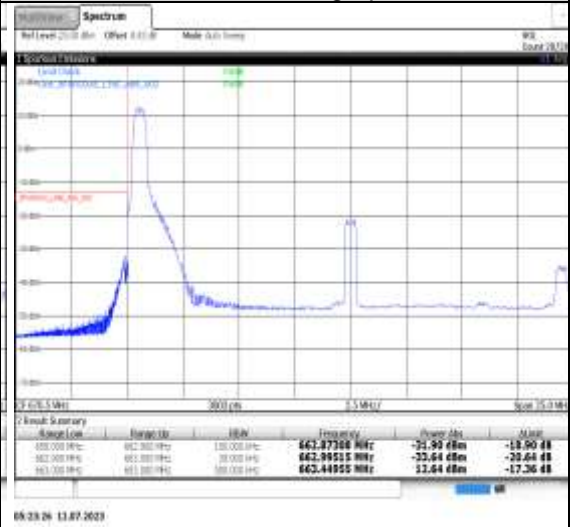
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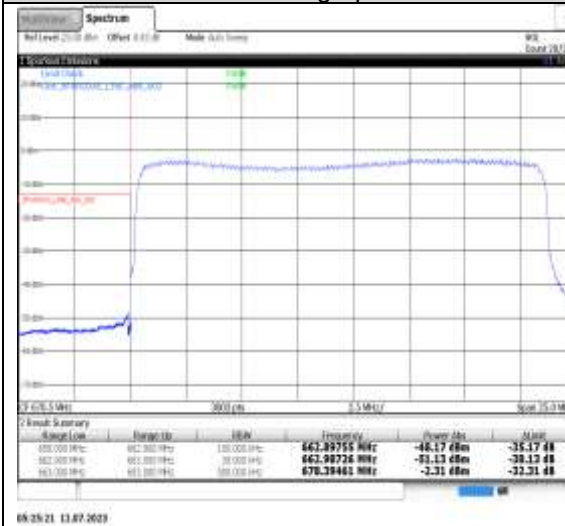
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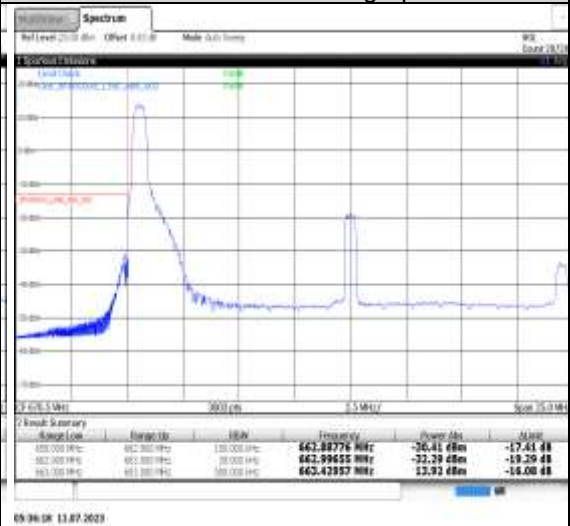
1-DC_66A_n71A-PC3-15-5+20-M+L-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_Left-1@0-1--PASS-see graph



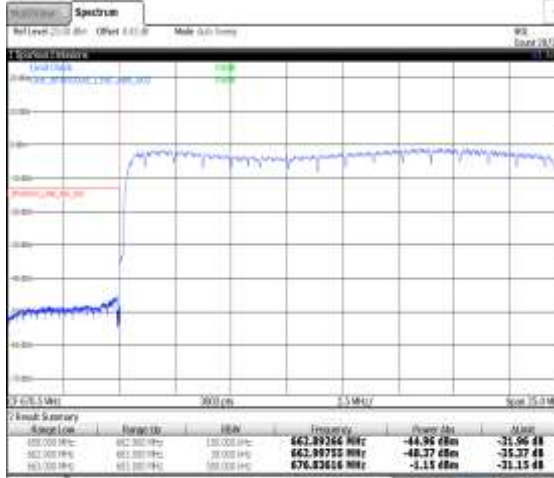
1-DC_66A_n71A-PC3-15-5+20-M+L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-100@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB_Left-1@0-1--PASS-see graph

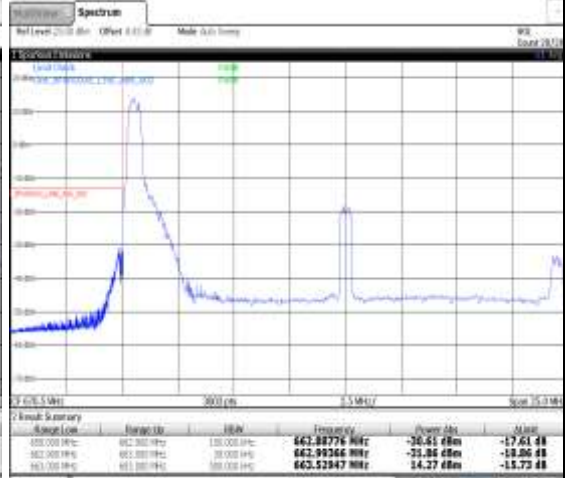


1-DC_66A_n71A-PC3-15-5+20-M+L-15-N/A-
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-PASS-see graph



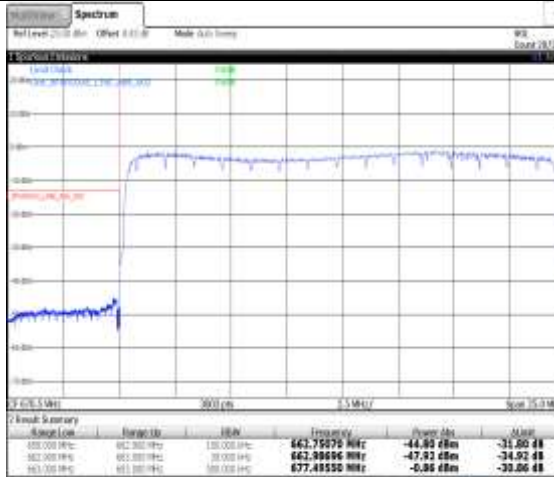
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-PASS-see graph



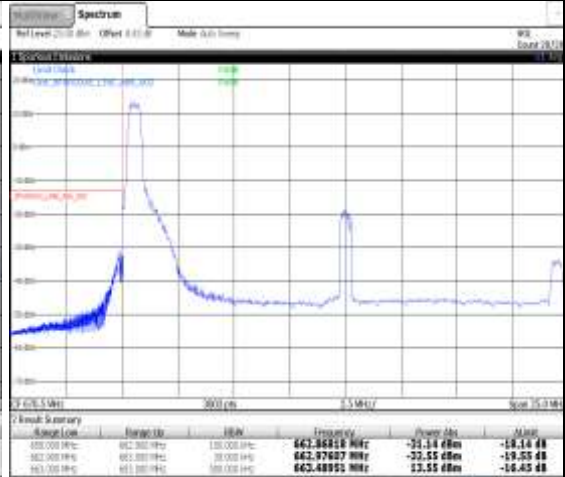
09:49:06 13.07.2023

1-DC_66A_n71A-PC3-15-5+20-M+L-18-N/A-
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-PASS-see graph



09:42:04 13.07.2023

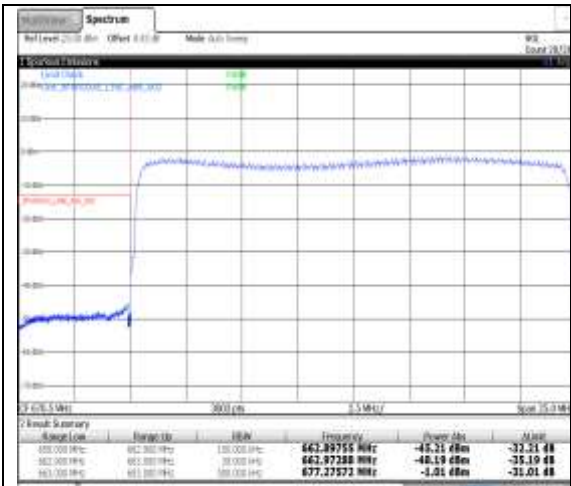
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-PASS-see graph



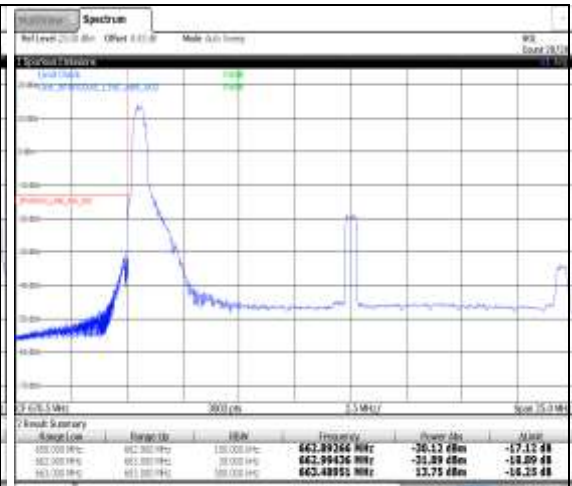
09:44:01 13.07.2023

1-DC_66A_n71A-PC3-15-5+20-M+L-21-N/A-
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-PASS-see graph

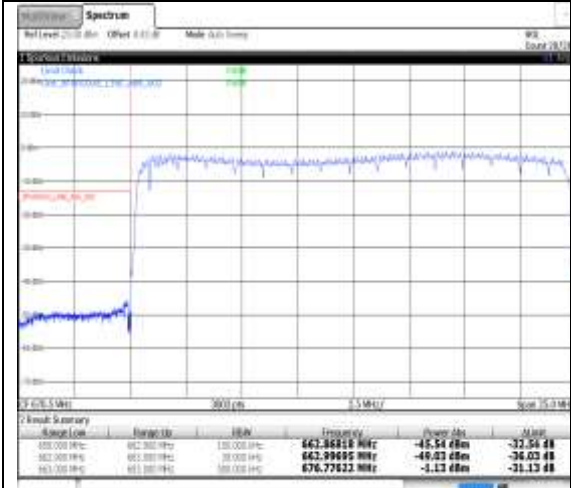
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-PASS-see graph



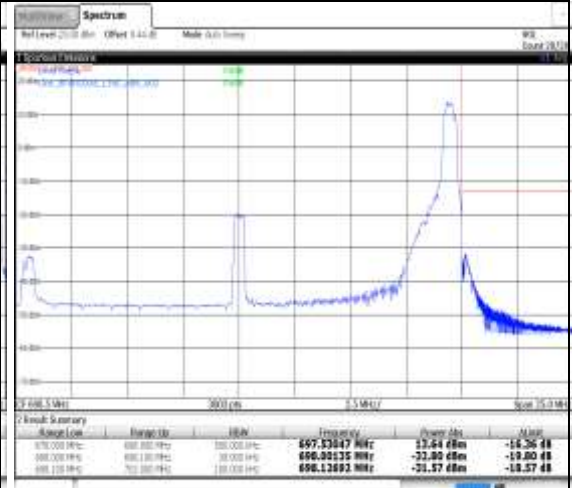
1-DC_66A_n71A-PC3-15-5+20-M+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-106@0-1--PASS-see graph



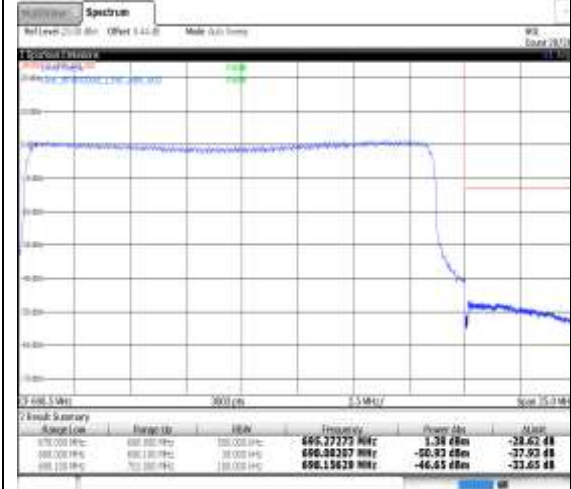
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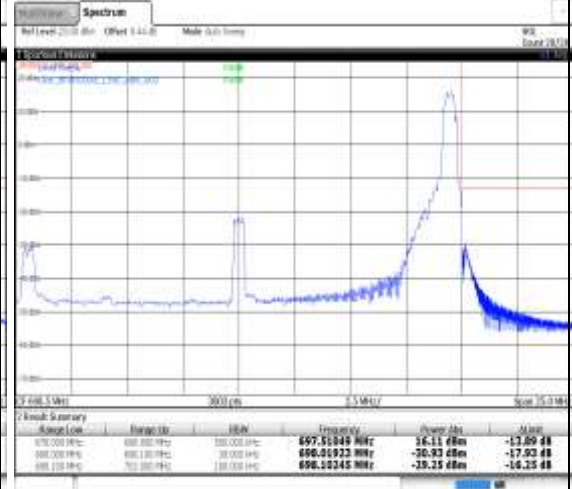
1-DC_66A_n71A-PC3-15-5+20-M+L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-106@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_Right-1@105-1--PASS-see graph

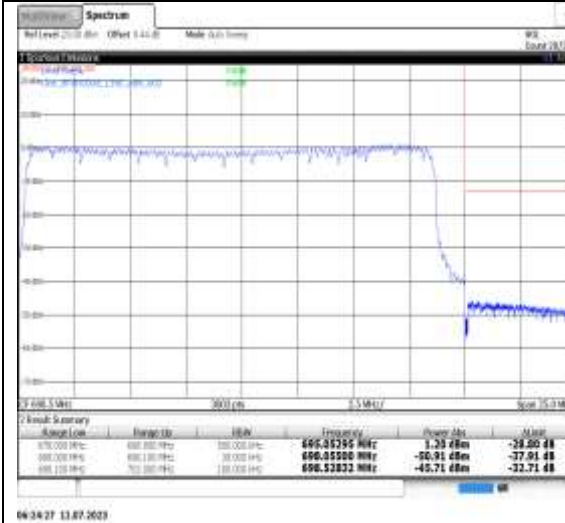


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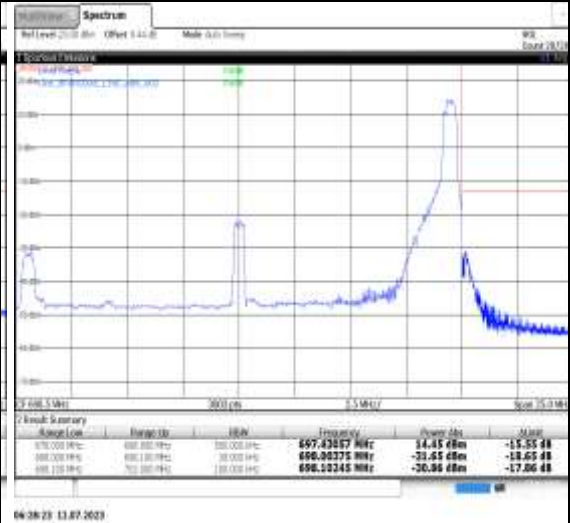


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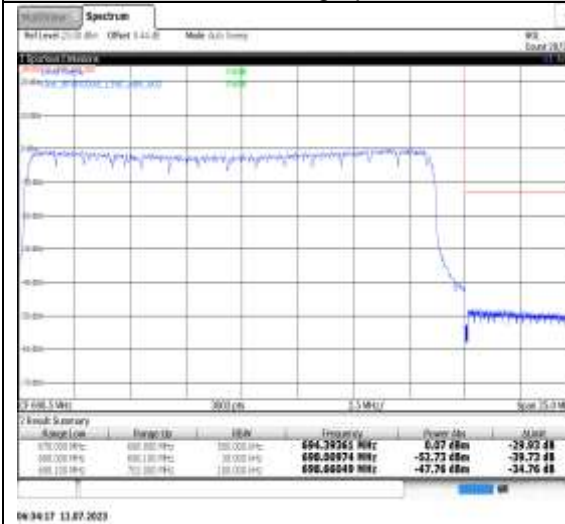
-PASS-see graph



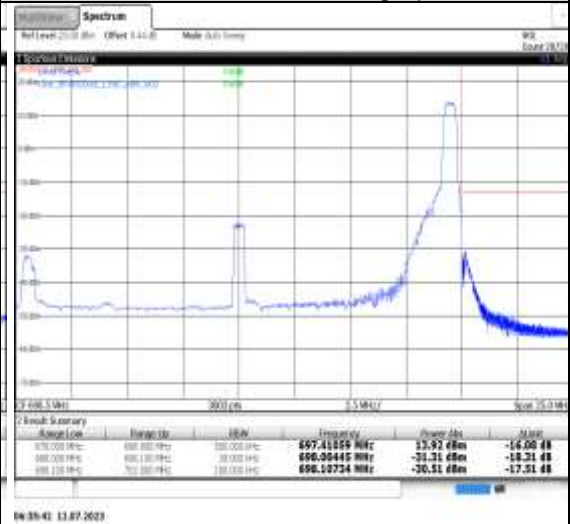
1@105-1--PASS-see graph



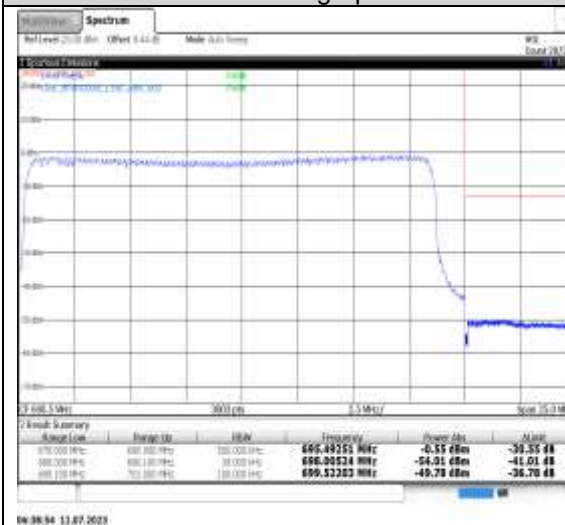
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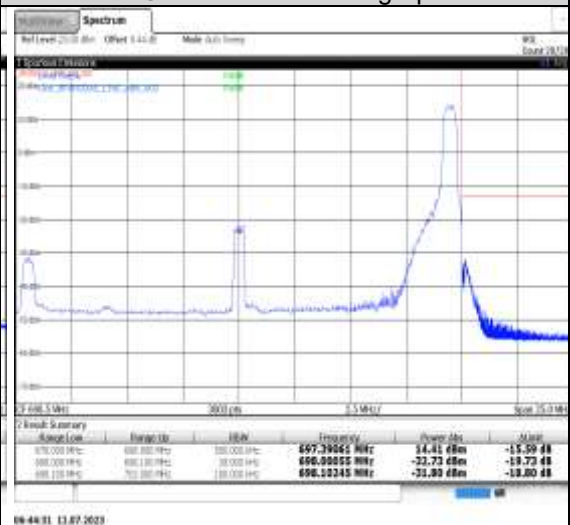
1-DC_66A_n71A-PC3-15-5+20-M+H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Right-1@105-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-100@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_Right-1@105-1--PASS-see graph



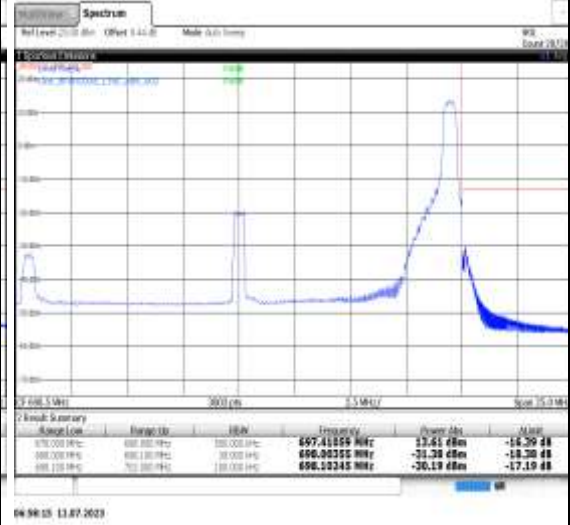
1-DC_66A_n71A-PC3-15-5+20-M+H-12-N/A-

1-DC_66A_n71A-PC3-15-5+20-M+H-14-N/A-

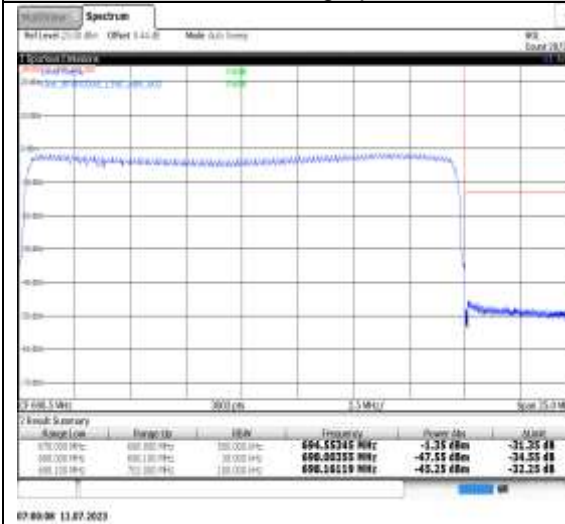
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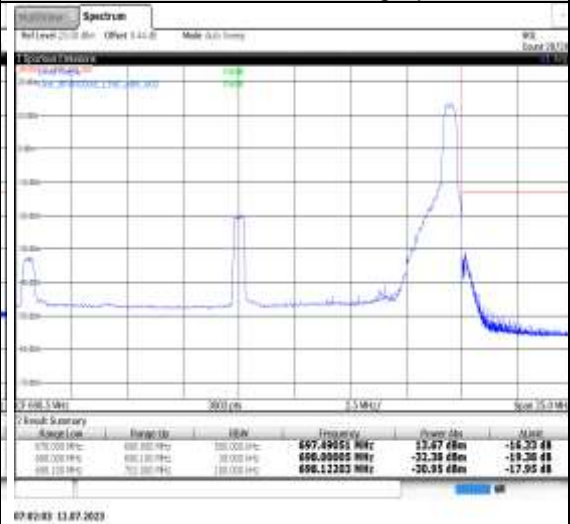
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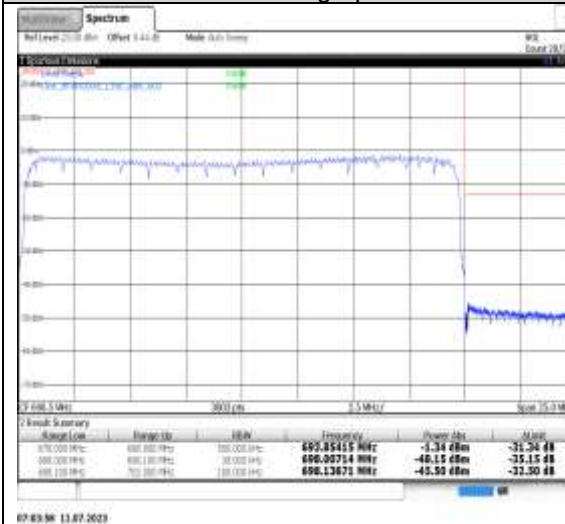
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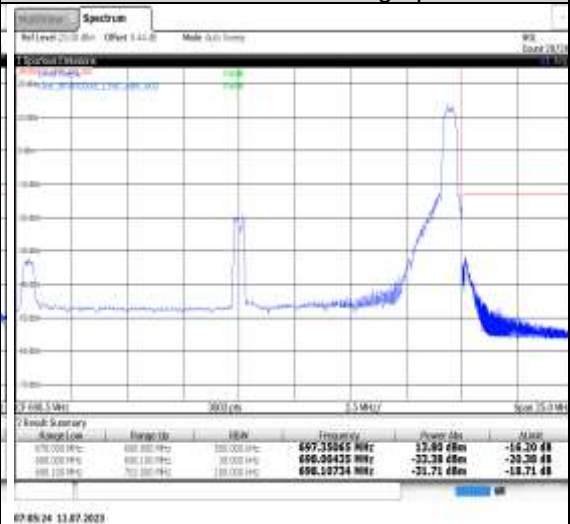
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1-DC_66A_n71A-PC3-15-5+20-M+H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-106@0-1--PASS-see graph



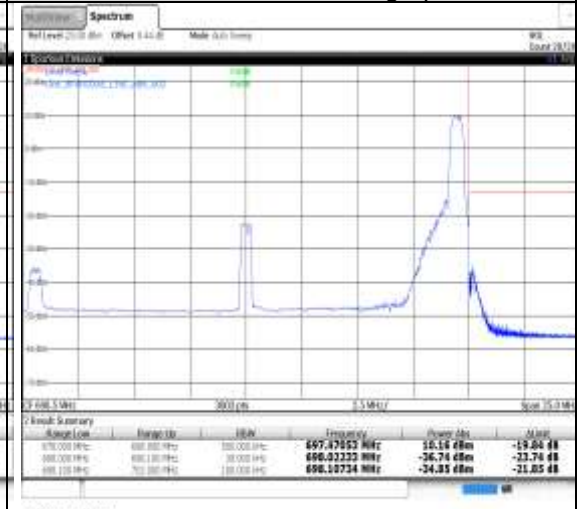
1-DC_66A_n71A-PC3-15-5+20-M+H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Right-1@105-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-106@0-1--PASS-see graph



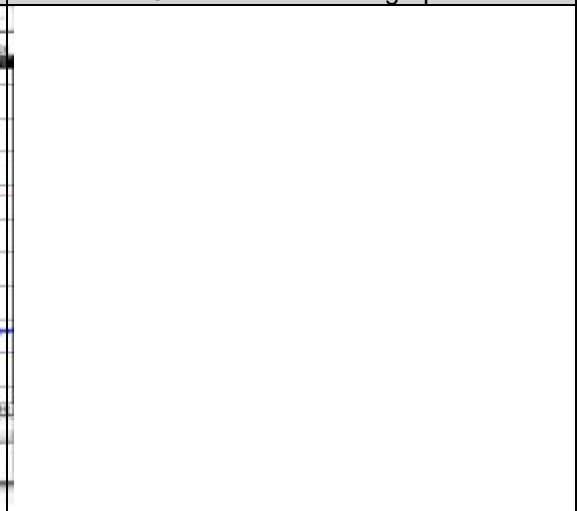
1-DC_66A_n71A-PC3-15-5+20-M+H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Right-1@105-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-106@0-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_Right-1@105-1--PASS-see graph



1-DC_66A_n71A-PC3-15-5+20-M+H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-106@0-1--PASS-see graph



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	0.009	0.15	-99.93	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	0.009	0.15	-101.58	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	0.009	0.15	-101.53	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	0.15	30	-93.36	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	0.15	30	-93.79	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	0.15	30	-93.24	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	30	1000	-59.75	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	30	1000	-59.77	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	30	1000	-59.53	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	1000	3000	-38.96	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	1000	3000	-37.87	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	1000	3000	-38.33	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	3000	12000	-34.82	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	3000	12000	-34.68	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	3000	12000	-35.38	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Left	12000	20000	-49.84	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Left	12000	20000	-49.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	12000	20000	-49.71	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Right	0.009	0.15	-105.32	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	0.009	0.15	-103.96	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	0.009	0.15	-102.29	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	0.15	30	-93.07	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Right	0.15	30	-93.47	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	0.15	30	-93.28	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	30	1000	-59.54	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	30	1000	-59.74	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Right	30	1000	-59.75	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15	Edge_1RB_Right	1000	3000	-38.28	-25	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	1000	3000	-40.49	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	1000	3000	-37.89	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	3000	12000	-38.41	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Right	3000	12000	-38.98	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	3000	12000	-43.75	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Edge_1RB_Right	12000	20000	-49.44	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	12000	20000	-49.68	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Edge_1RB_Right	12000	20000	-49.73	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	0.009	0.15	-104.86	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	0.009	0.15	-104.30	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	0.009	0.15	-104.34	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	0.15	30	-94.27	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	0.15	30	-93.35	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	0.15	30	-92.84	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	30	1000	-59.44	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	30	1000	-59.38	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	30	1000	-59.43	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	1000	3000	-38.83	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	1000	3000	-37.74	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	1000	3000	-37.88	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	3000	12000	-44.00	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	3000	12000	-40.44	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	3000	12000	-42.66	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+10 mhz	Outer_Full	12000	20000	-49.29	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+15 mhz	Outer_Full	12000	20000	-49.68	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-PI2BPSK	M+5 mhz	Outer_Full	12000	20000	-49.44	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	0.009	0.15	-102.25	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	0.009	0.15	-101.58	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	0.009	0.15	-102.26	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	0.15	30	-93.48	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	0.15	30	-93.98	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	0.15	30	-92.68	-45	PASS

DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	30	1000	-59.37	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	30	1000	-59.77	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	30	1000	-59.71	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	1000	3000	-39.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	1000	3000	-36.47	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	1000	3000	-39.05	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	3000	12000	-34.91	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	3000	12000	-35.04	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	3000	12000	-35.45	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Left	12000	20000	-49.48	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Left	12000	20000	-49.58	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Left	12000	20000	-49.80	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	0.009	0.15	-104.99	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	0.009	0.15	-105.04	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	0.009	0.15	-104.39	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	0.15	30	-92.74	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	0.15	30	-93.34	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	0.15	30	-92.95	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	30	1000	-59.88	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	30	1000	-59.48	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	30	1000	-59.22	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	1000	3000	-39.60	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	1000	3000	-36.12	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	1000	3000	-37.88	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	3000	12000	-40.17	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	3000	12000	-40.21	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	3000	12000	-40.62	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Edge_1RB_Right	12000	20000	-49.71	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Edge_1RB_Right	12000	20000	-49.78	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Edge_1RB_Right	12000	20000	-49.45	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	0.009	0.15	-104.69	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	0.009	0.15	-104.98	-55	PASS
DC_2A_n41	30	5+10	DFT-QPSK	M+15	Outer_Full	0.009	0.15	-104.40	-55	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Outer_Full	0.15	30	-93.76	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	0.15	30	-92.14	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	0.15	30	-93.88	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	30	1000	-59.70	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Outer_Full	30	1000	-59.47	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	30	1000	-58.94	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	1000	3000	-40.29	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	1000	3000	-37.35	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Outer_Full	1000	3000	-38.37	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Outer_Full	3000	12000	-44.04	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	3000	12000	-44.17	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	3000	12000	-44.08	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+10 mhz	Outer_Full	12000	20000	-49.46	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+15 mhz	Outer_Full	12000	20000	-49.82	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-QPSK	M+5 mhz	Outer_Full	12000	20000	-49.63	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-100.96	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-100.29	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-99.07	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-93.72	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-92.42	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-93.19	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.45	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	30	1000	-59.24	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	30	1000	-59.74	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-38.16	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-38.02	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-38.30	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-36.62	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-43.99	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-38.11	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.82	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.20	-25	PASS

DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.85	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-102.95	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-104.63	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-102.55	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.23	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-93.45	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-92.64	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.58	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	30	1000	-59.25	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.89	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-37.88	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-37.99	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-38.25	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-44.21	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-40.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-40.10	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.77	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.83	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.77	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	0.009	0.15	-104.63	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	0.009	0.15	-104.65	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	0.009	0.15	-104.84	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	0.15	30	-93.59	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	0.15	30	-92.88	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	0.15	30	-93.61	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	30	1000	-59.80	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	30	1000	-59.53	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	30	1000	-59.48	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	1000	3000	-38.28	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	1000	3000	-37.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	1000	3000	-39.54	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	3000	12000	-44.00	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	3000	12000	-44.34	-25	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	3000	12000	-44.08	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+5 mhz	Outer_Full	12000	20000	-49.55	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+10 mhz	Outer_Full	12000	20000	-49.61	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-16QAM	M+15 mhz	Outer_Full	12000	20000	-49.20	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-101.18	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-101.52	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-101.71	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-93.61	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-94.29	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-93.80	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.38	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	30	1000	-59.96	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	30	1000	-59.57	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-38.40	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-38.13	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-38.10	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-36.00	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-38.26	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-37.25	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.47	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.56	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.65	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-105.41	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-104.47	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-103.51	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.39	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-92.30	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-92.57	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.96	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.87	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	30	1000	-59.28	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-38.58	-25	PASS

DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-37.47	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-38.43	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-39.65	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-42.03	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-42.10	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.30	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.53	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.60	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	0.009	0.15	-105.68	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	0.009	0.15	-106.02	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	0.009	0.15	-105.15	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	0.15	30	-93.85	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	0.15	30	-93.56	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	0.15	30	-94.29	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	30	1000	-59.48	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	30	1000	-59.20	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	30	1000	-59.52	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	1000	3000	-38.08	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	1000	3000	-37.86	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	1000	3000	-39.83	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	3000	12000	-44.07	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	3000	12000	-44.25	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	3000	12000	-44.16	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+10 mhz	Outer_Full	12000	20000	-49.61	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+15 mhz	Outer_Full	12000	20000	-49.82	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-64QAM	M+5 mhz	Outer_Full	12000	20000	-49.54	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-103.10	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-103.89	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-102.60	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-93.66	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-93.12	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-93.62	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	30	1000	-59.83	-35	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.49	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	30	1000	-59.88	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-37.79	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-39.52	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-34.71	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-42.47	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-40.94	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-40.35	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.79	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.56	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.69	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-102.94	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-105.06	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-104.10	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.69	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-93.43	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-93.63	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.28	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.69	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	30	1000	-59.21	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-38.73	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-38.43	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-40.14	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-44.04	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-44.18	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-43.98	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.45	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.69	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.61	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	0.009	0.15	-104.76	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	0.009	0.15	-104.73	-55	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	0.009	0.15	-104.54	-55	PASS

DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	0.15	30	-93.23	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	0.15	30	-93.16	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	0.15	30	-93.80	-45	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	30	1000	-59.43	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	30	1000	-59.02	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	30	1000	-59.71	-35	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	1000	3000	-38.39	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	1000	3000	-37.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	1000	3000	-38.67	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	3000	12000	-43.76	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	3000	12000	-43.80	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	3000	12000	-43.93	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+15 mhz	Outer_Full	12000	20000	-49.73	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+10 mhz	Outer_Full	12000	20000	-49.67	-25	PASS
DC_2A_n41 A	30	5+10 0	DFT-256QAM	M+5 mhz	Outer_Full	12000	20000	-49.73	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	0.009	0.15	-100.15	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	0.009	0.15	-101.94	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	0.009	0.15	-100.47	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	0.15	30	-94.13	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	0.15	30	-93.97	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	0.15	30	-93.07	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	30	1000	-59.48	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	30	1000	-59.76	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	30	1000	-59.66	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	1000	3000	-34.86	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	1000	3000	-40.26	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	1000	3000	-39.03	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	3000	12000	-34.55	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	3000	12000	-36.11	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	3000	12000	-35.95	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Left	12000	20000	-49.71	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Left	12000	20000	-49.93	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Left	12000	20000	-49.73	-25	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	0.009	0.15	-103.80	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	0.009	0.15	-102.24	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	0.009	0.15	-100.75	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	0.15	30	-93.28	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	0.15	30	-93.52	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	0.15	30	-92.59	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	30	1000	-59.10	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	30	1000	-58.54	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	30	1000	-59.73	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	1000	3000	-37.82	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	1000	3000	-38.15	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	1000	3000	-35.90	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	3000	12000	-40.38	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	3000	12000	-41.34	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	3000	12000	-43.12	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Edge_1RB_Right	12000	20000	-49.92	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Edge_1RB_Right	12000	20000	-49.50	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Edge_1RB_Right	12000	20000	-49.74	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	0.009	0.15	-105.88	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	0.009	0.15	-105.13	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	0.009	0.15	-103.90	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	0.15	30	-92.35	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	0.15	30	-93.45	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	0.15	30	-93.45	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	30	1000	-59.55	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	30	1000	-59.97	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	30	1000	-59.54	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	1000	3000	-37.63	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	1000	3000	-36.21	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	1000	3000	-39.61	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	3000	12000	-44.19	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	3000	12000	-44.06	-25	PASS

DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	3000	12000	-44.04	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+5 mhz	Outer_Full	12000	20000	-49.50	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+10 mhz	Outer_Full	12000	20000	-49.78	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-QPSK	M+15 mhz	Outer_Full	12000	20000	-49.86	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-100.48	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-99.79	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-99.76	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-93.41	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-93.54	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-93.58	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.56	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	30	1000	-59.11	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	30	1000	-59.97	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-37.48	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-37.38	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-37.31	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-44.13	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-37.06	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-38.60	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.65	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.63	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.68	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-103.16	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-101.69	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-103.34	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-92.26	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-93.96	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.02	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	30	1000	-60.00	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.80	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.95	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-39.58	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-37.90	-25	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-37.74	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-42.14	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-41.90	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-40.52	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.63	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.44	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.72	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	0.009	0.15	-104.21	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	0.009	0.15	-103.13	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	0.009	0.15	-103.67	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	0.15	30	-93.82	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	0.15	30	-93.87	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	0.15	30	-93.78	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	30	1000	-59.98	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	30	1000	-59.17	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	30	1000	-59.26	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	1000	3000	-36.35	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	1000	3000	-34.48	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	1000	3000	-40.10	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	3000	12000	-43.94	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	3000	12000	-44.12	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	3000	12000	-44.21	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+5 mhz	Outer_Full	12000	20000	-49.42	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+10 mhz	Outer_Full	12000	20000	-49.41	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-16QAM	M+15 mhz	Outer_Full	12000	20000	-49.71	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-100.27	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-97.91	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-99.58	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-94.26	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-92.74	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-93.51	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.55	-35	PASS

DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	30	1000	-58.94	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	30	1000	-60.20	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-35.07	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-38.72	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-40.61	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-36.44	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-38.51	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-37.48	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.81	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.48	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.68	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-102.93	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-103.20	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-102.61	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-93.62	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-93.34	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.69	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	30	1000	-59.55	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.73	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.71	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-39.55	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-37.61	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-38.28	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-42.83	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-41.62	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-42.13	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.79	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.67	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.69	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	0.009	0.15	-105.01	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	0.009	0.15	-104.91	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	0.009	0.15	-103.51	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	0.15	30	-94.12	-45	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	0.15	30	-93.71	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	0.15	30	-93.08	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	30	1000	-59.79	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	30	1000	-59.46	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	30	1000	-59.05	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	1000	3000	-40.24	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	1000	3000	-35.00	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	1000	3000	-38.34	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	3000	12000	-43.95	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	3000	12000	-44.22	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	3000	12000	-44.28	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+10 mhz	Outer_Full	12000	20000	-49.52	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+5 mhz	Outer_Full	12000	20000	-49.79	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-64QAM	M+15 mhz	Outer_Full	12000	20000	-49.58	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	0.009	0.15	-102.06	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-103.01	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	0.009	0.15	-104.26	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-93.44	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	0.15	30	-93.00	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	0.15	30	-93.97	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.92	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	30	1000	-59.40	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	30	1000	-59.91	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	1000	3000	-37.43	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	1000	3000	-38.63	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-37.49	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-44.18	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	3000	12000	-43.38	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	3000	12000	-43.45	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.66	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Left	12000	20000	-49.61	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Left	12000	20000	-49.62	-25	PASS

DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-105.03	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	0.009	0.15	-104.99	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	0.009	0.15	-104.61	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	0.15	30	-93.27	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-94.13	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	0.15	30	-92.44	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	30	1000	-59.76	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.45	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	30	1000	-58.98	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	1000	3000	-37.48	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	1000	3000	-36.17	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-39.94	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	3000	12000	-43.99	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-44.02	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	3000	12000	-44.18	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Edge_1RB_Right	12000	20000	-49.60	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.75	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Edge_1RB_Right	12000	20000	-49.62	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	0.009	0.15	-104.98	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	0.009	0.15	-104.95	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	0.009	0.15	-104.84	-55	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	0.15	30	-91.92	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	0.15	30	-92.47	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	0.15	30	-93.86	-45	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	30	1000	-59.23	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	30	1000	-59.93	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	30	1000	-59.66	-35	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	1000	3000	-37.77	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	1000	3000	-36.25	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	1000	3000	-36.28	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	3000	12000	-43.66	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	3000	12000	-44.09	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	3000	12000	-44.00	-25	PASS

A		0		mhz						
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+15 mhz	Outer_Full	12000	20000	-49.59	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+10 mhz	Outer_Full	12000	20000	-49.46	-25	PASS
DC_2A_n41 A	30	5+10 0	CP-256QAM	M+5 mhz	Outer_Full	12000	20000	-49.74	-25	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.009	0.15	-80.71	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.15	30	-82.52	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	30	1000	-51.24	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	1000	3000	-35.30	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Left	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.009	0.15	-81.71	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.15	30	-83.89	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	30	1000	-53.05	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	1000	3000	-35.87	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	3000	12000	-43.67	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Edge_1RB_Right	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	0.009	0.15	-95.98	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	0.15	30	-84.89	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	30	1000	-45.72	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	1000	3000	-38.46	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	3000	12000	-43.90	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+L	Outer_Full	12000	20000	-49.62	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-79.85	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-84.28	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.03	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.98	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.36	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-81.31	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-82.12	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.65	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.22	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.74	-13	PASS

DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.65	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-101.07	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-84.84	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-47.81	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.98	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-81.31	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-84.66	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-50.63	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.72	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-82.10	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-88.48	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-42.50	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-35.92	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-44.03	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-94.87	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.15	30	-86.30	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	30	1000	-49.94	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	1000	3000	-34.47	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+L	Outer_Full	12000	20000	-49.45	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-81.28	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	0.15	30	-85.15	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	30	1000	-47.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	1000	3000	-36.80	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	3000	12000	-43.57	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.76	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-82.68	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	0.15	30	-83.03	-23	PASS

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DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	30	1000	-53.55	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	1000	3000	-36.43	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	0.009	0.15	-97.08	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	0.15	30	-86.41	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	30	1000	-43.90	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	1000	3000	-42.28	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+L	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.11	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	0.15	30	-87.03	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	30	1000	-52.26	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	1000	3000	-36.36	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	3000	12000	-44.01	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Left	12000	20000	-49.77	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-81.27	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	0.15	30	-87.32	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	30	1000	-51.95	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	1000	3000	-36.18	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	3000	12000	-43.95	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.42	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	0.009	0.15	-99.59	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	0.15	30	-90.46	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	30	1000	-50.99	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	1000	3000	-43.77	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	3000	12000	-43.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+L	Outer_Full	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-79.84	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-83.35	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.40	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-32.78	-13	PASS

DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.90	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.76	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-81.01	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-87.39	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-58.24	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-36.86	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.75	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-92.37	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-88.53	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-44.81	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-42.20	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-43.66	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-49.65	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.08	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-86.75	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-47.81	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.18	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.59	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-80.30	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-88.71	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-48.48	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-36.64	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.75	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.64	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-92.61	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-89.74	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-51.38	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-42.42	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-49.57	-13	PASS
DC_2A_n71	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.97	-33	PASS

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DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	0.15	30	-86.67	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	30	1000	-52.59	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	1000	3000	-37.86	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	3000	12000	-44.01	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.62	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-81.65	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	0.15	30	-87.45	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	30	1000	-57.94	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	1000	3000	-32.99	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	3000	12000	-43.97	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.82	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	0.009	0.15	-93.79	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	0.15	30	-90.36	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	30	1000	-52.49	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	1000	3000	-42.68	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+L	Outer_Full	12000	20000	-49.36	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	0.009	0.15	-78.79	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	0.15	30	-89.74	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	30	1000	-54.33	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	1000	3000	-38.17	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	3000	12000	-44.03	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Left	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-79.85	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	0.15	30	-88.94	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	30	1000	-50.02	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	1000	3000	-37.49	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	3000	12000	-43.68	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	0.009	0.15	-93.59	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	0.15	30	-88.24	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	30	1000	-52.19	-13	PASS

DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	1000	3000	-42.69	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+L	Outer_Full	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.009	0.15	-80.58	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.15	30	-89.16	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	30	1000	-50.60	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	1000	3000	-44.97	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Left	12000	20000	-49.62	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.009	0.15	-82.09	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.15	30	-89.63	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	30	1000	-50.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	1000	3000	-40.40	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	3000	12000	-43.95	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Edge_1RB_Right	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	0.009	0.15	-92.99	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	0.15	30	-90.64	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	30	1000	-53.27	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	1000	3000	-44.63	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	3000	12000	-43.93	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+M	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-81.28	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-86.24	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-43.13	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.39	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.97	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-82.24	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-86.06	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-58.62	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.06	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.18	-13	PASS
DC_2A_n71	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.73	-13	PASS

A										
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-99.06	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-87.37	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-47.08	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.39	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-43.65	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-83.19	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-88.37	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-47.45	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.80	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-44.04	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.84	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-83.68	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-88.65	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-46.65	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-32.83	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.40	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-96.64	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.15	30	-90.06	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	30	1000	-49.38	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	1000	3000	-45.95	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	3000	12000	-43.92	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+M	Outer_Full	12000	20000	-49.18	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-82.51	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	0.15	30	-84.90	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	30	1000	-56.26	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	1000	3000	-44.36	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	3000	12000	-43.97	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-83.68	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	0.15	30	-89.67	-23	PASS

DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	30	1000	-52.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	1000	3000	-40.99	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	0.009	0.15	-97.62	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	0.15	30	-90.61	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	30	1000	-48.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	1000	3000	-46.19	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	3000	12000	-43.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+M	Outer_Full	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-81.78	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	0.15	30	-87.74	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	30	1000	-52.12	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	1000	3000	-43.41	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	3000	12000	-43.72	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.79	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	0.15	30	-90.03	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	30	1000	-50.97	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	1000	3000	-40.86	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	3000	12000	-43.17	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.82	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	0.009	0.15	-98.25	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	0.15	30	-88.78	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	30	1000	-53.46	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	1000	3000	-46.16	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	3000	12000	-43.46	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+M	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-79.92	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-88.23	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-54.56	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.10	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.89	-13	PASS

A										
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.52	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-82.68	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-88.22	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-55.04	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.78	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.43	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-91.60	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-90.11	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-52.76	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-32.79	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-43.64	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-49.59	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-81.49	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-86.66	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.03	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-44.65	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.75	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.38	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.50	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-87.49	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-48.57	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-41.80	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-94.44	-33	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-89.85	-23	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-52.69	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-46.10	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-43.89	-13	PASS
DC_2A_n71 A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-82.44	-33	PASS

DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	0.15	30	-88.76	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	30	1000	-48.11	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	1000	3000	-44.65	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	3000	12000	-43.89	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-83.66	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	0.15	30	-88.77	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	30	1000	-48.46	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	1000	3000	-42.32	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	3000	12000	-43.74	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.73	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	0.009	0.15	-95.07	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	0.15	30	-91.35	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	30	1000	-49.34	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	1000	3000	-46.73	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	3000	12000	-43.72	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+M	Outer_Full	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-87.57	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	0.15	30	-92.36	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	30	1000	-48.38	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	1000	3000	-46.79	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.36	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-87.27	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	0.15	30	-90.56	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	30	1000	-56.64	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	1000	3000	-45.22	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	0.009	0.15	-99.86	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	0.15	30	-91.15	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	30	1000	-58.72	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	1000	3000	-39.77	-13	PASS

A										
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	3000	12000	-43.83	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+M	Outer_Full	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.009	0.15	-80.07	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.15	30	-87.02	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	30	1000	-59.48	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	1000	3000	-47.17	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	3000	12000	-43.94	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Left	12000	20000	-49.62	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.009	0.15	-81.09	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.15	30	-86.85	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	30	1000	-59.58	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	1000	3000	-39.73	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	3000	12000	-43.47	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Edge_1RB_Right	12000	20000	-49.46	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	0.009	0.15	-91.98	-33	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	0.15	30	-89.79	-23	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	30	1000	-55.61	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	1000	3000	-45.68	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	3000	12000	-43.91	-13	PASS
DC_2A_n71 A	15	5+5	DFT-PI2BPSK	M+H	Outer_Full	12000	20000	-49.60	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-80.96	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-87.76	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.58	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.76	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-49.37	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-81.16	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-88.60	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-59.14	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.69	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-42.71	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-49.55	-13	PASS

DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-92.82	-33	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-89.38	-23	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-47.49	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-47.49	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-43.69	-13	PASS
DC_2A_n71 A	15	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.17	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-86.44	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-56.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-46.63	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.67	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-82.16	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-85.71	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-52.36	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-45.62	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-42.34	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-49.15	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-97.13	-33	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.15	30	-87.66	-23	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	30	1000	-52.56	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	1000	3000	-47.20	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	3000	12000	-43.92	-13	PASS
DC_2A_n71 A	15	5+5	DFT-16QAM	M+H	Outer_Full	12000	20000	-49.79	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.57	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	0.15	30	-86.38	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	30	1000	-43.16	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	1000	3000	-45.59	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	3000	12000	-43.81	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Left	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	0.009	0.15	-82.15	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	0.15	30	-83.72	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	30	1000	-52.02	-13	PASS

A										
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	1000	3000	-44.32	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	3000	12000	-43.17	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Edge_1RB_Right	12000	20000	-49.78	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	0.009	0.15	-97.17	-33	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	0.15	30	-88.53	-23	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	30	1000	-54.11	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	1000	3000	-45.72	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	3000	12000	-43.95	-13	PASS
DC_2A_n71 A	15	5+5	DFT-64QAM	M+H	Outer_Full	12000	20000	-49.36	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	0.009	0.15	-81.70	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	0.15	30	-88.47	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	30	1000	-50.84	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	1000	3000	-45.88	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Left	12000	20000	-49.43	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	0.009	0.15	-80.97	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	0.15	30	-87.09	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	30	1000	-52.28	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	1000	3000	-45.27	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	3000	12000	-43.02	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Edge_1RB_Right	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	0.009	0.15	-100.56	-33	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	0.15	30	-91.83	-23	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	30	1000	-50.02	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	1000	3000	-47.67	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+5	DFT-256QAM	M+H	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-81.33	-33	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-84.59	-23	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-57.13	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.43	-13	PASS
DC_2A_n71 A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.72	-13	PASS

DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-49.60	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-80.40	-33	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-85.31	-23	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-48.19	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.77	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-42.33	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-49.48	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-100.48	-33	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-86.21	-23	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-42.95	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-46.00	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71_A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-49.59	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.66	-33	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-89.63	-23	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.48	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-45.98	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.77	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-49.81	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-83.19	-33	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-88.18	-23	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.93	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-46.12	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-42.93	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-49.57	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-92.79	-33	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-89.82	-23	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-54.06	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-45.80	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-43.90	-13	PASS
DC_2A_n71_A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-49.59	-13	PASS
DC_2A_n71_A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.25	-33	PASS
DC_2A_n71_A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	0.15	30	-91.39	-23	PASS

A										
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	30	1000	-46.20	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	1000	3000	-47.03	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	3000	12000	-43.83	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Left	12000	20000	-49.37	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	0.009	0.15	-81.96	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	0.15	30	-87.78	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	30	1000	-54.88	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	1000	3000	-45.50	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	3000	12000	-43.60	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Edge_1RB_Right	12000	20000	-49.72	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	0.009	0.15	-94.52	-33	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	0.15	30	-91.37	-23	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	30	1000	-47.88	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	1000	3000	-45.79	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+5	CP-64QAM	M+H	Outer_Full	12000	20000	-49.55	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	0.009	0.15	-81.32	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	0.15	30	-88.78	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	30	1000	-46.85	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	1000	3000	-46.87	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	3000	12000	-43.93	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Left	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	0.009	0.15	-80.55	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	0.15	30	-88.11	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	30	1000	-49.28	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	1000	3000	-45.87	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	3000	12000	-43.31	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Edge_1RB_Right	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	0.009	0.15	-93.90	-33	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	0.15	30	-90.91	-23	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	30	1000	-48.58	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	1000	3000	-45.76	-13	PASS

DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	3000	12000	-43.60	-13	PASS
DC_2A_n71 A	15	5+5	CP-256QAM	M+H	Outer_Full	12000	20000	-49.83	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.009	0.15	-79.87	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.15	30	-88.79	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	30	1000	-59.61	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	1000	3000	-40.65	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	3000	12000	-43.95	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	12000	20000	-49.69	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.009	0.15	-79.44	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.15	30	-85.69	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	30	1000	-59.82	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	1000	3000	-36.48	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	3000	12000	-43.93	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	12000	20000	-49.76	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	0.009	0.15	-79.35	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	0.15	30	-87.23	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	30	1000	-59.25	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	1000	3000	-40.43	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	3000	12000	-43.88	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+L	Outer_Full	12000	20000	-49.91	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-80.34	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.26	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-59.72	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-34.78	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.68	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.75	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-82.35	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-85.41	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-59.49	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.93	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.09	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.63	-13	PASS
DC_2A_n71	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-97.06	-33	PASS

A										
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-89.89	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-49.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-32.87	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-49.77	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-81.31	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-85.83	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-59.51	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-36.48	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.54	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-83.86	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-87.38	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-59.48	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-41.18	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.76	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-100.97	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-91.11	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-50.75	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-46.20	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-43.78	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-81.02	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	0.15	30	-87.87	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	30	1000	-59.46	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	1000	3000	-37.03	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	3000	12000	-43.60	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.44	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-83.79	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	0.15	30	-86.83	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	30	1000	-57.40	-13	PASS

DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	1000	3000	-41.23	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	3000	12000	-43.70	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.62	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	0.009	0.15	-101.99	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	0.15	30	-91.69	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	30	1000	-51.43	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	1000	3000	-46.19	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	3000	12000	-43.79	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+L	Outer_Full	12000	20000	-49.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.41	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	0.15	30	-87.23	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	30	1000	-59.69	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	1000	3000	-32.85	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	3000	12000	-43.89	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Left	12000	20000	-49.38	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-82.49	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	0.15	30	-87.82	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	30	1000	-59.10	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	1000	3000	-40.85	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	3000	12000	-43.67	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.80	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	0.009	0.15	-102.03	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	0.15	30	-91.95	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	30	1000	-51.01	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	1000	3000	-46.56	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	3000	12000	-43.77	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+L	Outer_Full	12000	20000	-49.42	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-80.30	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-88.85	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-59.19	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.12	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.88	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.52	-13	PASS

A										
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-82.11	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-89.66	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-59.63	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.22	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-98.58	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-90.71	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-49.49	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-47.07	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-43.91	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.96	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-86.76	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-59.42	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.92	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-83.22	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-86.15	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-59.26	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-40.90	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-97.95	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-91.20	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-49.54	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-47.00	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-43.91	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-49.67	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-81.48	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	0.15	30	-86.35	-23	PASS

DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	30	1000	-59.33	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	1000	3000	-38.20	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	3000	12000	-43.96	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.60	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-83.04	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	0.15	30	-87.82	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	30	1000	-59.30	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	1000	3000	-41.52	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	3000	12000	-43.90	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.72	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	0.009	0.15	-99.57	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	0.15	30	-91.68	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	30	1000	-49.60	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	1000	3000	-45.63	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+L	Outer_Full	12000	20000	-49.59	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	0.009	0.15	-80.08	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	0.15	30	-88.30	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	30	1000	-57.87	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	1000	3000	-38.44	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Left	12000	20000	-49.60	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-82.26	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	0.15	30	-90.00	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	30	1000	-58.88	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	1000	3000	-42.38	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	3000	12000	-43.87	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.57	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	0.009	0.15	-104.76	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	0.15	30	-93.84	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	30	1000	-54.40	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	1000	3000	-32.90	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	3000	12000	-43.85	-13	PASS

A										
DC_2A_n71 A	15	5+20	CP-256QAM	M+L	Outer_Full	12000	20000	-49.64	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.009	0.15	-80.47	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.15	30	-88.84	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	30	1000	-59.69	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	1000	3000	-40.35	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	3000	12000	-43.85	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	12000	20000	-49.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.009	0.15	-81.78	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.15	30	-88.32	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	30	1000	-59.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	1000	3000	-45.10	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	3000	12000	-43.56	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	0.009	0.15	-99.33	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	0.15	30	-91.25	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	30	1000	-59.39	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	1000	3000	-44.50	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	3000	12000	-43.86	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+M	Outer_Full	12000	20000	-49.42	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-82.99	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-88.11	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-59.82	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.76	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.94	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-79.76	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-88.91	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-59.49	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.35	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.69	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.45	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-99.71	-33	PASS

DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-89.20	-23	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-59.41	-13	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.98	-13	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-43.90	-13	PASS
DC_2A_n71_A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-49.54	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-82.31	-33	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-86.18	-23	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-59.52	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-39.49	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.09	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.45	-33	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-87.04	-23	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-59.20	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-44.47	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-42.46	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.76	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-100.16	-33	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-89.70	-23	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-59.39	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-47.12	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-43.74	-13	PASS
DC_2A_n71_A	15	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-49.45	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-82.43	-33	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	0.15	30	-86.31	-23	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	30	1000	-59.50	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	1000	3000	-40.08	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	3000	12000	-43.62	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.72	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.88	-33	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	0.15	30	-87.17	-23	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	30	1000	-59.31	-13	PASS
DC_2A_n71_A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	1000	3000	-44.86	-13	PASS

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DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	3000	12000	-43.96	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	0.009	0.15	-101.20	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	0.15	30	-91.31	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	30	1000	-59.39	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	1000	3000	-45.11	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	3000	12000	-43.76	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+M	Outer_Full	12000	20000	-49.19	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-86.34	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	0.15	30	-90.39	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	30	1000	-59.93	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	1000	3000	-42.25	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	3000	12000	-43.88	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.51	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.39	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	0.15	30	-91.62	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	30	1000	-59.39	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	1000	3000	-42.44	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	3000	12000	-43.77	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	0.009	0.15	-104.78	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	0.15	30	-92.53	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	30	1000	-59.35	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	1000	3000	-47.38	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	3000	12000	-43.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+M	Outer_Full	12000	20000	-49.59	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-81.65	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-85.88	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-59.38	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.98	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.71	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.75	-13	PASS

DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-81.78	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-86.23	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-59.13	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-44.35	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.52	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.60	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-100.79	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-88.84	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-58.77	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-45.75	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.69	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-82.71	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-89.71	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-59.64	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-40.02	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.87	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.35	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-82.69	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-90.32	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-59.34	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-44.64	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.80	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.50	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-102.50	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-91.51	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-59.43	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-46.13	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-43.52	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-49.84	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-83.41	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	0.15	30	-86.24	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	30	1000	-59.23	-13	PASS

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DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	1000	3000	-40.62	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	3000	12000	-43.77	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.78	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-83.10	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	0.15	30	-85.52	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	30	1000	-59.39	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	1000	3000	-44.45	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	3000	12000	-43.83	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.66	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	0.009	0.15	-101.52	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	0.15	30	-91.78	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	30	1000	-59.48	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	1000	3000	-47.26	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	3000	12000	-43.80	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+M	Outer_Full	12000	20000	-49.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-87.40	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	0.15	30	-91.93	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	30	1000	-58.82	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	1000	3000	-43.75	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.55	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-86.45	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	0.15	30	-90.42	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	30	1000	-59.50	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	1000	3000	-45.88	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	3000	12000	-43.71	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.52	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	0.009	0.15	-105.42	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	0.15	30	-90.96	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	30	1000	-59.27	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	1000	3000	-47.27	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	3000	12000	-43.91	-13	PASS

DC_2A_n71 A	15	5+20	CP-256QAM	M+M	Outer_Full	12000	20000	-49.54	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.009	0.15	-81.47	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.15	30	-86.93	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	30	1000	-59.66	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	1000	3000	-44.46	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	3000	12000	-43.67	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	12000	20000	-49.74	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.009	0.15	-82.27	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.15	30	-83.63	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	30	1000	-59.62	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	1000	3000	-45.80	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	3000	12000	-41.26	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	12000	20000	-49.50	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	0.009	0.15	-99.31	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	0.15	30	-89.71	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	30	1000	-59.66	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	1000	3000	-44.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	3000	12000	-43.38	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+H	Outer_Full	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-81.76	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-85.00	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-59.42	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.65	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-49.56	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-81.16	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.87	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-59.36	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-44.72	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-42.33	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-49.67	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-99.56	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-89.36	-23	PASS

A										
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-59.48	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.05	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-43.59	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-49.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-83.31	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-88.40	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-59.42	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-44.51	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.97	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-82.12	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-85.76	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-59.02	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-45.04	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.37	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-49.64	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-100.87	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-90.83	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-59.52	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-47.54	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	3000	12000	-43.98	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+H	Outer_Full	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	0.009	0.15	-83.13	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	0.15	30	-85.81	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	30	1000	-59.69	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	1000	3000	-44.70	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	3000	12000	-43.43	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Left	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	0.009	0.15	-82.50	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	0.15	30	-86.37	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	30	1000	-59.07	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	1000	3000	-45.10	-13	PASS

DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	3000	12000	-41.89	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Edge_1RB_Right	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	0.009	0.15	-101.81	-33	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	0.15	30	-91.21	-23	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	30	1000	-59.02	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	1000	3000	-44.48	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	3000	12000	-43.73	-13	PASS
DC_2A_n71 A	15	5+20	DFT-64QAM	M+H	Outer_Full	12000	20000	-49.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.00	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	0.15	30	-87.02	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	30	1000	-59.33	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	1000	3000	-44.60	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	3000	12000	-43.99	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Left	12000	20000	-49.49	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	0.009	0.15	-81.79	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	0.15	30	-88.05	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	30	1000	-59.37	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	1000	3000	-45.02	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	3000	12000	-43.56	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Edge_1RB_Right	12000	20000	-49.57	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	0.009	0.15	-105.26	-33	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	0.15	30	-93.20	-23	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	30	1000	-59.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	1000	3000	-47.53	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	3000	12000	-43.98	-13	PASS
DC_2A_n71 A	15	5+20	DFT-256QAM	M+H	Outer_Full	12000	20000	-49.31	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-81.70	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-86.94	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-59.24	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.95	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.51	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-49.67	-13	PASS
DC_2A_n71	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-80.02	-33	PASS

A										
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-87.37	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-59.61	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-44.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.17	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-49.61	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-98.99	-33	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-91.27	-23	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-59.53	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-45.71	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-43.83	-13	PASS
DC_2A_n71 A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-79.77	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-86.41	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-59.44	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-44.81	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.90	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-49.50	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-81.02	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-86.10	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-59.47	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-44.98	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-49.60	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-97.59	-33	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-91.04	-23	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-59.55	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-45.50	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-49.55	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	0.009	0.15	-82.13	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	0.15	30	-89.34	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	30	1000	-59.40	-13	PASS

DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	1000	3000	-45.44	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	3000	12000	-43.99	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Left	12000	20000	-49.71	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	0.009	0.15	-82.62	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	0.15	30	-87.85	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	30	1000	-58.76	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	1000	3000	-45.61	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Edge_1RB_Right	12000	20000	-49.52	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	0.009	0.15	-100.60	-33	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	0.15	30	-90.37	-23	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	30	1000	-59.64	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	1000	3000	-47.67	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	3000	12000	-43.81	-13	PASS
DC_2A_n71 A	15	5+20	CP-64QAM	M+H	Outer_Full	12000	20000	-49.55	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	0.009	0.15	-81.93	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	0.15	30	-90.09	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	30	1000	-59.45	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	1000	3000	-45.13	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	3000	12000	-43.98	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Left	12000	20000	-49.77	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	0.009	0.15	-79.21	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	0.15	30	-89.89	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	30	1000	-59.00	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	1000	3000	-45.84	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	3000	12000	-43.79	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Edge_1RB_Right	12000	20000	-49.63	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	0.009	0.15	-104.46	-33	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	0.15	30	-93.10	-23	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	30	1000	-59.53	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	1000	3000	-45.74	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	3000	12000	-43.62	-13	PASS
DC_2A_n71 A	15	5+20	CP-256QAM	M+H	Outer_Full	12000	20000	-49.59	-13	PASS

A										
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	0.009	0.15	-82.42	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	0.15	30	-84.02	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	30	1000	-59.56	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	1000	3000	-38.23	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	3000	12000	-43.75	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Left	12000	20000	-49.58	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	0.009	0.15	-82.35	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	0.15	30	-83.27	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	30	1000	-59.46	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	1000	3000	-43.55	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	3000	12000	-43.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Edge_1RB_Right	12000	20000	-49.75	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	0.009	0.15	-97.99	-33	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	0.15	30	-89.90	-23	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	30	1000	-59.77	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	1000	3000	-45.83	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	3000	12000	-43.84	-13	PASS
DC_2A_n71 A	15	5+20	DFT-PI2BPSK	M+5 mhz	Outer_Full	12000	20000	-49.44	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	0.009	0.15	-81.23	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	0.15	30	-85.71	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	30	1000	-59.49	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	1000	3000	-38.93	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	3000	12000	-43.87	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Left	12000	20000	-49.68	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	0.009	0.15	-81.16	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	0.15	30	-84.53	-23	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	30	1000	-59.63	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	1000	3000	-34.74	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	3000	12000	-43.83	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Edge_1RB_Right	12000	20000	-49.85	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	0.009	0.15	-98.72	-33	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	0.15	30	-89.24	-23	PASS

DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	30	1000	-59.76	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	1000	3000	-45.96	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	3000	12000	-43.68	-13	PASS
DC_2A_n71 A	15	5+20	DFT-QPSK	M+5 mhz	Outer_Full	12000	20000	-49.68	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	0.009	0.15	-82.87	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	0.15	30	-89.10	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	30	1000	-59.62	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	1000	3000	-39.57	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	3000	12000	-43.82	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Left	12000	20000	-49.81	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	0.009	0.15	-82.41	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	0.15	30	-87.85	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	30	1000	-59.17	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	1000	3000	-44.22	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	3000	12000	-43.04	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Edge_1RB_Right	12000	20000	-49.77	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	0.009	0.15	-99.22	-33	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	0.15	30	-90.92	-23	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	30	1000	-59.33	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	1000	3000	-46.04	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	3000	12000	-43.62	-13	PASS
DC_2A_n71 A	15	5+20	DFT-16QAM	M+5 mhz	Outer_Full	12000	20000	-49.59	-13	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.009	0.15	-99.96	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	0.15	30	-90.81	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	30	1000	-59.66	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	1000	3000	-48.10	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	3000	12000	-44.51	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	12000	20000	-49.35	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.009	0.15	-100.39	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	0.15	30	-89.80	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	30	1000	-59.74	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	1000	3000	-46.59	-25	PASS
DC_66A_n4	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	3000	12000	-26.33	-25	PASS

1A										
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	12000	20000	-49.58	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	0.009	0.15	-104.25	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	0.15	30	-90.78	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	30	1000	-59.10	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	1000	3000	-43.49	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	3000	12000	-33.51	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	12000	20000	-49.58	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-99.45	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-91.71	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-59.63	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.96	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-44.67	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.52	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-100.47	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-90.35	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-59.37	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-48.08	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-27.40	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.74	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-104.32	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-91.97	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-59.63	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-44.86	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-34.96	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-49.64	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-99.25	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-92.19	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-59.91	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-48.07	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-38.97	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.59	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-101.44	-55	PASS

DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-91.47	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-59.55	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-47.53	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-28.11	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.43	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-104.35	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-89.53	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-59.24	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-44.85	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-36.76	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-49.62	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-100.20	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	0.15	30	-88.24	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	30	1000	-59.07	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	1000	3000	-48.29	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	3000	12000	-36.06	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.51	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-99.96	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	0.15	30	-91.76	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	30	1000	-59.78	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	1000	3000	-47.72	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	3000	12000	-29.93	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.48	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	0.009	0.15	-106.67	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	0.15	30	-93.14	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	30	1000	-59.72	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	1000	3000	-48.48	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	3000	12000	-37.40	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+L	Outer_Full	12000	20000	-49.45	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	0.009	0.15	-102.90	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	0.15	30	-90.64	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	30	1000	-59.87	-35	PASS
DC_66A_n4	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	1000	3000	-46.73	-25	PASS

1A										
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	3000	12000	-39.99	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	12000	20000	-49.59	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-102.85	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	0.15	30	-88.46	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	30	1000	-59.88	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	1000	3000	-48.07	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	3000	12000	-31.65	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.61	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	0.009	0.15	-106.10	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	0.15	30	-91.11	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	30	1000	-59.81	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	1000	3000	-47.21	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	3000	12000	-40.08	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+L	Outer_Full	12000	20000	-49.38	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-97.70	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-90.23	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-59.79	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-47.75	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-32.28	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-49.78	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-99.52	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-90.89	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-59.72	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-48.22	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-27.66	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-49.62	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-104.86	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-90.93	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-59.18	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-44.82	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-37.13	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-49.60	-25	PASS

DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-97.89	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-92.88	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-59.93	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-48.14	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-39.38	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-49.62	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-98.01	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-88.11	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-59.30	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-48.00	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-29.56	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-49.47	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-104.76	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-90.46	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-59.53	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-45.29	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-37.25	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-49.71	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	0.009	0.15	-98.47	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	0.15	30	-92.51	-45	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	30	1000	-59.30	-35	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	1000	3000	-48.46	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	3000	12000	-39.70	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	12000	20000	-49.72	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	0.009	0.15	-98.01	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	0.15	30	-87.52	-45	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	30	1000	-59.86	-35	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	1000	3000	-47.89	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	3000	12000	-29.68	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	12000	20000	-49.43	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	0.009	0.15	-105.87	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	0.15	30	-91.44	-45	PASS
DC_66A_n4	30	5+20	CP-64QAM	M+L	Outer_Full	30	1000	-59.45	-35	PASS

1A										
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	1000	3000	-44.95	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	3000	12000	-38.27	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+L	Outer_Full	12000	20000	-49.53	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	3000	12000	-34.55	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	0.009	0.15	-102.05	-55	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	0.15	30	-91.15	-45	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	30	1000	-59.78	-35	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	1000	3000	-47.83	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	3000	12000	-35.76	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	12000	20000	-49.75	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	0.009	0.15	-105.01	-55	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	0.15	30	-89.59	-45	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	30	1000	-59.09	-35	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	1000	3000	-48.17	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	3000	12000	-42.05	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+L	Outer_Full	12000	20000	-49.45	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.009	0.15	-100.80	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	0.15	30	-90.00	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	30	1000	-59.85	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	1000	3000	-47.62	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	3000	12000	-36.64	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	12000	20000	-49.70	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.009	0.15	-102.67	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	0.15	30	-88.75	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	30	1000	-59.94	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	1000	3000	-47.66	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	3000	12000	-33.57	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	12000	20000	-49.58	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	0.009	0.15	-104.18	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	0.15	30	-91.78	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	30	1000	-59.84	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	1000	3000	-44.23	-25	PASS

DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	3000	12000	-41.18	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	12000	20000	-49.64	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-100.46	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-90.17	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-59.49	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-47.37	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-36.26	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.55	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-101.04	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-90.51	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-59.21	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.71	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-44.00	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.68	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-104.49	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-88.21	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-59.46	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.01	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-43.86	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-49.51	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-99.54	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-93.21	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-59.48	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-47.56	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-37.24	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.57	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-102.00	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-88.50	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-59.33	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-48.36	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-34.15	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.57	-25	PASS
DC_66A_n4	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-104.41	-55	PASS

1A											
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-92.46	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-59.85	-35	PASS	
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-45.78	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-43.71	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-49.41	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-101.69	-55	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	0.15	30	-93.19	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	30	1000	-59.62	-35	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	1000	3000	-48.02	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	3000	12000	-38.16	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.69	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-101.79	-55	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	0.15	30	-93.11	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	30	1000	-59.07	-35	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	1000	3000	-46.52	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	3000	12000	-37.36	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.41	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	0.009	0.15	-104.26	-55	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	0.15	30	-92.90	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	30	1000	-59.76	-35	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	1000	3000	-46.34	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	3000	12000	-44.14	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+M	Outer_Full	12000	20000	-49.66	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-104.18	-55	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	0.15	30	-91.27	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	30	1000	-59.37	-35	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	1000	3000	-47.85	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	3000	12000	-44.11	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.59	-25	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-103.28	-55	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	0.15	30	-93.70	-45	PASS	
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	30	1000	-59.26	-35	PASS	

DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	1000	3000	-47.83	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	3000	12000	-41.24	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.54	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	0.009	0.15	-105.82	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	0.15	30	-88.40	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	30	1000	-59.51	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	1000	3000	-47.16	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	3000	12000	-43.80	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+M	Outer_Full	12000	20000	-49.52	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-100.18	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-93.02	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-59.68	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-47.92	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-37.79	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-49.39	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-100.26	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-91.07	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-59.72	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-48.23	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-37.91	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-49.25	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-105.24	-55	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-91.12	-45	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-59.81	-35	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-44.64	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.90	-25	PASS
DC_66A_n4 1A	30	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-49.76	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-97.70	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-92.85	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-59.65	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-47.65	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-39.21	-25	PASS
DC_66A_n4	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-49.58	-25	PASS

1A										
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-100.12	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-91.25	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-60.02	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-48.02	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-35.03	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-49.44	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-105.38	-55	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-88.18	-45	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-59.11	-35	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-45.34	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-44.06	-25	PASS
DC_66A_n4 1A	30	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-49.47	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	0.009	0.15	-97.10	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	0.15	30	-93.17	-45	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	30	1000	-59.32	-35	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	1000	3000	-47.70	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	3000	12000	-39.68	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	12000	20000	-49.63	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	0.009	0.15	-99.74	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	0.15	30	-90.53	-45	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	30	1000	-59.77	-35	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	1000	3000	-47.43	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	3000	12000	-43.98	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	12000	20000	-49.54	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	0.009	0.15	-104.98	-55	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	0.15	30	-91.61	-45	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	30	1000	-59.27	-35	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	1000	3000	-45.25	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	3000	12000	-44.16	-25	PASS
DC_66A_n4 1A	30	5+20	CP-64QAM	M+M	Outer_Full	12000	20000	-49.55	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	0.009	0.15	-104.47	-55	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	0.15	30	-92.01	-45	PASS

DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	30	1000	-59.50	-35	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	1000	3000	-47.20	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	3000	12000	-42.84	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	12000	20000	-49.34	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	0.009	0.15	-104.78	-55	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	0.15	30	-93.64	-45	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	30	1000	-59.64	-35	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	1000	3000	-47.76	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	3000	12000	-43.96	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	12000	20000	-49.57	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	0.009	0.15	-103.50	-55	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	0.15	30	-93.32	-45	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	30	1000	-59.87	-35	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	1000	3000	-47.84	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	3000	12000	-44.05	-25	PASS
DC_66A_n4 1A	30	5+20	CP-256QAM	M+M	Outer_Full	12000	20000	-49.34	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.009	0.15	-105.33	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	0.15	30	-93.00	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	30	1000	-59.89	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	1000	3000	-48.27	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	3000	12000	-39.68	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	12000	20000	-49.61	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.009	0.15	-104.79	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	0.15	30	-90.20	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	30	1000	-59.70	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	1000	3000	-48.42	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	3000	12000	-38.57	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	12000	20000	-49.52	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	0.009	0.15	-105.20	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	0.15	30	-91.40	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	30	1000	-59.90	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	1000	3000	-45.11	-25	PASS
DC_66A_n4	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	3000	12000	-43.02	-25	PASS

1A										
DC_66A_n4 1A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	12000	20000	-49.58	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-105.15	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-89.67	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-59.65	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.78	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-38.23	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-49.52	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-106.01	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-91.25	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-59.69	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-47.76	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-39.49	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-49.64	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-106.39	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-87.22	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-59.04	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-44.30	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-44.03	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-49.20	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-104.21	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-88.35	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-59.36	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-48.37	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-39.31	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-49.39	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-105.67	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-92.10	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-59.84	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-47.76	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-40.09	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-49.49	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-105.89	-55	PASS

DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-92.11	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-59.62	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-43.74	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	3000	12000	-43.89	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	12000	20000	-49.47	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	0.009	0.15	-104.52	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	0.15	30	-91.34	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	30	1000	-59.55	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	1000	3000	-47.63	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	3000	12000	-41.54	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	12000	20000	-49.49	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	0.009	0.15	-103.94	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	0.15	30	-93.72	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	30	1000	-58.69	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	1000	3000	-47.27	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	3000	12000	-40.87	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	12000	20000	-49.68	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	0.009	0.15	-105.66	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	0.15	30	-88.26	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	30	1000	-59.38	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	1000	3000	-44.15	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	3000	12000	-43.77	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-64QAM	M+H	Outer_Full	12000	20000	-49.75	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	0.009	0.15	-105.23	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	0.15	30	-89.15	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	30	1000	-59.69	-35	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	1000	3000	-48.11	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	3000	12000	-43.23	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	12000	20000	-49.59	-25	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	0.009	0.15	-105.87	-55	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	0.15	30	-92.35	-45	PASS
DC_66A_n4 1A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	30	1000	-59.15	-35	PASS
DC_66A_n4	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	1000	3000	-47.77	-25	PASS