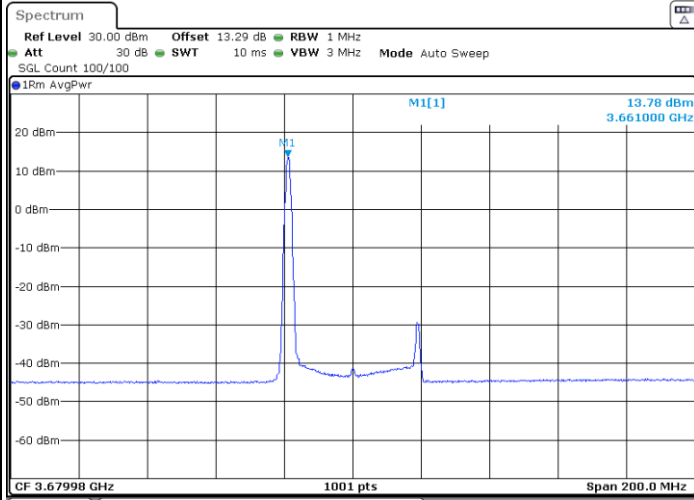




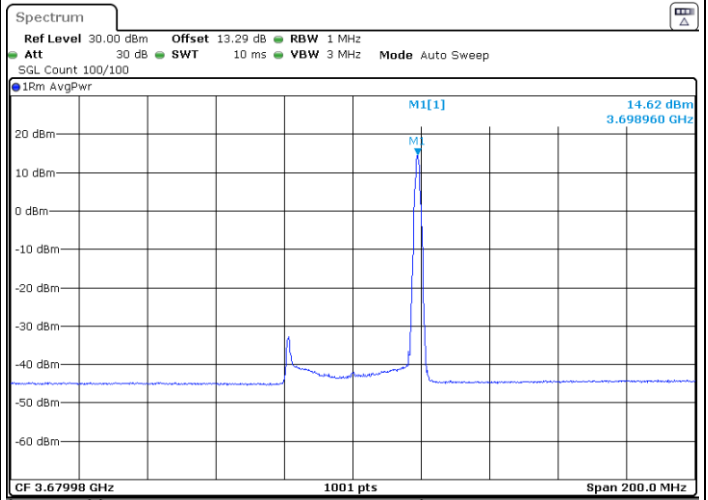
FR1 Part 96 n48 / 40MHz

16QAM

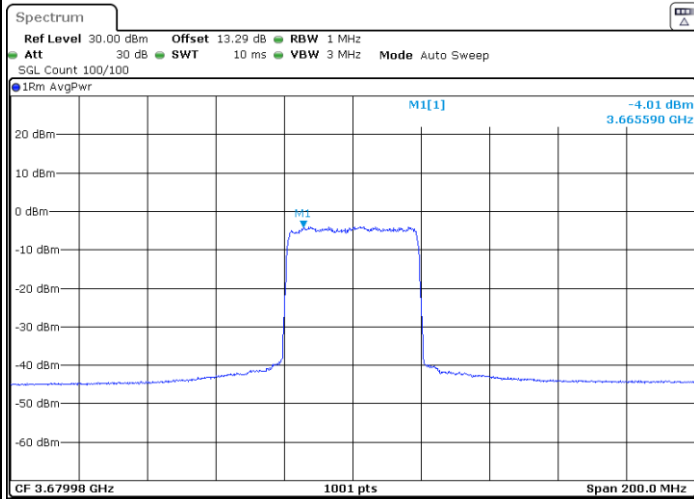
Highest Channel / 1RB0



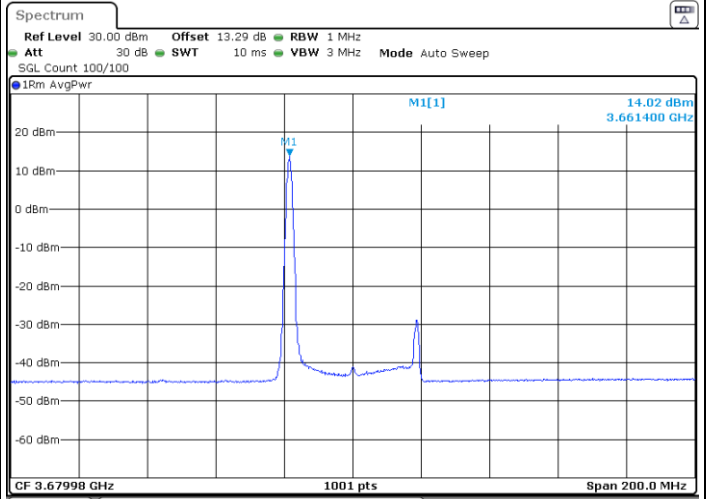
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1



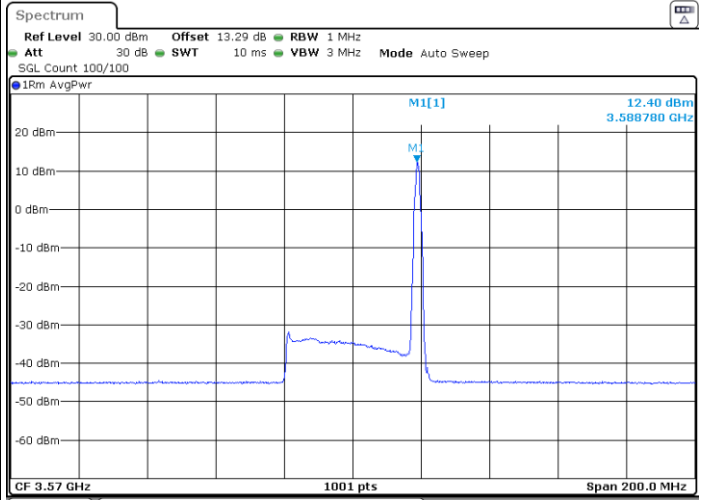
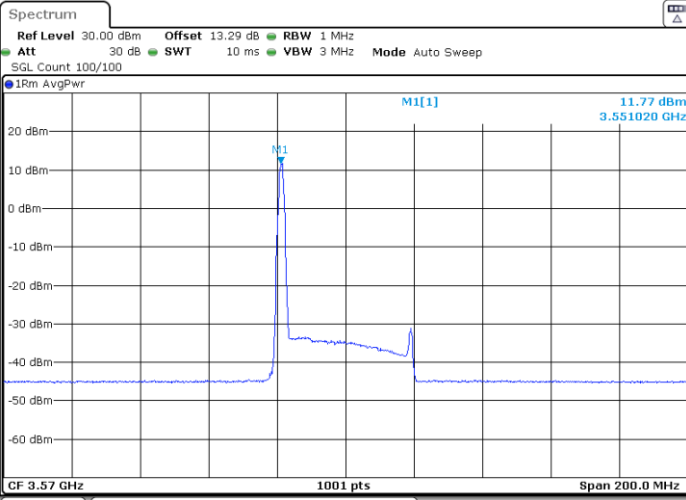


FR1 Part 96 n48 / 40MHz

64QAM

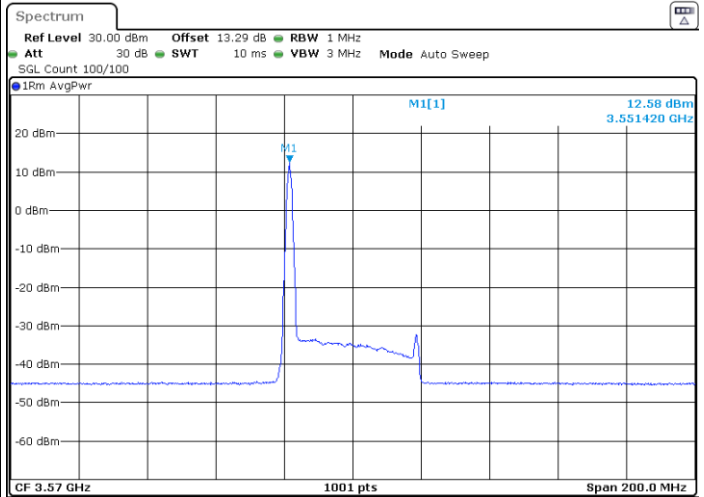
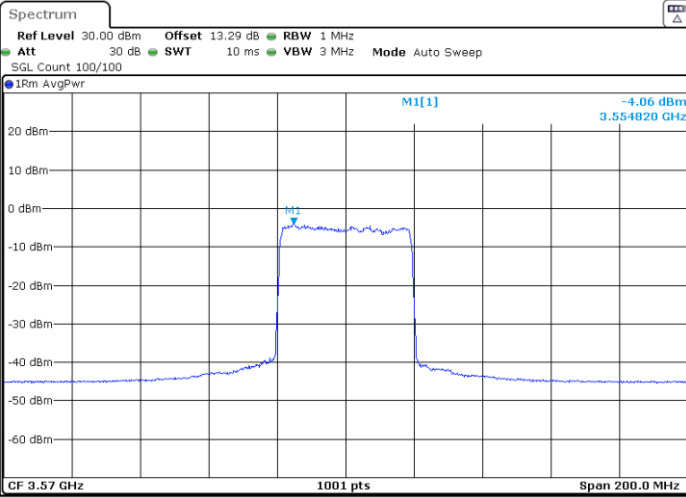
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1

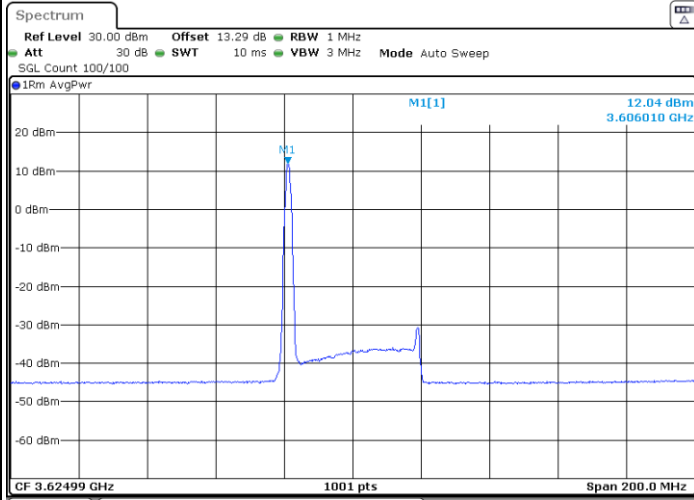




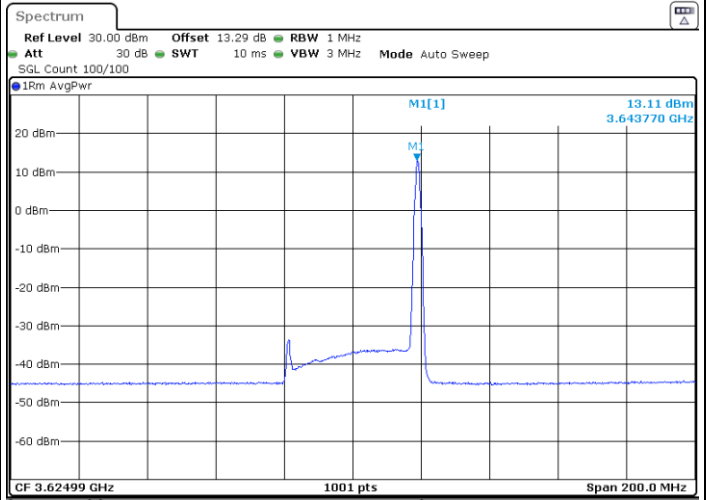
FR1 Part 96 n48 / 40MHz

64QAM

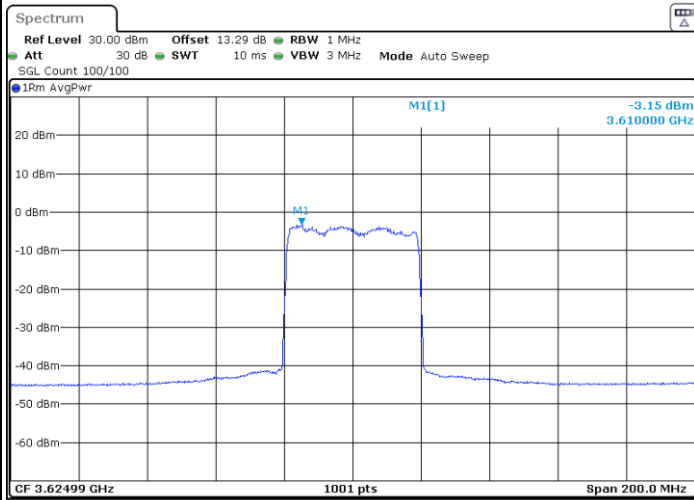
Middle Channel / 1RB0



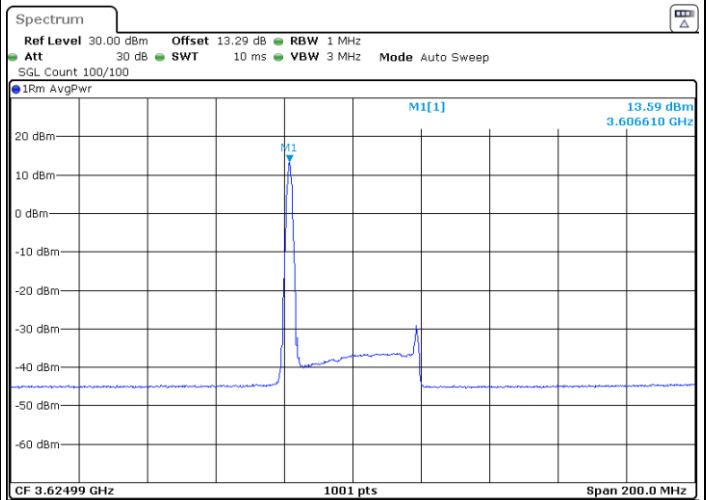
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1



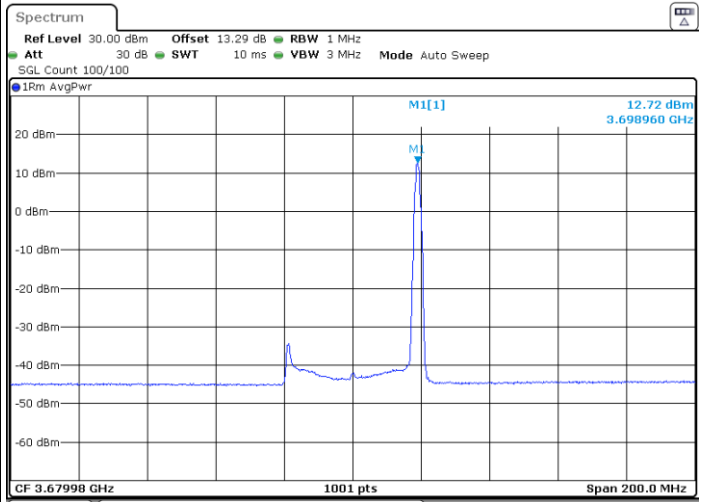
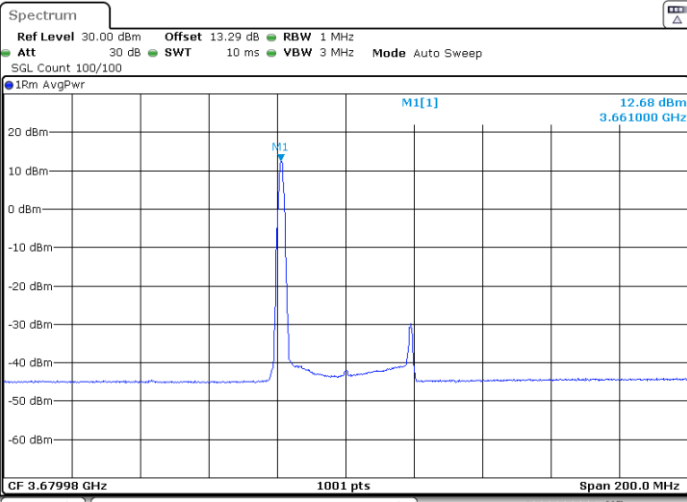


FR1 Part 96 n48 / 40MHz

64QAM

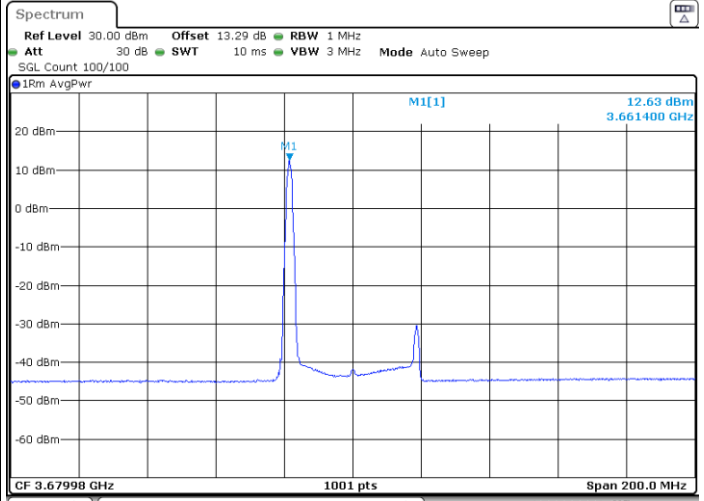
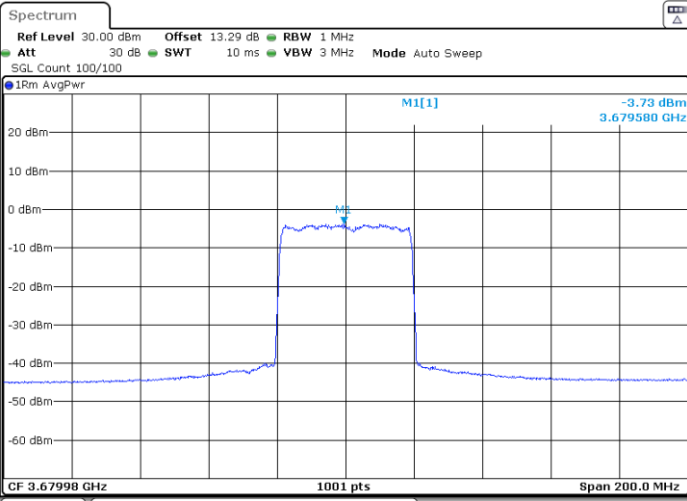
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1



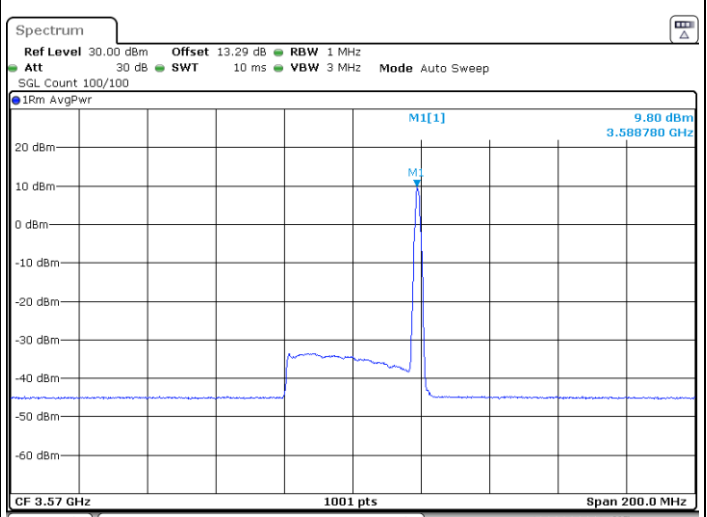
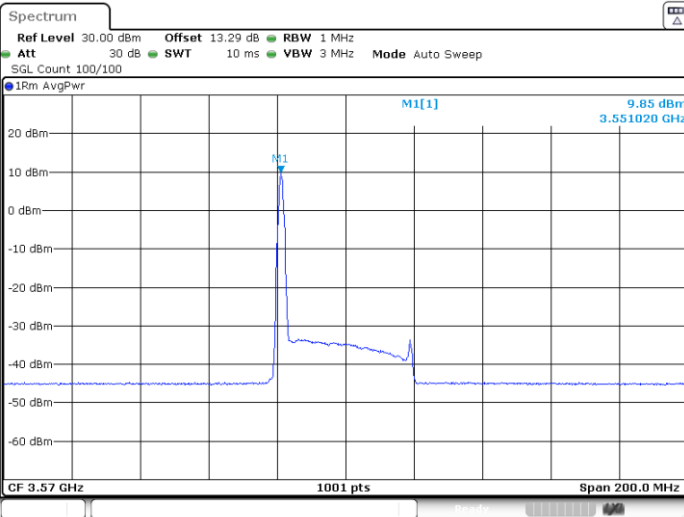


FR1 Part 96 n48 / 40MHz

256QAM

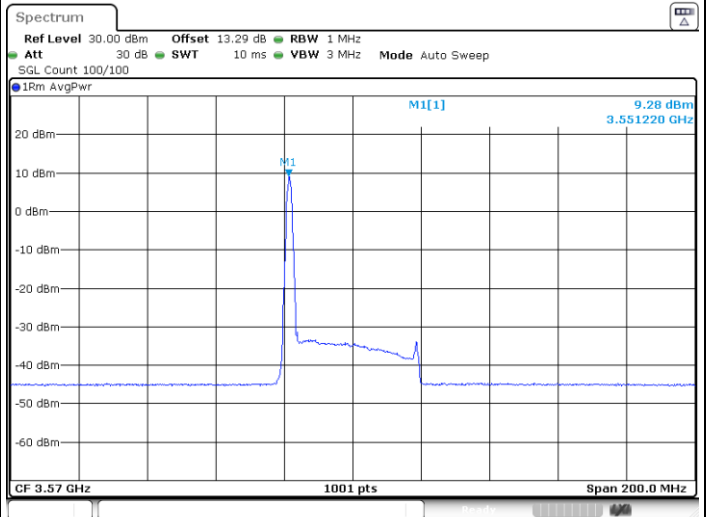
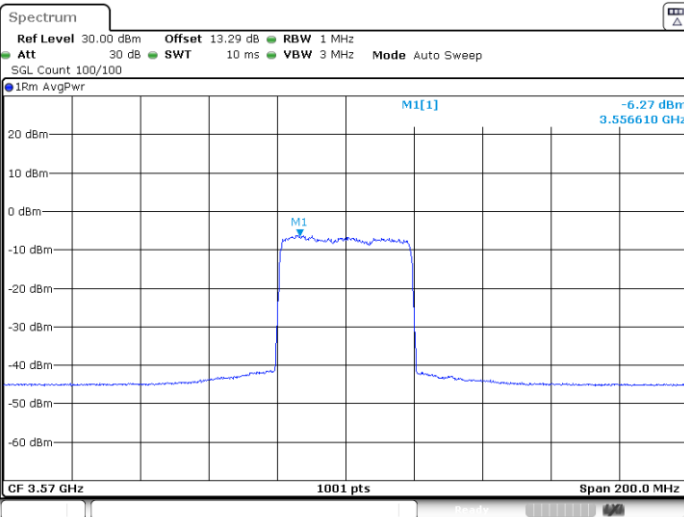
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1



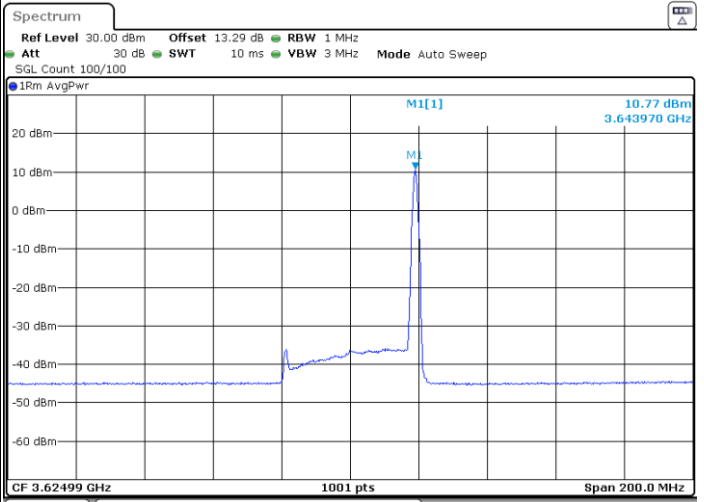
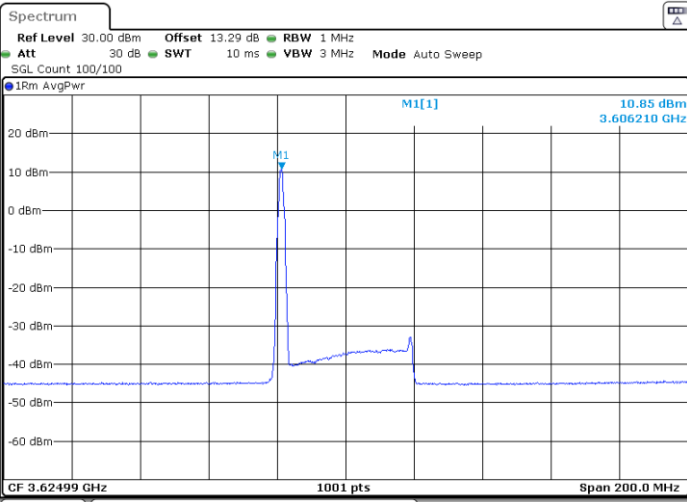


FR1 Part 96 n48 / 40MHz

256QAM

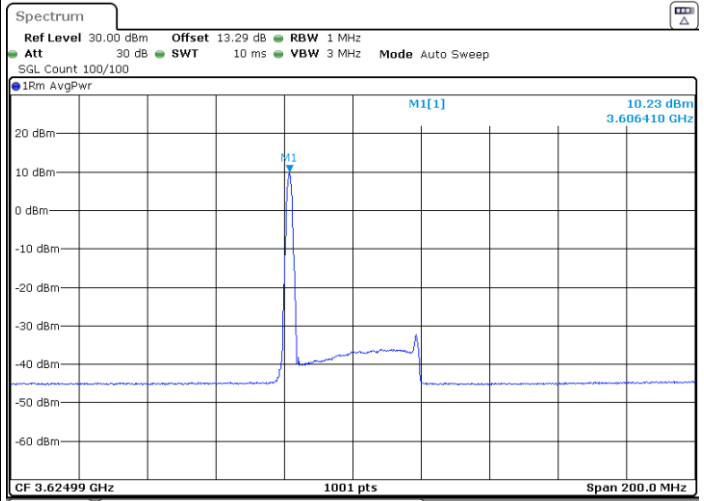
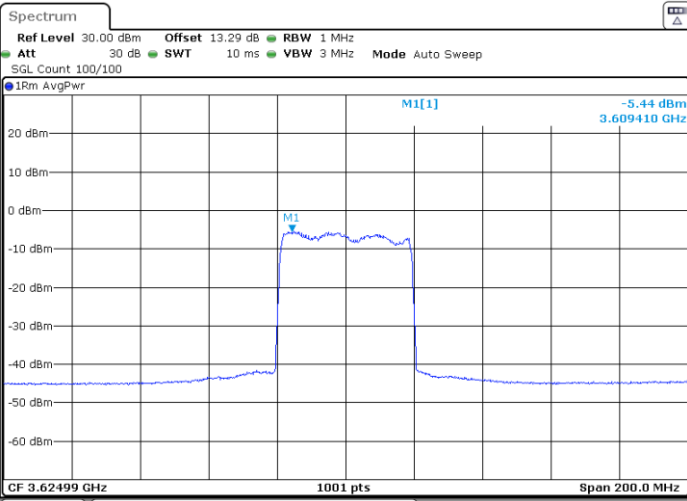
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



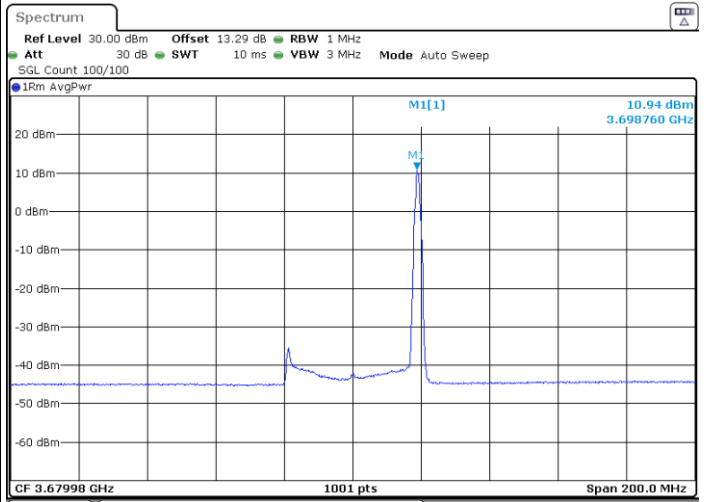
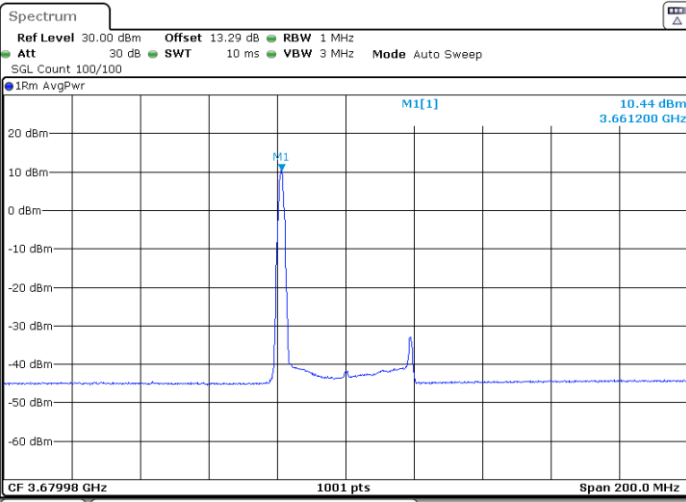


FR1 Part 96 n48 / 40MHz

256QAM

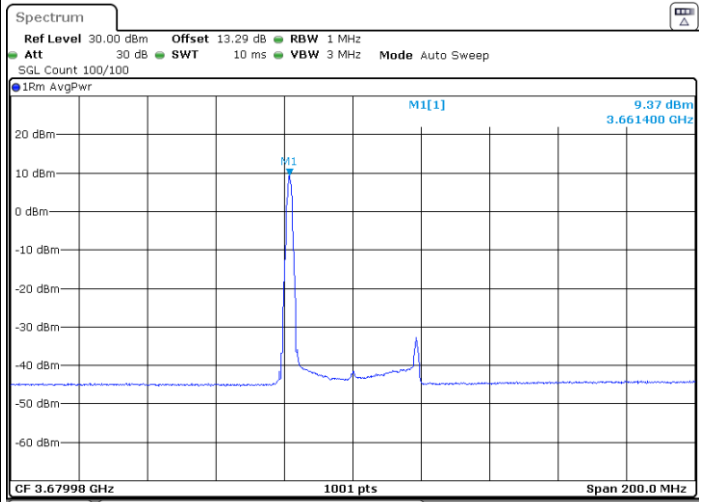
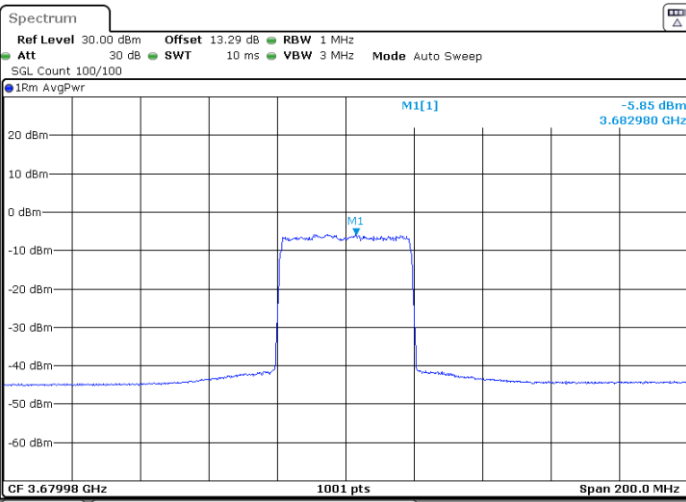
Highest Channel / 1RB0

Highest Channel / 1RBmax



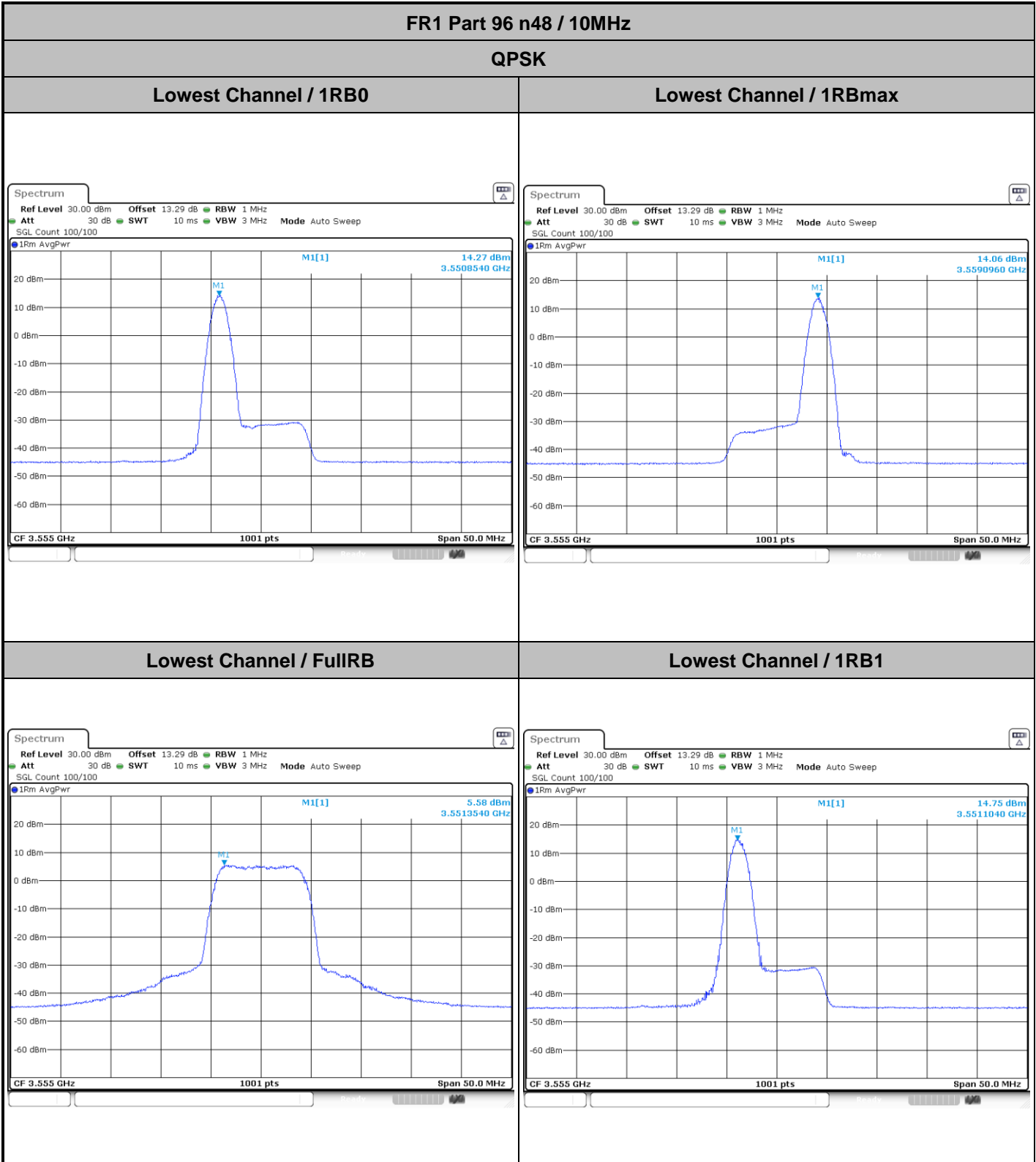
Highest Channel / FullRB

Highest Channel / 1RB1





Conducted PSD – ANT3



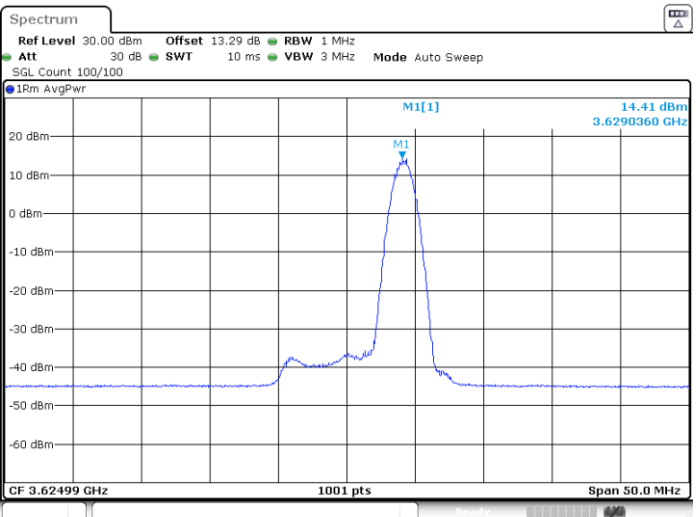
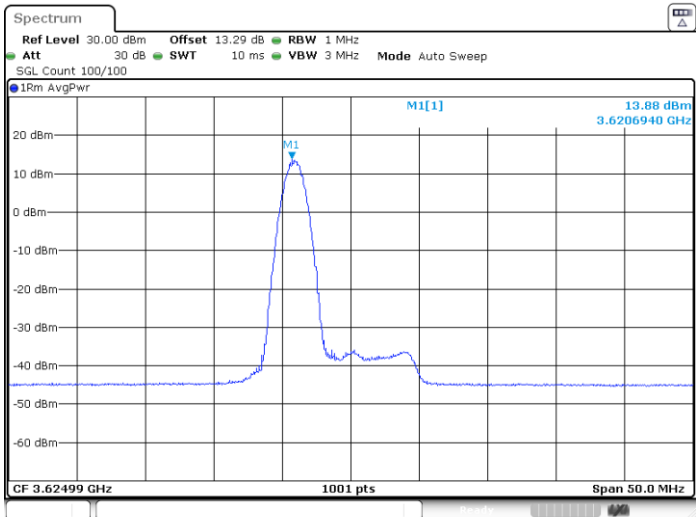


FR1 Part 96 n48 / 10MHz

QPSK

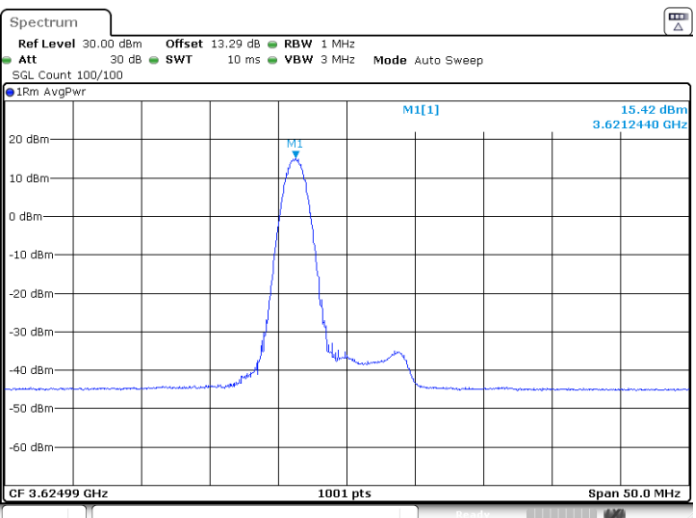
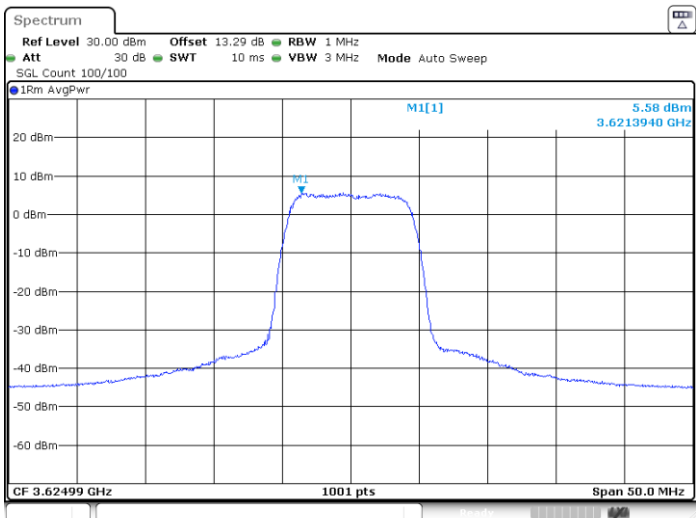
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



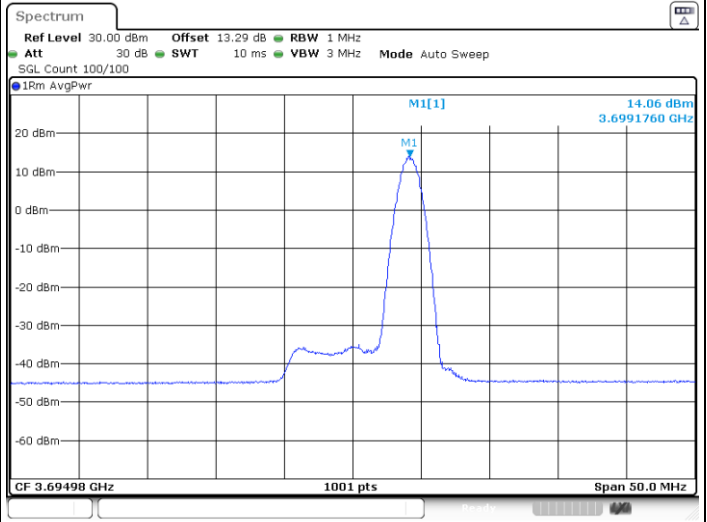
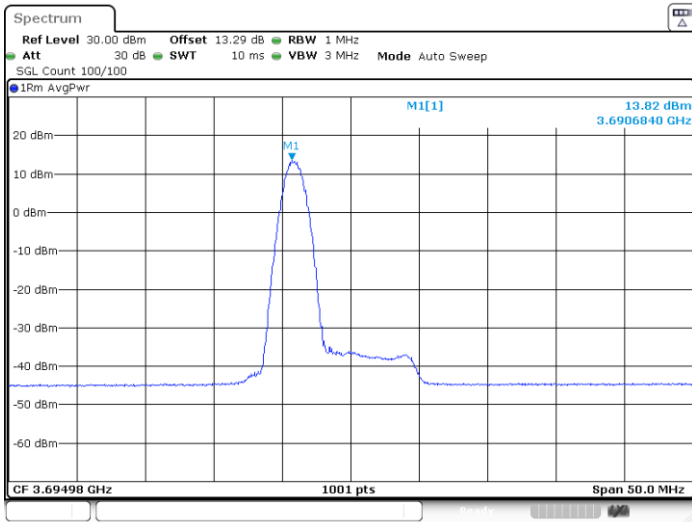


FR1 Part 96 n48 / 10MHz

QPSK

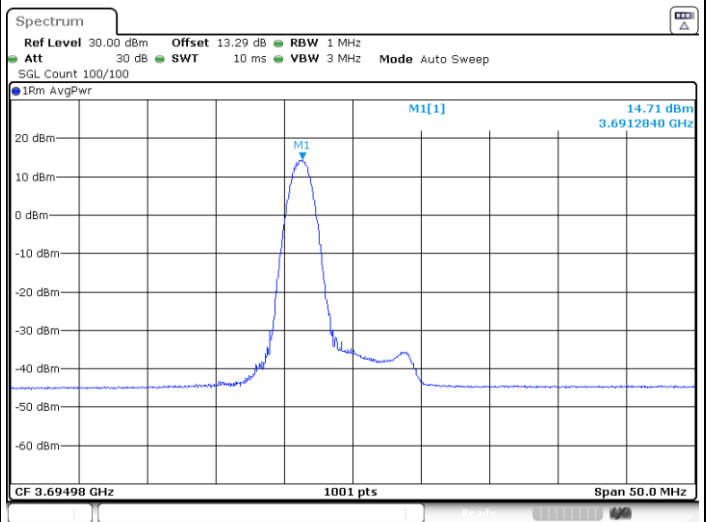
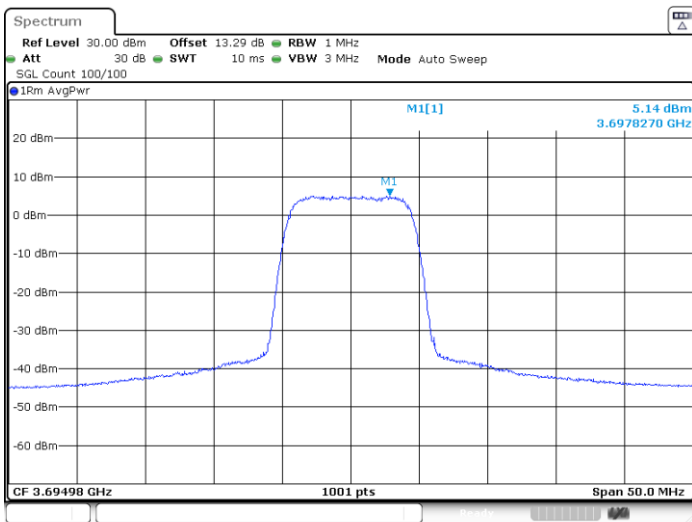
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullIRB

Highest Channel / 1RB1

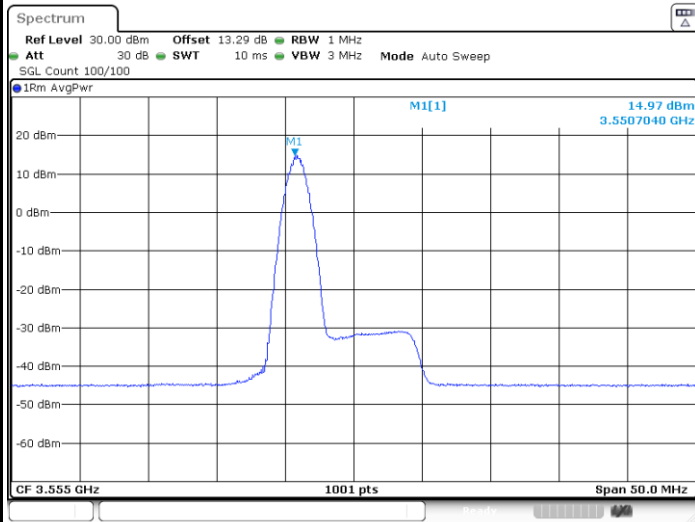




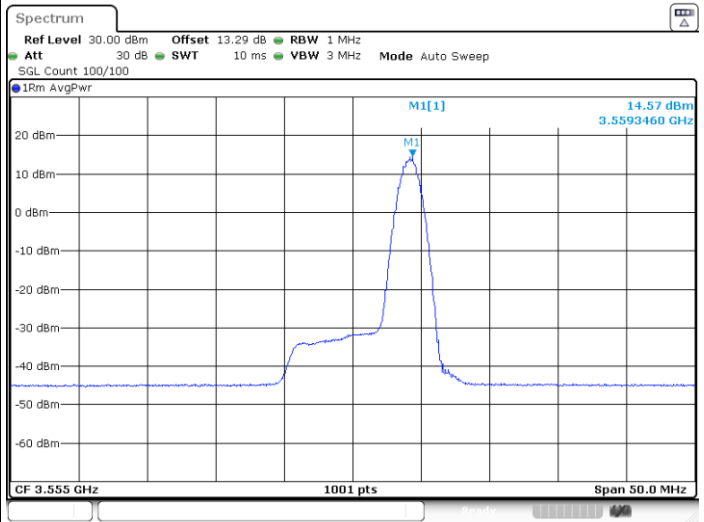
FR1 Part 96 n48 / 10MHz

16QAM

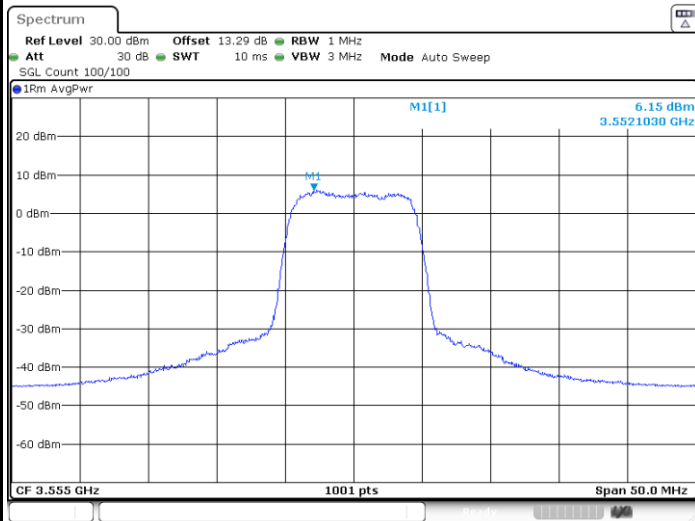
Lowest Channel / 1RB0



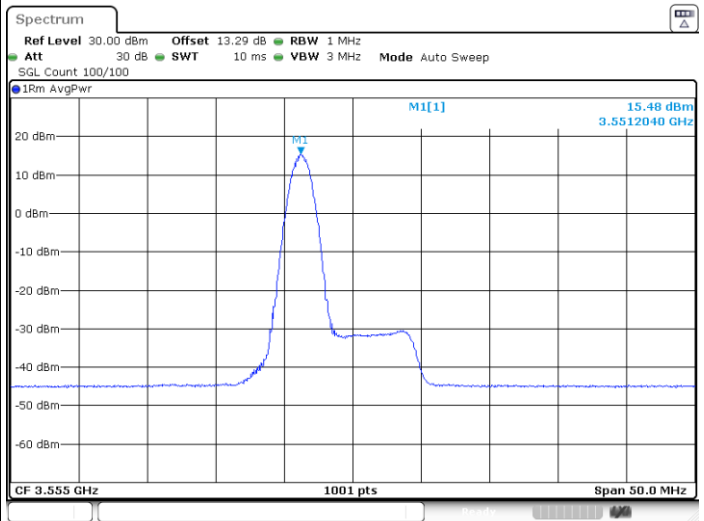
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1



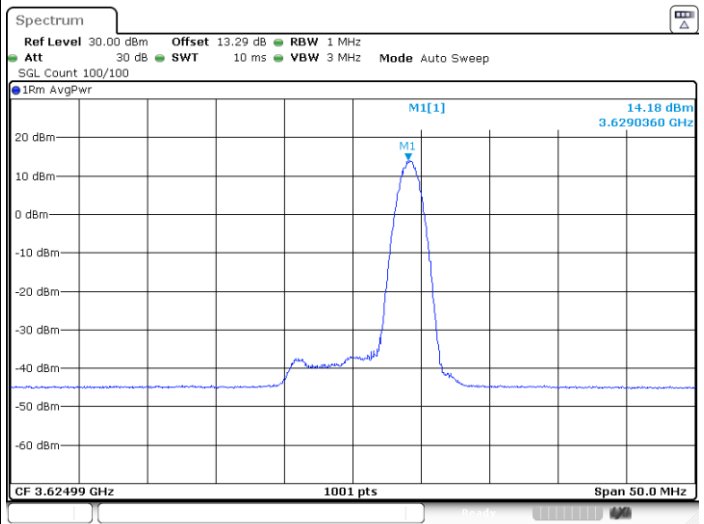
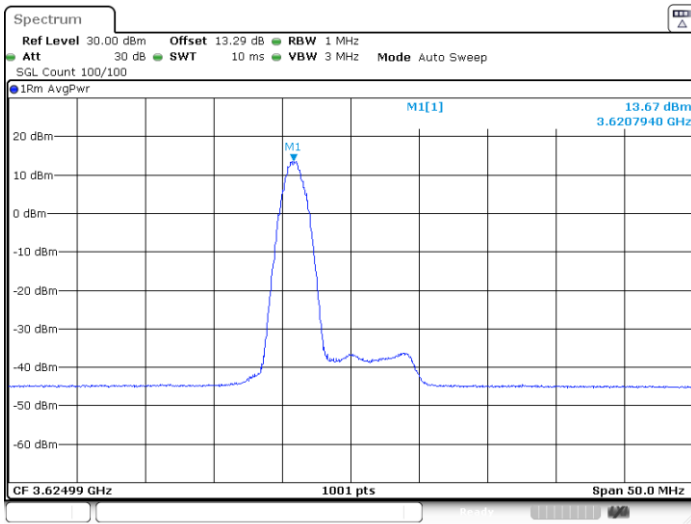


FR1 Part 96 n48 / 10MHz

16QAM

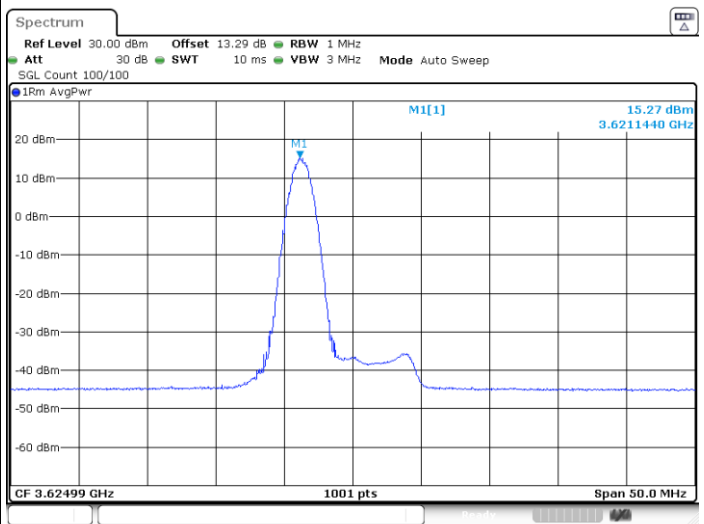
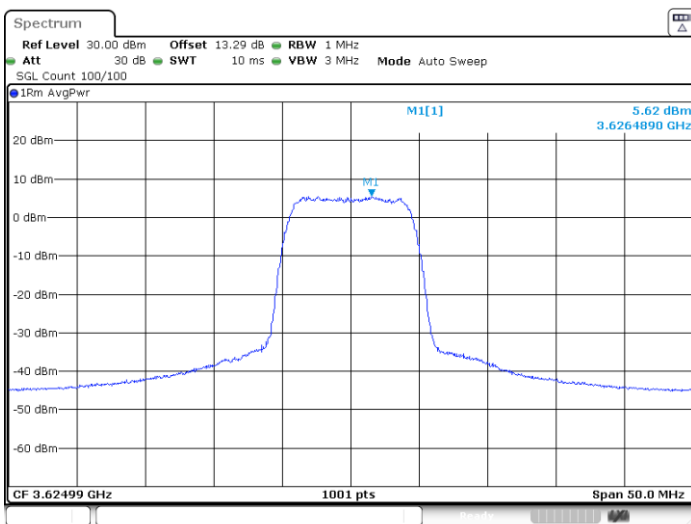
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullIRB

Middle Channel / 1RB1

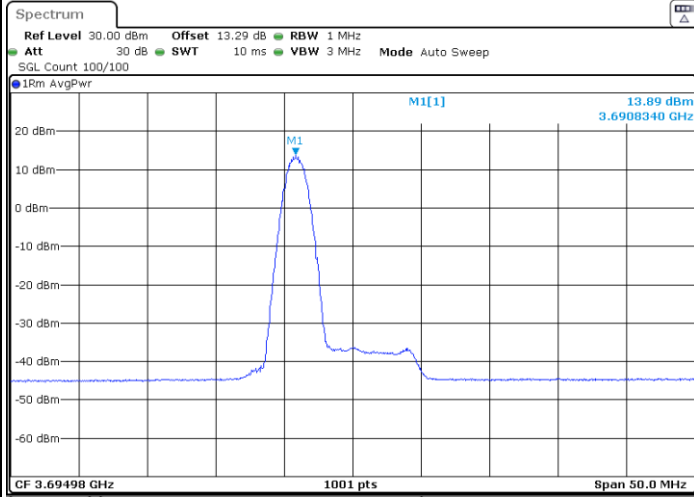




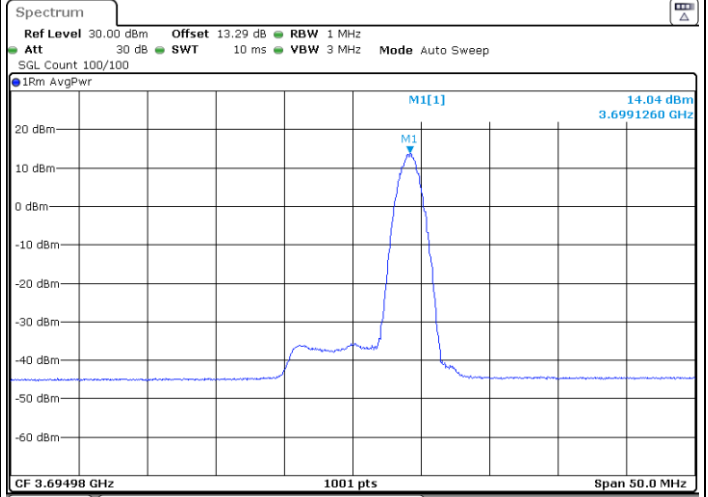
FR1 Part 96 n48 / 10MHz

16QAM

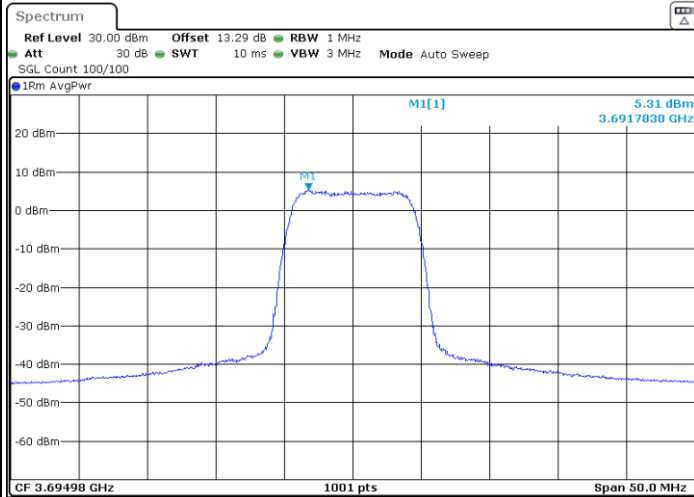
Highest Channel / 1RB0



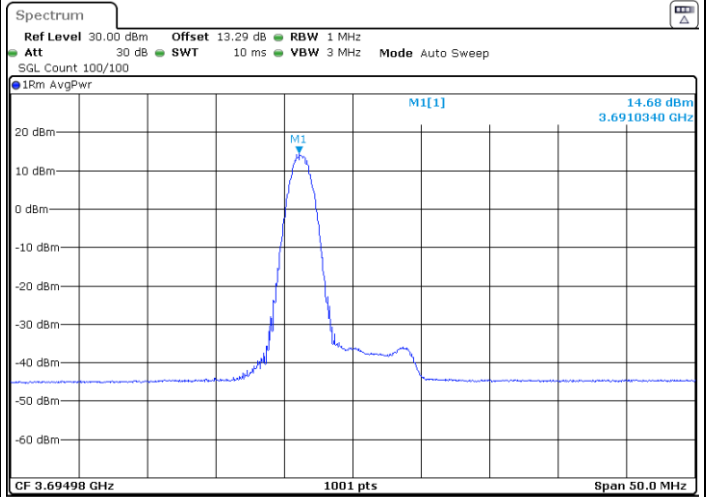
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1



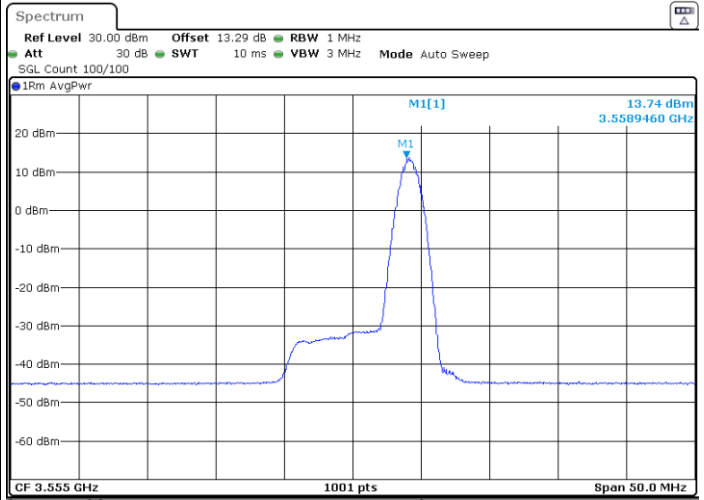
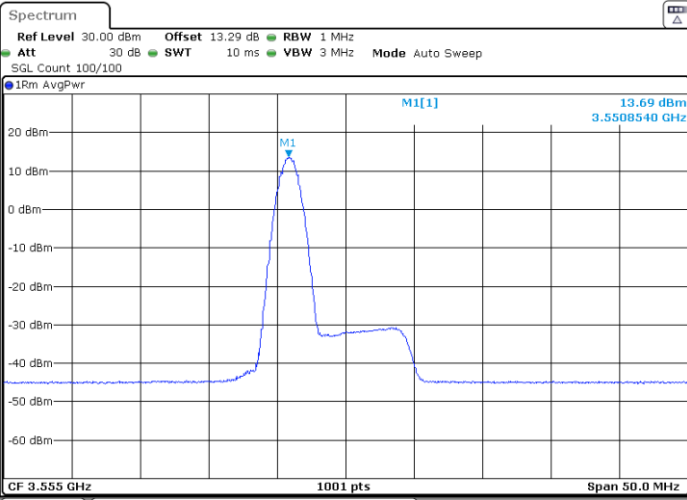


FR1 Part 96 n48 / 10MHz

64QAM

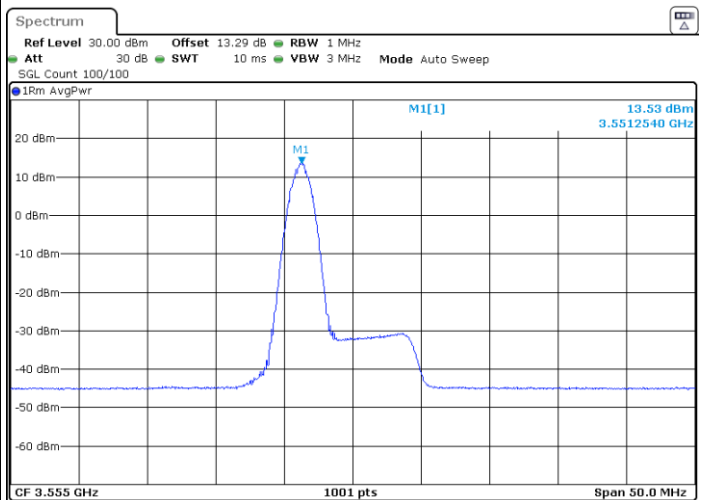
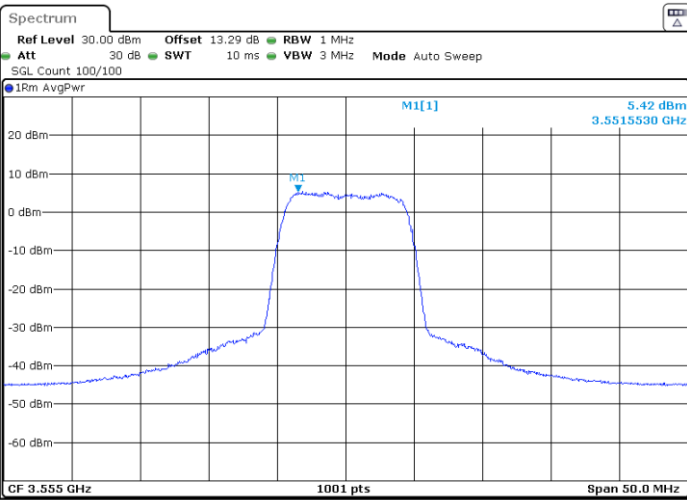
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1

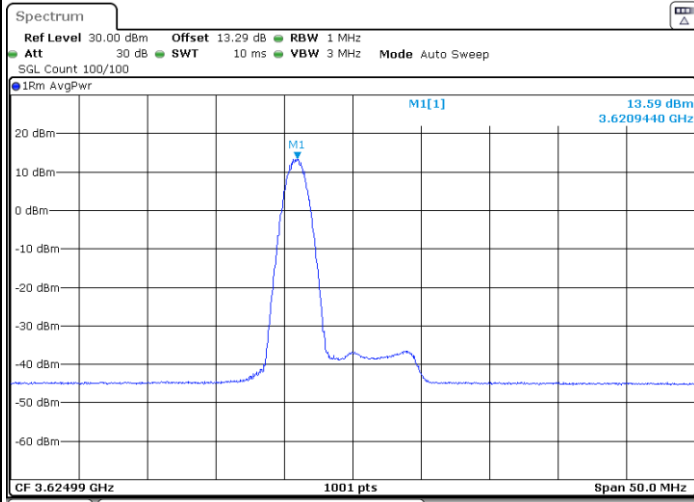




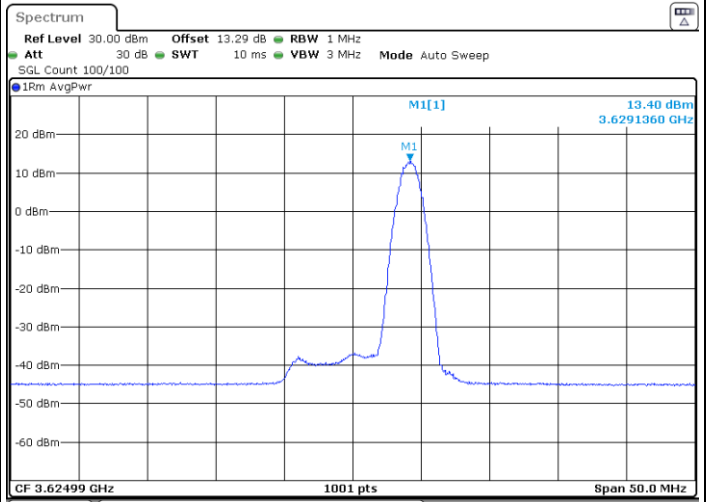
FR1 Part 96 n48 / 10MHz

64QAM

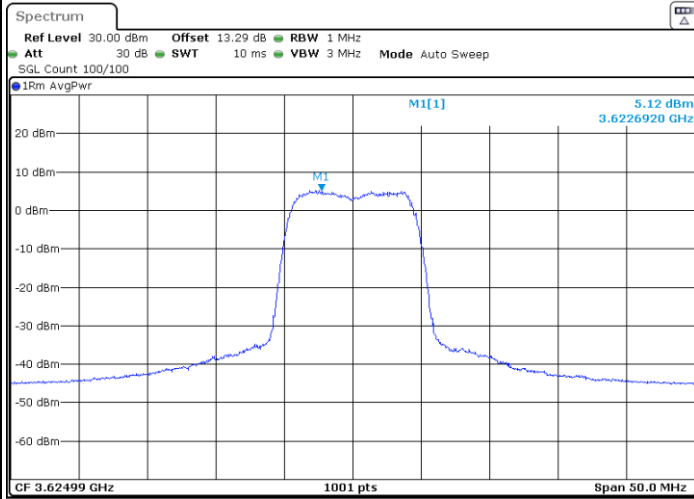
Middle Channel / 1RB0



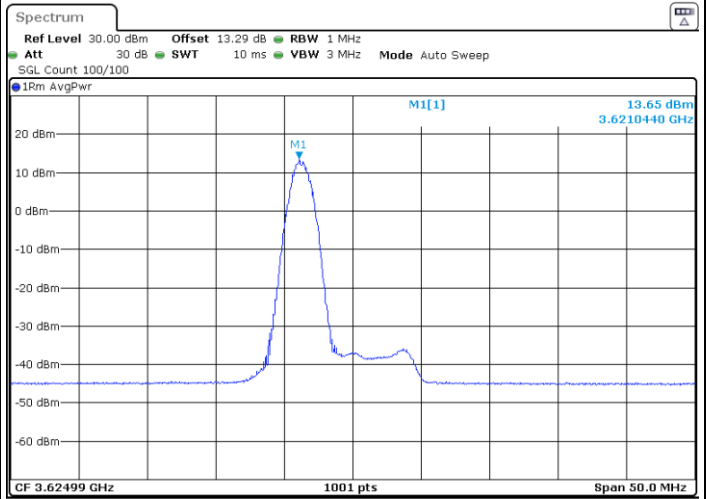
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1



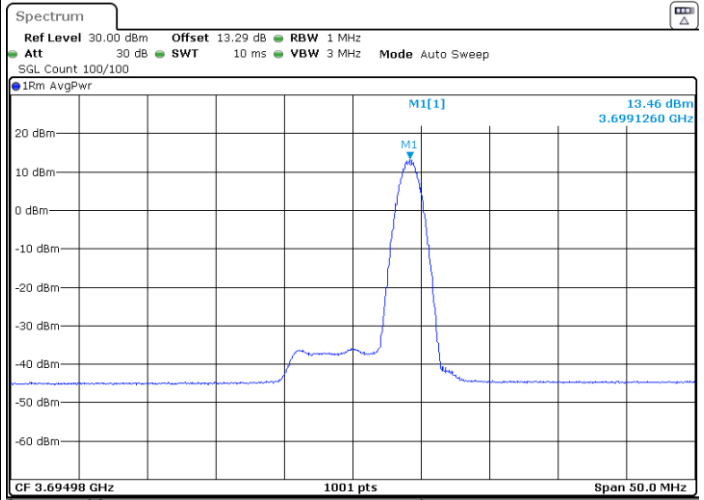
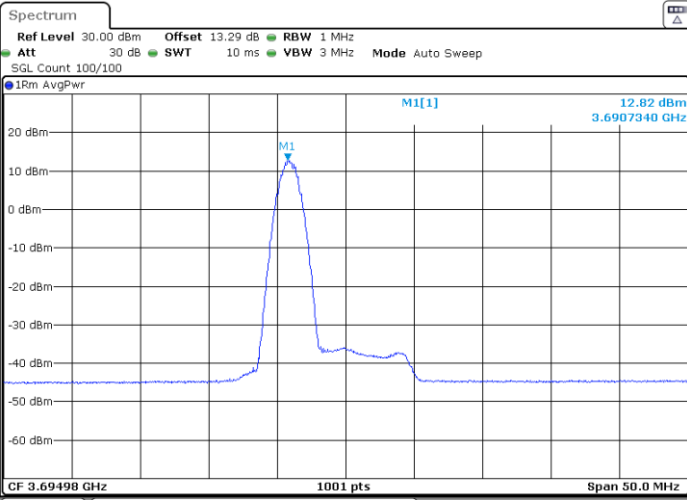


FR1 Part 96 n48 / 10MHz

64QAM

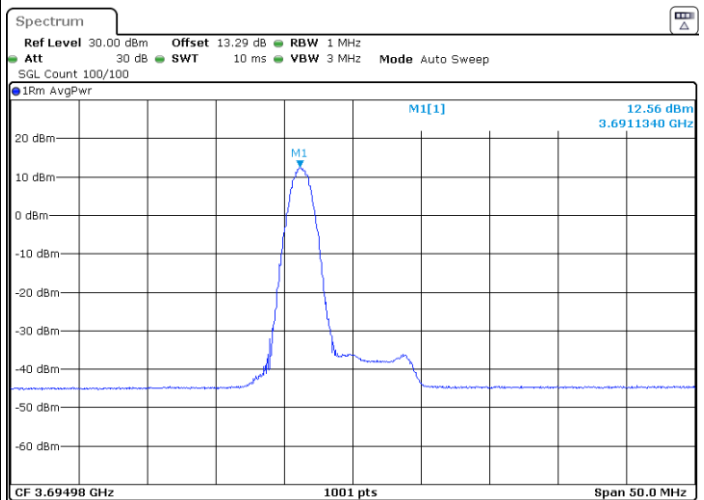
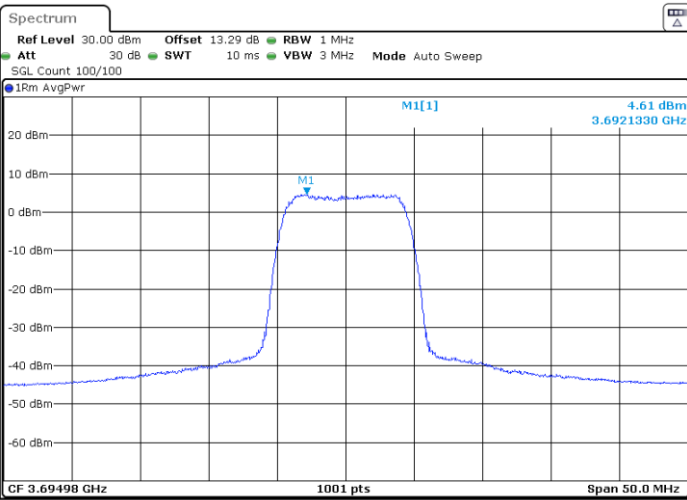
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullIRB

Highest Channel / 1RB1



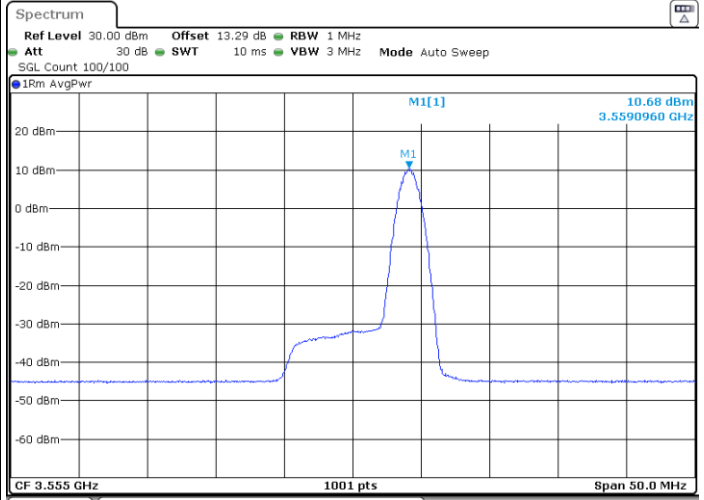
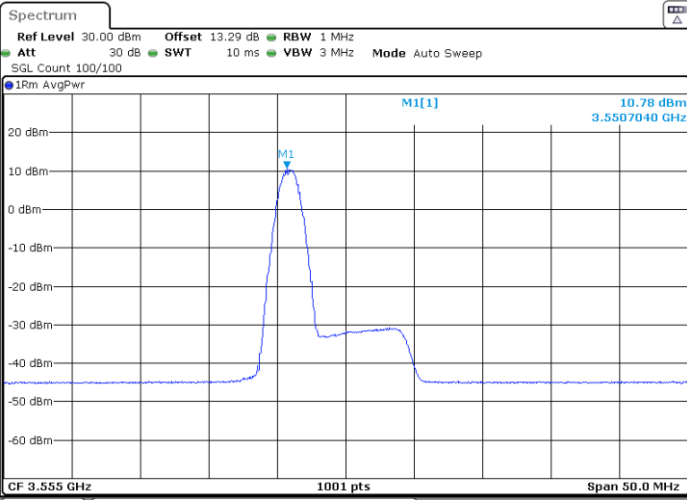


FR1 Part 96 n48 / 10MHz

256QAM

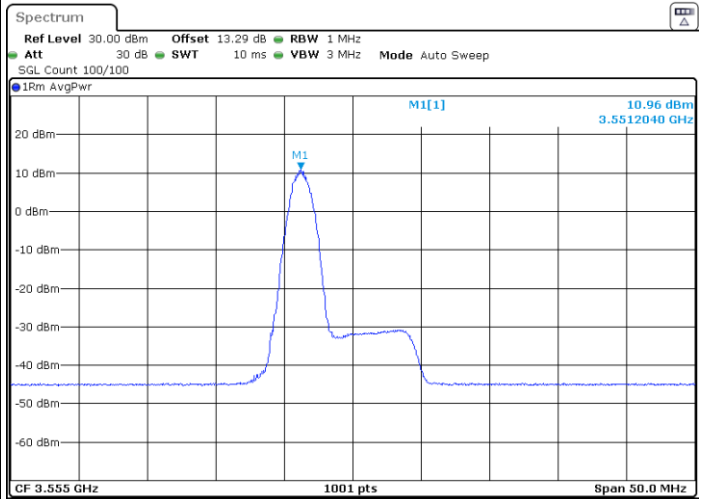
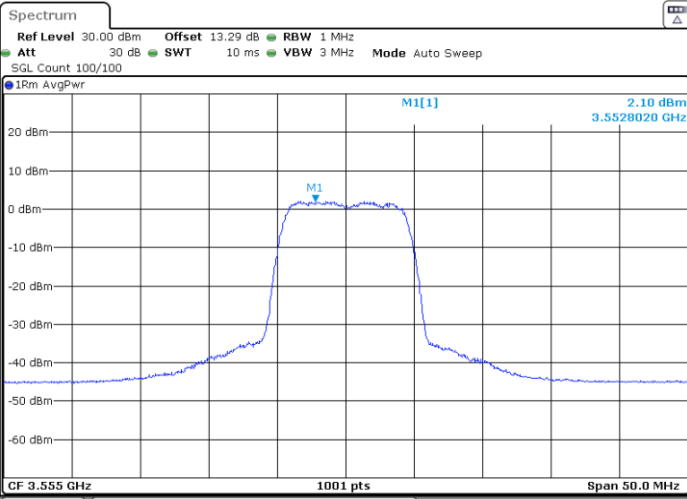
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1



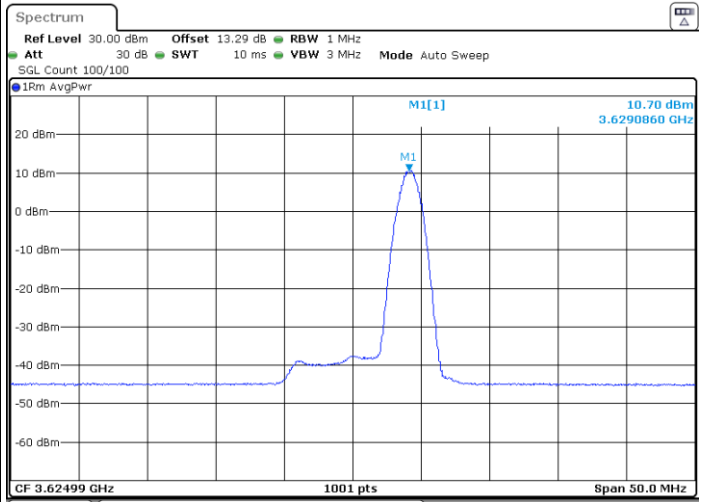
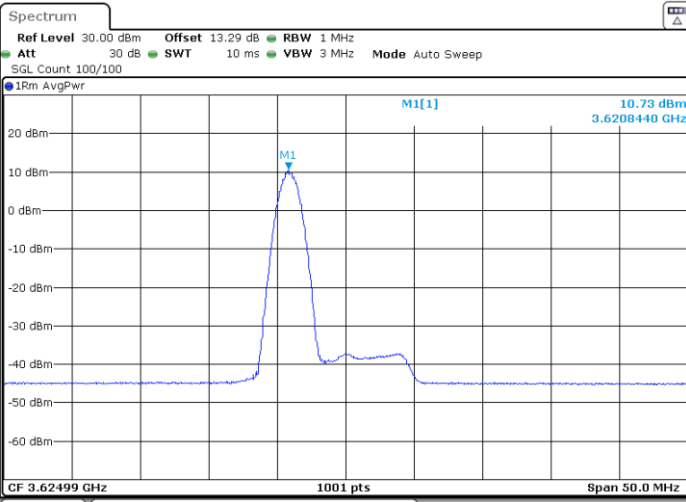


FR1 Part 96 n48 / 10MHz

256QAM

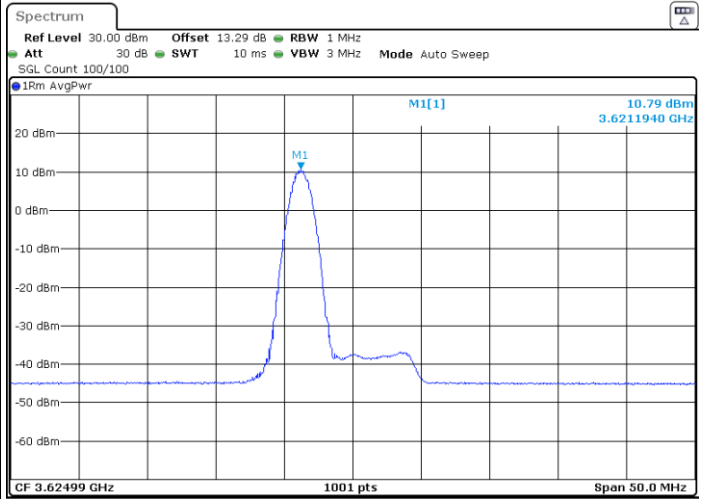
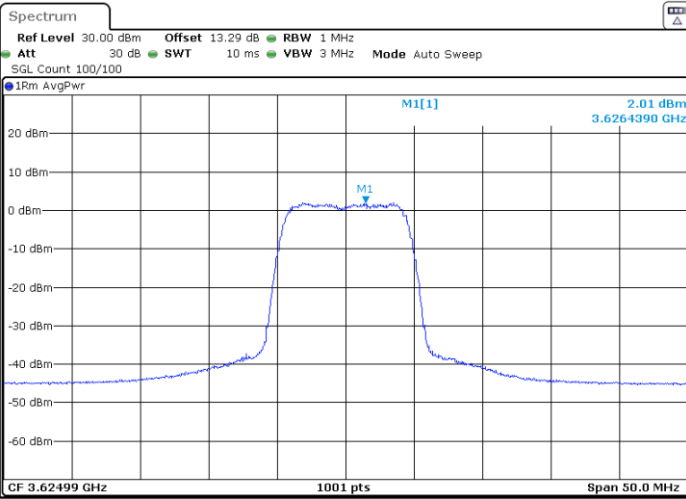
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



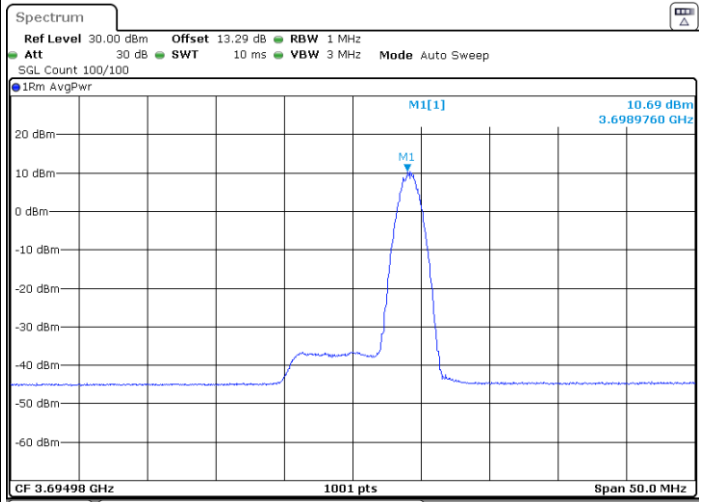
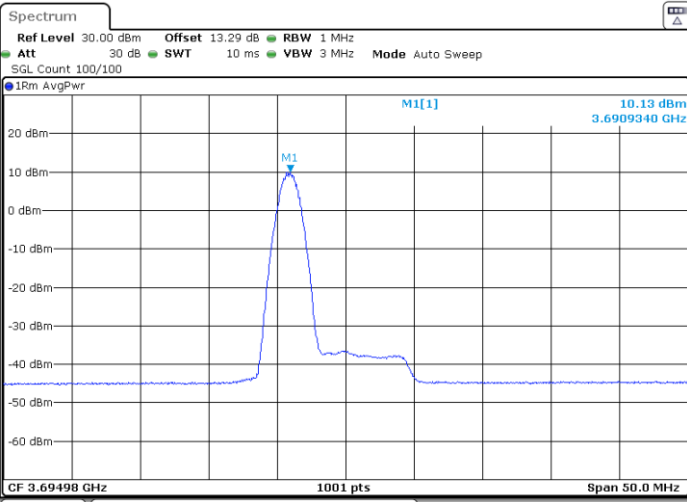


FR1 Part 96 n48 / 10MHz

256QAM

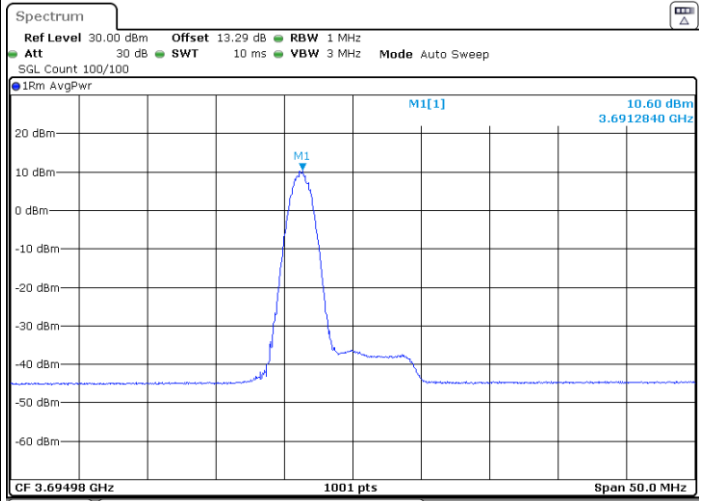
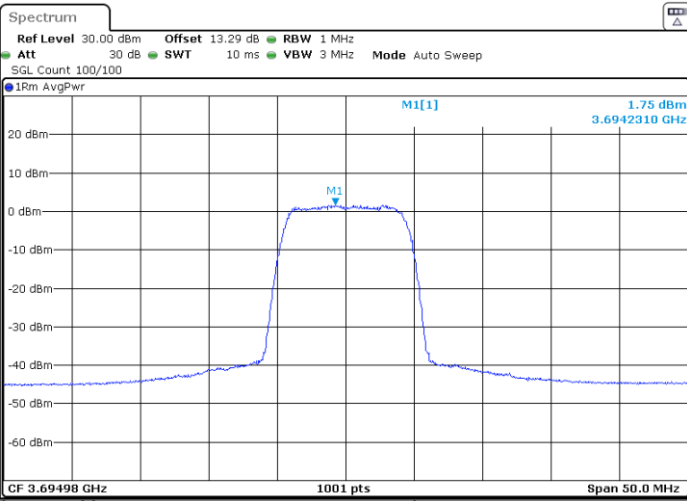
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1



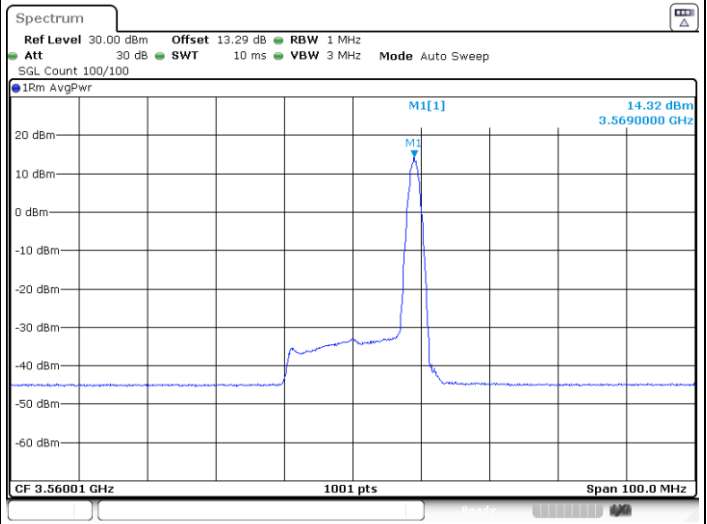
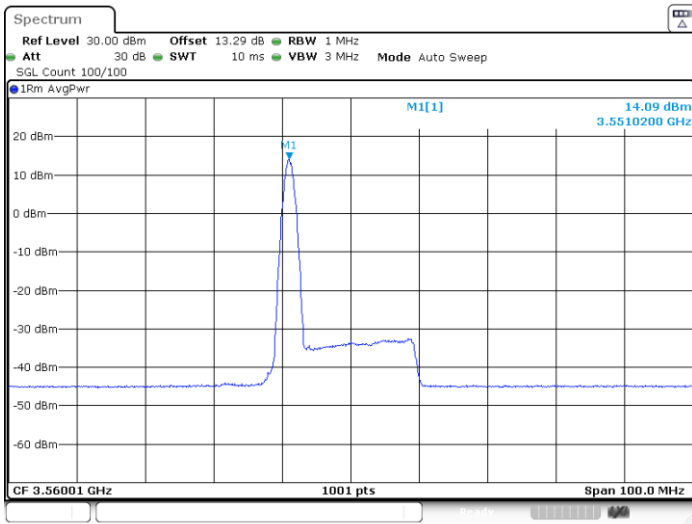


FR1 Part 96 n48 / 20MHz

QPSK

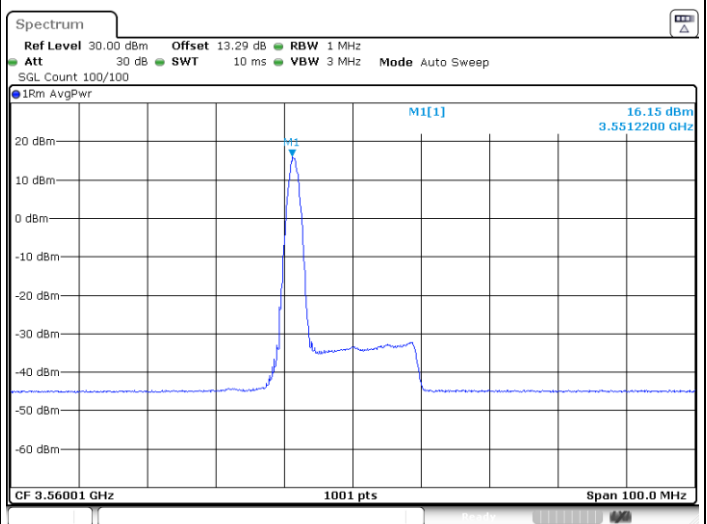
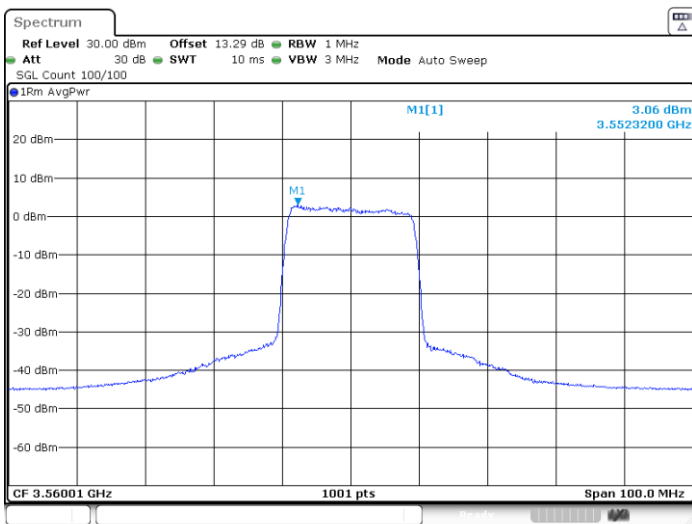
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullIRB

Lowest Channel / 1RB1



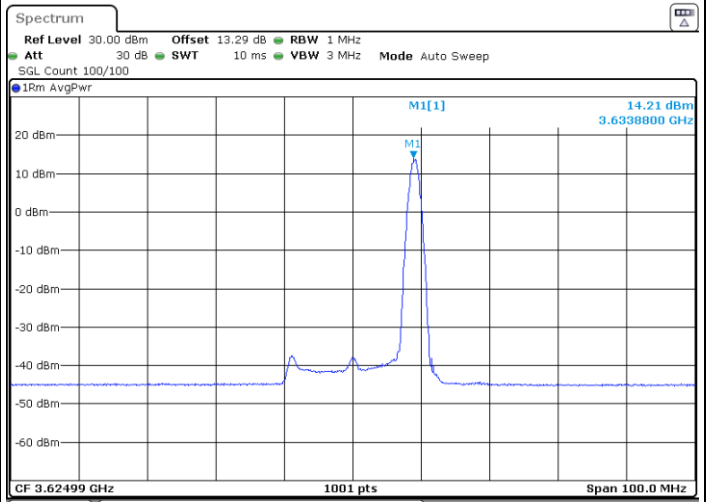
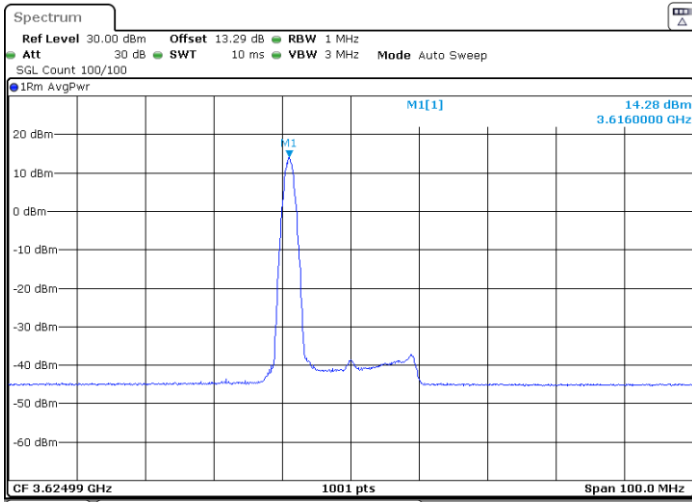


FR1 Part 96 n48 / 20MHz

QPSK

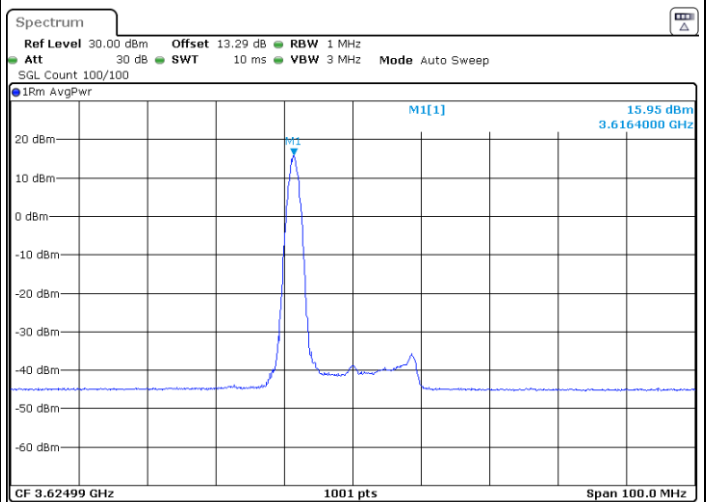
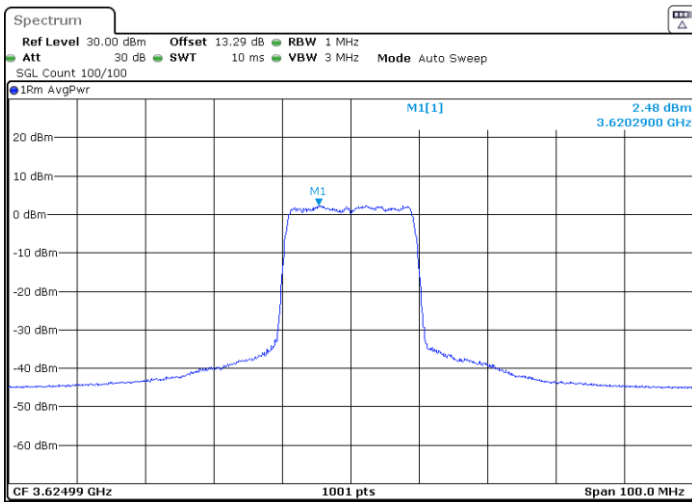
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



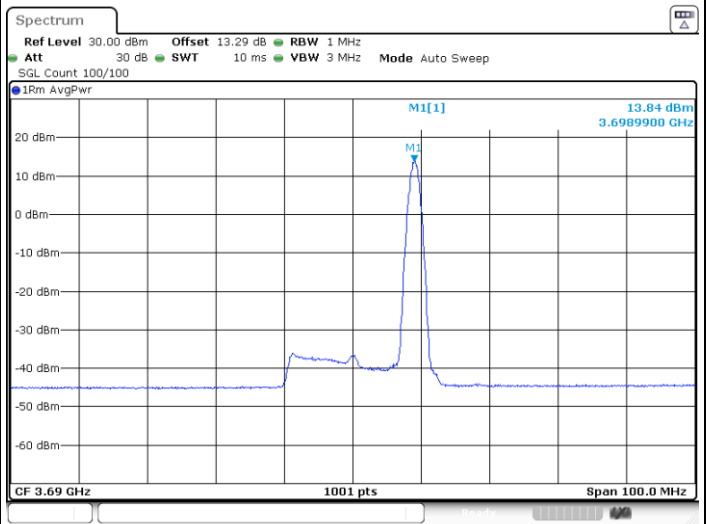
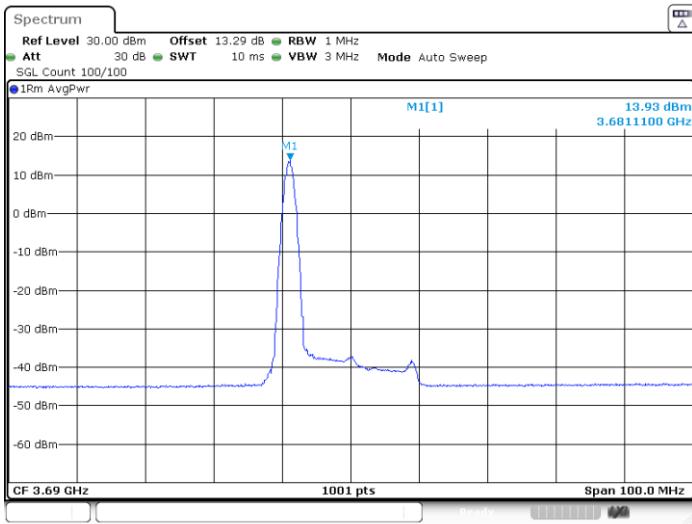


FR1 Part 96 n48 / 20MHz

QPSK

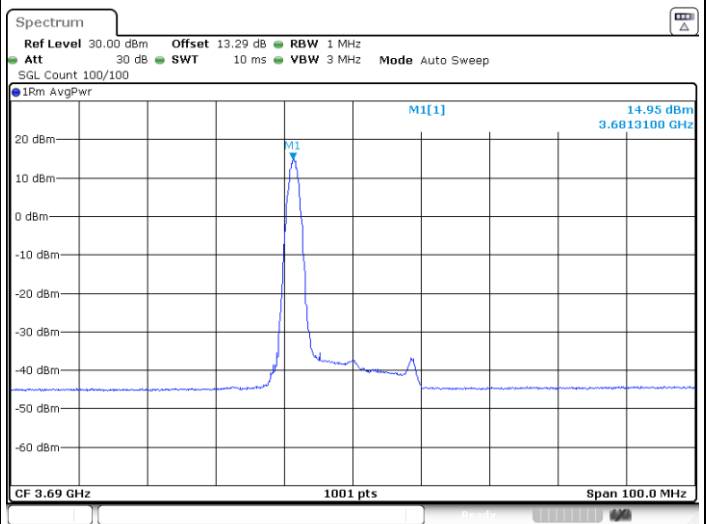
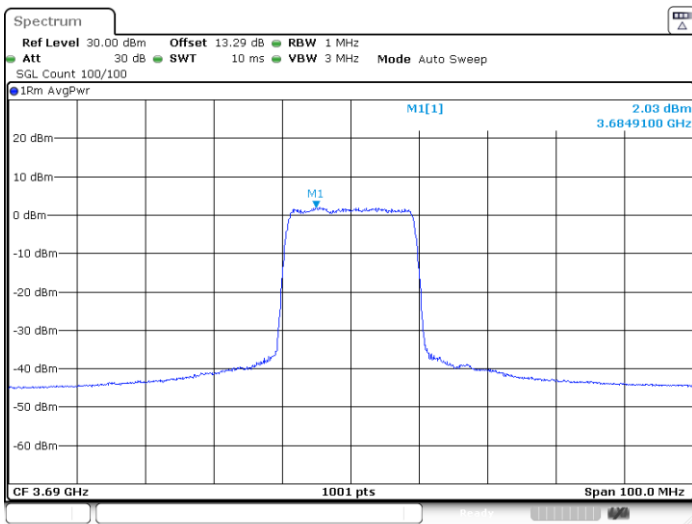
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullIRB

Highest Channel / 1RB1



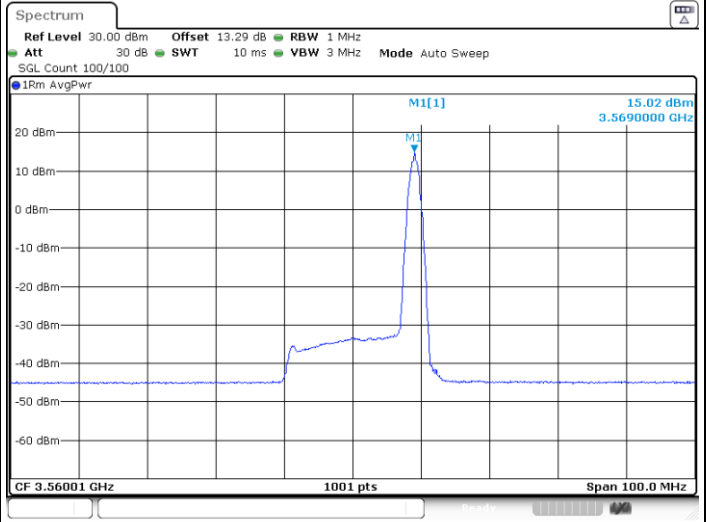
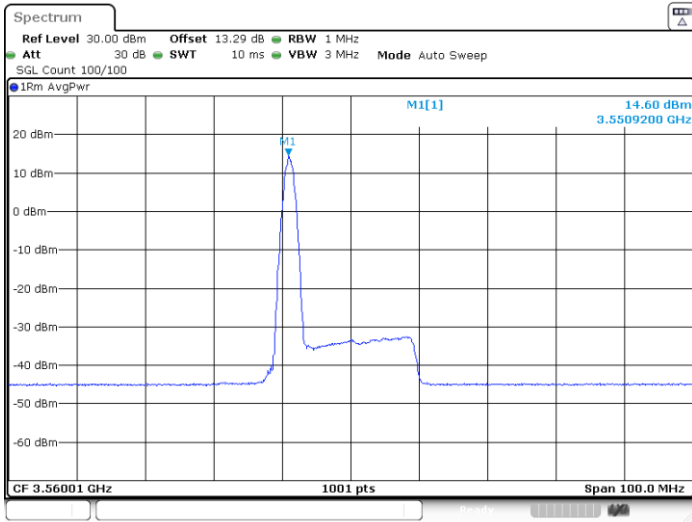


FR1 Part 96 n48 / 20MHz

16QAM

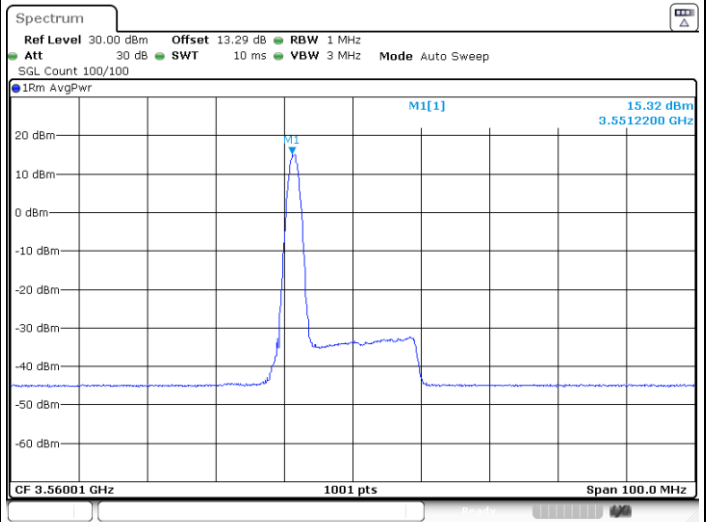
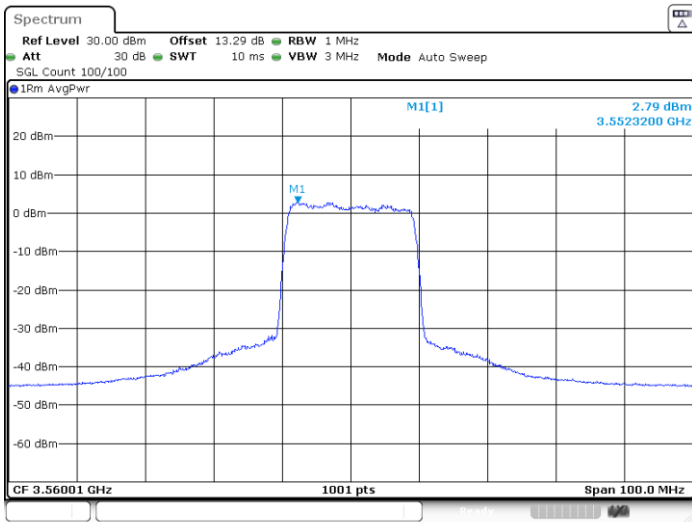
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullIRB

Lowest Channel / 1RB1



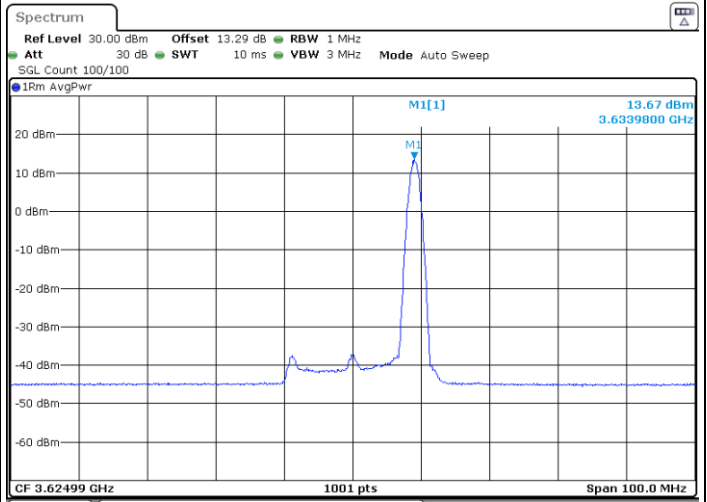
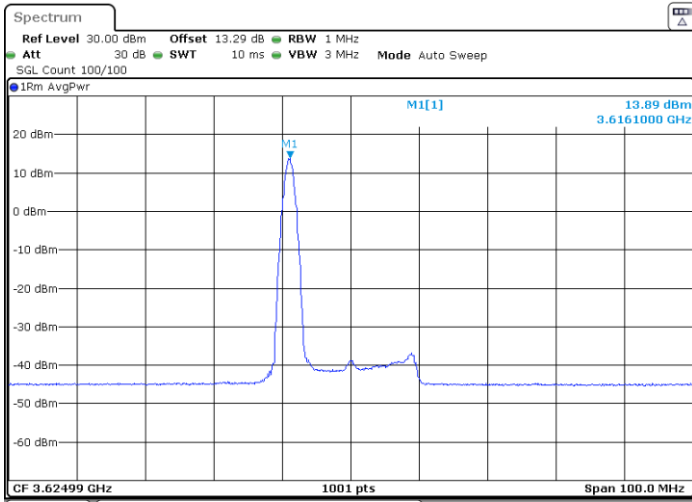


FR1 Part 96 n48 / 20MHz

16QAM

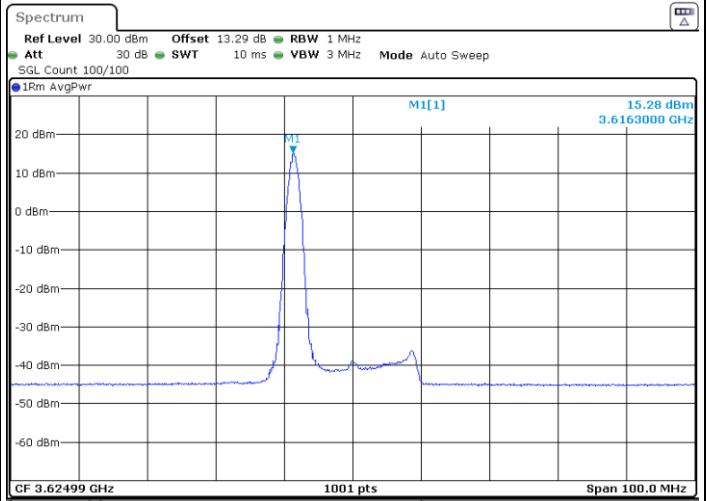
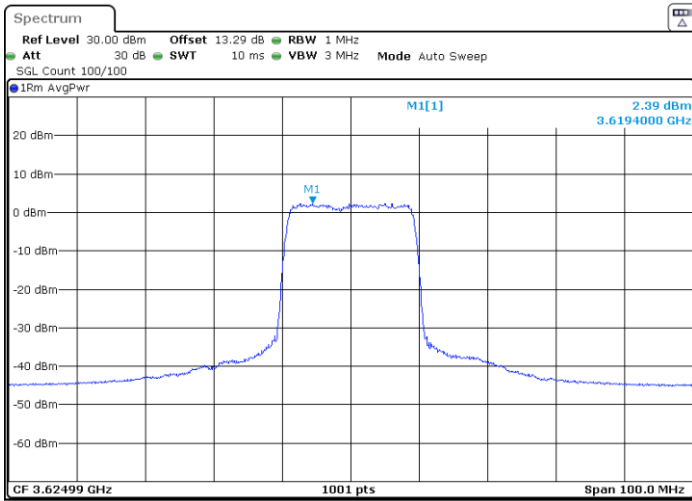
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullIRB

Middle Channel / 1RB1



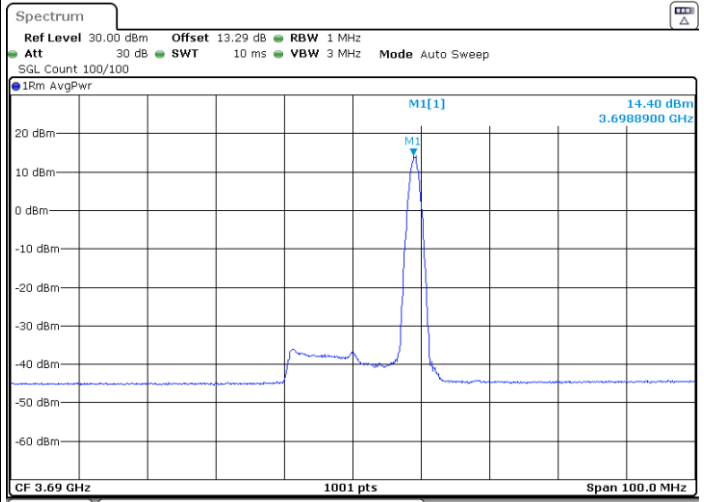
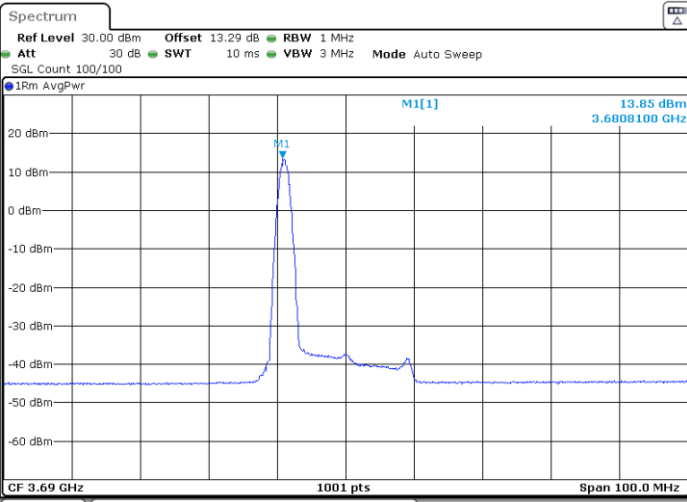


FR1 Part 96 n48 / 20MHz

16QAM

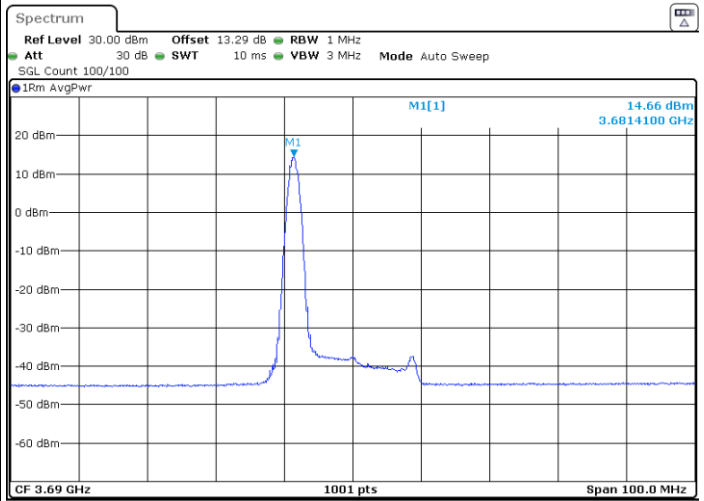
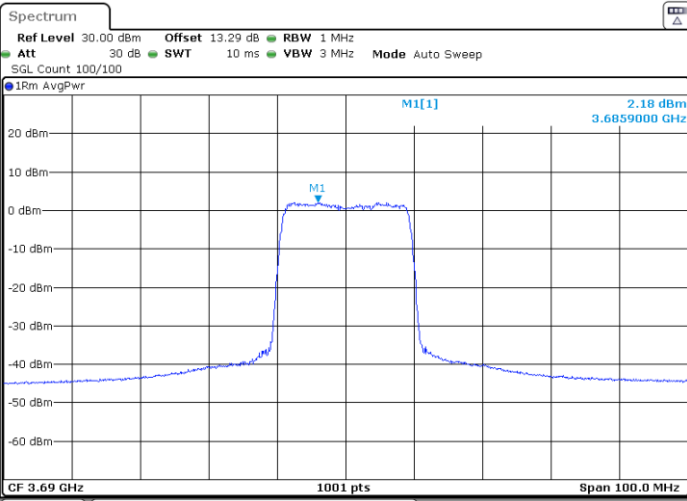
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1

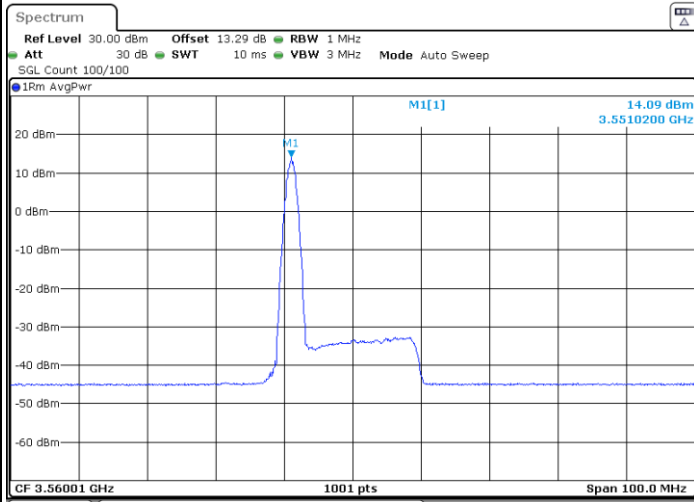




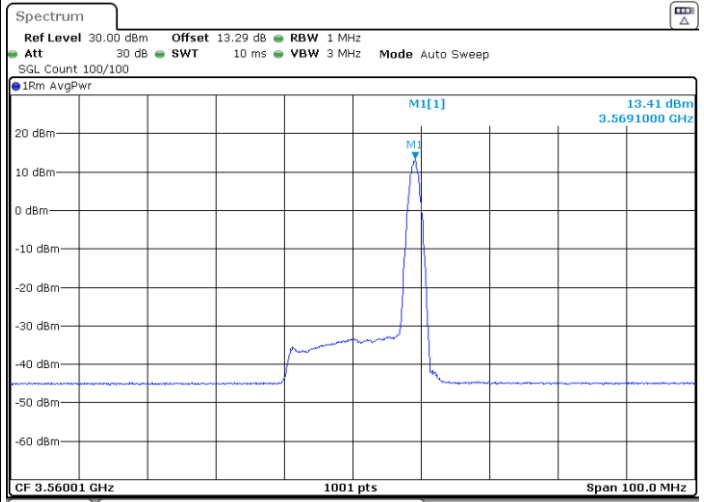
FR1 Part 96 n48 / 20MHz

64QAM

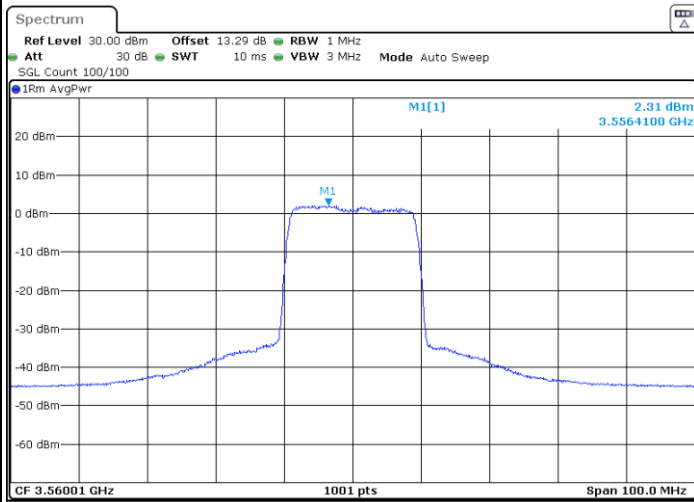
Lowest Channel / 1RB0



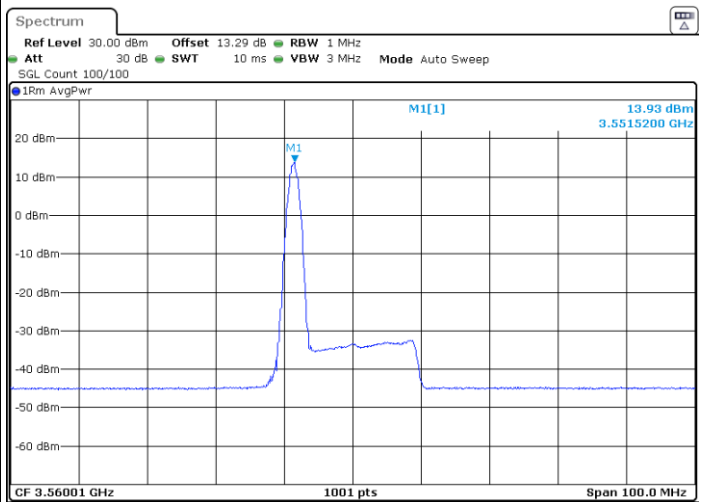
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1



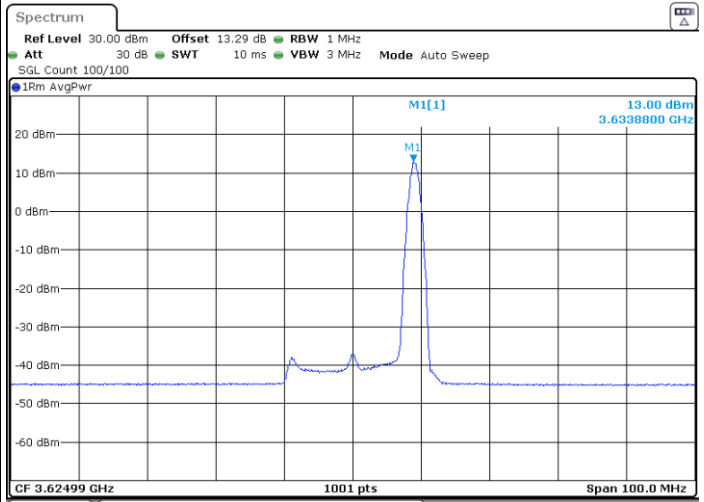
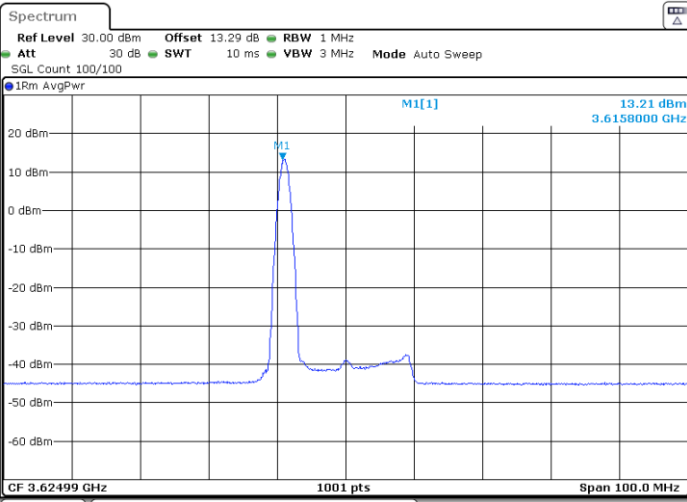


FR1 Part 96 n48 / 20MHz

64QAM

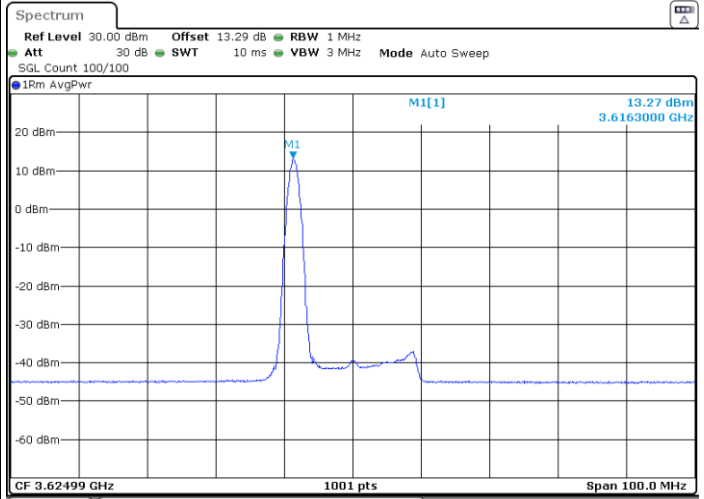
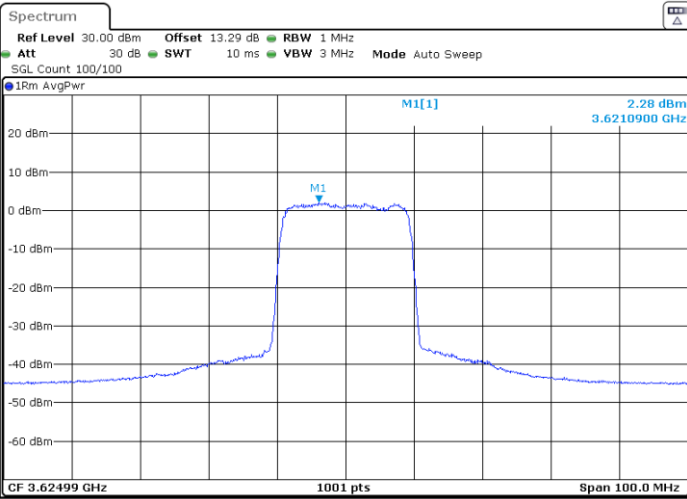
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



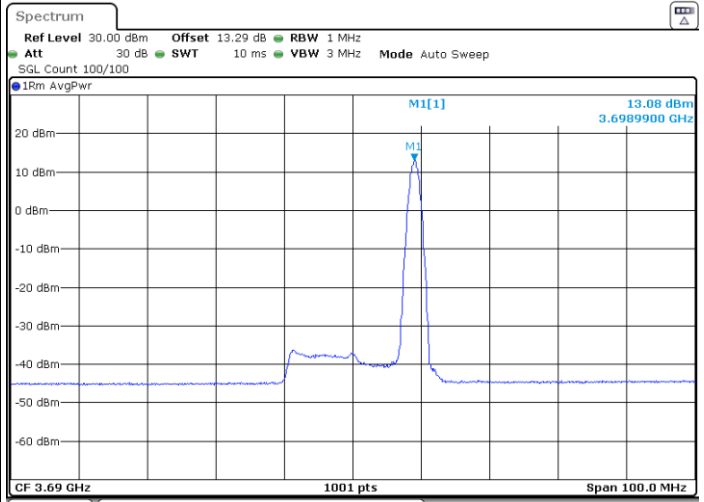
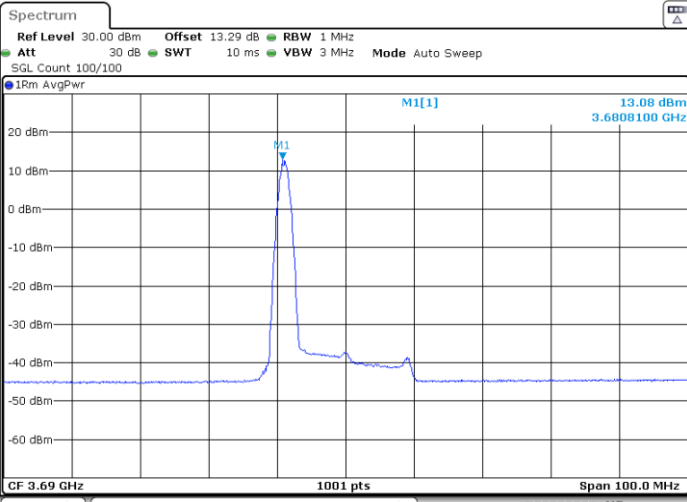


FR1 Part 96 n48 / 20MHz

64QAM

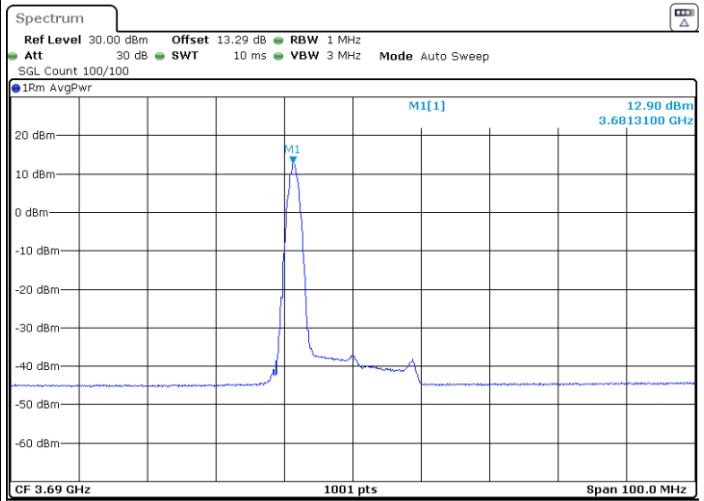
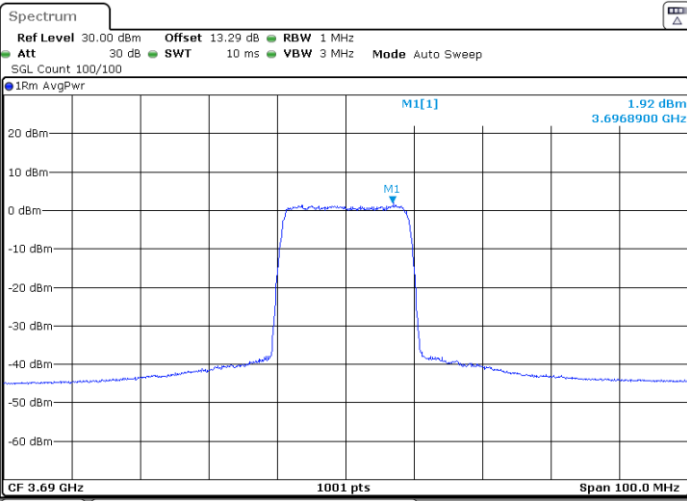
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1



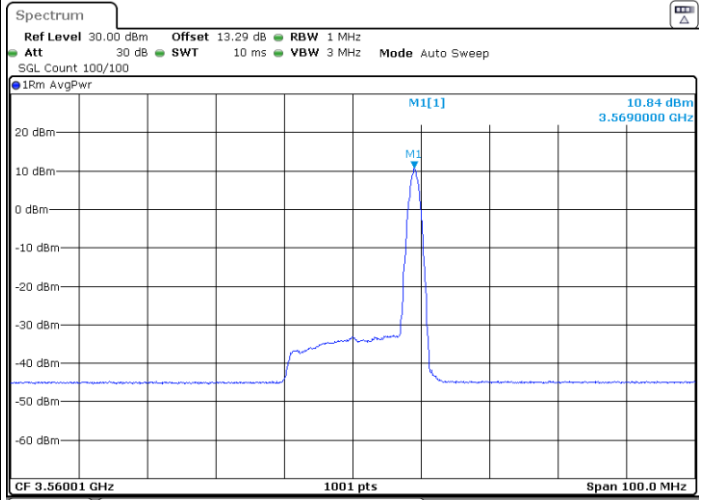
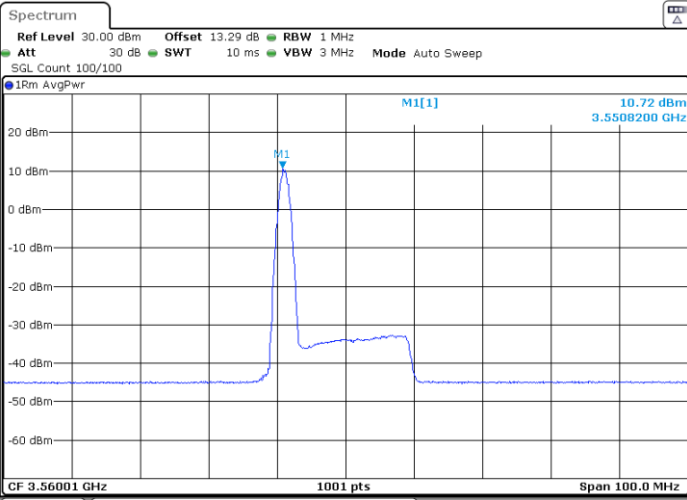


FR1 Part 96 n48 / 20MHz

256QAM

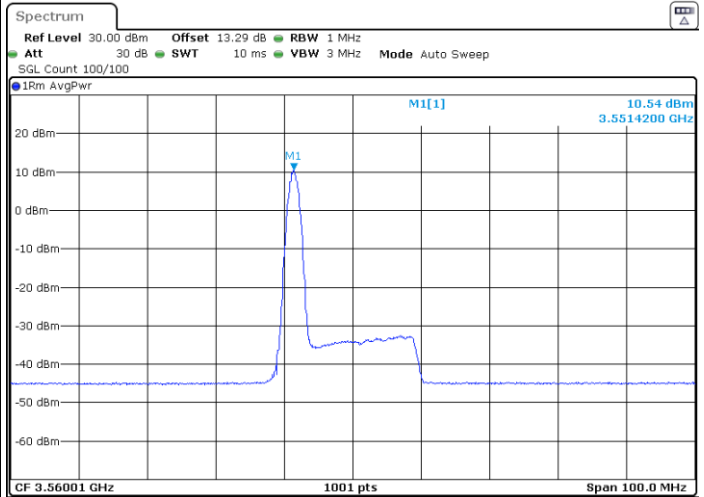
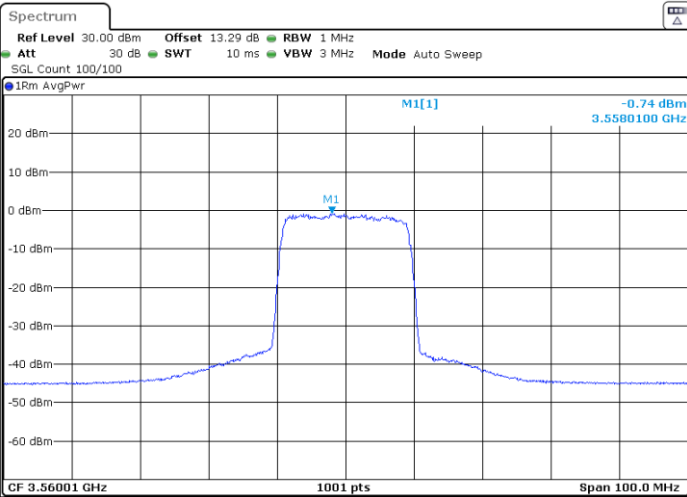
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1



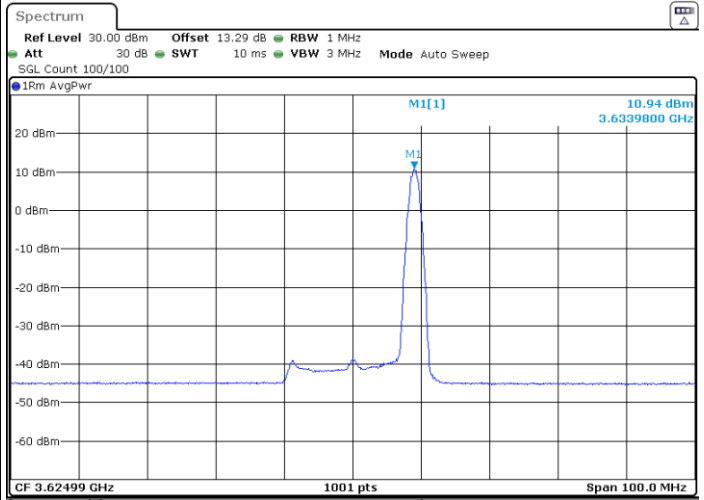
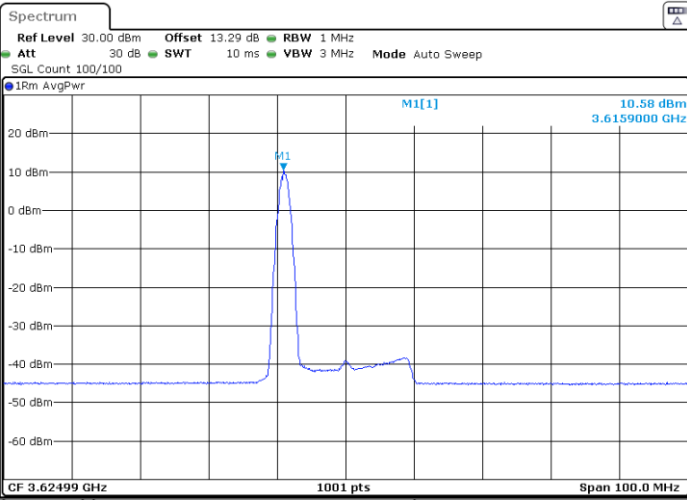


FR1 Part 96 n48 / 20MHz

256QAM

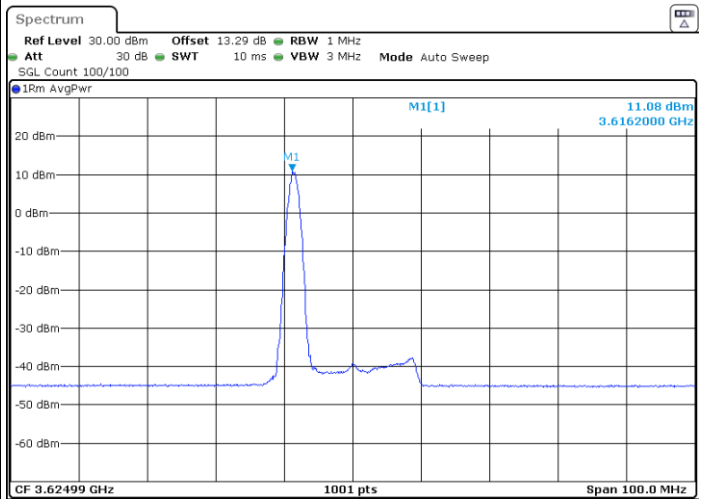
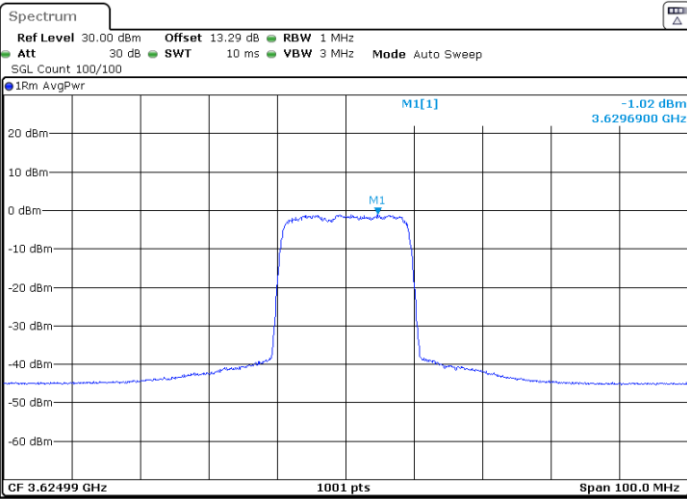
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1



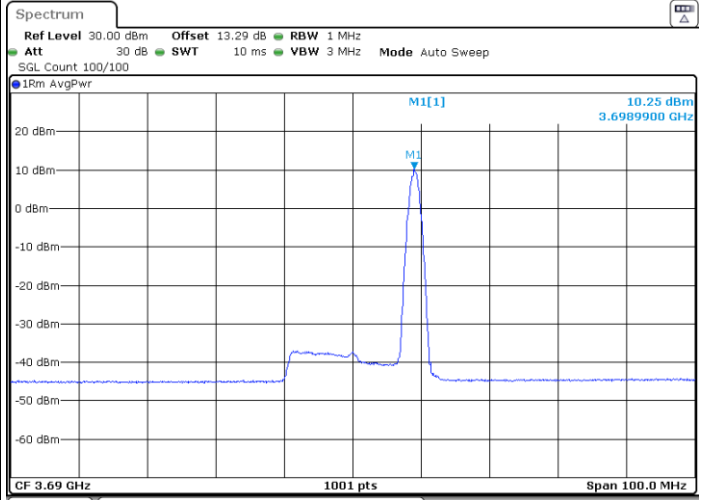
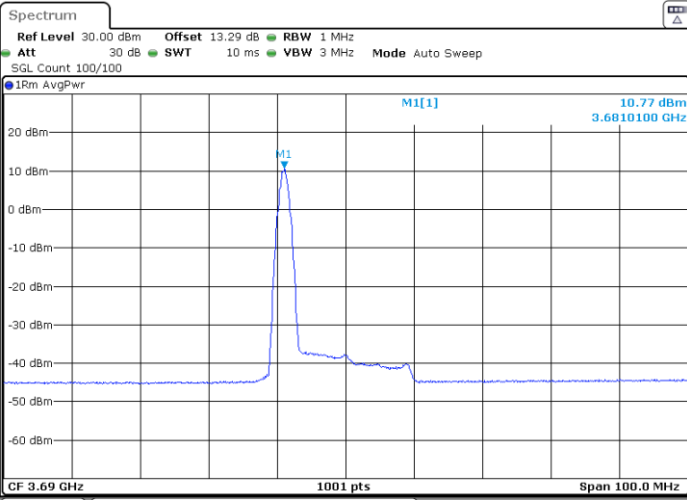


FR1 Part 96 n48 / 20MHz

256QAM

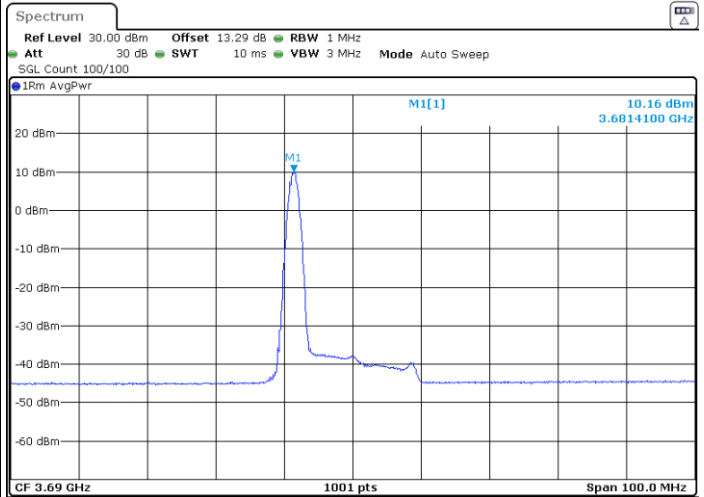
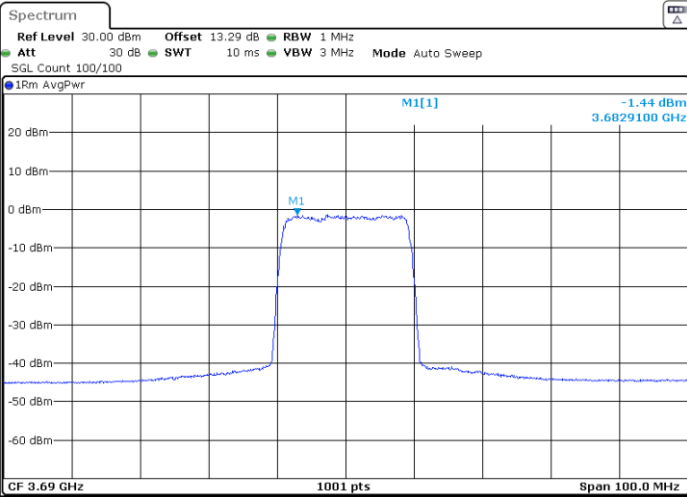
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1

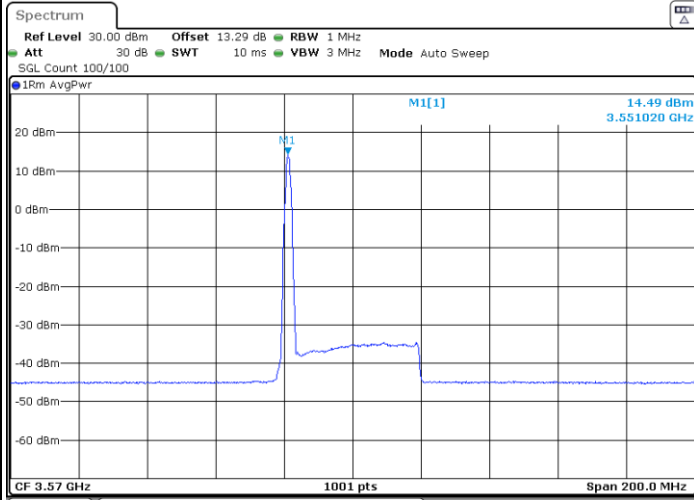




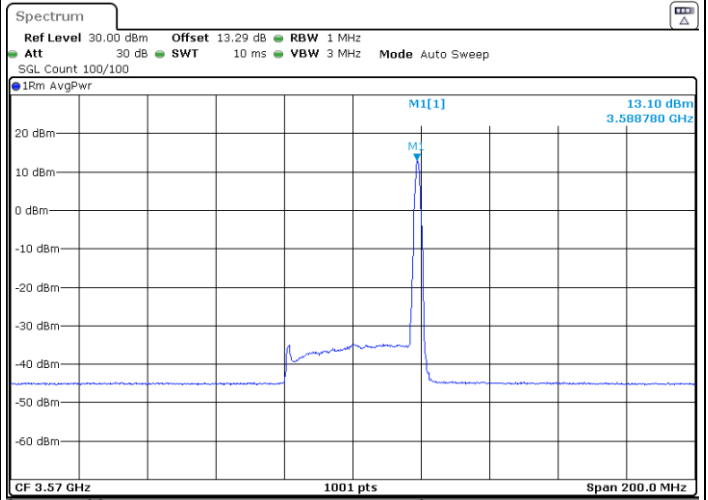
FR1 Part 96 n48 / 40MHz

QPSK

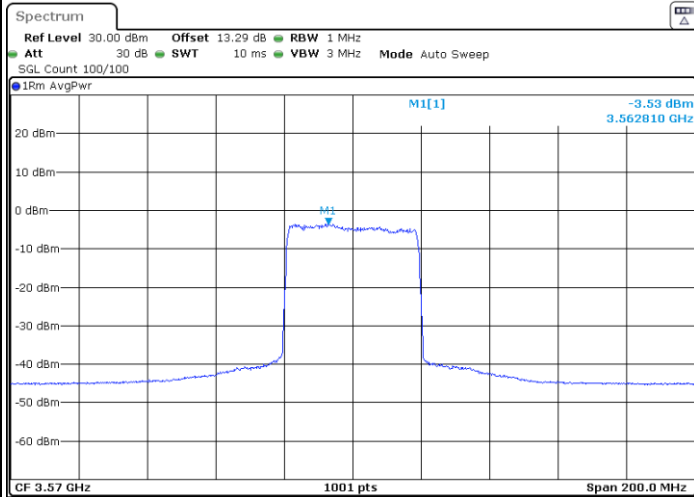
Lowest Channel / 1RB0



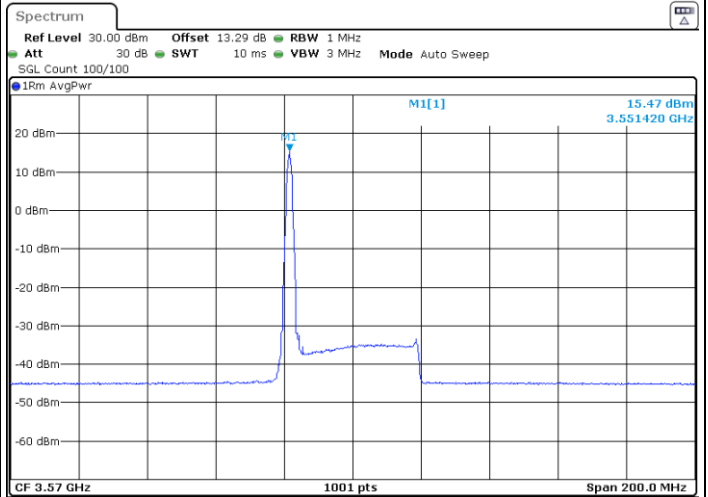
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

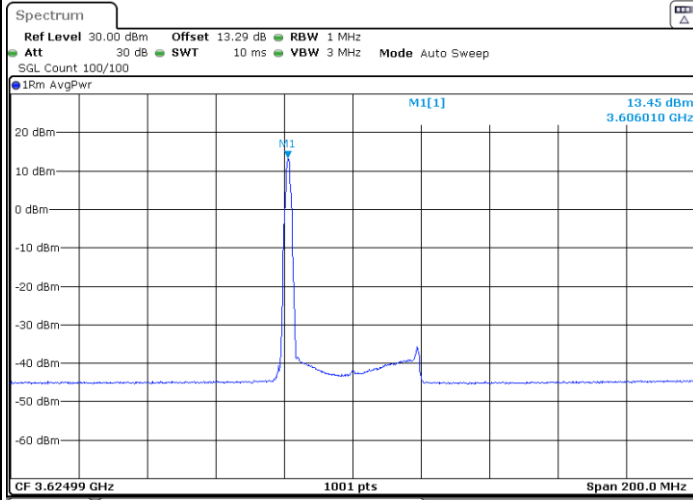




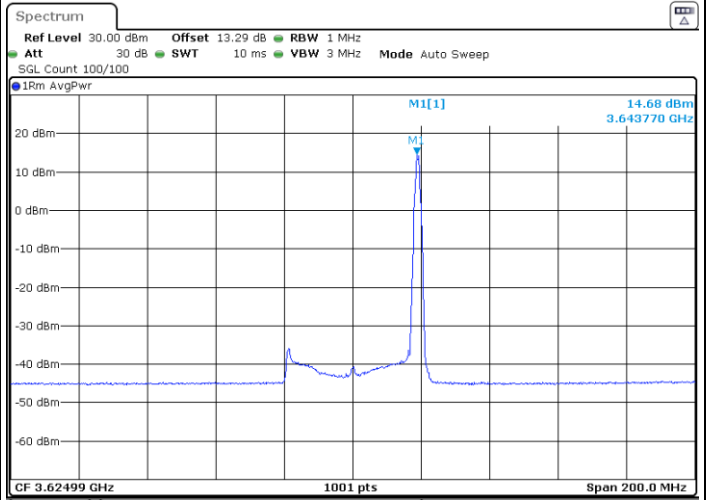
FR1 Part 96 n48 / 40MHz

QPSK

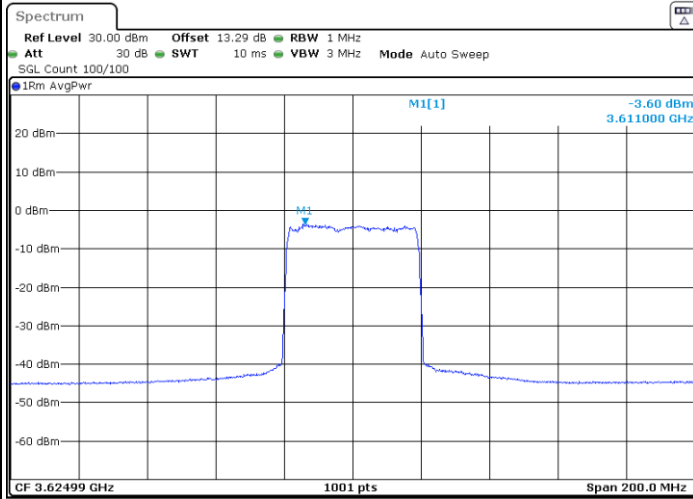
Middle Channel / 1RB0



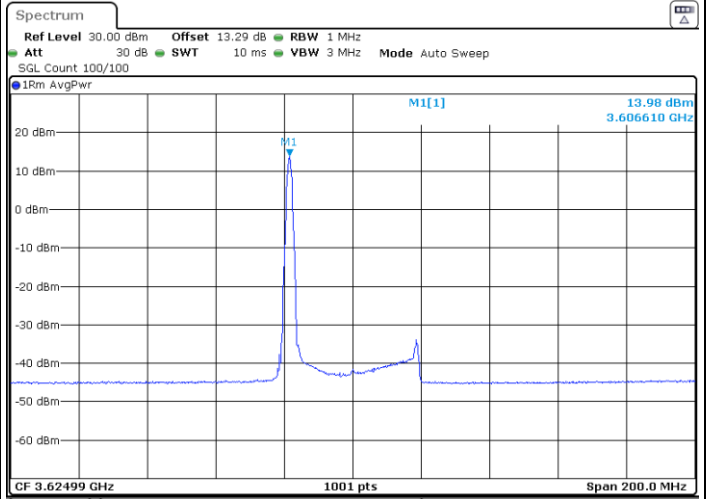
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1



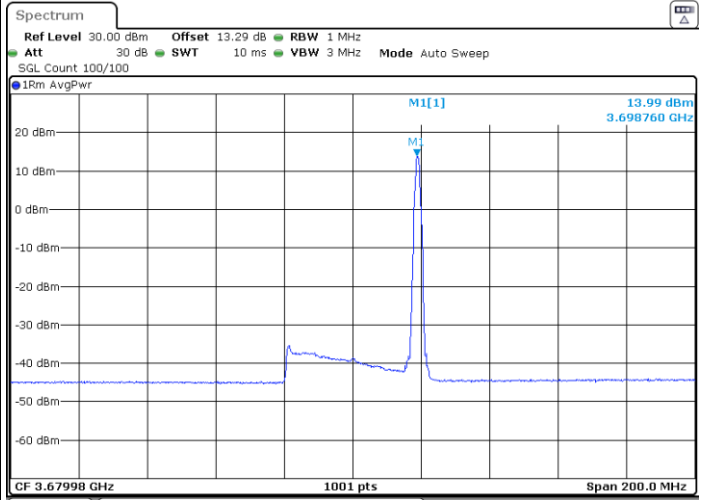
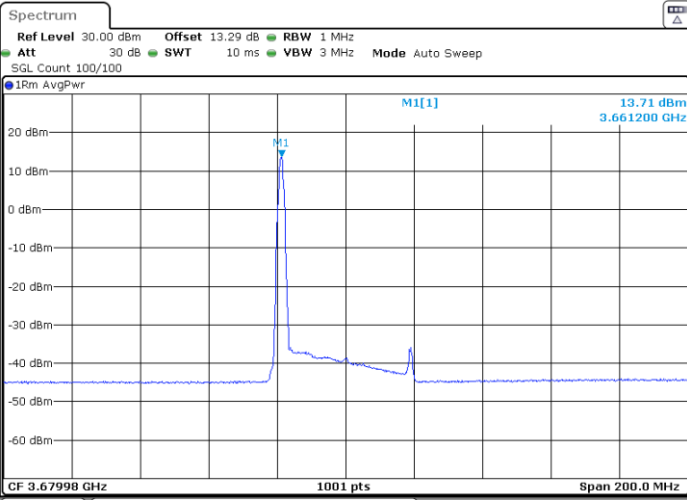


FR1 Part 96 n48 / 40MHz

QPSK

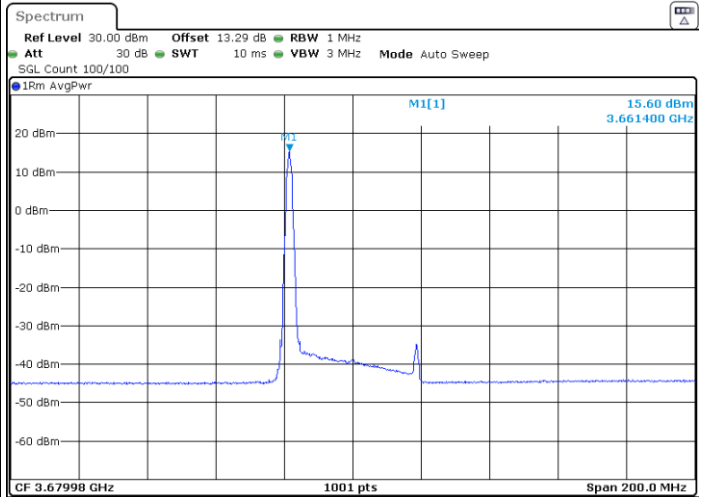
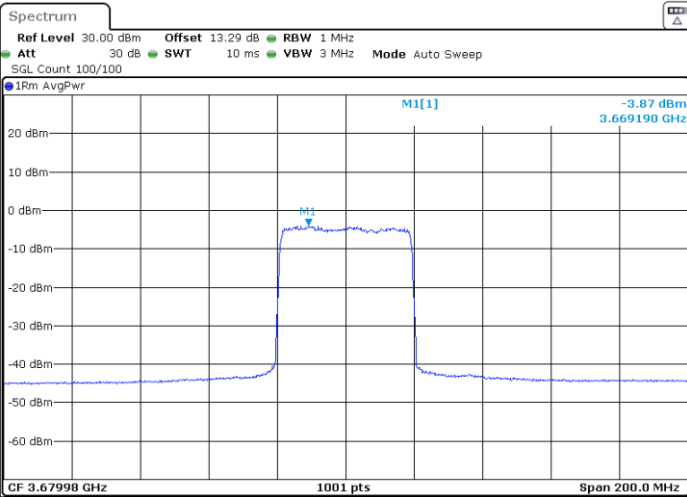
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB

Highest Channel / 1RB1

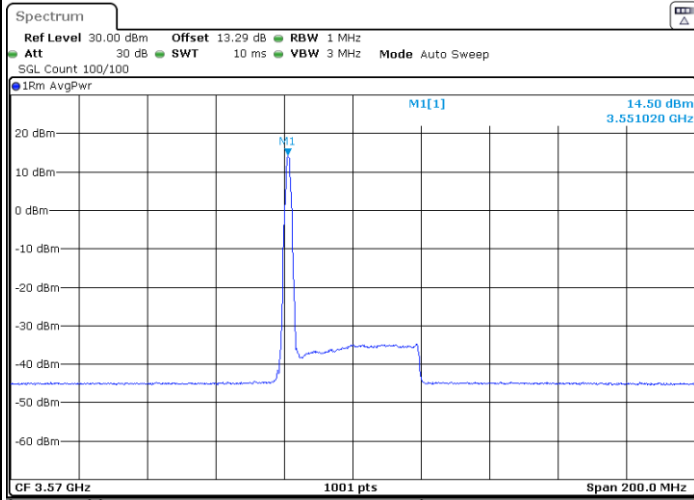




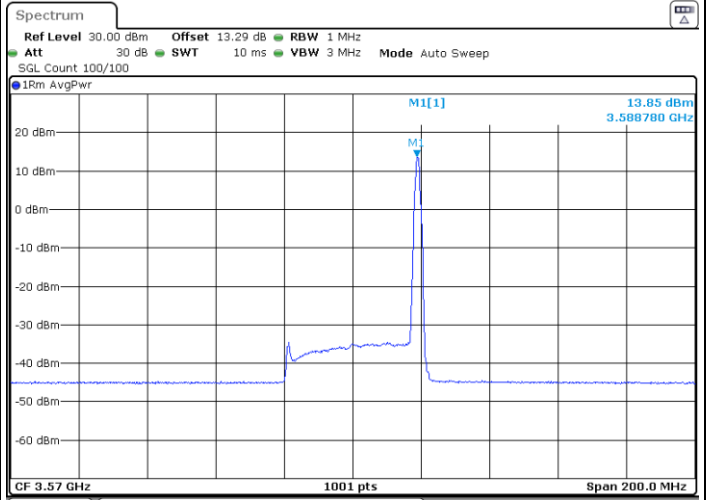
FR1 Part 96 n48 / 40MHz

16QAM

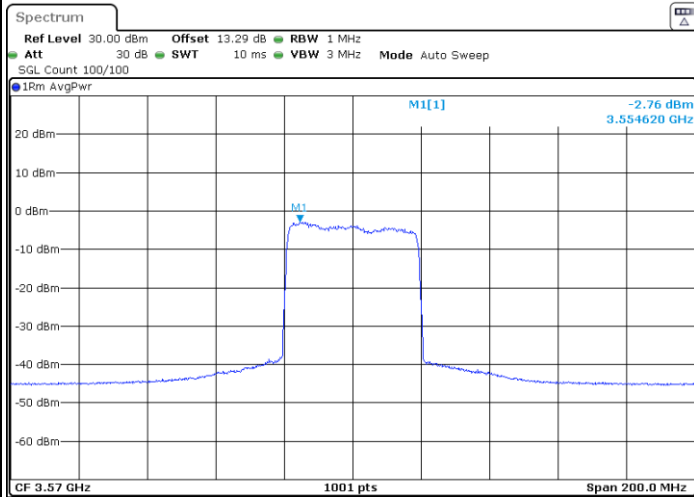
Lowest Channel / 1RB0



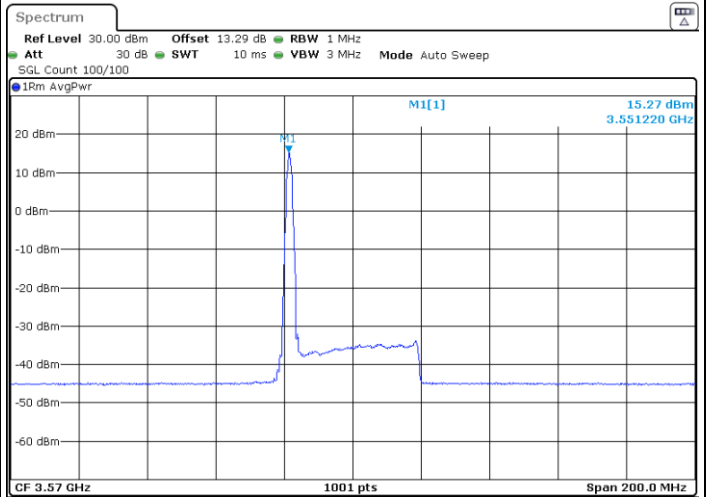
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1



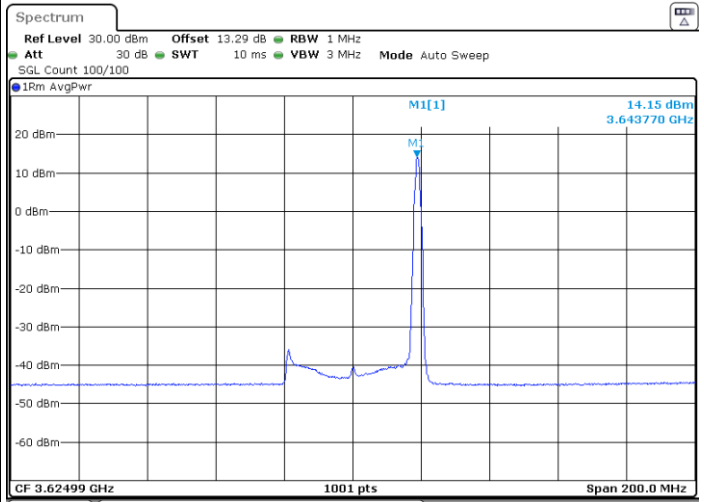
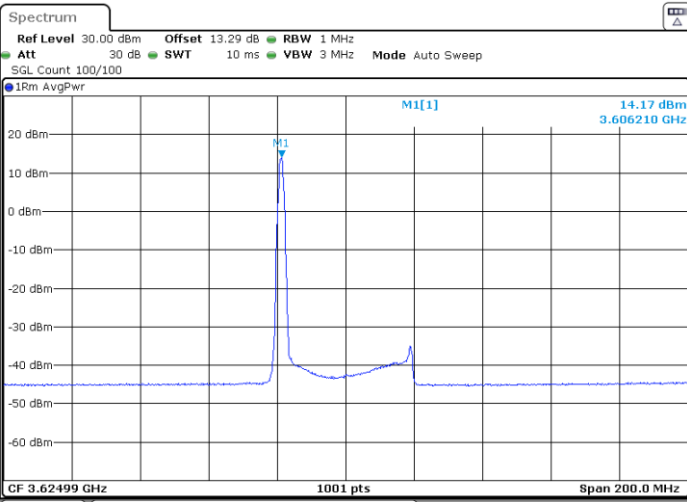


FR1 Part 96 n48 / 40MHz

16QAM

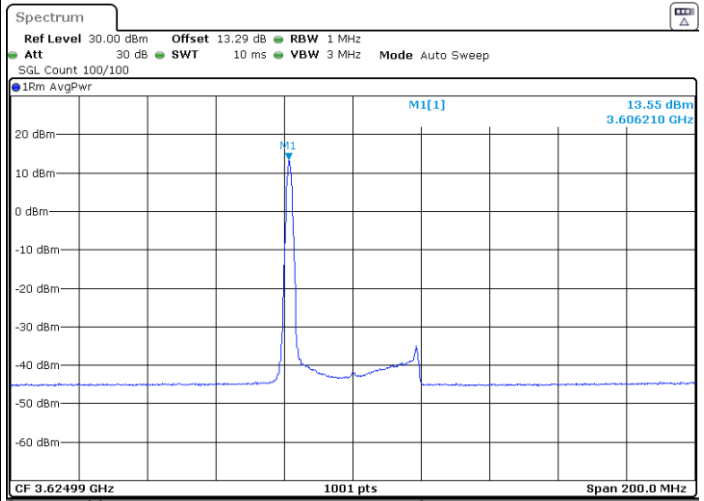
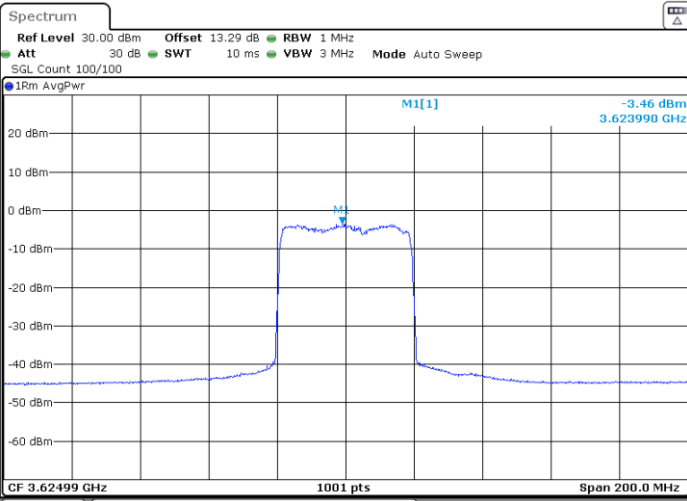
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1

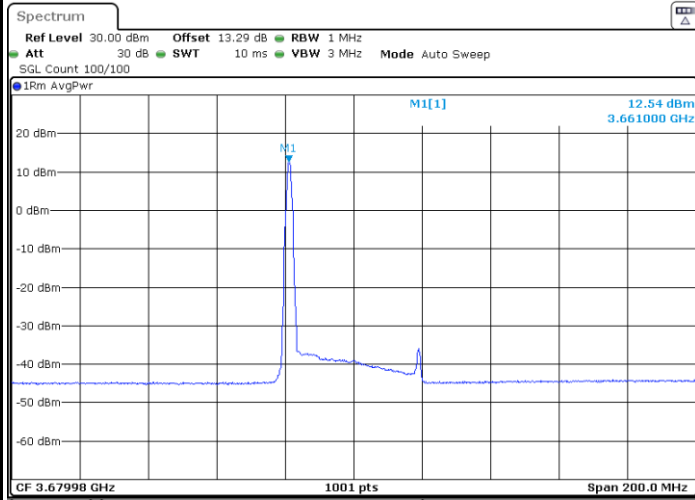




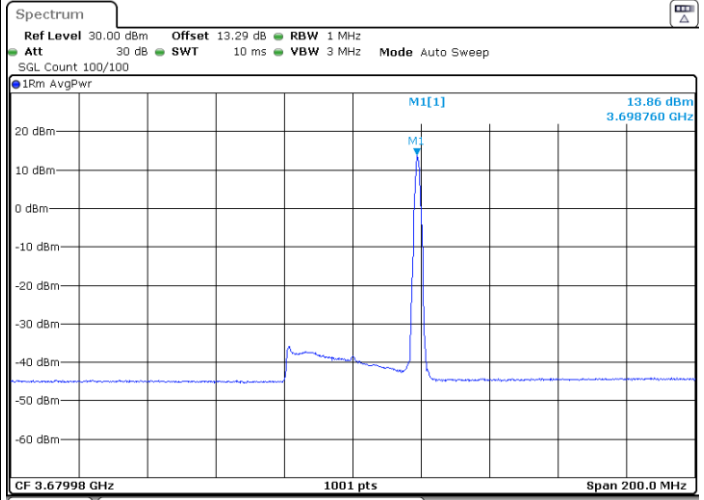
FR1 Part 96 n48 / 40MHz

16QAM

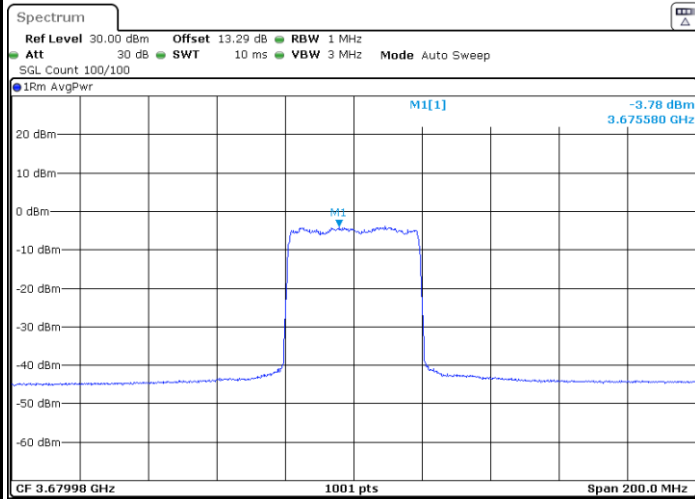
Highest Channel / 1RB0



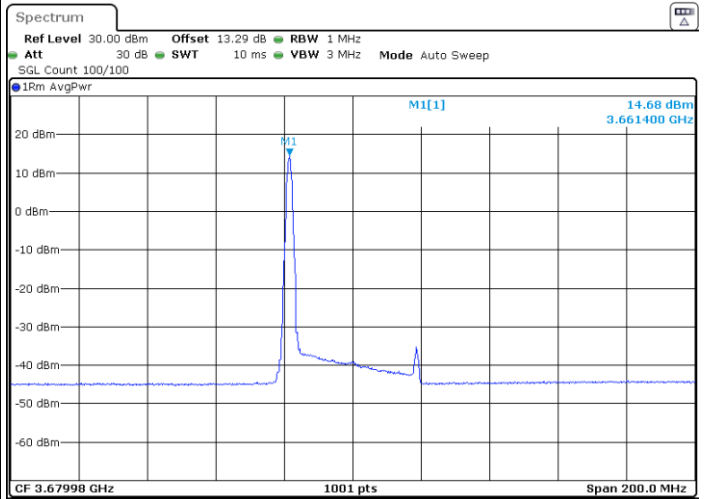
Highest Channel / 1RBmax



Highest Channel / FullIRB



Highest Channel / 1RB1

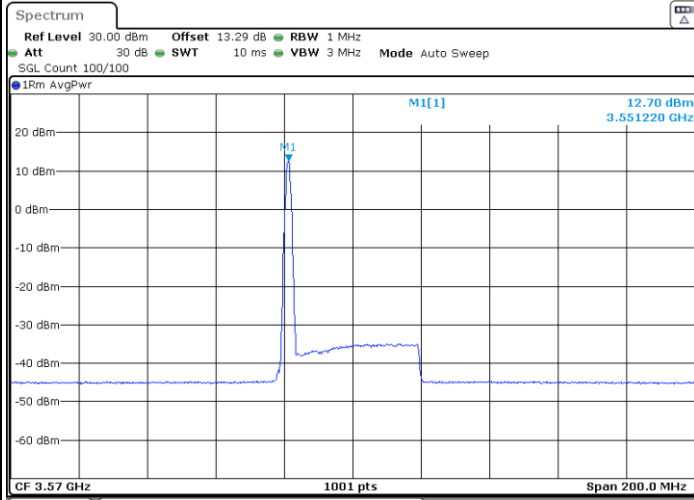




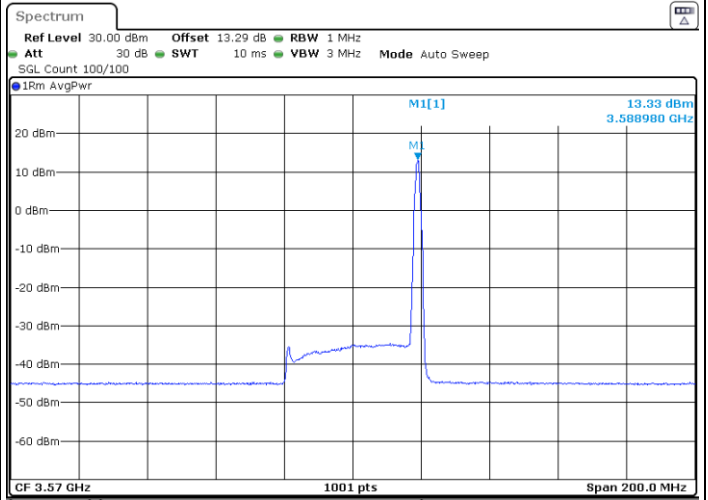
FR1 Part 96 n48 / 40MHz

64QAM

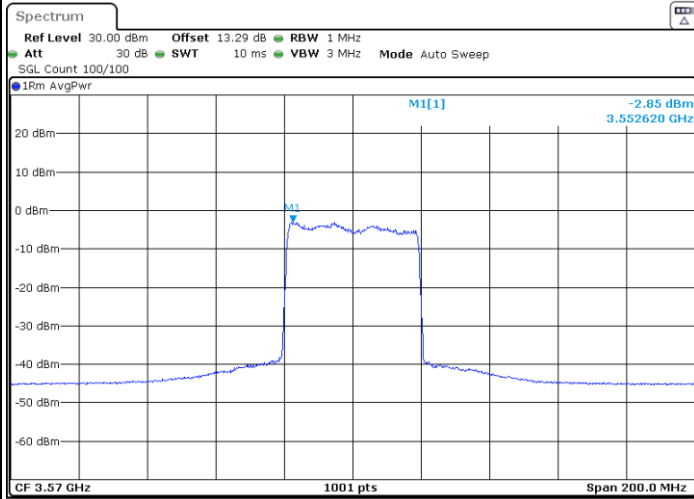
Lowest Channel / 1RB0



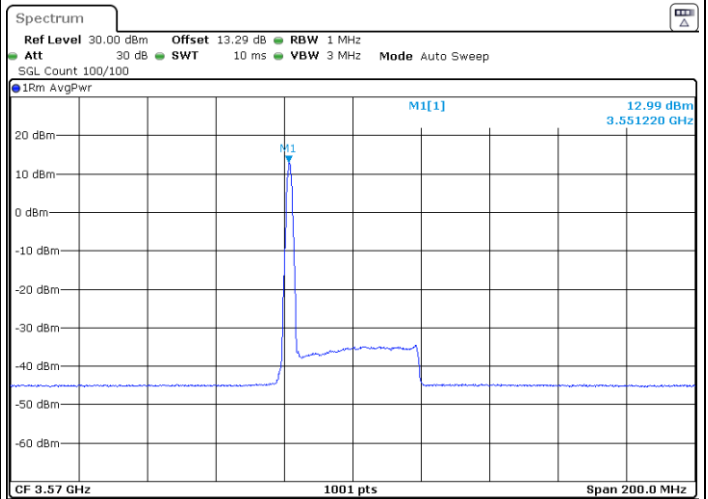
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1



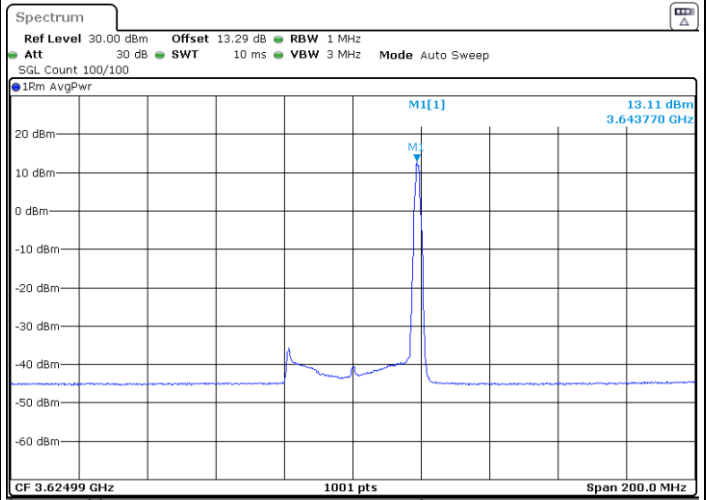
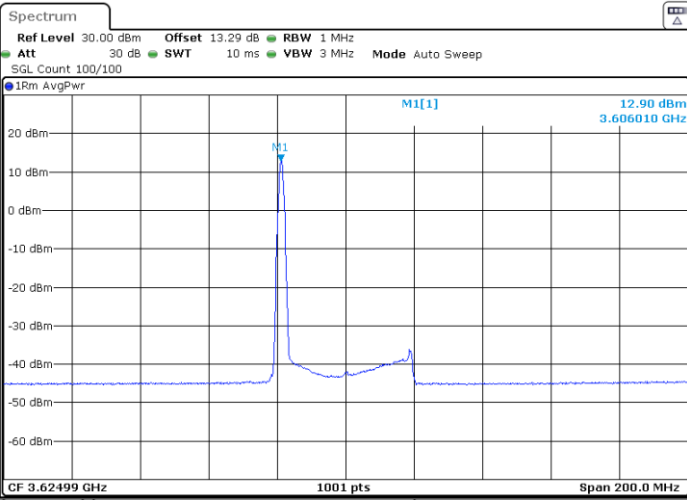


FR1 Part 96 n48 / 40MHz

64QAM

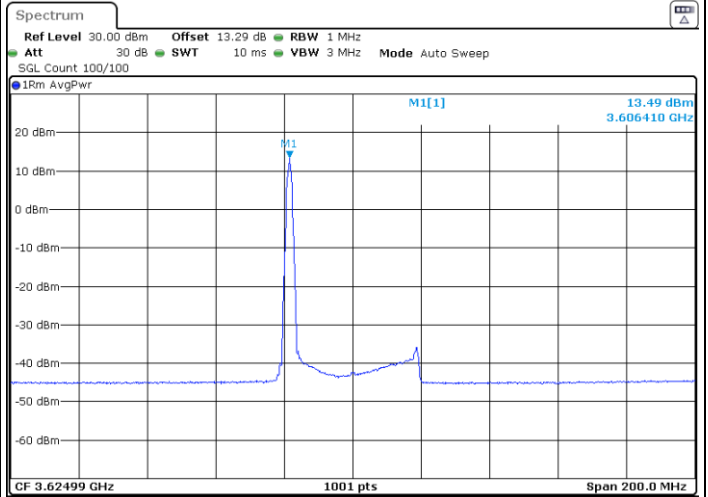
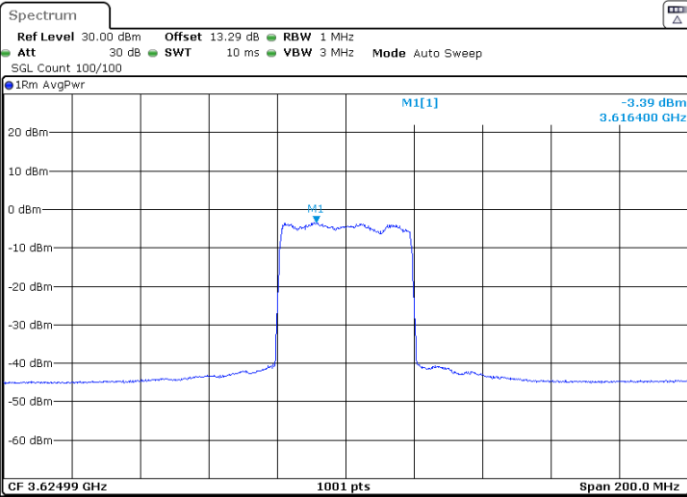
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullRB

Middle Channel / 1RB1

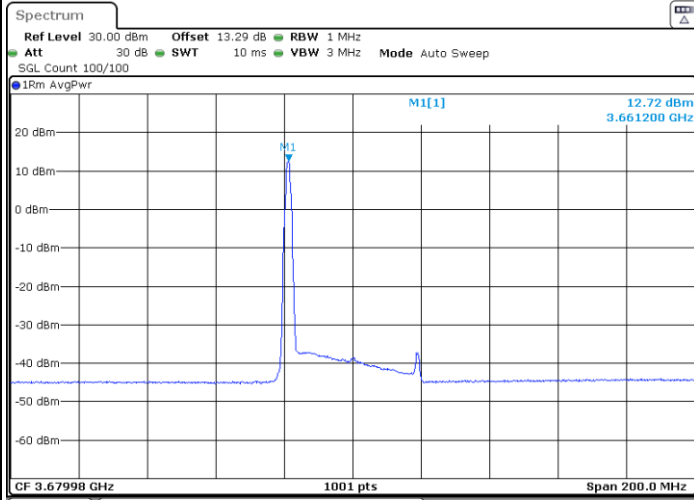




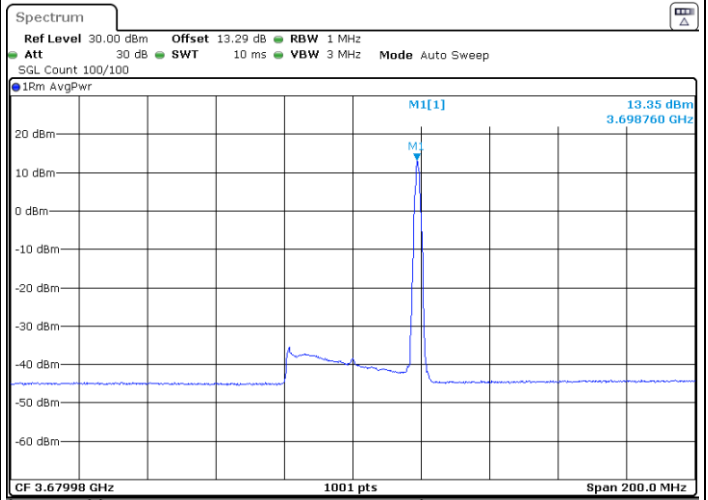
FR1 Part 96 n48 / 40MHz

64QAM

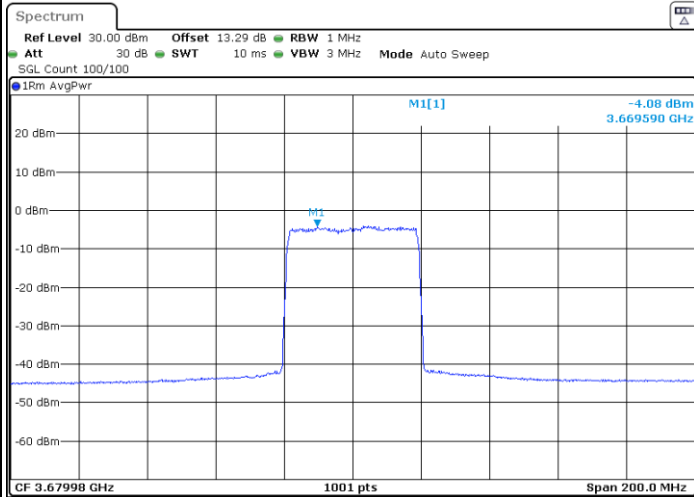
Highest Channel / 1RB0



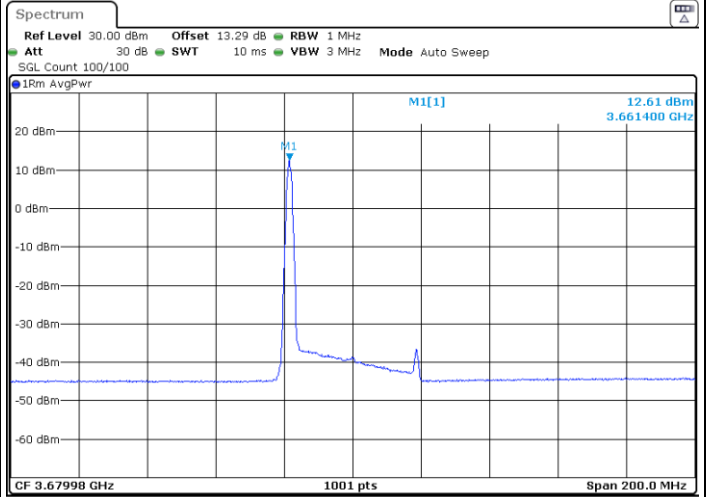
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1



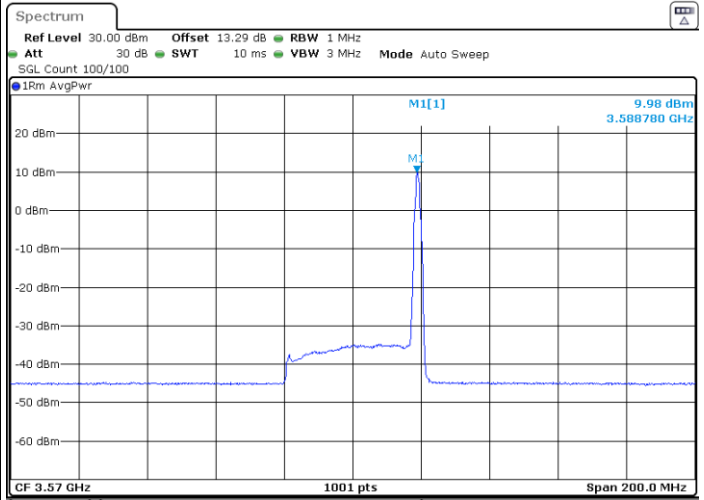
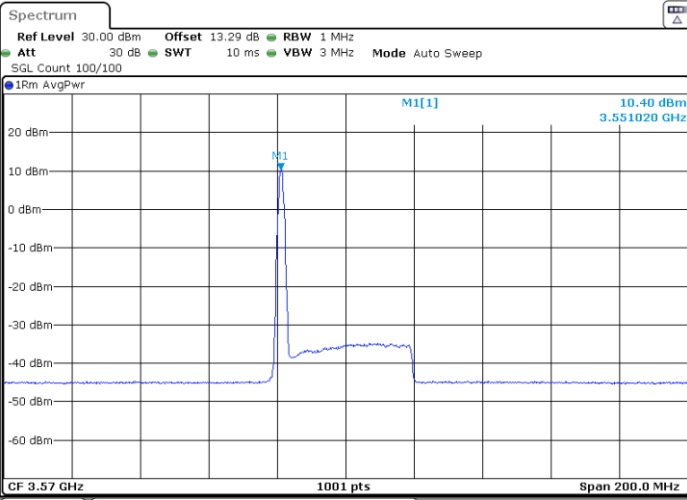


FR1 Part 96 n48 / 40MHz

256QAM

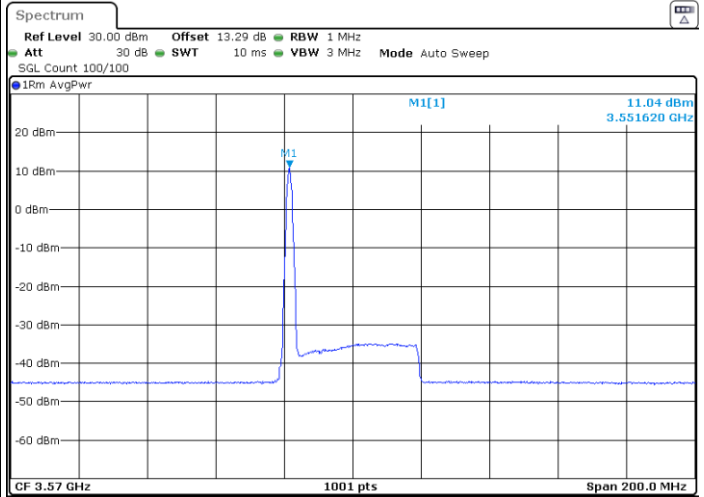
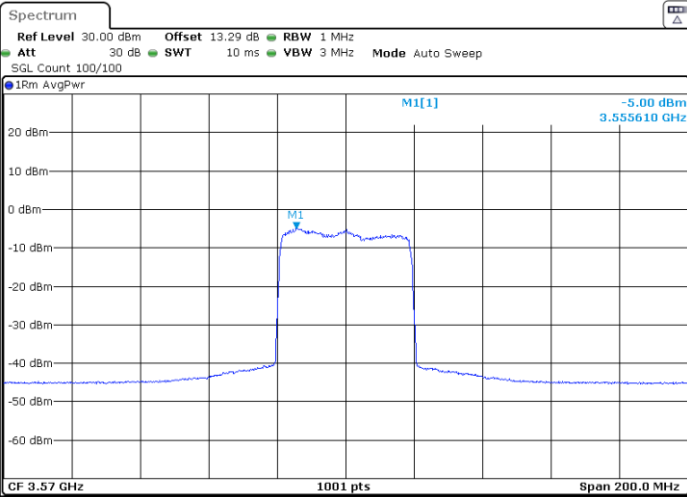
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1



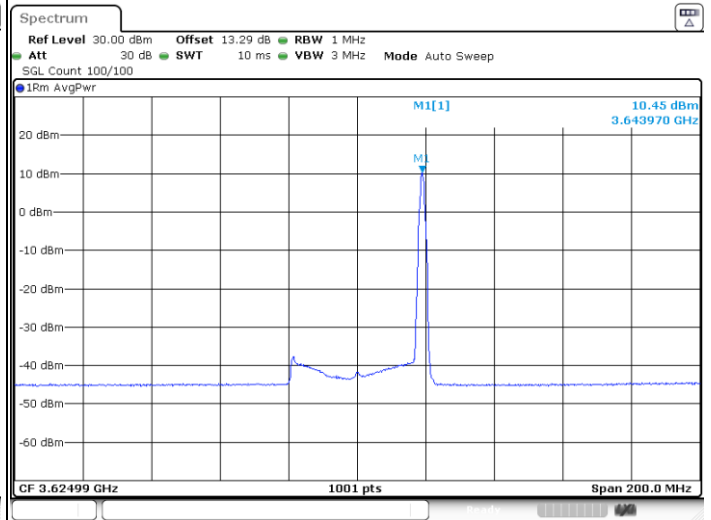
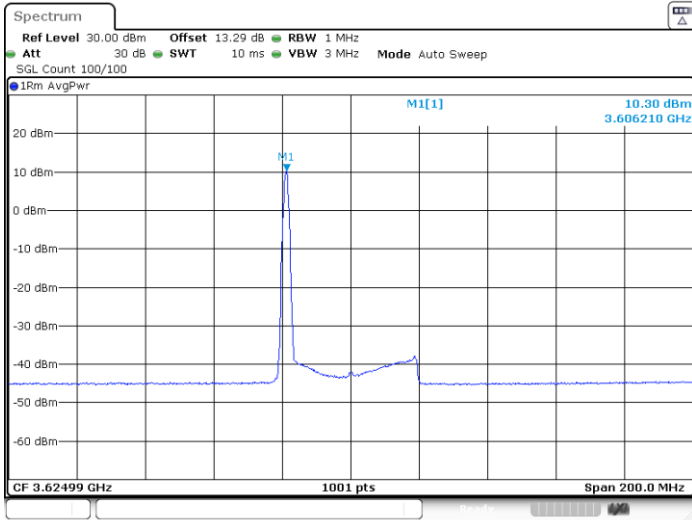


FR1 Part 96 n48 / 40MHz

256QAM

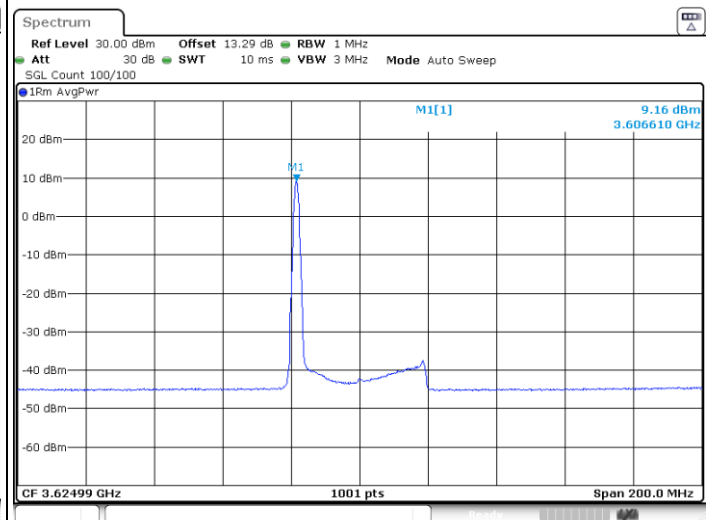
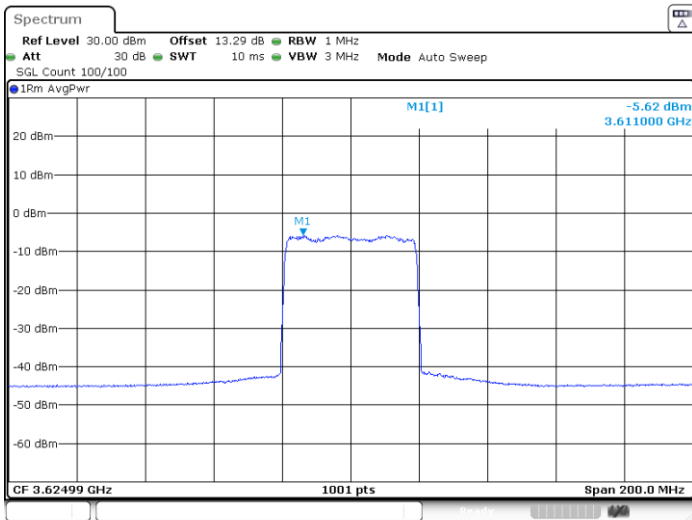
Middle Channel / 1RB0

Middle Channel / 1RBmax



Middle Channel / FullIRB

Middle Channel / 1RB1



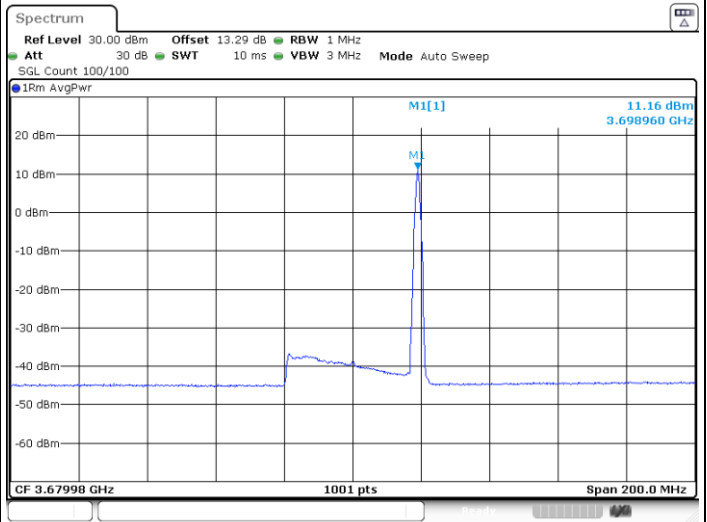
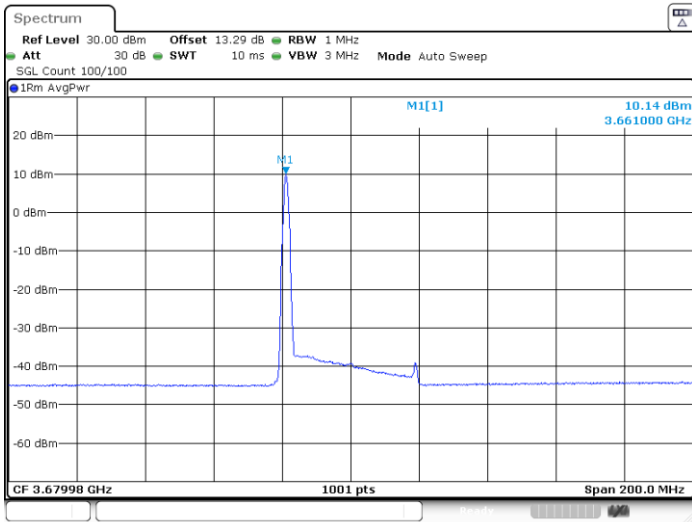


FR1 Part 96 n48 / 40MHz

256QAM

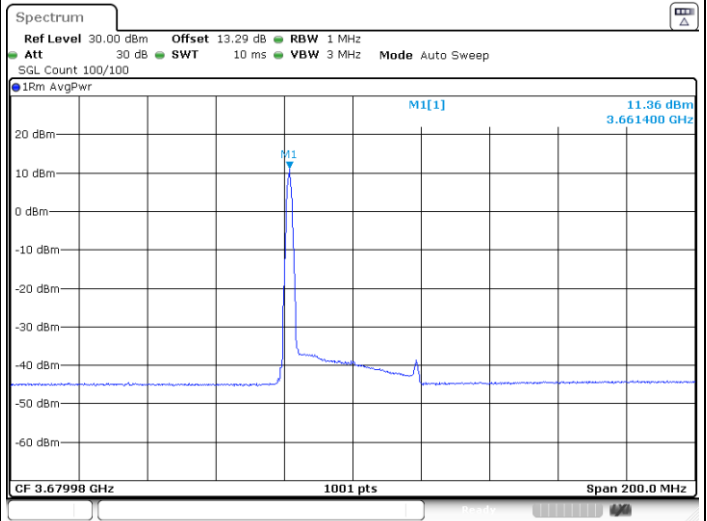
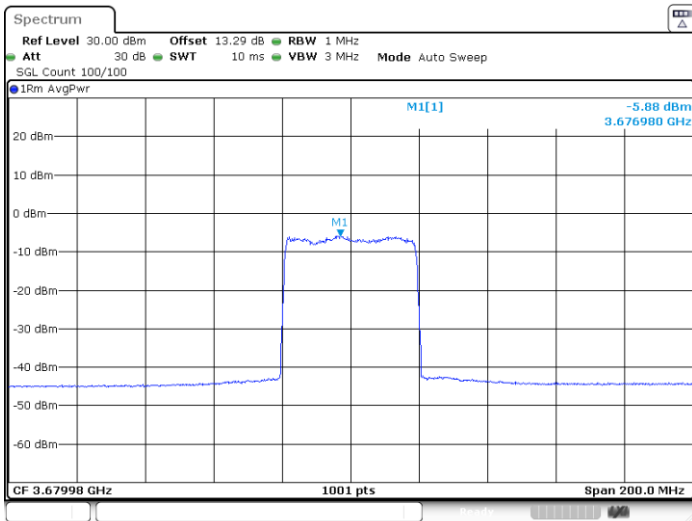
Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullIRB

Highest Channel / 1RB1





FR1 n48

Peak-to-Average Ratio

Mode	FR1 Part96 N48 /10MHz / DFT-S OFDM		
Mod.	40M		Limit: 13dB
RB Size	BPSK	QPSK	Result
Middle CH	5.35	5.43	PASS

