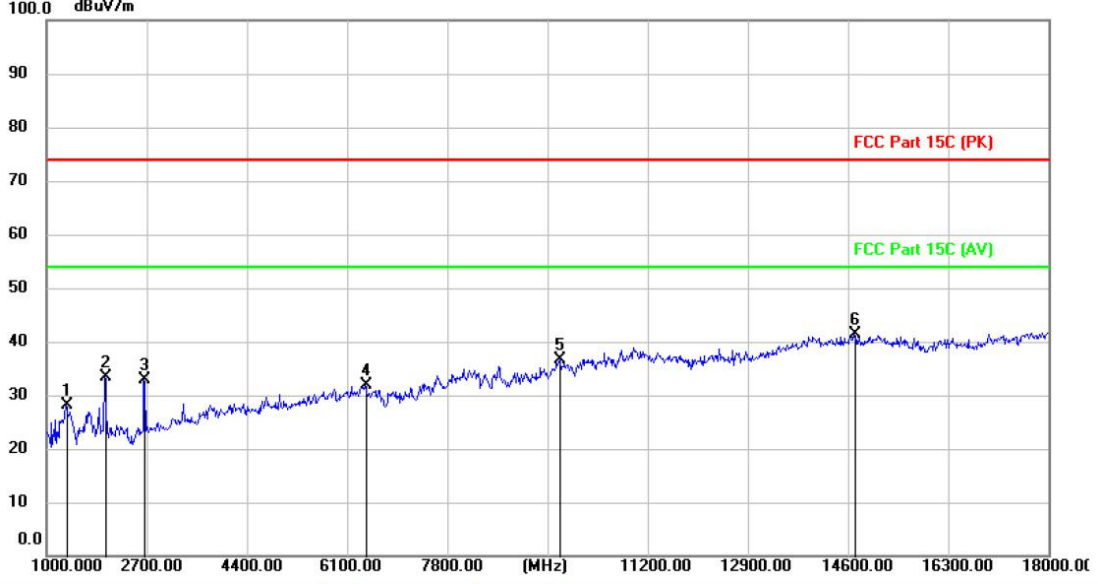


Ant. Pol.	Vertical						
Test Mode:	TX 2DH5 Mode 2480MHz						
Remark:	No report for the emission which more than 10 dB below the prescribed limit.						
							
No. Mk.	Freq.	Reading Level	Correct Factor	Measurement	Limit	Over	Detector
	MHz	(dBuV)	(dB/m)	(dBuV/m)	(dBuV/m)	(dB)	
1	1328.100	44.37	-16.30	28.07	74.00	-45.93	peak
2	1994.500	47.50	-14.08	33.42	74.00	-40.58	peak
3	2662.600	46.00	-13.09	32.91	74.00	-41.09	peak
4	6421.300	37.17	-5.30	31.87	74.00	-42.13	peak
5	9709.100	35.14	1.52	36.66	74.00	-37.34	peak
6 *	14703.700	34.71	6.61	41.32	74.00	-32.68	peak
Measurement = Reading level + Correct Factor							

3.11. Pseudorandom Frequency Hopping Sequence

LIMIT

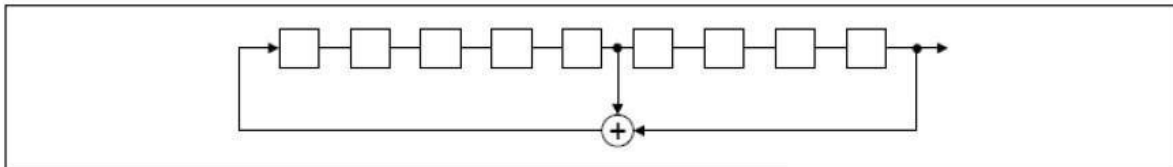
FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(1):

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20 dB bandwidth of the hop-ping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400–2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW. The system shall hop to channel frequencies that are selected at the system hopping rate from a pseudo randomly ordered list of hopping frequencies. Each frequency must be used equally on the average by each transmitter. The system receivers shall have input bandwidths that match the hop-ping channel bandwidths of their corresponding transmitters and shall shift frequencies in synchronization with the transmitted signals.

TEST RESULTS

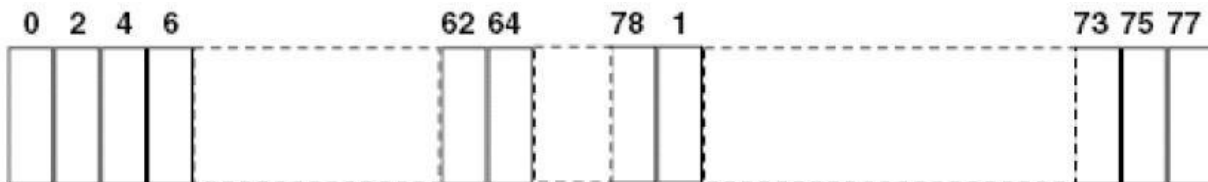
The pseudorandom frequency hopping sequence may be generated in a nine-stage shift register whose 5th and 9th stage outputs are added in a modulo-two addition stage. And the result is fed back to the input of the first stage. The sequence begins with the first one of 9 consecutive ones, for example: the shift register is initialized with nine ones.

- Number of shift register stages: 9
- Length of pseudo-random sequence: $2^9 - 1 = 511$ bits
- Longest sequence of zeros: 8 (non-inverted signal)



Linear Feedback Shift Register for Generation of the PRBS sequence

An example of pseudorandom frequency hopping sequence as follows:



Each frequency used equally one the average by each transmitter.

The system receiver have input bandwidths that match the hopping channel bandwidths of their corresponding transmitter and shift frequencies in synchronization with the transmitted signals.

4.EUT TEST PHOTOS

Reference to the document No.: Test Photos.

5. PHOTOGRAPHS OF EUT CONSTRUCTIONAL

Reference to the document No.: External Photos and Internal Photos.

*****THE END*****