承認書 SPECIFICATION FOR APPROVAL

客户名稱 CUSTOMER	:	
客户料號 CUSTOMER'S P/N	:	
料號 PART NUMBER	:	WAN5010F245H07
規格 DESCRIPTION	:	Chip Antenna 5010 M-Ant 2.4~2.5G Type H07
版本 VERSION	:	V1.2
日期 ISSUE DATE	:	2020/04/08

	工 程 部 R&D CENTER	
承 認 APPROVAL	確認 CHECKED	製 作 DRAWN
Ray	Tennyson	Snow



萬誠科技股份有限公司

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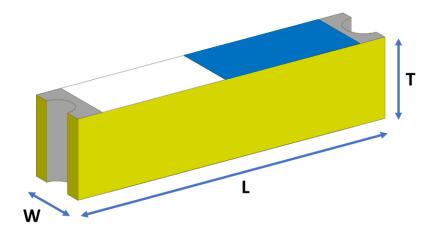
OneWave Electronic Co., Ltd.

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5010 Chip antenna

For Bluetooth / WLAN Applications



P/N: WAN5010F245H07

	Dimension (mm)
L	5.02 ± 0.20
W	1.12 ± 0.20
Т	1.25 ± 0.20

Part Number Information

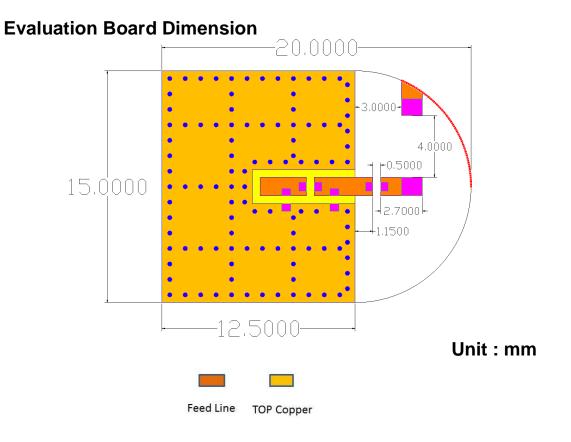
WA	<u>AN</u>	<u>5010</u>	<u>F</u>	<u>245</u>	H	<u>07</u>		
A	4	В	С	D	Ε	F		
				ſ				
A Product Series				Antenna				
В	Dimension L x W			Dimension L x W 5.0X1.0mm (± 0.2mm)).2mm)
С	Material		Hig	gh K mate	rial			
D	D Working Frequency		2.	.4 ~ 2.5GI	Hz			
Е	Feeding mode		Monopol	e & Singl	e Feeding			
F	Antenna type				Туре = 07	7		

1. Electrical Specification

Specification					
Part Number	WAN5010F245H07				
Central Frequency	2450	MHz			
Bandwidth	100 (Min.)	MHz			
Return Loss	-10 (Max)	dB			
Peak Gain	3.09	dBi			
Impedance	50	Ohm			
Operating Temperature	-40~+110	°C			
Maximum Power	4	W			
Resistance to Soldering Heats	10(@260°C)	sec.			
Polarization	Linear				
Azimuth Beamwidth	Omni-directional				
Termination	Cu / Sn (Leadless)				

Remark : Bandwidth & Peak Gain was measured under evaluation board of next page

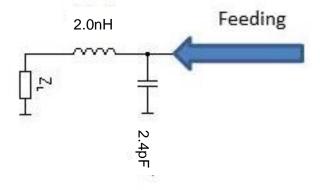
2. Recommended PCB Pattern



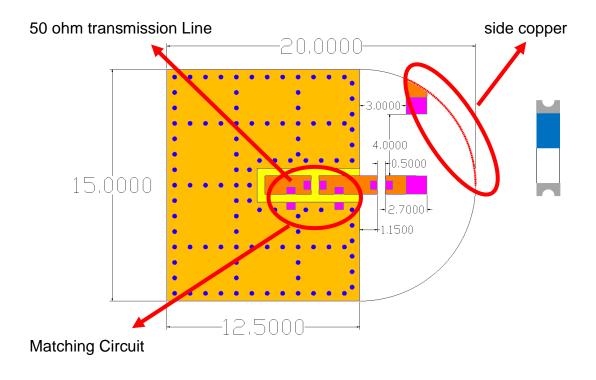
Suggested Matching Circuit

重要資訊:

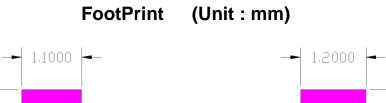
匹配元件建議使用精準度高的電感±0.1~0.3nH、電容±0.1pF







Layout Dimensions in Clearance area(Size=15.0*7.50mm)



4.0000 -

6.3000-

. 1.3500

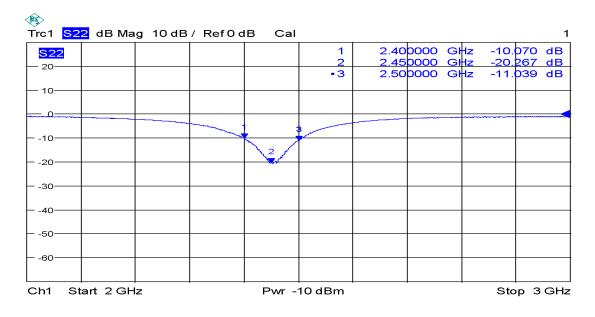
. 1

1.3500

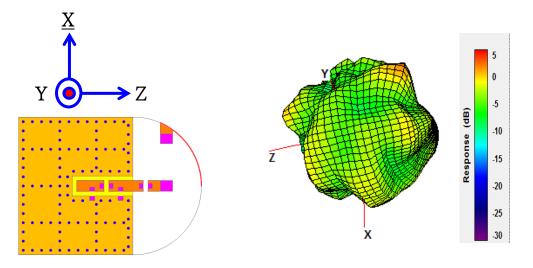
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3. Measurement Results

Return Loss

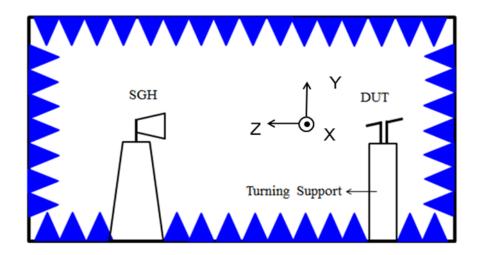


Radiation Pattern



	Efficiency	Peak Gain	Directivity
2400MHz	37.83 %	2.42 dBi	5.35 dBi
2450MHz	44.57 %	3.09 dBi	5.23 dBi
2500MHz	36.79 %	2.68 dBi	5.29 dBi

Chamber Coordinate System





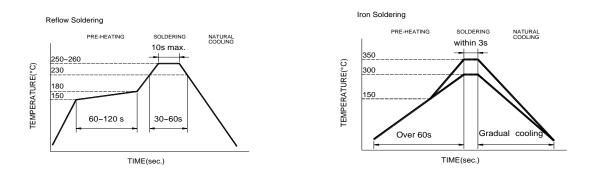
4.Reliability and Test Condictions

ITEM	REQUIREMENTS	TEST CONDITION			
Solderability	1. Wetting shall exceed 90% coverage 2. No visible mechanical damage TEMP (°C) 230°C 150°C 60sec	Pre-heating temperature:150°C /60sec. Solder temperature:230±5°C Duration:4±1sec. Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin			
Solder heat Resistance	1. No visible mechanical damage 2. Central Freq. change :within ± 6% TEMP (°C) 260°C 10±0.5 sec. 150°C 60sec	Pre-heating temperature:150°C /60sec. Solder temperature:260±5°C Duration:10±0.5sec. Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin The device should be reflow soldered(230 ±5°C for 10sec.) to a tinned copper substrate A dynometer force gauge should be applied the side of the component. The device must with-ST-F 0.5 Kg without failure of the termination attached to component.			
Component Adhesion (Push test)	1. No visible mechanical damage				
Component Adhesion (Pull test)	1. No visible mechanical damage	Insert 10cm wire into the remaining open eye bend ,the ends of even wire lengths upward and wind together. Terminal shall not be remarkably damaged.			
Thermal shock	1. No visible mechanical damage2. Central Freq. change :within $\pm 6\%$ PhaseTemperature(°C)Time(min)1 $\pm 110\pm 5^{\circ}$ C 30 ± 3 2RoomWithin Temperature3 $-40\pm 2^{\circ}$ C 30 ± 3 4Room TemperatureWithin Sec	+110°C => $30\pm3min$ -40°C => $30\pm3min$ Test cycle:10 cycles The chip shall be stabilized at normal condition for 2~3 hours before measuring.			
Resistance to High Temperature	 No visible mechanical damage Central Freq. change :within ±6% No disconnection or short circuit. 	Temperature: +110±5℃ Duration: 1000±12hrs The chip shall be stabilized at normal condition for 2~3 hours before measuring.			
Resistance to Low Temperature	 No visible mechanical damage Central Freq. change :within ±6% No disconnection or short circuit. 	Temperature:-40±5℃ Duration: 1000±12hrs The chip shall be stabilized at normal condition for 2~3 hours before measuring.			
Humidity	 No visible mechanical damage Central Freq. change :within ±6% No disconnection or short circuit. 	Temperature: 40±2°C Humidity: 90% to 95% RH Duration: 1000±12hrs The chip shall be stabilized at normal condition for 2~3 hours before measuring.			

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5.Soldering and Mounting

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

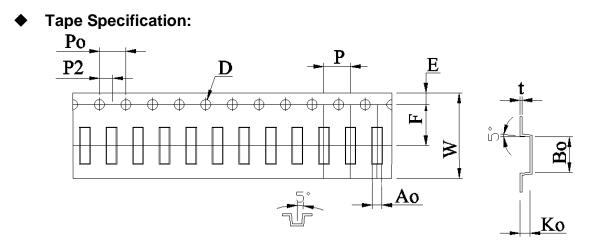


Recommended temperature profiles for re-flow soldering in Figure 1.

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

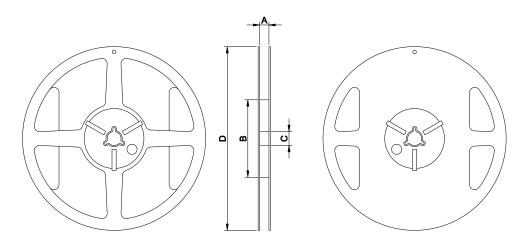
- Preheat circuit and products to $150^\circ C$
- · Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm
- 280°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 3 sec.

6.Packaging Information



W	Ao	Во	Ко	Р	F	Е	D	D1	Ро	P2	t
12.0	1.30	5.50	1.50	4.00	5.50	1.75	1.50	0.50	4.00	2.00	0.25
±0.30	±0.10	±0.10	±0.10	±0.10	±0.05	±0.10	±0.10	±0.10	±0.10	±0.10	±0.05

Reel Specification: (7", Φ180 mm)



7" x 12 mm

Tape Width(mm)	A(mm)	B(mm)	C(mm)	D(mm)	Chip/Reel(pcs)
12	12±1.0	60±2	13.5±0.5	178±2	3000

7. Storage and Transportation Information

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~ 40 $^\circ \! \mathbb{C}$ and 30~70% RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation Conditions

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.