

RISC 32-bit CPU
DC-96MHz operation
73KB data RAM
8KB I-cache 2way
1KB Rocache 1way
64 Vectored interrupts
8 Levels interrupt priority
Flexible I/O
17 GPIO pins
All GPIO pins can be programmable as input
or output individually
All GPIO pins are internal pull-up/pull-down
selectable individually
CMOS/TTL level schmitt triggered input
External wake up/interrupt on all GPIOs
Peripheral Feature
One Full Speed USB OTG controller
Four Multi-function 32-bit timers, support
capture and PWM mode
Three full-duplex advanced UART(DMA)
Three SPI interface supports host and device
mode (DMA)
One IIC interface supports host and device
mode
RTC,with alarm clock and time base to wake
up the chip
16-bit PWM generator for motor driving
Three IQ Encoder
8 channels 10-bit ADC
1 channel 8 levels Low Power Detector
Embedded PMU support low power mode
2 Crystal Oscillator
Watchdog
Power-on reset
Bluetooth Feature
CMOS single-chip fully-integrated radio and
baseband

Compliant with Bluetooth

Bluetooth Piconet and Scatternet support

Meet class2 and class3 transmitting power requirement

Support GFSK and $\pi/4$ DQPSK all packet types

Receiver with -92dBm sensitivity

Support

a2dp\avctp\avdtp\avrcp\hfp\spp\smp\att\gap\
gatt\rfcomm\sdp\l2cap profile

Power Supply

DC 3.7V form battery or DC 5V form USB port