APPLICANT LETTERHEAD

Date: 13/12/2021

DEKRA Testing and Certification, S.A.U. Parque Tecnológico de Andalucía C/ Severo Ochoa 2 & 6 29590 Campanillas Málaga, España

Ref: Confidentiality request for FCC ID: 2ASAL1407

To whom it may concern:

Pursuant to §0.457(d)(1)(ii) and §0.459 of the Commission's Rules (47 C.F.R.) and §552 (b)(4) of the Freedom of Information Act, **LEDGER SAS** hereby request requests that a part of the subject FCC application be held confidential to avoid release of sensitive information of the product to the public.

For the product stated above, we request that the following information be withheld from public disclose:

Type of Confidentiality Requested		<u>Exhibit</u>
☐ Short Term	□ Permanent	Block Diagrams
Short Term		External Photos
Short Term		Internal Photos
☐ Short Term	□ Permanent	Operation Description
☐ Short Term	□ Permanent	Parts List & Placement/BOM
☐ Short Term	Permanent	Tune-Up Procedure
☐ Short Term	□ Permanent	Schematics
Short Term		Test Setup Photos
Short Term		User's Manual

Permanent Confidentiality:

The above materials contain trade secrets and proprietary information not customarily released to the public. The public disclosure of these matters might be harmful to us and provide unjustified benefits to our competitors.

Short-Term Confidentiality:

We hereby request short-term confidentiality for the product stated above to avoid premature release of sensitive information prior to marketing or release of the product to the public.

Release date of short term confidentiality is:

45 days from grant date marked	П
90 days from grant date marked	П
135 days from grant date marked	
180 days from grant date marked	$\overline{\boxtimes}$
Specific data:	

We are also aware that we are responsible to notify AT4 wireless in the event information regarding the product or the product is made available to the public. AT4 wireless will then release the documents listed above for public disclosure pursuant to FCC Public Notice DA 04-1705.

Sincerely,

By: Christian Kaluza

Title: Head of Hardware Engineering

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