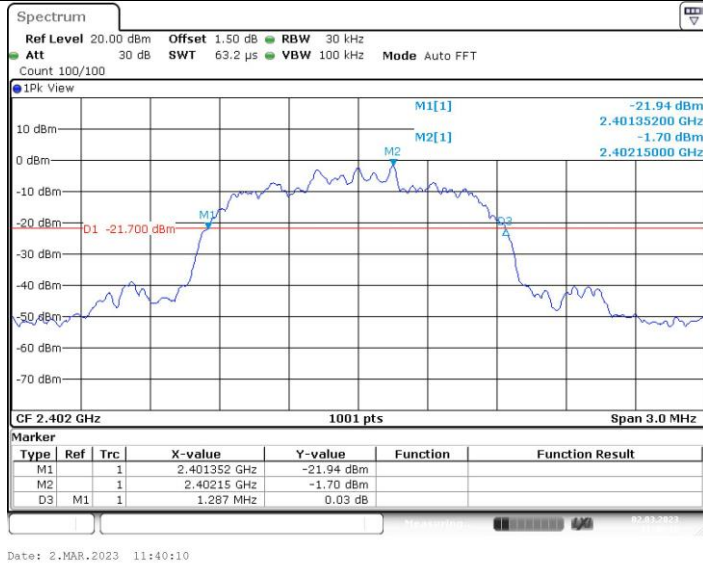


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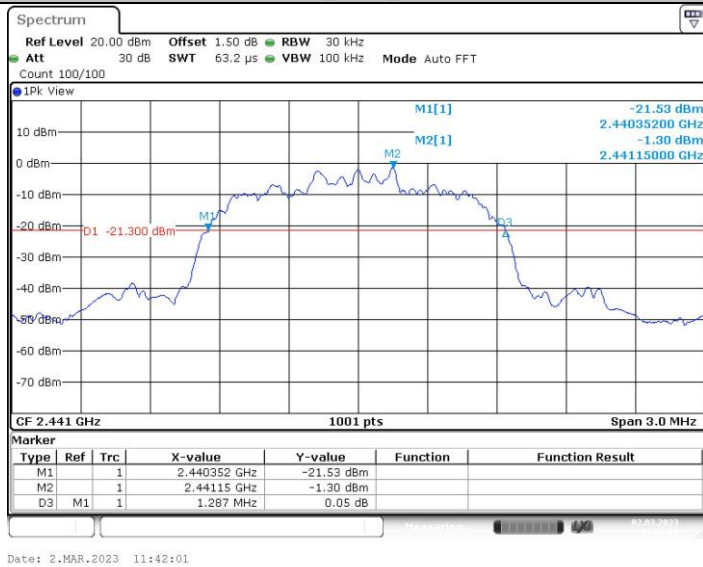
1-2/F., Building 2, Jiaquan Building, Guanlan High-Tech Park, Shenzhen, Guangdong, China
Tel.: (86)755-27521059

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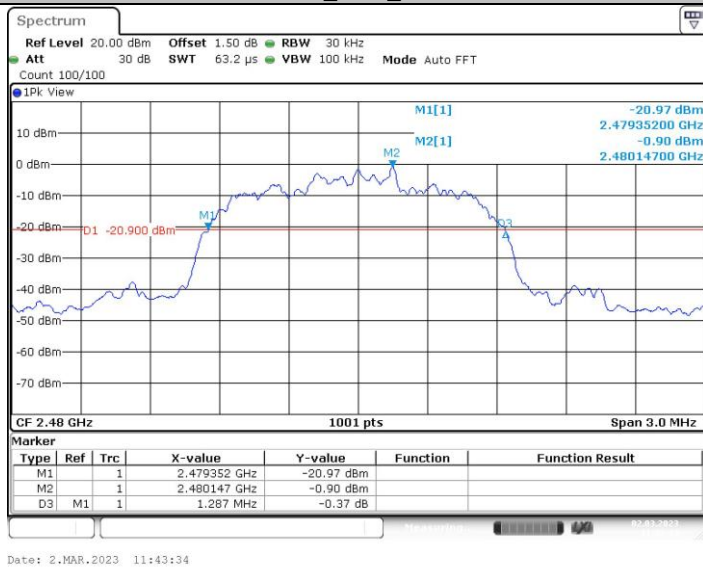




2DH5_Ant1_2441



2DH5_Ant1_2480



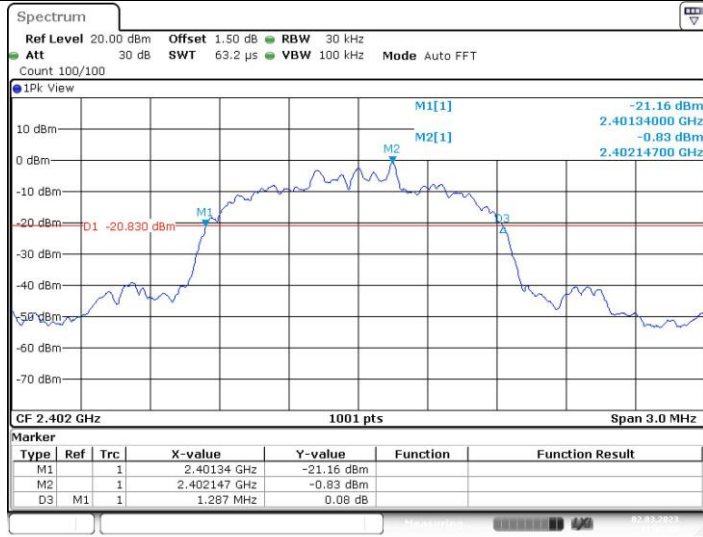
3DH5_Ant1_2402

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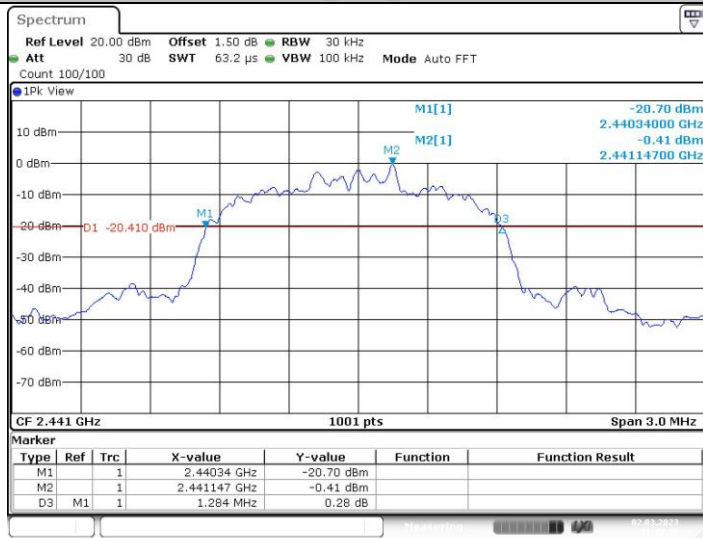
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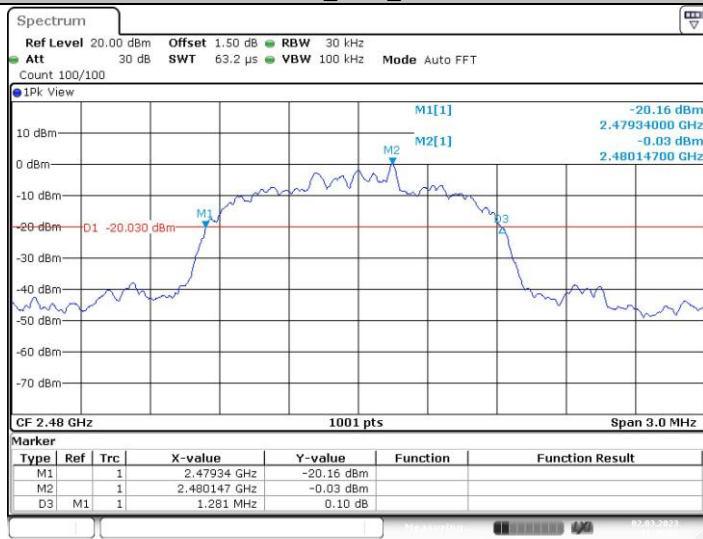
Date: 2.MAR.2023 11:45:24

3DH5_Ant1_2441



Date: 2.MAR.2023 11:47:36

3DH5_Ant1_2480



Date: 2.MAR.2023 11:49:07

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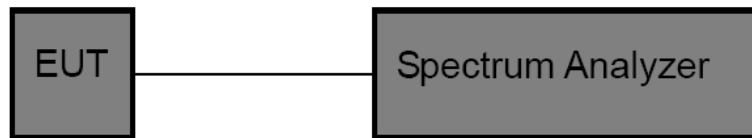
3.6. Channel Separation

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(1)/ RSS-247 5.1 b :

Test Item	Limit	Frequency Range(MHz)
Channel Separation	>25KHz or >two-thirds of the 20 dB bandwidth Which is greater	2400~2483.5

Test Configuration



Test Procedure

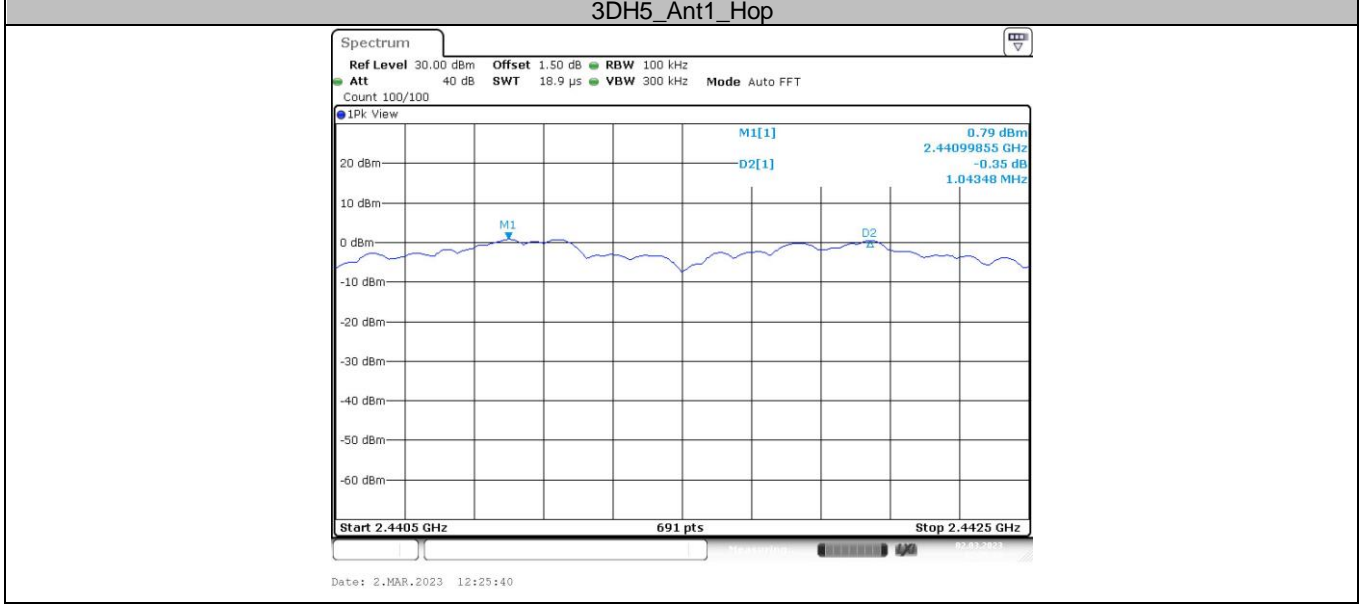
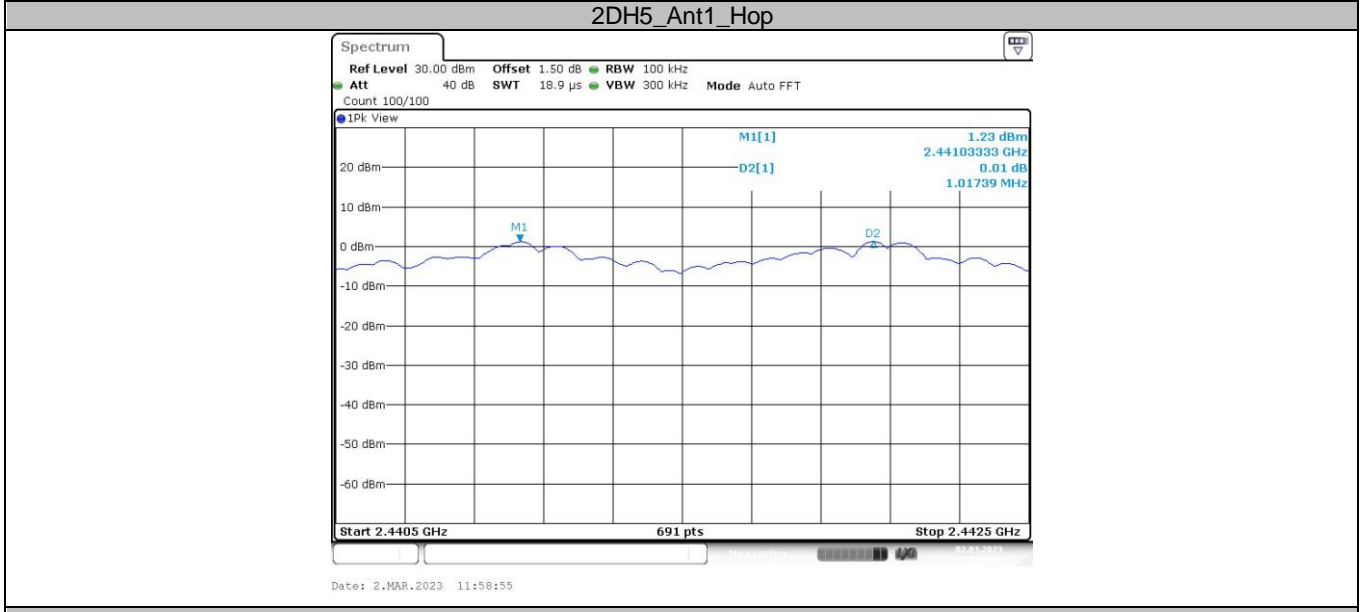
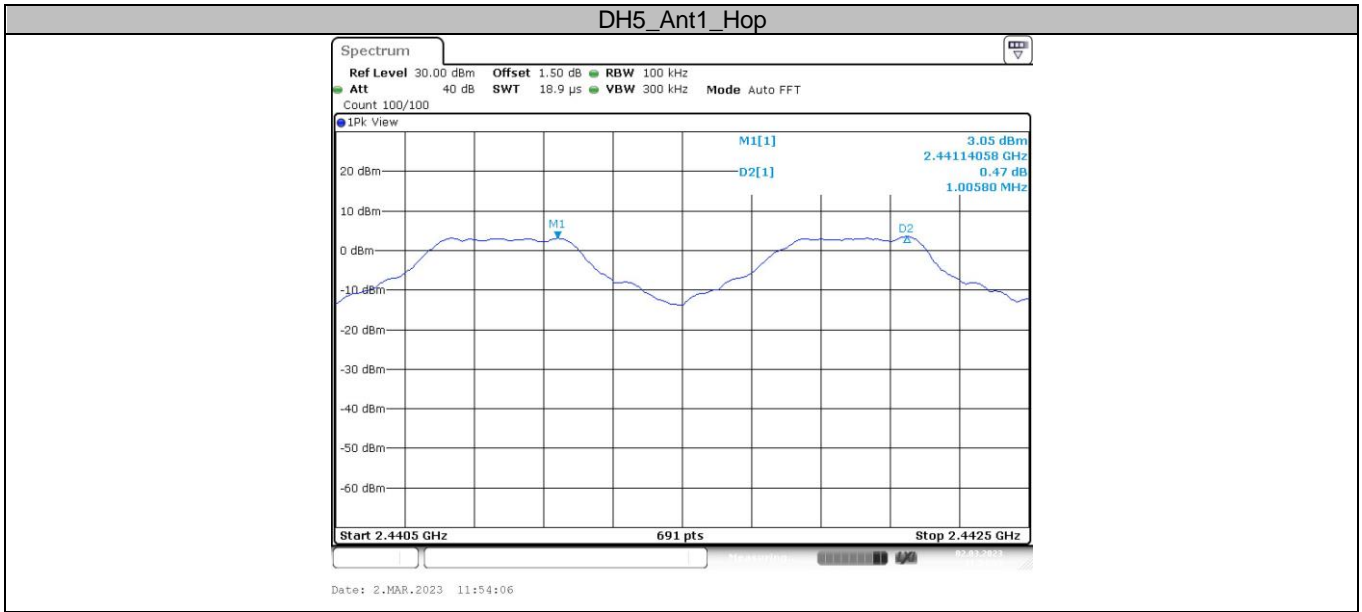
7. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
8. Spectrum Setting:
 - (1) Set RBW = 100 kHz.
 - (2) Set the video bandwidth (VBW) ≥ 3 RBW.
 - (3) Detector = Peak.
 - (4) Trace mode = Max hold.
 - (5) Sweep = Auto couple.

Test Mode

Please refer to the clause 2.4.

Test Results

Test Mode	Frequency(MHz)	Result[MHz]	Limit[MHz]	Verdict
GFSK	Hop_2441	1.006	≥ 0.693	PASS
$\pi/4$ -DQPSK	Hop_2441	1.017	≥ 0.860	PASS
8-DPSK	Hop_2441	1.043	≥ 0.860	PASS



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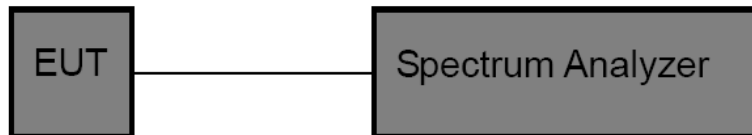
3.7. Number of Hopping Channel

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(iii)/ RSS-247 5.1 d:

Section	Test Item	Limit
15.247 (a)(iii)/ RSS-247 5.1 d:	Number of Hopping Channel	>15

Test Configuration



Test Procedure

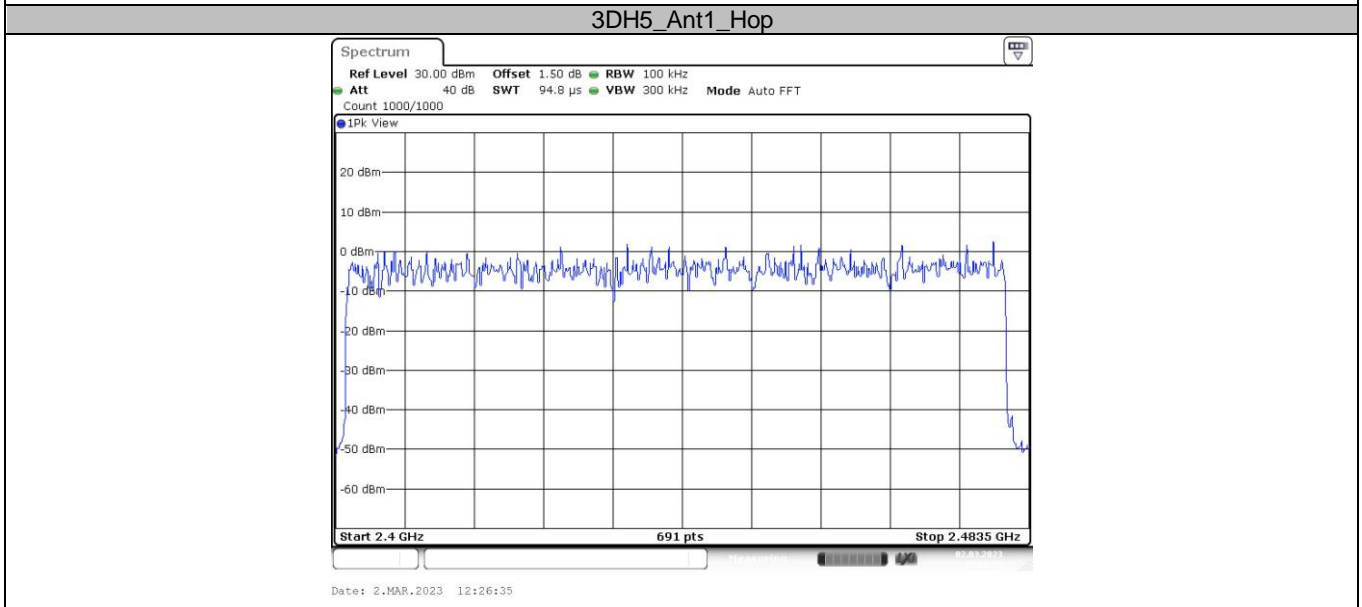
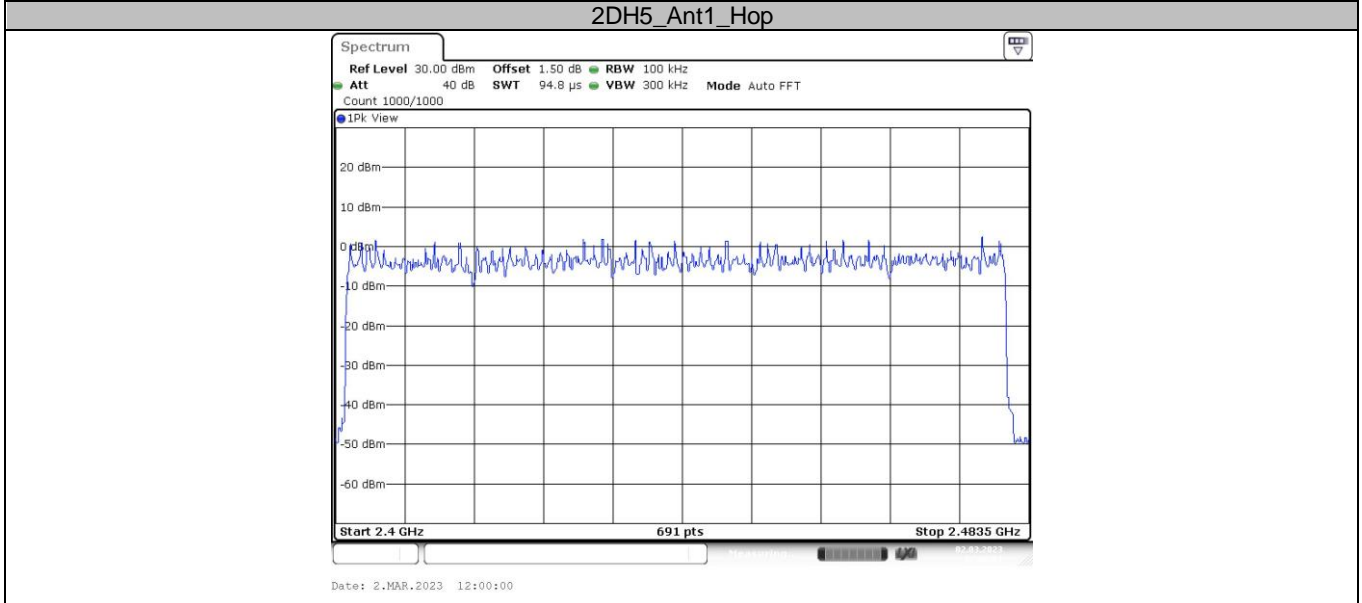
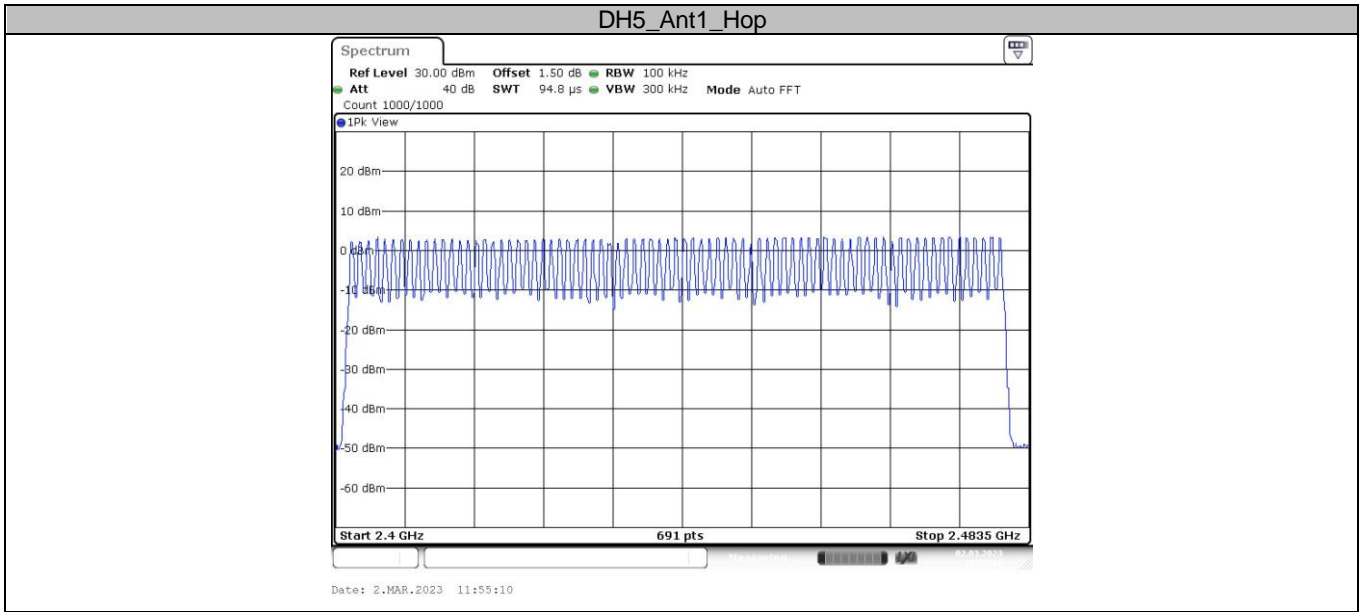
- The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
- Spectrum Setting:
 - Peak Detector: RBW=100 kHz, VBW≥RBW, Sweep time= Auto.

Test Mode

Please refer to the clause 2.4.

Test Result

Modulation type	Channel number	Limit	Result
GFSK	79	≥15.00	Pass
$\pi/4$ -DQPSK	79		
8DPSK	79		



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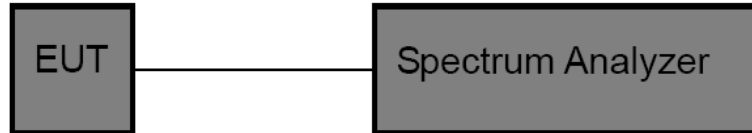


3.8. Dwell Time

Limit

Section	Test Item	Limit
15.247(a)(iii)/ RSS-247 5.1 d	Average Time of Occupancy	0.4 sec

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
 - (1) Spectrum Setting: RBW=1MHz, VBW≥RBW.
 - (2) Use video trigger with the trigger level set to enable triggering only on full pulses.
 - (3) Sweep Time is more than once pulse time.
 - (4) Set the center frequency on any frequency would be measure and set the frequency span to zero.
 - (5) Measure the maximum time duration of one single pulse.
 - (6) Set the EUT for packet transmitting.

Test Mode

Please refer to the clause 2.4.

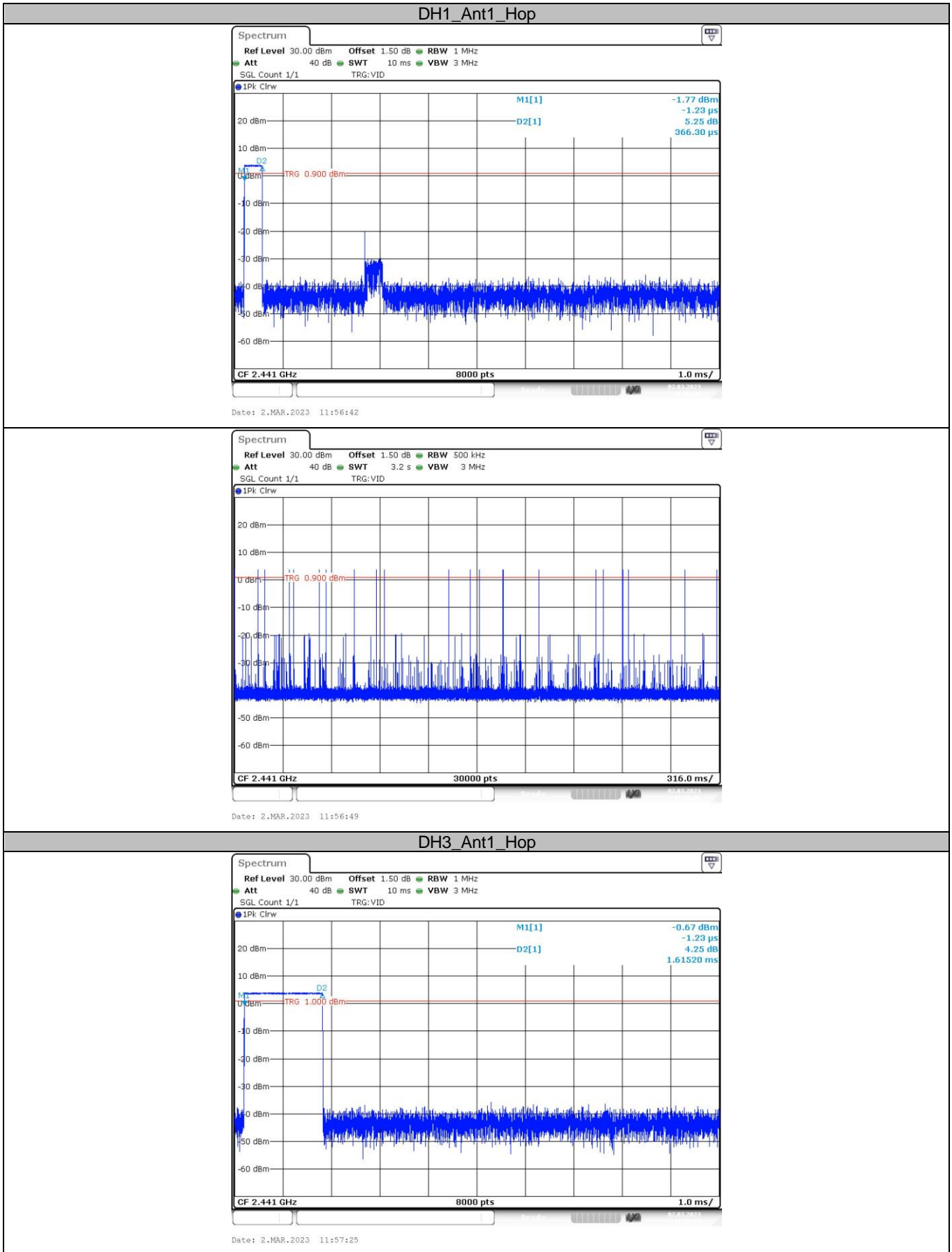
**Test Result**

Modulation type	Channel	Frequency (MHz)	Pulse Time (ms)	Total of Dwell (ms)	Period Time (ms)	Limit (Second)	Result
GFSK	DH1	2441	0.37	118.4	31.60	≤ 0.40	Pass
	DH3	2441	1.62	259.2	31.60		
	DH5	2441	2.86	305.1	31.60		
π / 4-DQPSK	2DH1	2441	0.38	121.6	31.60	≤ 0.40	Pass
	2DH3	2441	1.62	259.2	31.60		
	2DH5	2441	2.86	305.1	31.60		
8-DPSK	3DH1	2441	0.38	121.6	31.60	≤ 0.40	Pass
	3DH3	2441	1.62	259.2	31.60		
	3DH5	2441	2.86	305.1	31.60		

Note: 1DH1/2DH1/3DH1 Total of Dwell= Pulse Time*(1600/2)*31.6/79

1DH3/2DH3/3DH3 Total of Dwell= Pulse Time*(1600/4)*31.6/79

1DH5/2DH5/3DH5 Total of Dwell= Pulse Time*(1600/6)*31.6/79



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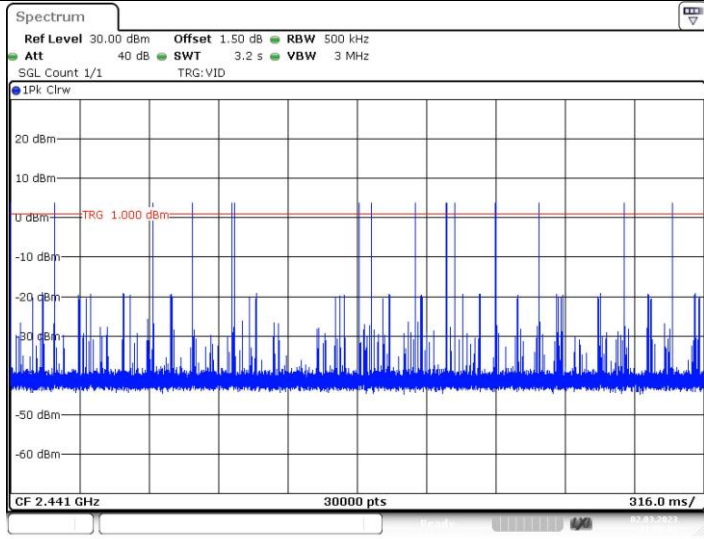
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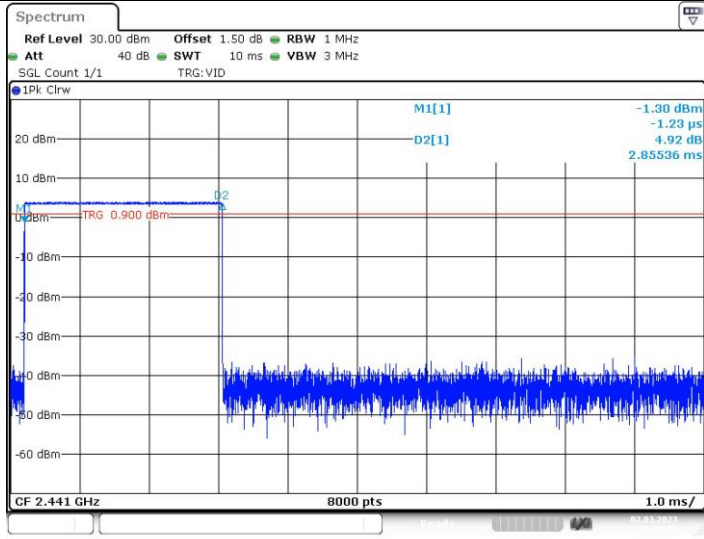


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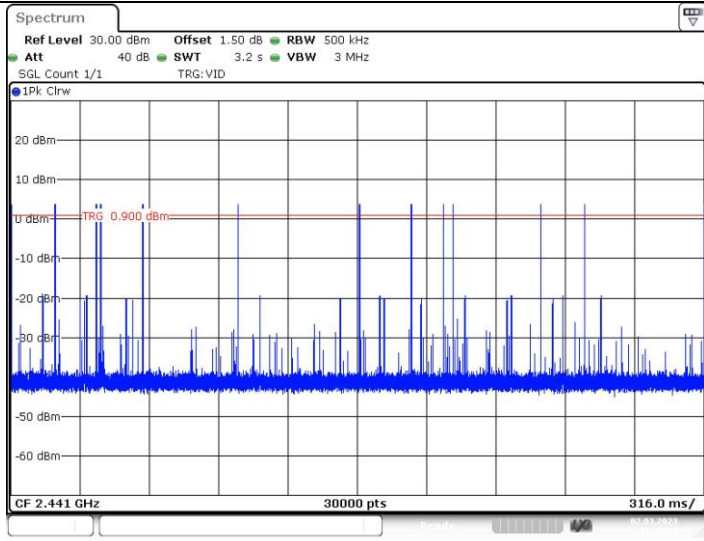


Date: 2.MAR.2023 11:57:31

DH5_Ant1_Hop



Date: 2.MAR.2023 11:55:27



Date: 2.MAR.2023 11:55:34

2DH1_Ant1_Hop

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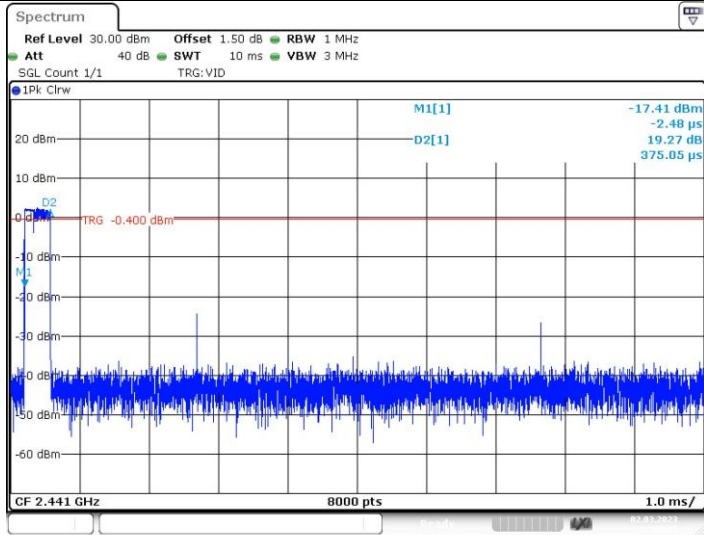
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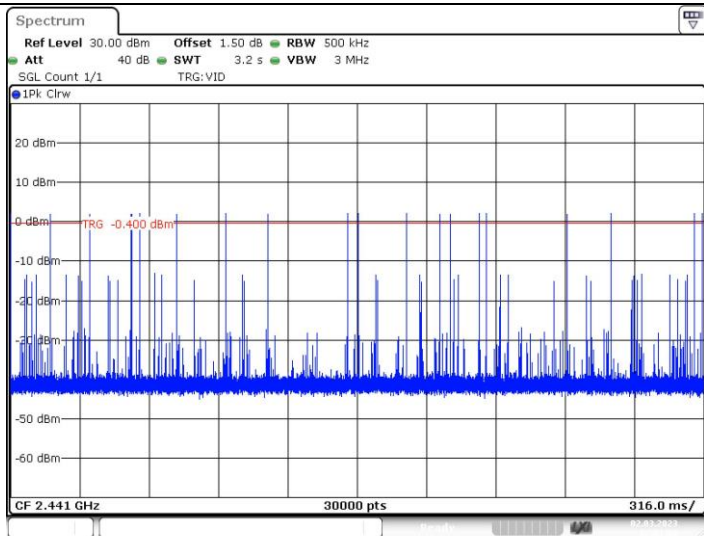
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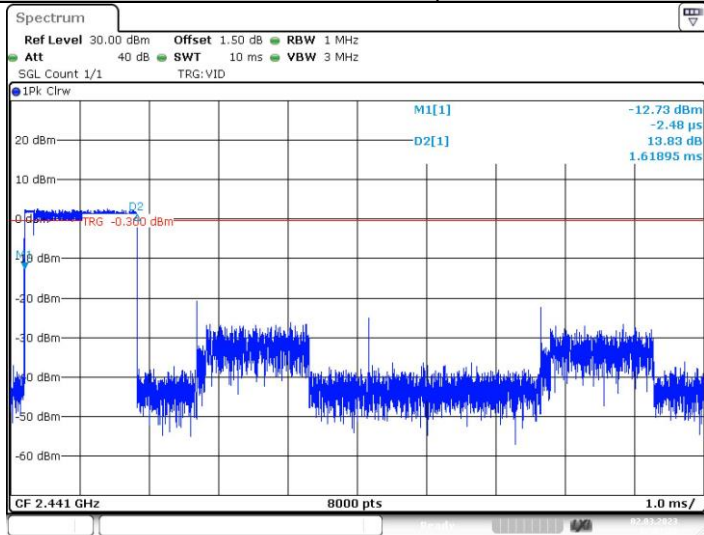


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Date: 2.MAR.2023 12:01:00

2DH3_Ant1_Hop



Date: 2.MAR.2023 12:01:26

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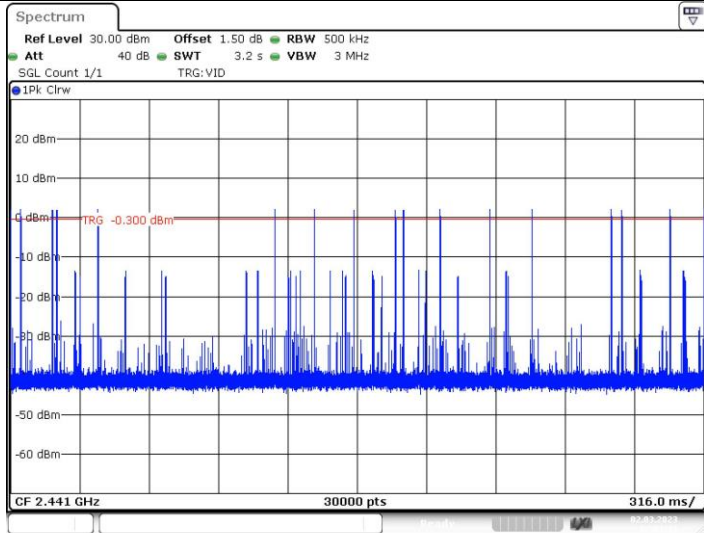
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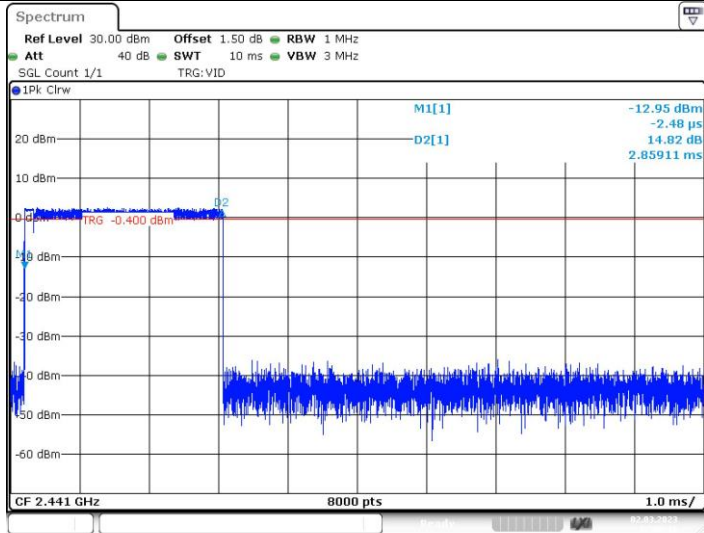


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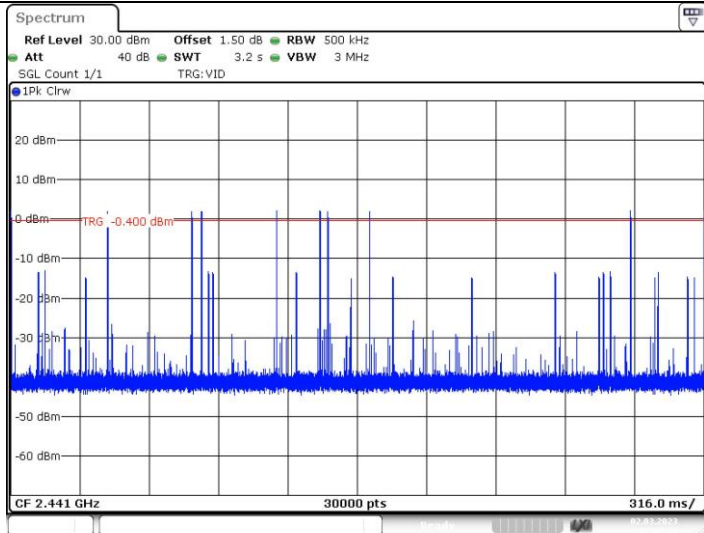


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2DH5_Ant1_Hop



Date: 2.MAR.2023 12:00:18



Date: 2.MAR.2023 12:00:24

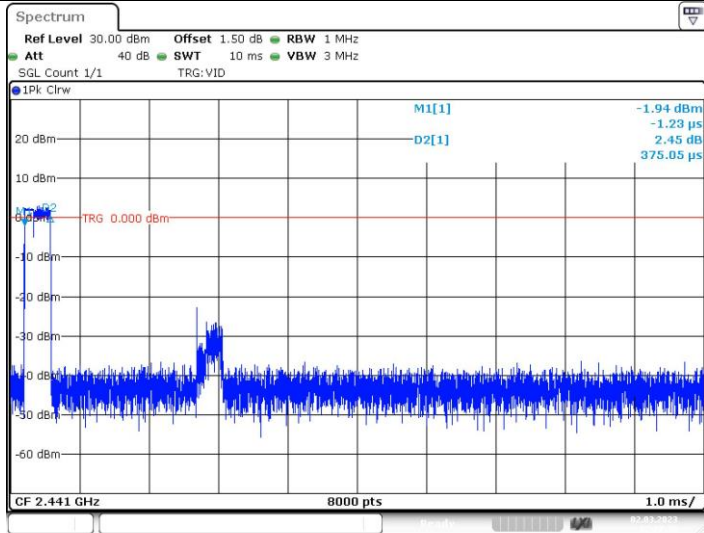
3DH1_Ant1_Hop

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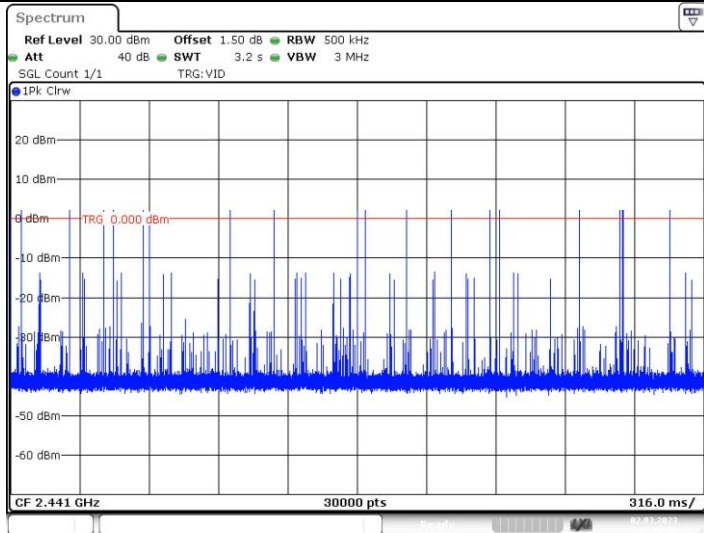
1-2/F., Building 2, Jiaquan Building, Guanlan High-Tech Park, Shenzhen, Guangdong, China
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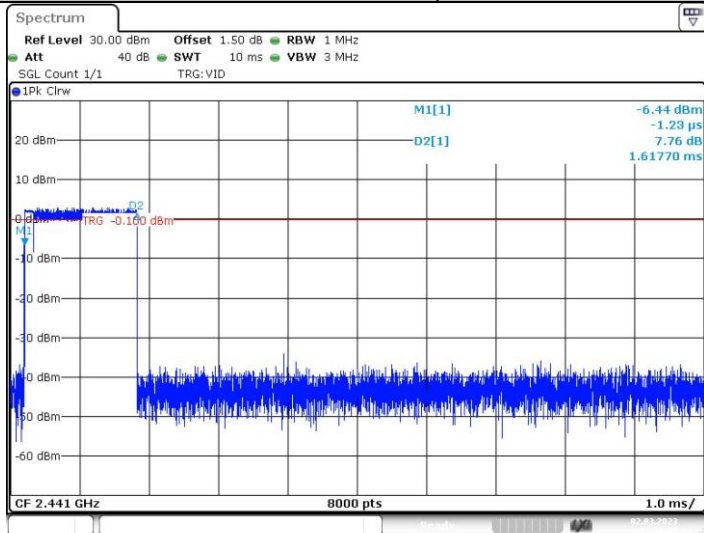


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3DH3_Ant1_Hop



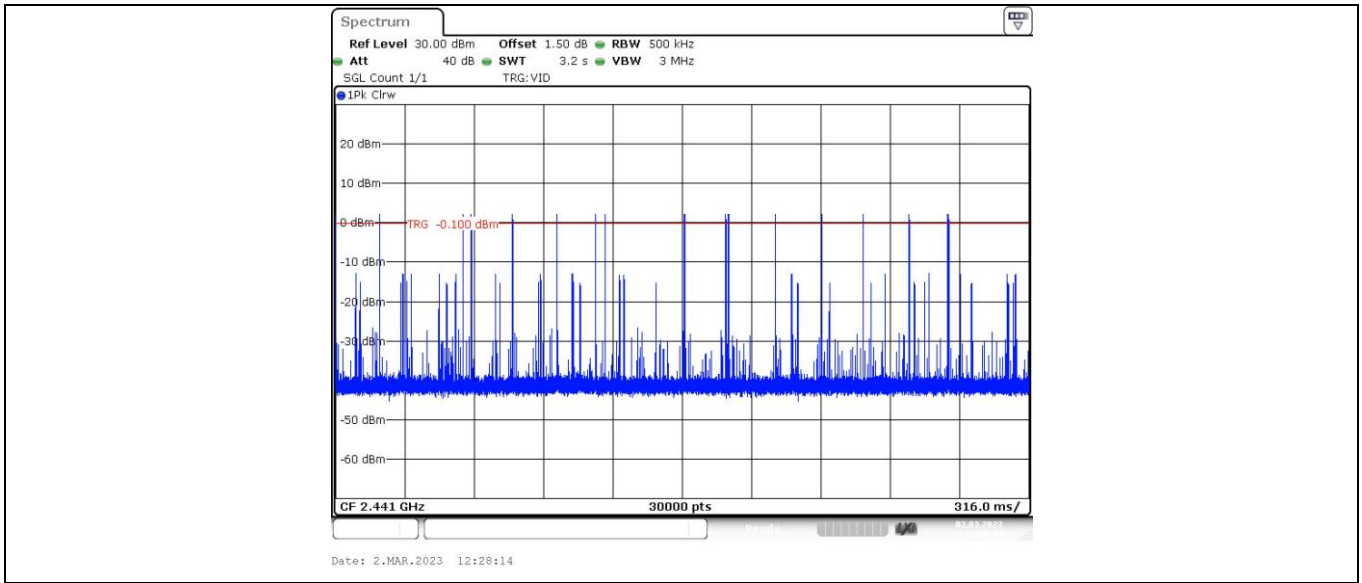
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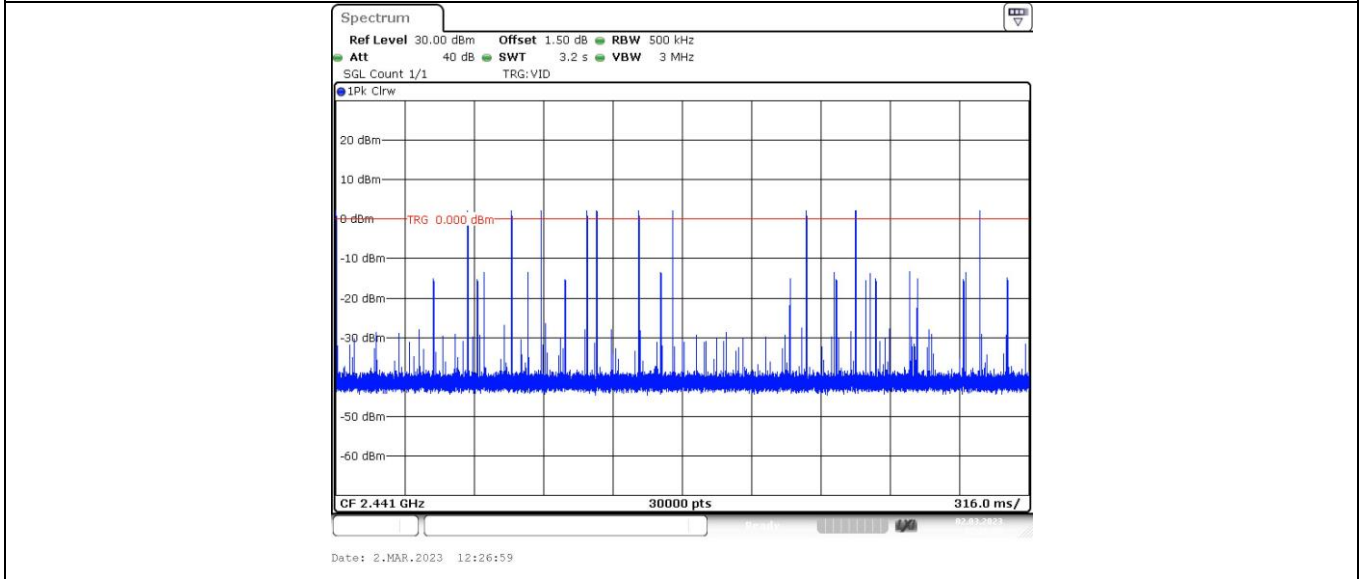
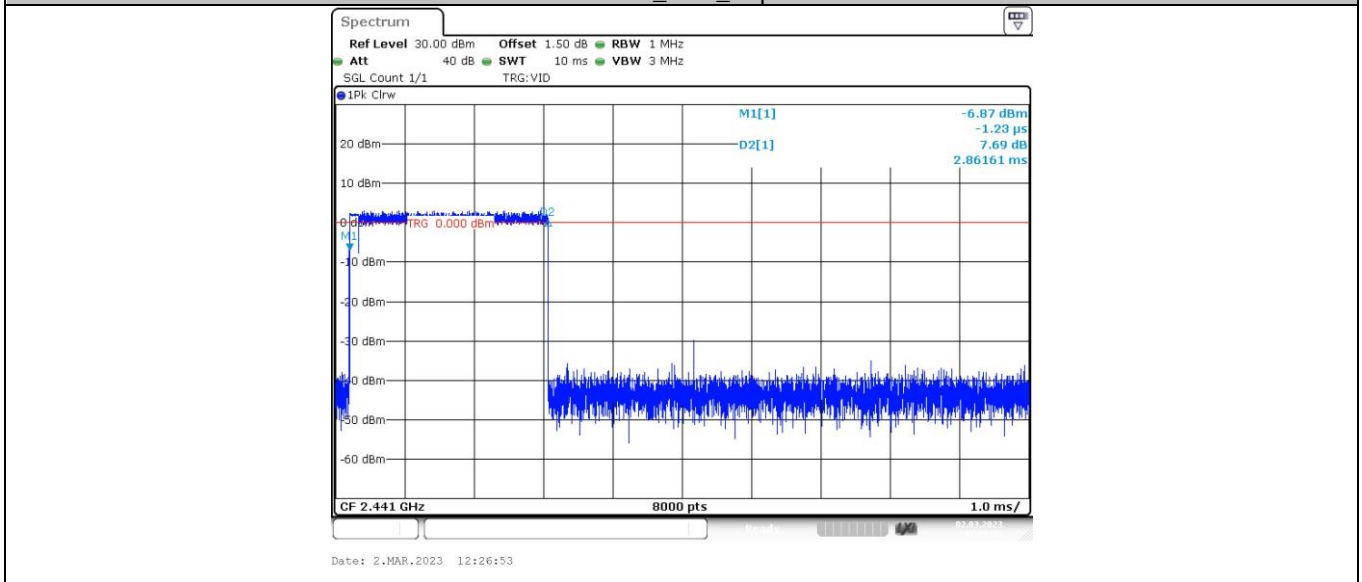
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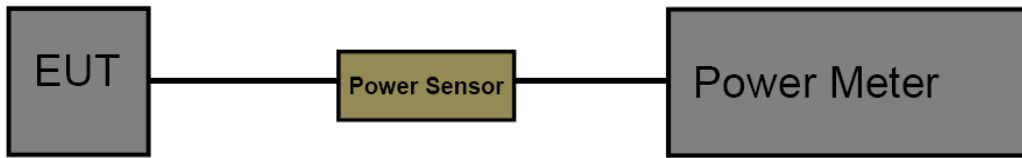
3.9. Peak Output Power

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (b)(1) / RSS-247 5.4 b:

Test Item	Limit	Frequency Range(MHz)
Peak Output Power	Hopping Channels>75 Power<1W(30dBm) Other <125mW(21dBm)	2400~2483.5

Test Configuration



Test Procedure

1. The EUT was directly connected to the Power Meter and antenna output port as show in the block diagram above.
2. Read the power value in the test software and record it.

Test Mode

Please refer to the clause 2.4.

Test Result

Test Mode	Frequency (MHz)	Result[dBm]	Limit[dBm]	Verdict
GFSK	2402	3.69	<=30	PASS
	2441	4.03	<=30	PASS
	2480	4.32	<=30	PASS
$\pi/4$ -DQPSK	2402	2.43	<=30	PASS
	2441	2.74	<=30	PASS
	2480	2.93	<=30	PASS
8-DPSK	2402	2.68	<=30	PASS
	2441	3.1	<=30	PASS
	2480	3.46	<=30	PASS

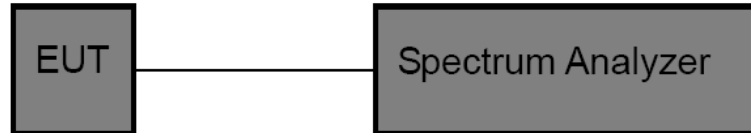


3.10. Duty Cycle

Limit

None, for report purposes only.

Test Configuration



Test Procedure

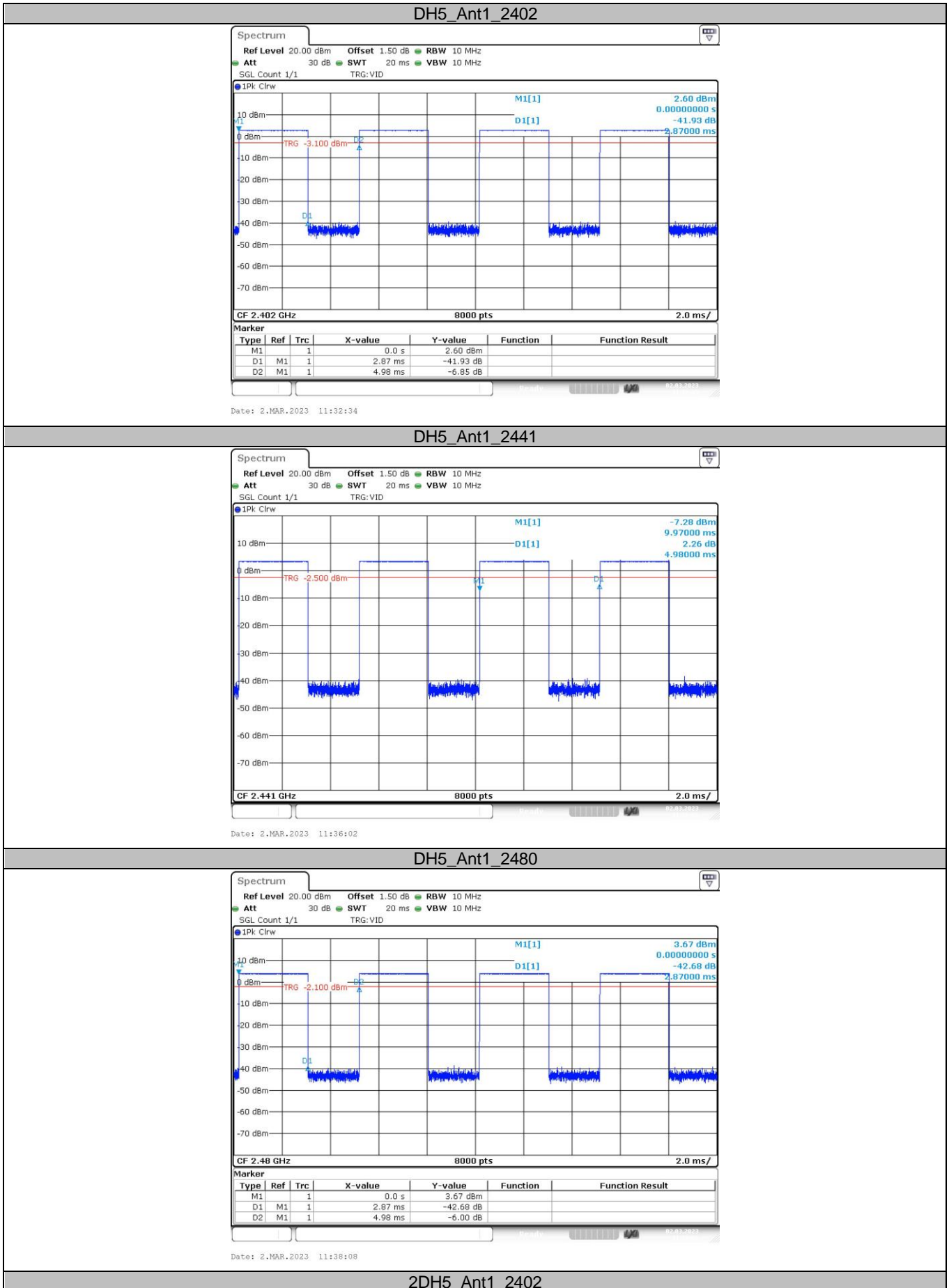
1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. The EUT was directly connected to the Spectrum Analyzer and antenna output port as show in the block diagram above. The measurement according to section 10.2 of KDB 558074 D01 DTS Meas Guidance v05r02.
3. Spectrum Setting:
Set analyzer center frequency to test channel center frequency.
Set the span to 0Hz
Set the RBW to 10MHz
Set the VBW to 10MHz
Detector: Peak
Sweep time: Auto
Allow trace to fully stabilize. Then use the peak marker function to determine the maximum amplitude level.

Test Mode

Please refer to the clause 2.4.

Test Result

Test Mode	Frequency (MHz)	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
GFSK	2402	2.87	4.98	57.63	0.20	1
	2441	2.87	4.98	57.63	0.20	1
	2480	2.87	4.98	57.63	0.20	1
$\pi/4$ -DQPSK	2402	2.87	4.98	57.63	0.20	1
	2441	2.88	4.99	57.72	0.20	1
	2480	2.87	4.98	57.63	0.20	1
8-DPSK	2402	2.88	4.99	57.72	0.20	1
	2441	2.87	4.98	57.63	0.20	1
	2480	2.88	4.98	57.83	0.20	1

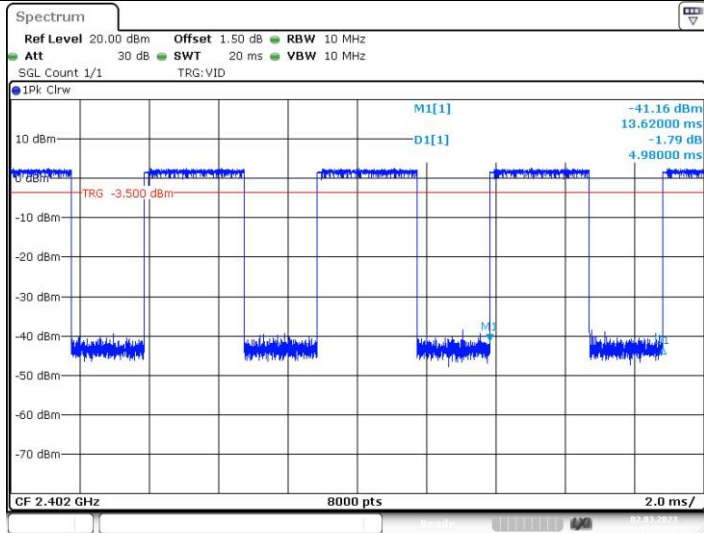


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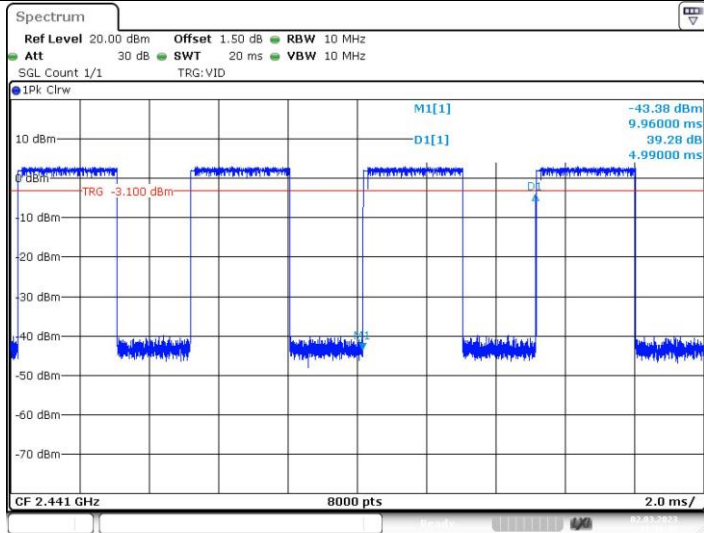
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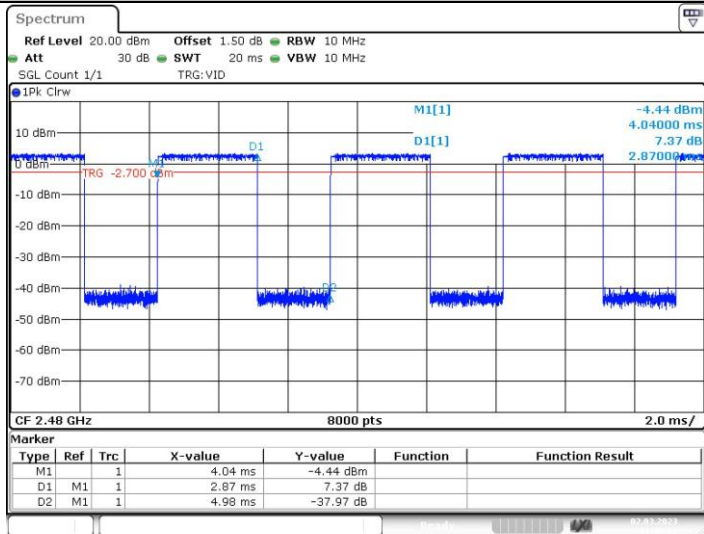
Date: 2.MAR.2023 11:39:57

2DH5_Ant1_2441



Date: 2.MAR.2023 11:41:48

2DH5_Ant1_2480



Date: 2.MAR.2023 11:43:21

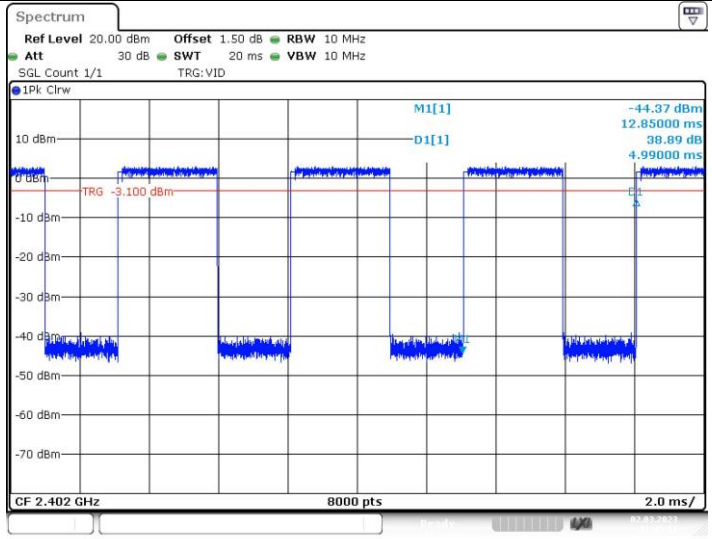
3DH5_Ant1_2402

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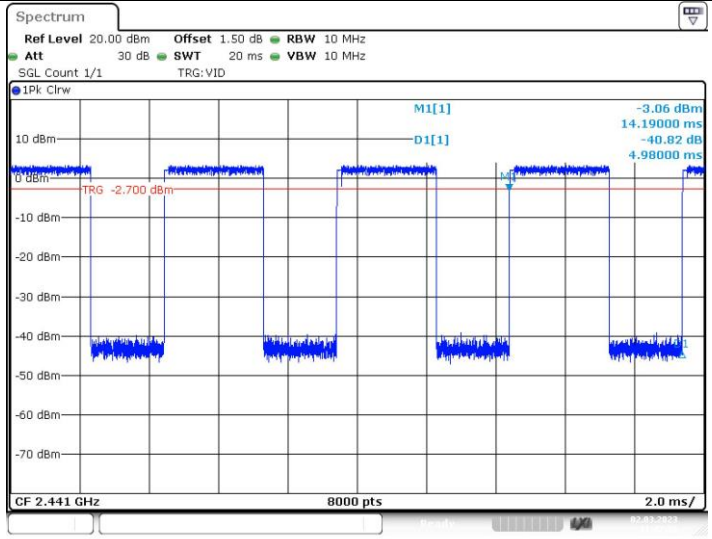
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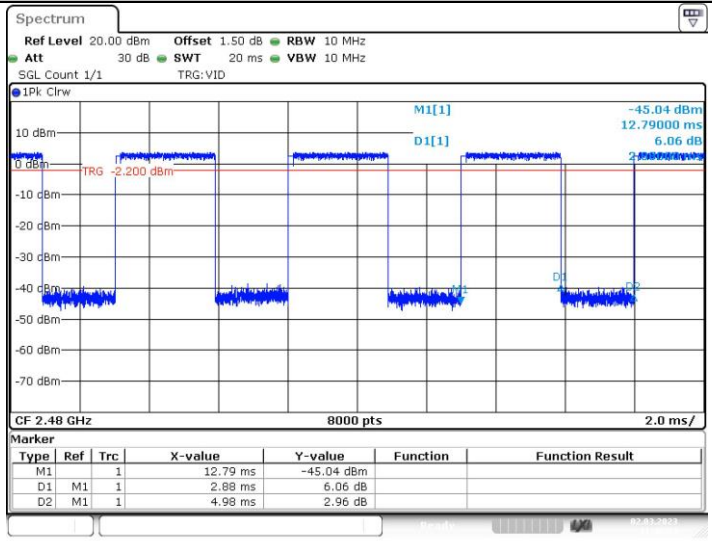
Date: 2.MAR.2023 11:45:11

3DH5_Ant1_2441



Date: 2.MAR.2023 11:47:23

3DH5_Ant1_2480



Date: 2.MAR.2023 11:48:54

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3.11. Antenna Requirement

Requirement

FCC CFR Title 47 Part 15 Subpart C Section 15.203:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

FCC CFR Title 47 Part 15 Subpart C Section 15.247(c) (1)(i):

(i) Systems operating in the 2400~2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.

Test Result

Complies

Directional gain = $G_{ANT} = 5\text{dBi}$

Note: Bluetooth mode only supports SISO mode and does not support MIMO transmission.

*****THE END*****