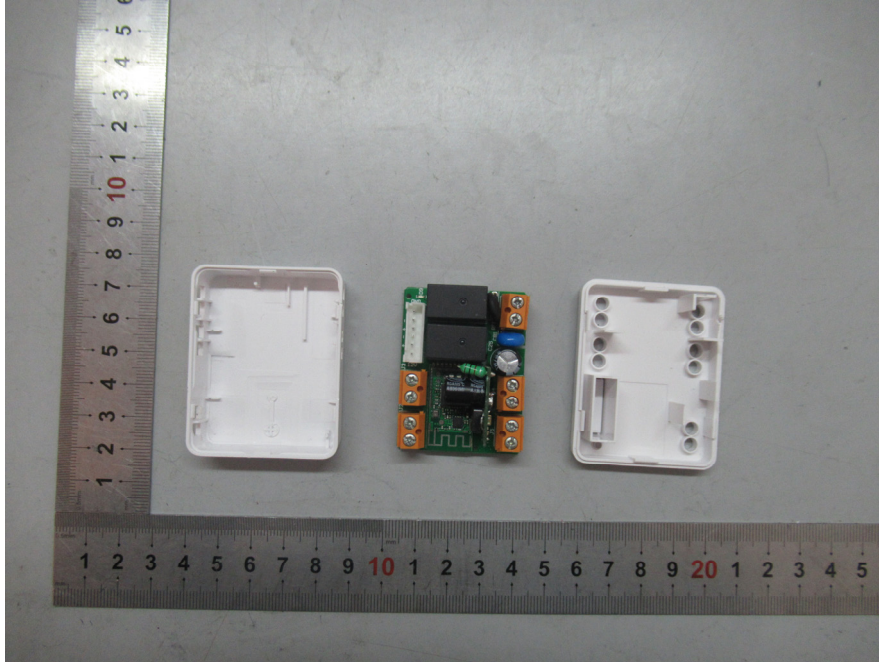
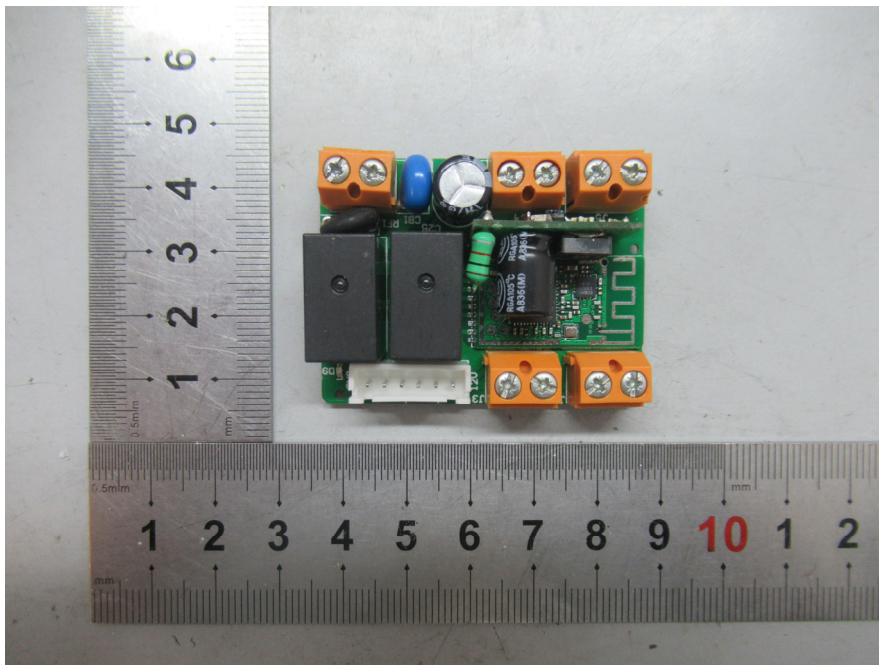


EXHIBIT B - EUT INTERNAL PHOTOGRAPHS

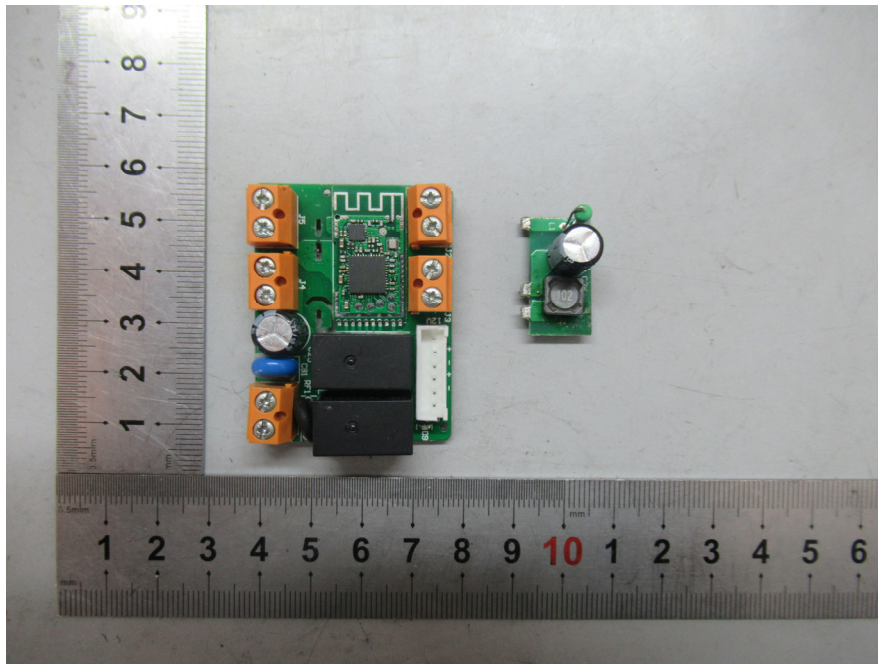
EUT(TERNCY-SM01-D2) - Cover off View-1



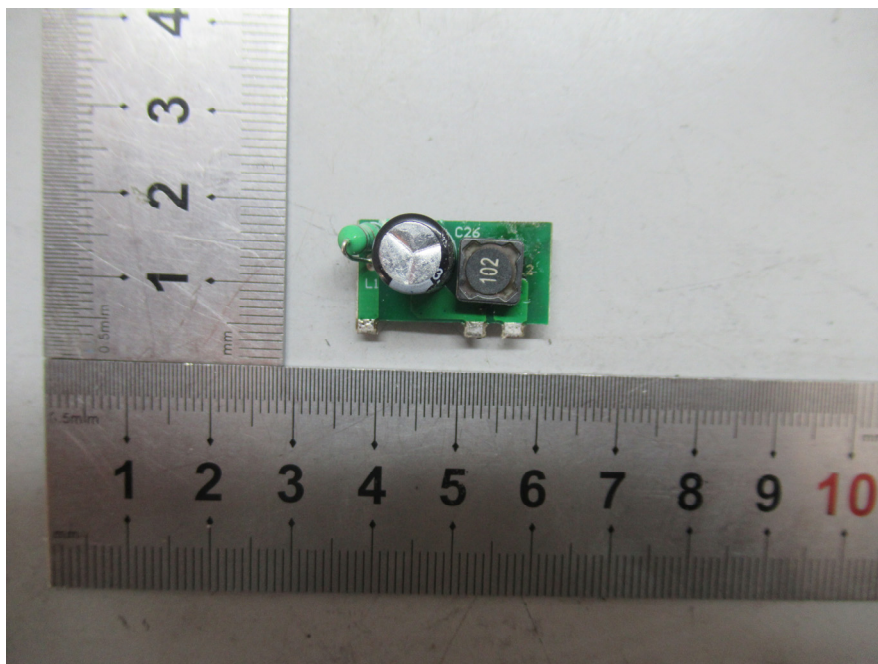
EUT(TERNCY-SM01-D2) – Cover off View-2



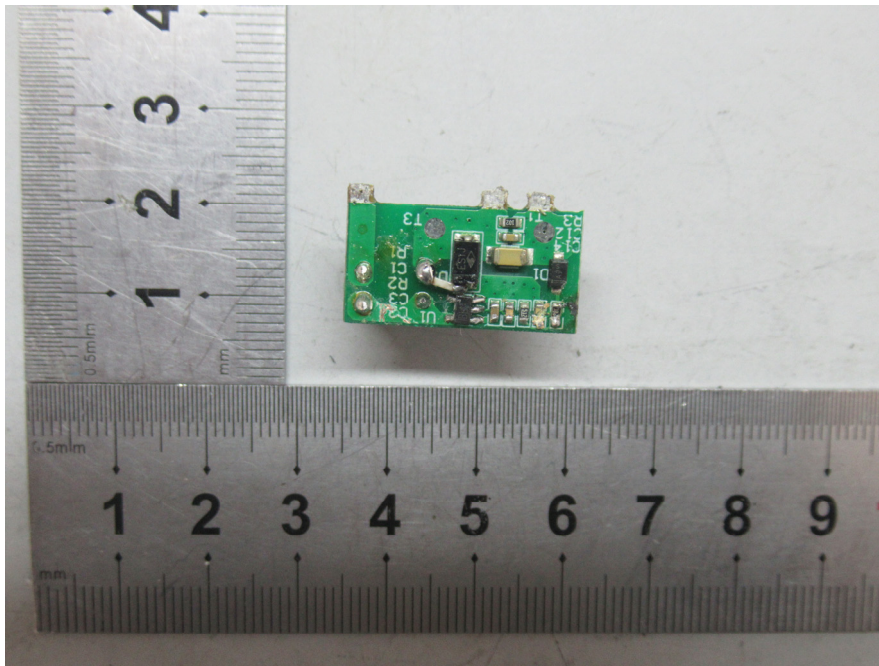
EUT(TERNCY-SM01-D2) – Cover off View-3



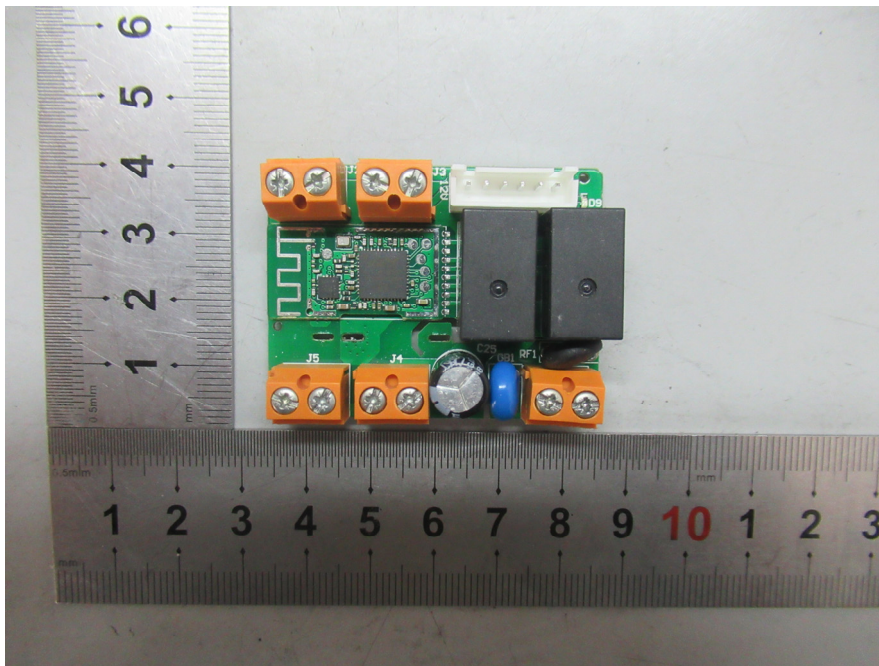
EUT(TERNCY-SM01-D2) – PCB-1 Top View



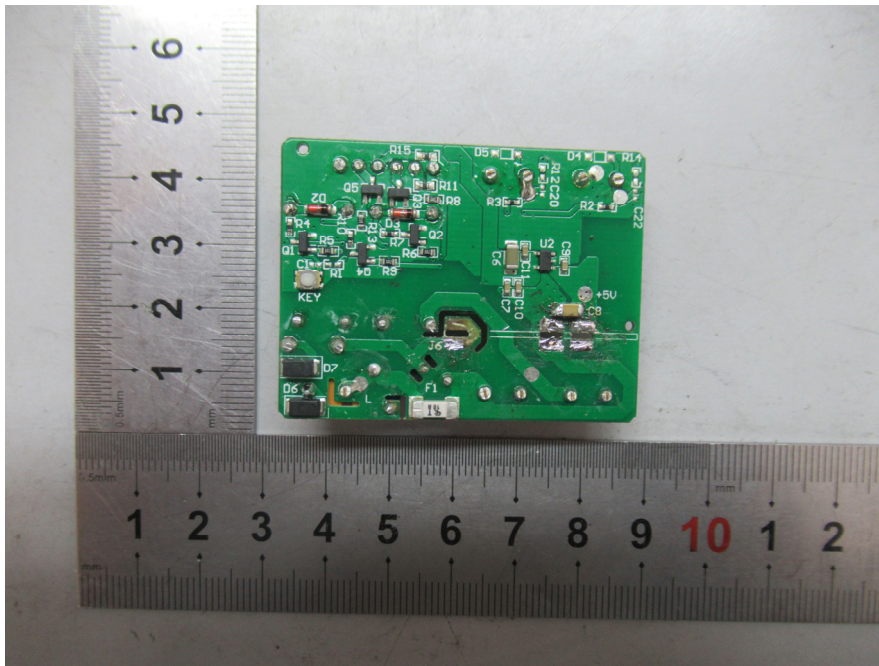
EUT(TERNCY-SM01-D2) – PCB-1 Bottom View



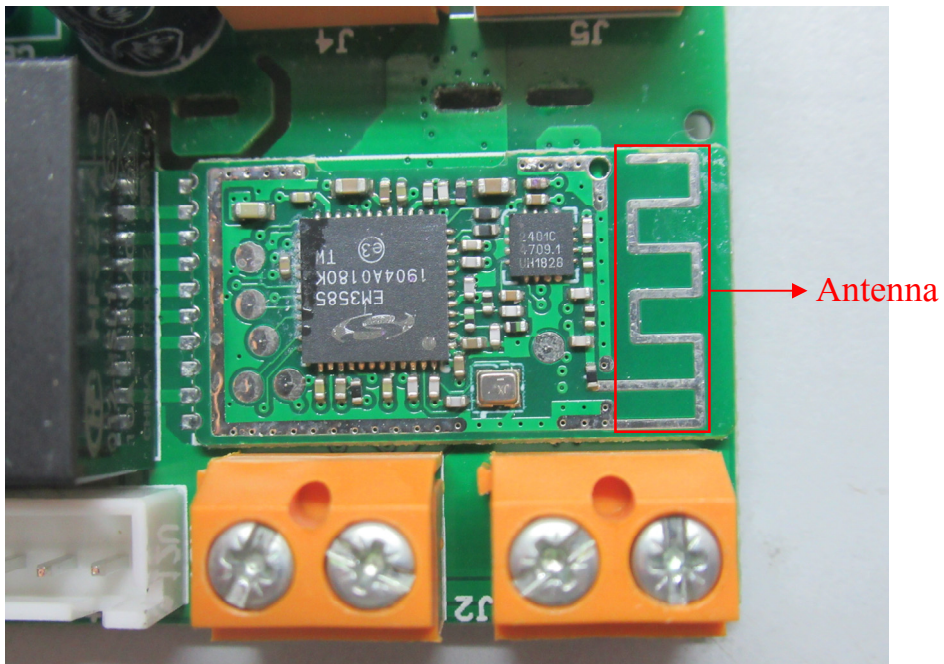
EUT(TERNCY-SM01-D2) – PCB-2 Top View



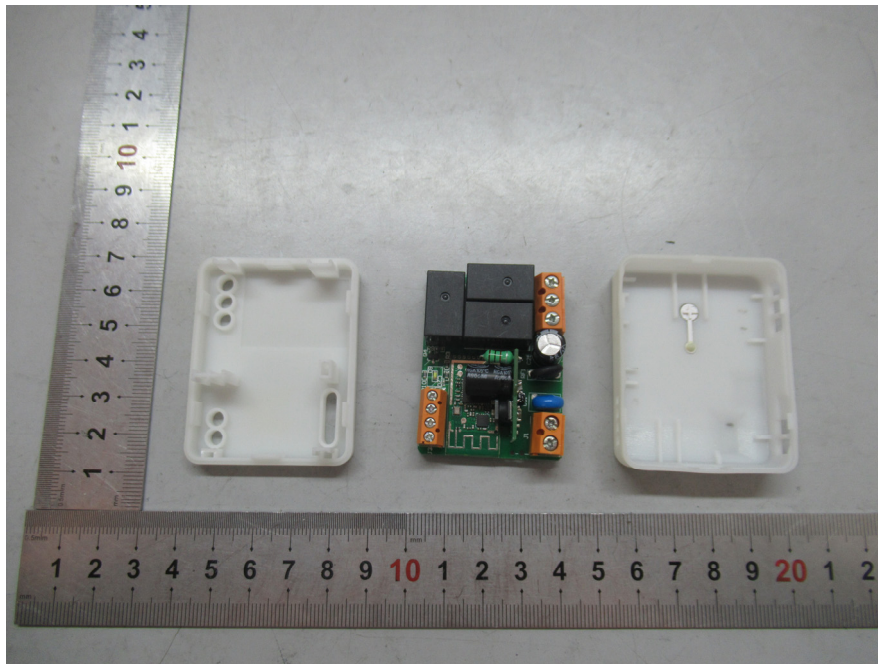
EUT(TERNCY-SM01-D2) – PCB-2 Bottom View



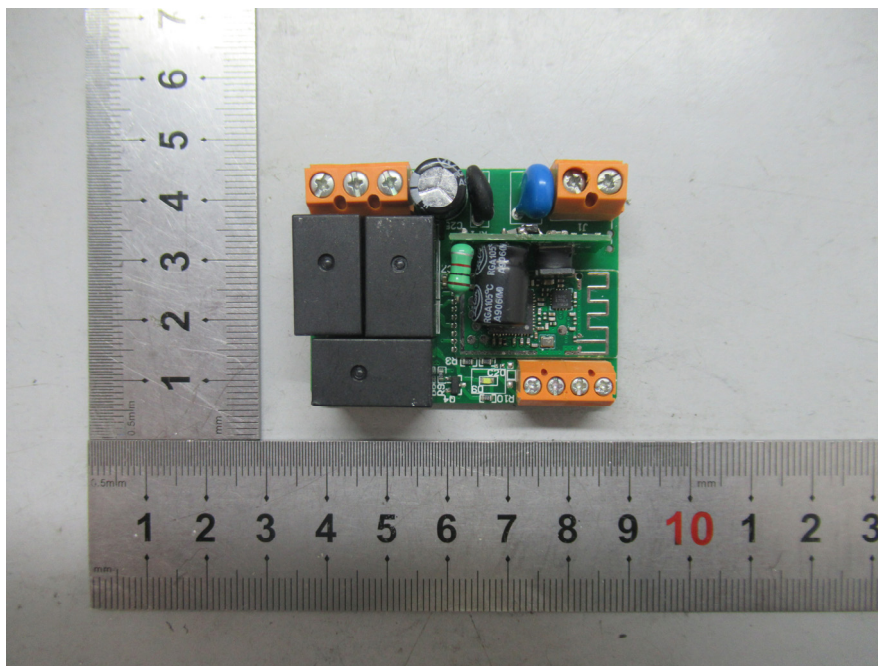
EUT(TERNCY-SM01-D2) – PCB-2 Top Chip View



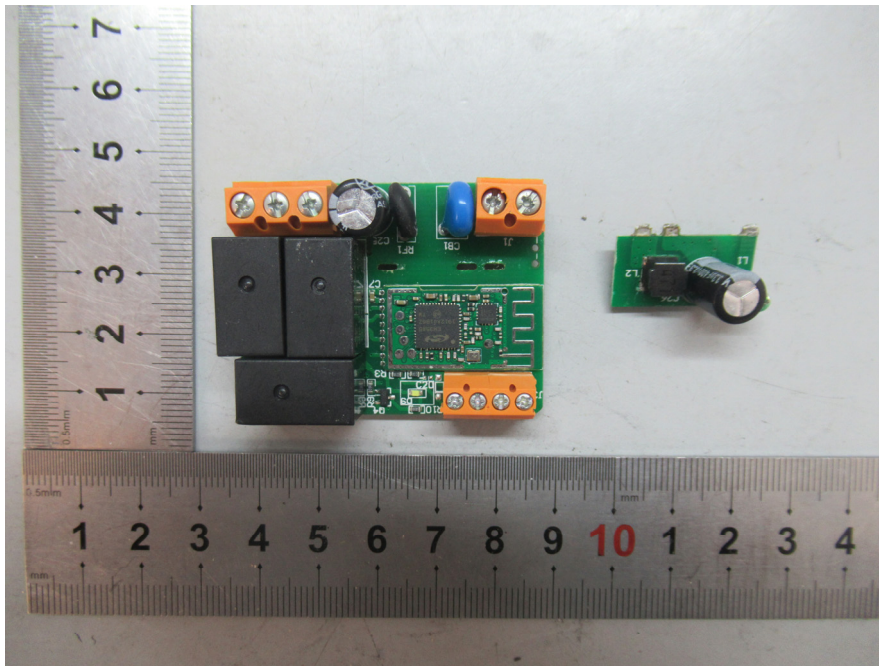
EUT(TERNCY-SM01-D3) – Cover off View-1



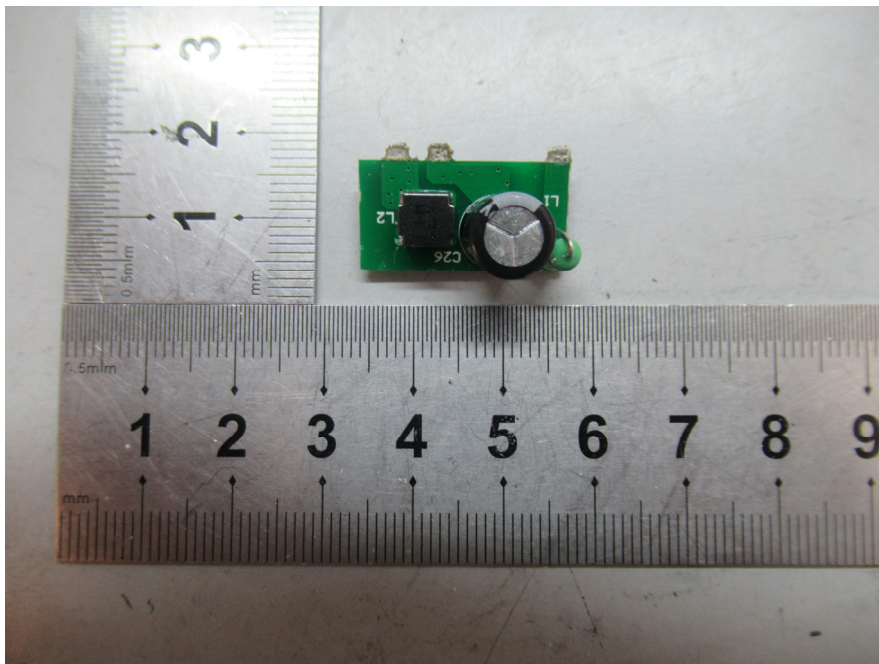
EUT(TERNCY-SM01-D3) – Cover off View-2



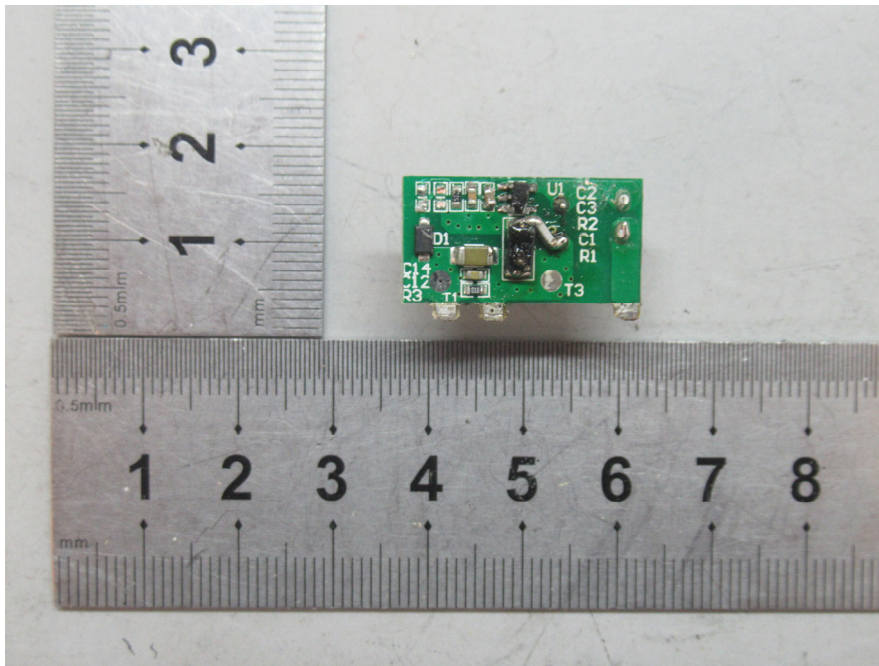
EUT(TERNCY-SM01-D3) – Cover off View-3



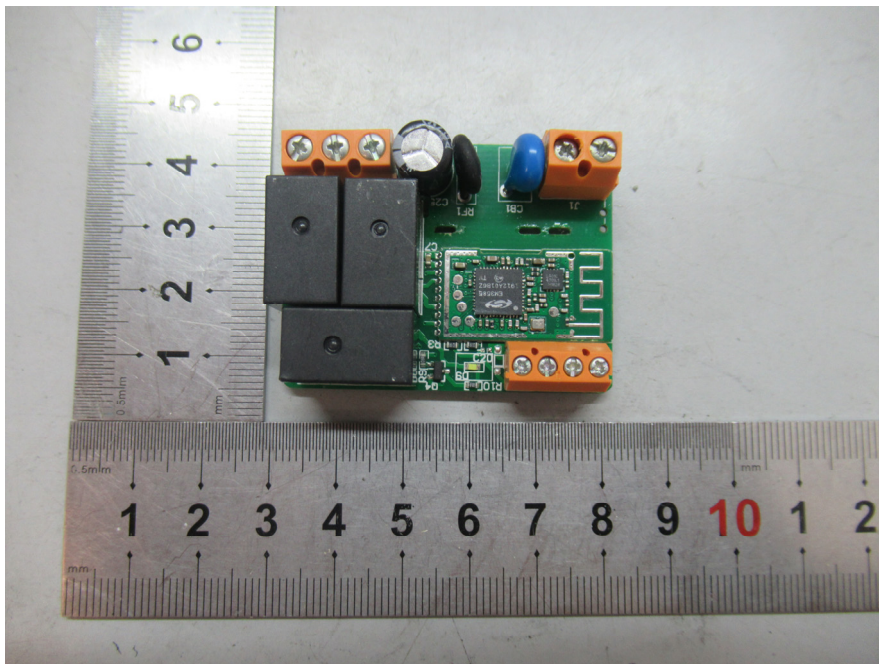
EUT(TERNCY-SM01-D3) – PCB-1 Top View



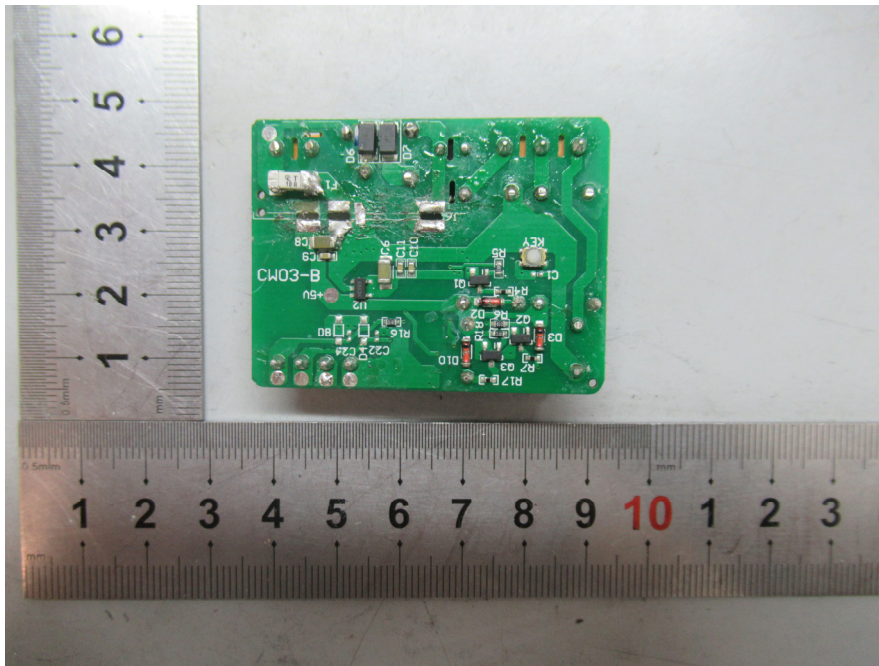
EUT(TERNCY-SM01-D3) – PCB-1 Bottom View



EUT(TERNCY-SM01-D3) – PCB-2 Top View



EUT(TERNCY-SM01-D3) – PCB-2 Bottom View



EUT(TERNCY-SM01-D3) – PCB-2 Top Chip View

