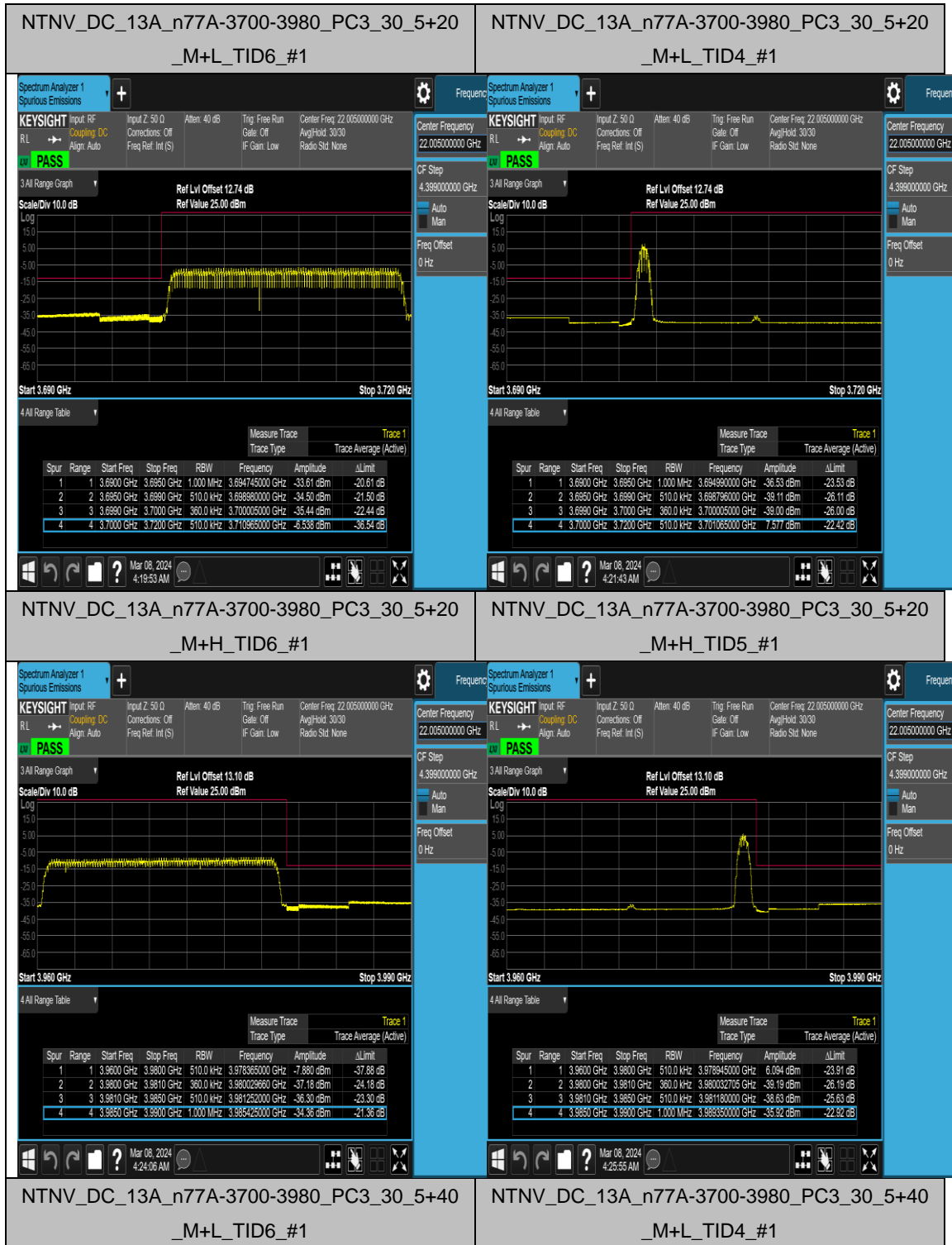


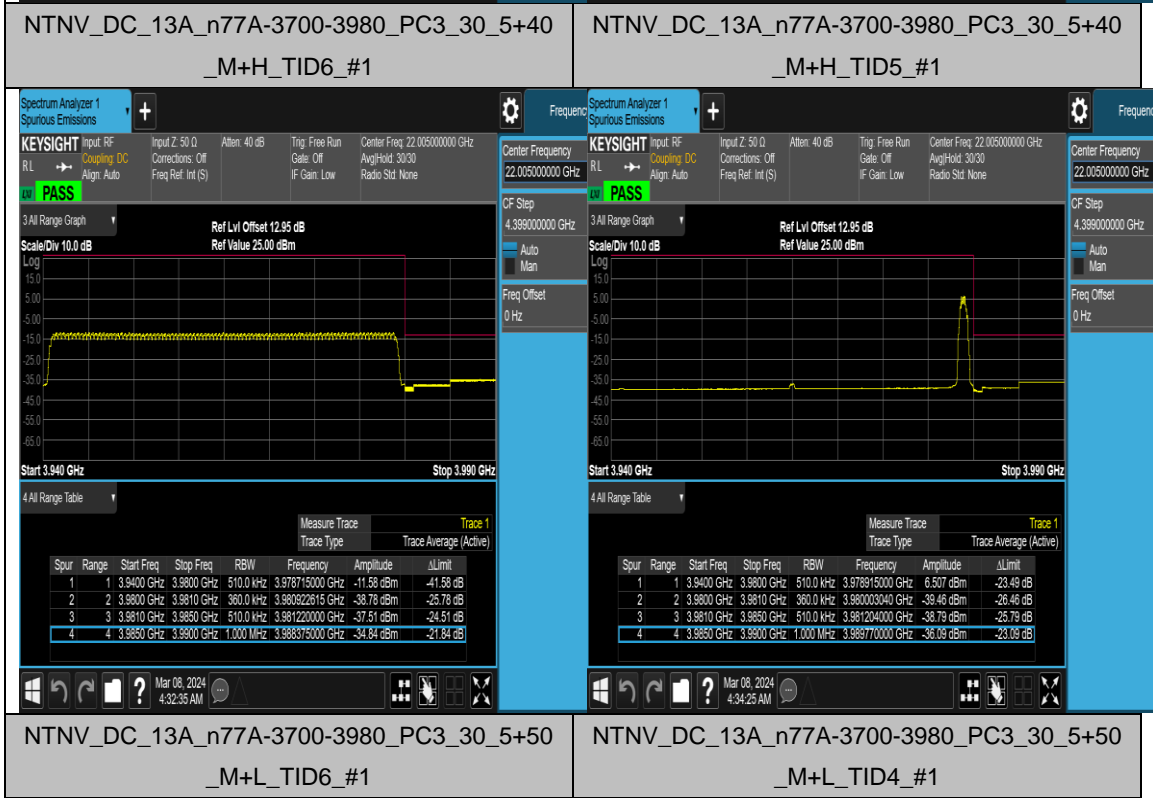
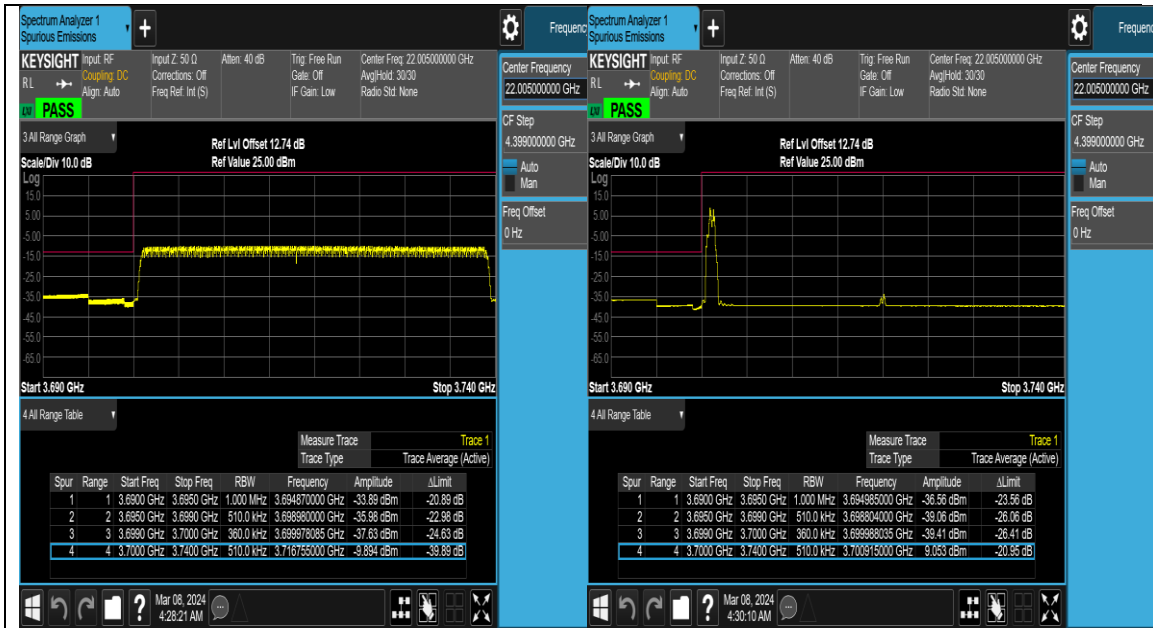
Appendix D: Band Edge for NSA**Test Result**

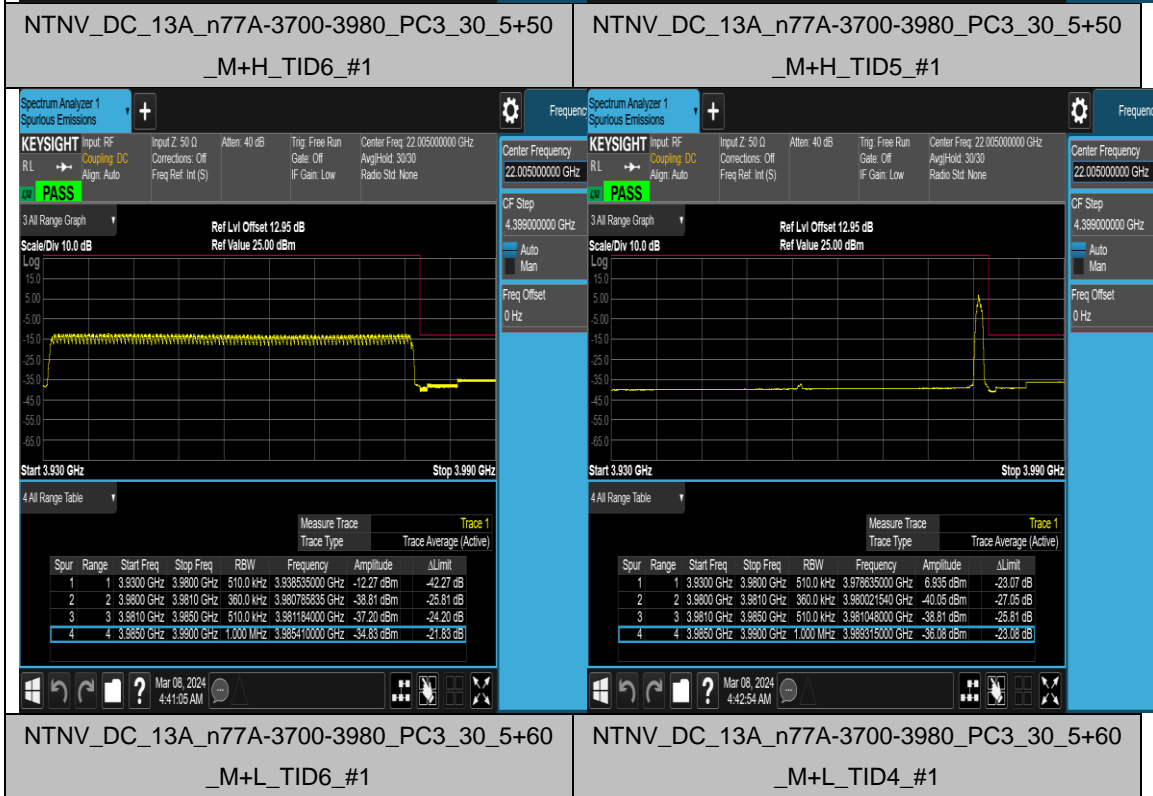
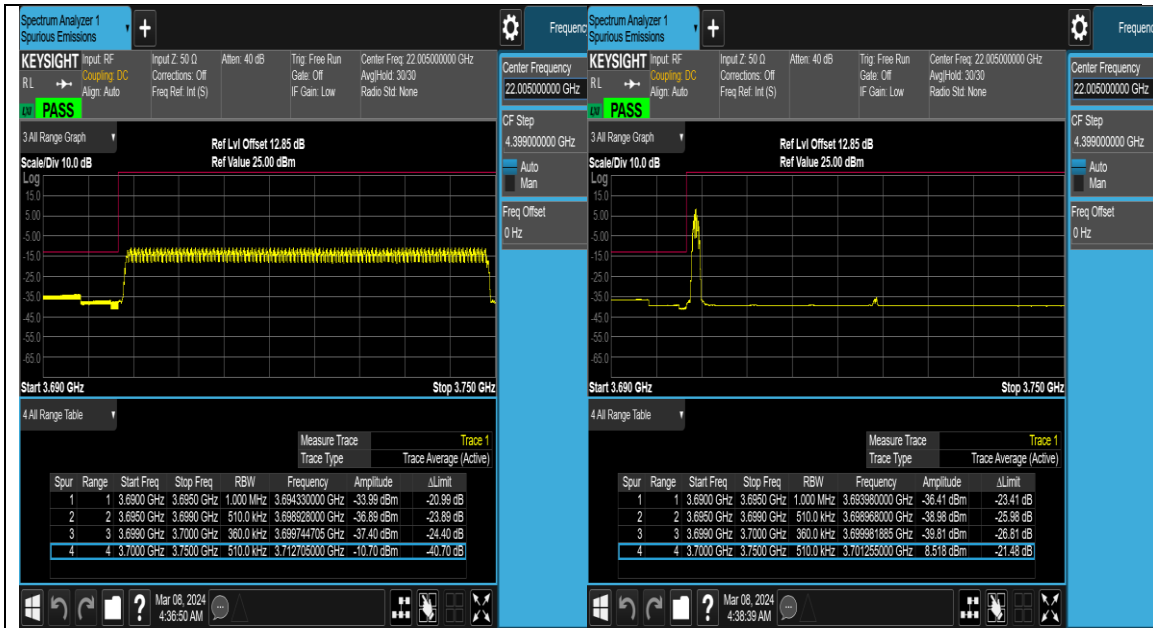
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+H	Outer_Full	PASS

DC_13A_n77A-3700-3 980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Outer_Full	PASS
DC_13A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	PASS

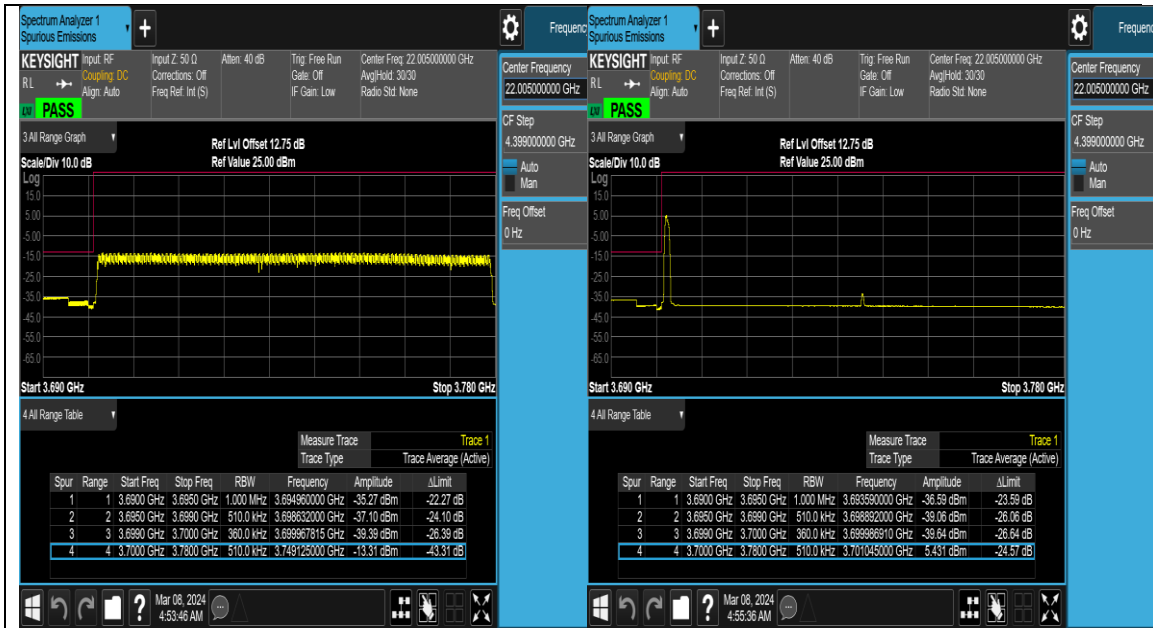
Test Graphs



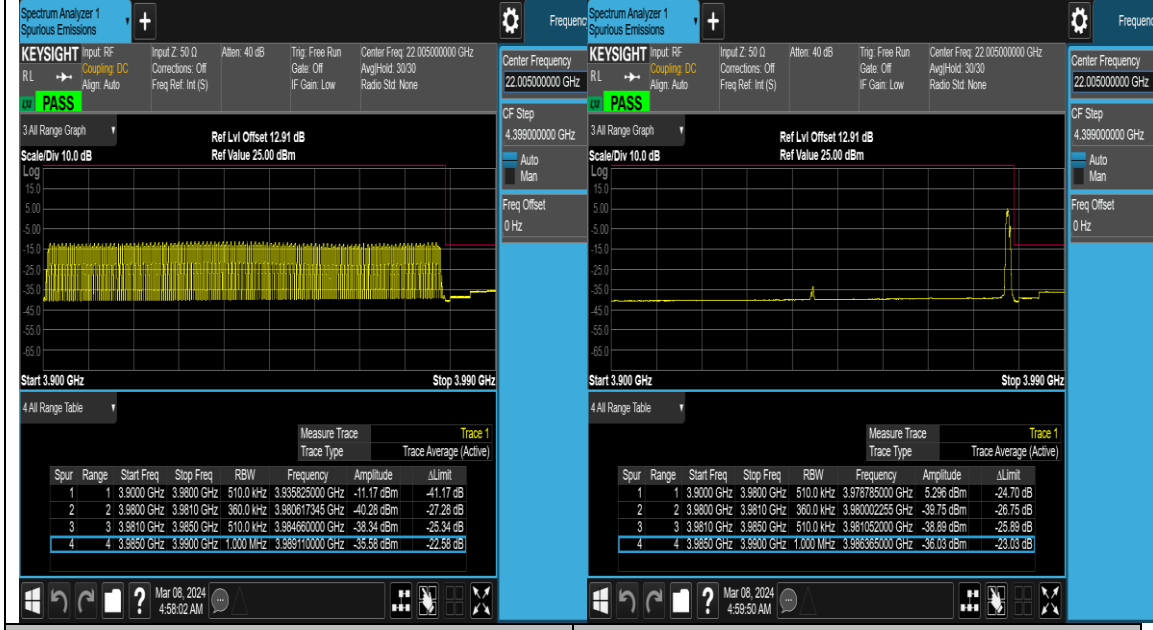




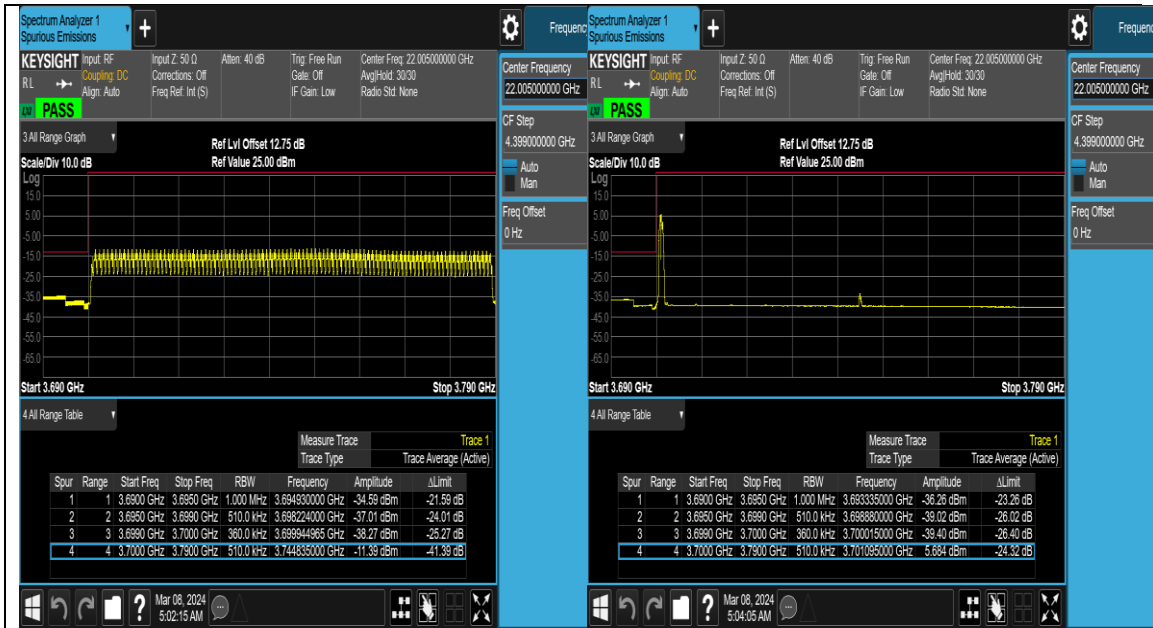




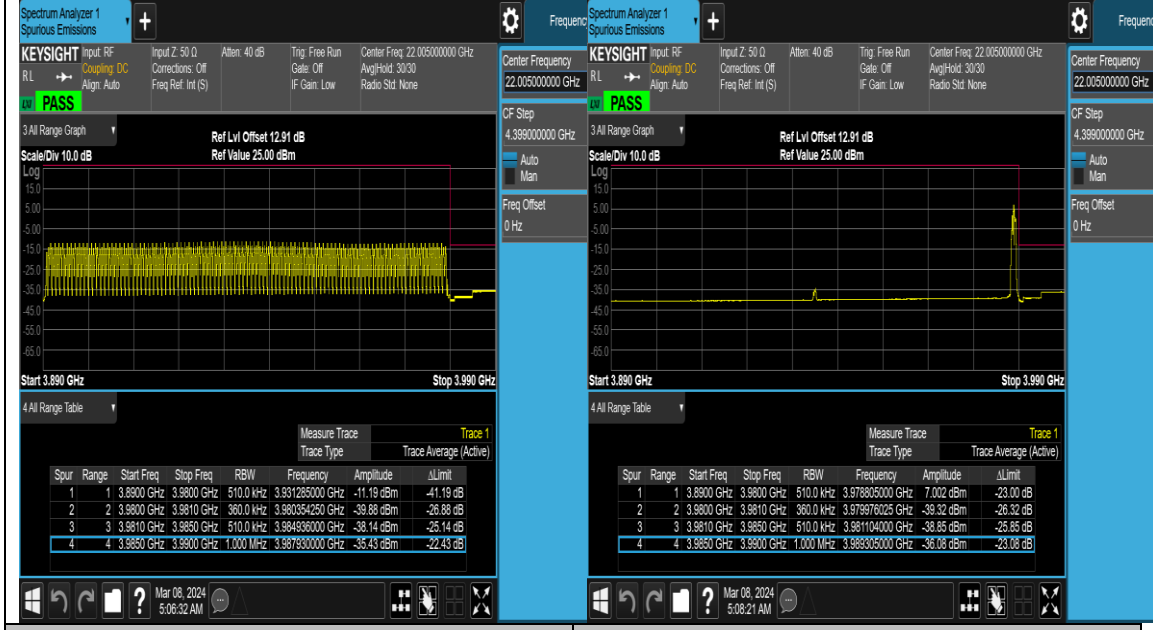
NTNV_DC_13A_n77A-3700-3980_PC3_30_5+80
 _M+H_TID6_#1



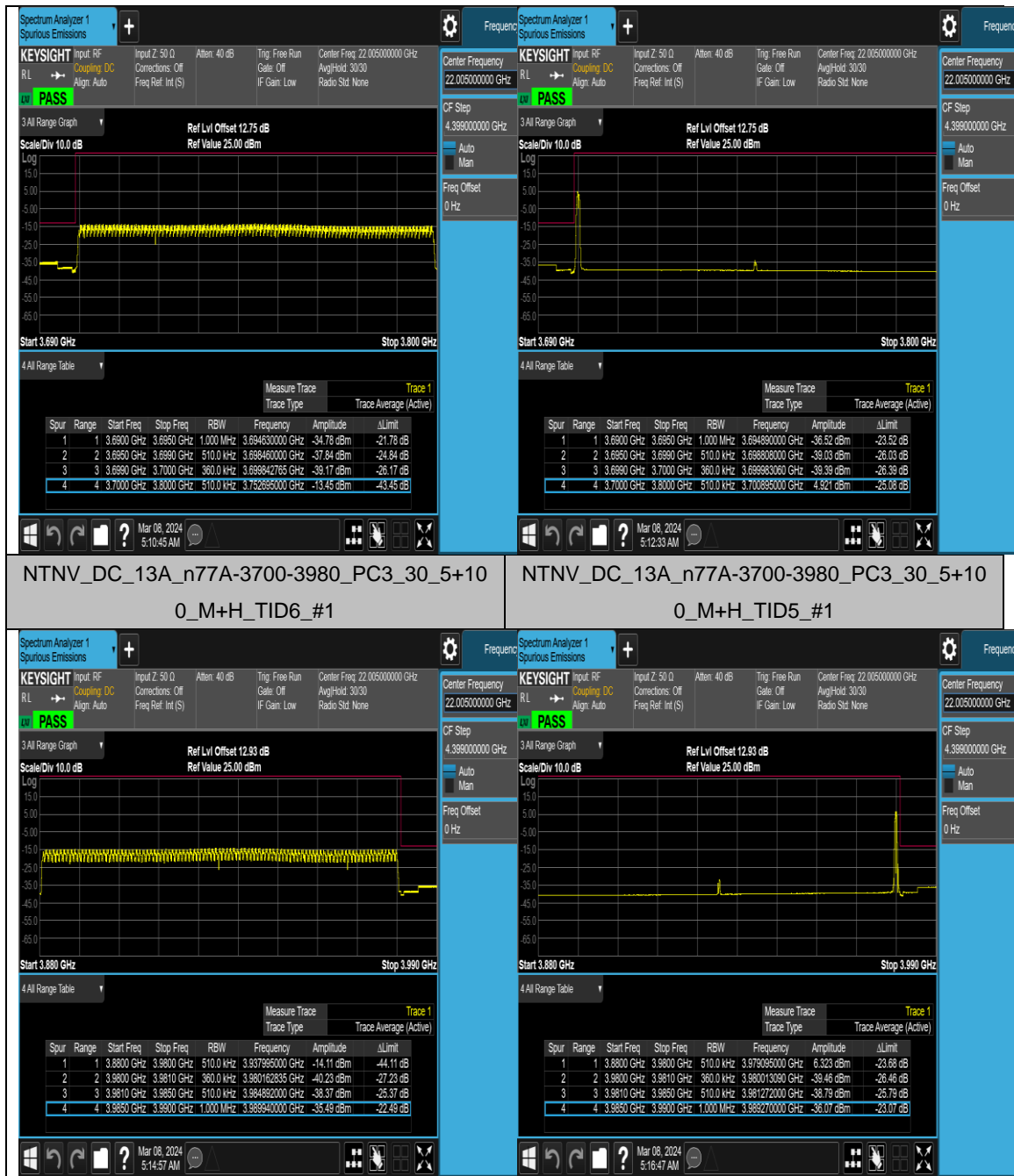
NTNV_DC_13A_n77A-3700-3980_PC3_30_5+90
 _M+L_TID6_#1



NTNV_DC_13A_n77A-3700-3980_PC3_30_5+90
 _M+H_TID6_#1



NTNV_DC_13A_n77A-3700-3980_PC3_30_5+10
 0_M+L_TID6_#1



Appendix E: Conducted Spurious Emission for NSA**Test Result**

Band	SC S	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.35	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-71.48	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-51.95	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	1000	6000	-30.32	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-31.07	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-34.69	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.26	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.12	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.34	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.76	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.02	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.55	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.47	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.95	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.81	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.12	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.55	-13	PASS

DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.10	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-72.69	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-51.97	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	1000	6000	-30.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	6000	26000	-30.91	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	26000	40000	-34.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.12	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.12	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.64	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.61	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.03	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.43	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.90	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.58	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-51.14	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.16	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-73.50	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-52.31	-23	PASS
DC_13A_n77A	30	5+20	CP-QPSK	M+M	Outer_Full	1000	6000	-30.86	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-31.13	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-34.69	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.58	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.76	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.33	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.55	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.90	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.46	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-49.91	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.63	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-70.76	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-51.68	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	6000	26000	-31.28	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	26000	40000	-34.77	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.39	-43	PASS

DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.48	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.63	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.89	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-30.95	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.47	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.61	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.49	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.30	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.12	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-61.78	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-72.41	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-33.03	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-31.13	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-34.68	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.27	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-70.53	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-39.39	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.79	-13	PASS
DC_13A_n77A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.22	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.64	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.72	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.51	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.34	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.88	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.58	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-73.08	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-52.25	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	1000	6000	-30.72	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	6000	26000	-31.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	26000	40000	-34.36	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.02	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.05	-33	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.49	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.85	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.48	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.71	-43	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.74	-33	PASS

DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.25	-23	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.18	-13	PASS
DC_13A_n77A -3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.24	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-71.24	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-52.27	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	1000	6000	-29.33	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-31.30	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-61.43	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.65	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.54	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.11	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.36	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.43	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.02	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.89	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.22	-13	PASS
DC_13A_n77A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.53	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.48	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	0.15	30	-73.43	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	30	1000	-49.60	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	1000	6000	-28.18	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	26000	40000	-34.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.78	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.56	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.19	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.88	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.05	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.38	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.12	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.10	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-49.58	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.76	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-30.89	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.17	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-72.93	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-51.35	-23	PASS

DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	1000	6000	-30.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-34.32	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.42	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.83	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.91	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.07	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.77	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.35	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.30	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.69	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.75	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	0.009	0.15	-61.95	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	0.15	30	-72.67	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	30	1000	-52.30	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	26000	40000	-34.65	-13	PASS
DC_13A_n77A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.03	-43	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.15	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-49.38	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.73	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-31.10	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.92	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.06	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.36	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.80	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.10	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.43	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.66	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-71.23	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-51.58	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	1000	6000	-29.46	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.36	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.50	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-49.72	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.79	-13	PASS

DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.25	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.66	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.68	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.13	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.06	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.61	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.36	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	0.15	30	-73.72	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	30	1000	-52.84	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	1000	6000	-28.64	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	6000	26000	-31.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	26000	40000	-34.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.71	-43	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.19	-33	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.36	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.41	-43	PASS
DC_13A_n77A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.56	-33	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.59	-23	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.73	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.38	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-72.08	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-51.65	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	1000	6000	-29.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-31.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-34.60	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-61.90	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.61	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.99	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.08	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.63	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.14	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.73	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.07	-13	PASS

DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.59	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.60	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	0.15	30	-72.49	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	30	1000	-51.49	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	1000	6000	-30.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	6000	26000	-31.28	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Outer_Full	26000	40000	-34.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.60	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.27	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.86	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.36	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.13	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.88	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.25	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.19	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.71	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-73.09	-33	PASS
DC_13A_n77A	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-44.15	-23	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	1000	6000	-30.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-30.96	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-34.66	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.10	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.58	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.19	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.07	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.69	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.66	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.47	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.71	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.89	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.05	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	0.009	0.15	-61.99	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	0.15	30	-72.83	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	30	1000	-41.63	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	1000	6000	-30.77	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	6000	26000	-31.17	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	26000	40000	-34.62	-13	PASS

DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.39	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.21	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-51.77	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-31.22	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.76	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-63.35	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.40	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.20	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.19	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-61.60	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-72.47	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-52.07	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	1000	6000	-30.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-31.32	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-34.79	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.42	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.53	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.34	-23	PASS
DC_13A_n77A	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.80	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.01	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.58	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.27	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.22	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.59	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.27	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	0.15	30	-72.19	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	30	1000	-51.88	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	1000	6000	-29.56	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Outer_Full	26000	40000	-34.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.27	-43	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.95	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.31	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.53	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.00	-43	PASS

DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.46	-33	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.19	-23	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.79	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.18	-13	PASS
DC_13A_n77A -3700-3980	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.66	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.19	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-73.10	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-52.57	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	1000	6000	-29.92	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-34.43	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.22	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.50	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.74	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.92	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.23	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.48	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.72	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.16	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-51.41	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.95	-13	PASS
DC_13A_n77A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.28	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.94	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	0.15	30	-73.88	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	30	1000	-51.72	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	1000	6000	-28.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	6000	26000	-31.18	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	26000	40000	-34.60	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.65	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-71.02	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.57	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.72	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.35	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.94	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.16	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-49.49	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.88	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.25	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.47	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-72.96	-33	PASS

DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-26.17	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	1000	6000	-30.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-30.99	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-34.72	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.85	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.81	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.93	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.32	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.62	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.17	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.00	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.24	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.91	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.10	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.66	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.05	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	0.15	30	-72.17	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	30	1000	-52.44	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	6000	26000	-31.30	-13	PASS
DC_13A_n77A	30	5+60	CP-16QAM	M+M	Outer_Full	26000	40000	-34.76	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.72	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.25	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.20	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.94	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-31.25	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.59	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.84	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.55	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.87	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.07	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.72	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.92	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-72.96	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-50.03	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	1000	6000	-29.80	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-31.00	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-34.46	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.02	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.73	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.46	-23	PASS

DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.03	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.61	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.57	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-51.55	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.03	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.46	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.69	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	0.15	30	-71.02	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	30	1000	-48.87	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	1000	6000	-30.08	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	6000	26000	-31.19	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	26000	40000	-34.64	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-60.95	-43	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.67	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.73	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.85	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.11	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.76	-13	PASS
DC_13A_n77A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.53	-43	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.98	-33	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.21	-23	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.73	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-72.64	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-51.74	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	1000	6000	-29.62	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-31.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-34.66	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.65	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.25	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.44	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.47	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.79	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-51.92	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.88	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.47	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	0.009	0.15	-63.24	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	0.15	30	-71.38	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	30	1000	-51.16	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	1000	6000	-29.39	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	6000	26000	-31.10	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	26000	40000	-34.60	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.27	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.54	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.93	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.87	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.30	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.66	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-60.78	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.63	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.29	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.80	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.62	-43	PASS
DC_13A_n77A	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-71.97	-33	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-27.58	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	1000	6000	-30.89	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-30.95	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.48	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.00	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.52	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.46	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.40	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.26	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.23	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.30	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	0.15	30	-72.22	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	30	1000	-52.33	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	1000	6000	-30.83	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	6000	26000	-31.22	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	26000	40000	-34.70	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-63.13	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.25	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-32.41	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.90	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.48	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.39	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-49.69	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.92	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.21	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-72.52	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-50.59	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	1000	6000	-30.36	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-31.27	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.34	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.09	-33	PASS
DC_13A_n77A	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-38.34	-23	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.77	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.23	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.73	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.44	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.03	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.80	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.22	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.63	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.47	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	0.15	30	-72.85	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	30	1000	-52.32	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	1000	6000	-30.32	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	6000	26000	-30.99	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	26000	40000	-34.28	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.40	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.52	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.04	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.91	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.02	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.78	-13	PASS

DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.49	-43	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.42	-33	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.09	-23	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.80	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.09	-13	PASS
DC_13A_n77A -3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.43	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.80	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-71.85	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-49.75	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	1000	6000	-30.08	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-31.30	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.77	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.70	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.75	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.63	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.40	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.45	-23	PASS
DC_13A_n77A	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.75	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.79	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.88	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	0.15	30	-71.78	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	30	1000	-52.08	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	1000	6000	-29.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	6000	26000	-31.05	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Outer_Full	26000	40000	-34.55	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.09	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.42	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.83	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.88	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.08	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.58	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.07	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.51	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.71	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.62	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.68	-43	PASS

DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-70.91	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-52.21	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	1000	6000	-30.79	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-31.04	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.78	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.29	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.98	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.86	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.48	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.62	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.91	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.16	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.23	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	0.15	30	-70.71	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	30	1000	-50.85	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	1000	6000	-30.78	-13	PASS
DC_13A_n77A	30	5+90	CP-16QAM	M+M	Outer_Full	6000	26000	-31.24	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	26000	40000	-34.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.44	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.31	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.20	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.77	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-31.28	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.61	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.85	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.45	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-49.71	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.97	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.30	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-70.47	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-51.06	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	1000	6000	-29.85	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-34.78	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-60.98	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.98	-33	PASS

DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.09	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.82	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.15	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.56	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.87	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.05	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.98	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.29	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.68	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.46	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	0.15	30	-72.26	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	30	1000	-51.47	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	1000	6000	-30.25	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	6000	26000	-31.13	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Outer_Full	26000	40000	-34.50	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.33	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.78	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.37	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.21	-13	PASS
DC_13A_n77A	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.78	-13	PASS

-3700-3980										
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.46	-43	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.64	-33	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.62	-23	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.84	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.21	-13	PASS
DC_13A_n77A -3700-3980	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.71	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-63.20	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-73.75	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-51.55	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	6000	-30.07	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-31.22	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-34.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.18	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.01	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.05	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	6000	-30.85	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-31.22	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-34.64	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.99	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.08	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.31	-23	PASS

DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	6000	-30.90	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-34.59	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.009	0.15	-63.08	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	0.15	30	-73.13	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	30	1000	-49.81	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	1000	6000	-29.96	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	6000	26000	-31.12	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Outer_Full	26000	40000	-34.79	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.60	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.02	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.85	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	1000	6000	-30.86	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	6000	26000	-31.18	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Left	26000	40000	-34.65	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.03	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.68	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.81	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	1000	6000	-30.90	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	6000	26000	-31.27	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+L	Edge_1RB_Right	26000	40000	-34.78	-13	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.15	-43	PASS

-3700-3980		0								
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-71.74	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-51.57	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	6000	-30.90	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-31.27	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-34.68	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.36	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.94	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.66	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	6000	-30.93	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-31.23	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-34.75	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.21	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.24	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.48	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	6000	-30.96	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-31.20	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-34.74	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.21	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	0.15	30	-73.26	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	30	1000	-52.58	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	1000	6000	-30.88	-13	PASS

DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	6000	26000	-31.12	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Outer_Full	26000	40000	-34.55	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.93	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.97	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-49.97	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	1000	6000	-30.85	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	6000	26000	-30.88	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Left	26000	40000	-34.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.29	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-71.25	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-49.69	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	1000	6000	-30.92	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	6000	26000	-31.28	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+M	Edge_1RB_Right	26000	40000	-34.43	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.27	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-72.94	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-49.80	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	6000	-30.52	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-31.24	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-34.64	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.64	-43	PASS
DC_13A_n77A	30	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.81	-33	PASS

-3700-3980		0								
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.02	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	6000	-30.90	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-31.14	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-34.67	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.92	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.38	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-51.69	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	6000	-30.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-31.26	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-34.60	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.00	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	0.15	30	-72.64	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	30	1000	-51.09	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	1000	6000	-30.10	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	6000	26000	-31.13	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Outer_Full	26000	40000	-34.76	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.82	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.30	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.35	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	1000	6000	-30.92	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	6000	26000	-31.12	-13	PASS

DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Left	26000	40000	-34.81	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.75	-43	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-73.84	-33	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.92	-23	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	1000	6000	-30.73	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	6000	26000	-31.19	-13	PASS
DC_13A_n77A -3700-3980	30	5+10 0	CP-16QAM	M+H	Edge_1RB_Right	26000	40000	-34.72	-13	PASS

Test Graphs

