

Appendix for 12A_n25A

Product Name: 5G CPE

Model No: S3

Appendix A: Average Power Output for SA

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	21.11	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Inner_Full	22.76	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.32	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	21.45	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	21.30	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Inner_Full	22.35	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.14	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.02	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	21.06	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Inner_Full	23.03	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.30	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.33	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	21.39	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Inner_Full	22.42	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.30	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.07	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	21.57	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Inner_Full	23.37	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.60	PC3	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.36	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	21.62	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Inner_Full	22.71	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.54	PC3	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.84	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	21.39	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Inner_Full	22.75	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.45	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.57	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	21.45	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Inner_Full	22.50	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	21.23	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.53	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	21.39	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Inner_Full	22.98	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.57	PC3	PASS

DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.26	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	21.39	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Inner_Full	22.23	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.73	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.89	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	21.55	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Inner_Full	22.87	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.51	PC3	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.86	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	21.55	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Inner_Full	22.70	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.41	PC3	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.52	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	21.43	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Inner_Full	23.15	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.67	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.90	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	21.49	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Inner_Full	22.61	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	20.86	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.19	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	21.32	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Inner_Full	22.87	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.74	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.31	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	21.52	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Inner_Full	22.42	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	22.08	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.58	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	21.89	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Inner_Full	23.36	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.59	PC3	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.75	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	21.91	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Inner_Full	22.84	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.78	PC3	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.81	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	21.76	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Inner_Full	23.27	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.75	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.78	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	21.75	PC3	PASS

DC_12A_n25A	15	5+20	CP-16QAM	M+L	Inner_Full	22.86	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.66	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	21.68	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	21.70	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Inner_Full	23.08	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.95	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.33	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	21.75	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Inner_Full	22.66	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	22.30	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.74	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	21.82	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Inner_Full	23.34	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.70	PC3	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.85	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	21.91	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Inner_Full	22.85	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.67	PC3	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.84	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

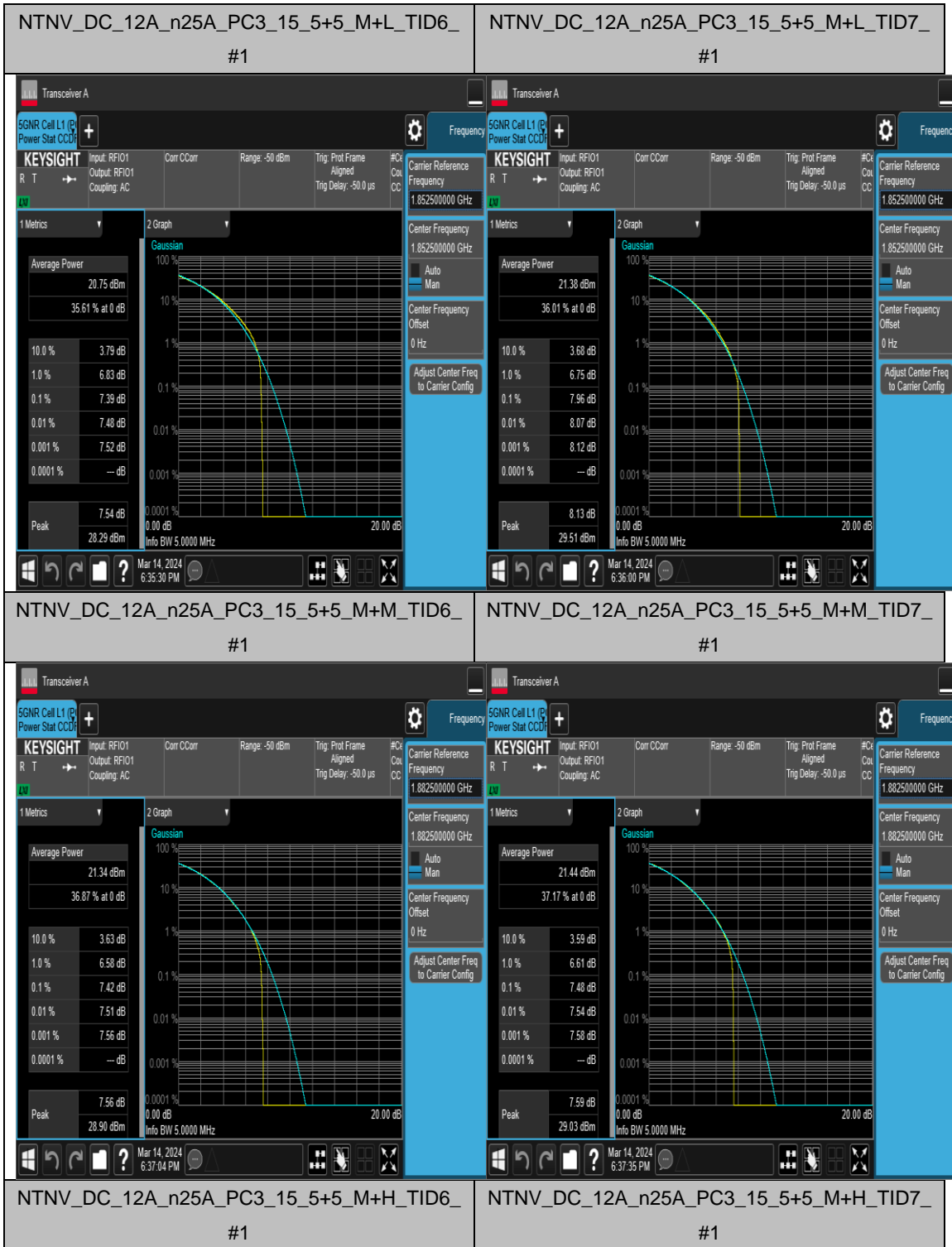
Peak-to-Average Ratio(CCDF)

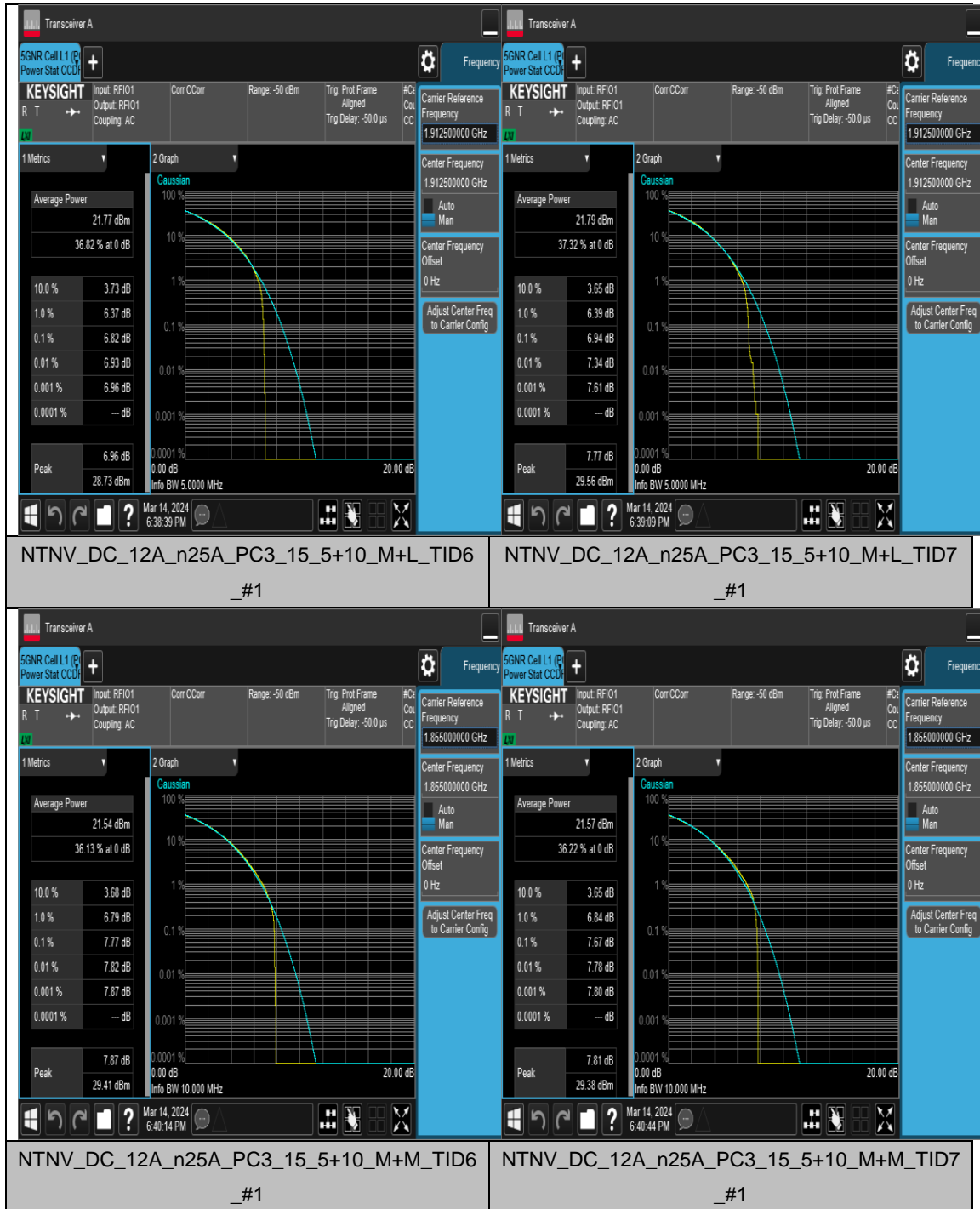
Test Result

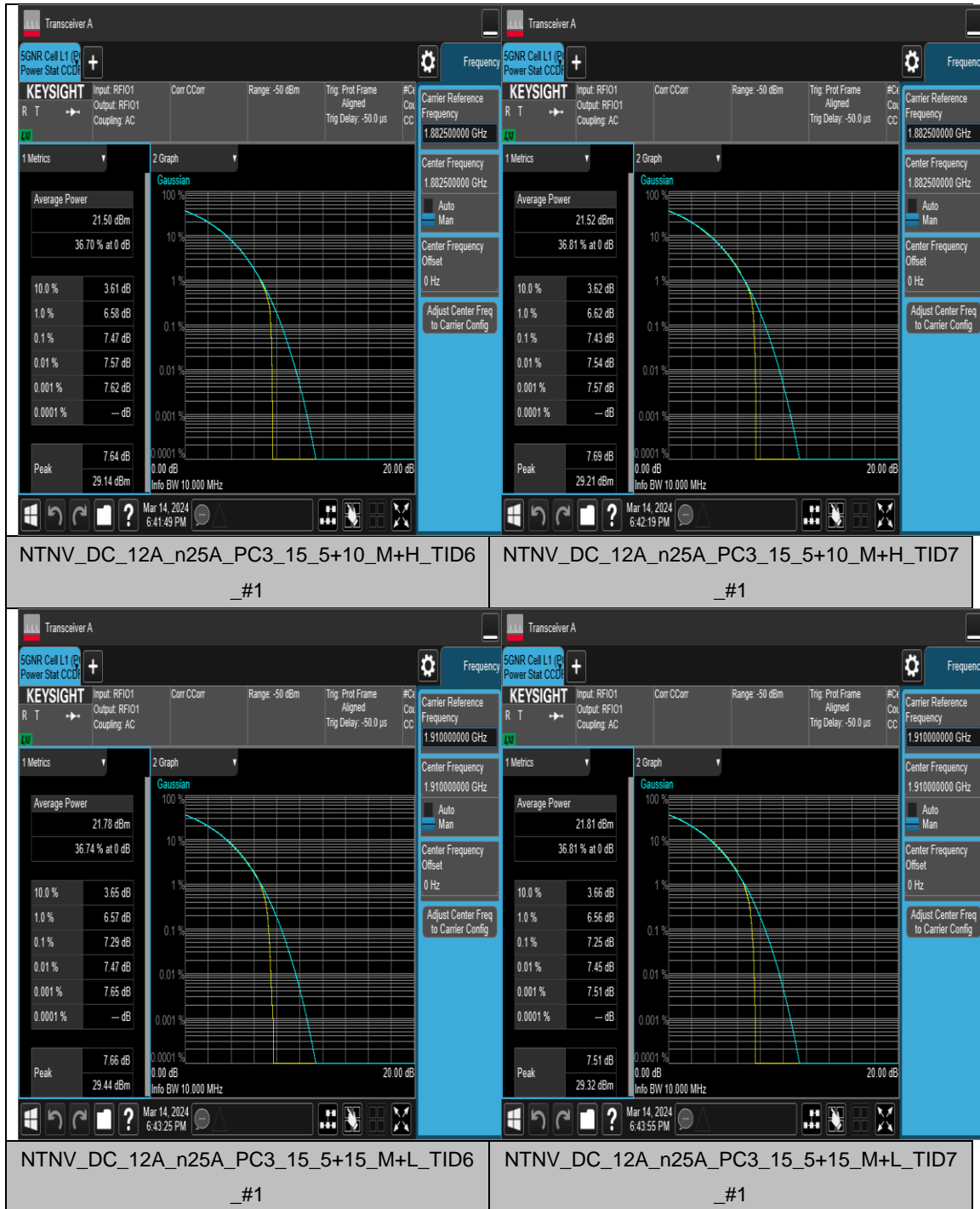
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_12A_n 25A	15	5+5	CP-QPSK	M+L	Outer_Full	7.39	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+L	Outer_Full	7.96	≤13	PASS
DC_12A_n 25A	15	5+5	CP-QPSK	M+M	Outer_Full	7.42	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+M	Outer_Full	7.48	≤13	PASS
DC_12A_n 25A	15	5+5	CP-QPSK	M+H	Outer_Full	6.82	≤13	PASS
DC_12A_n 25A	15	5+5	CP-16QAM	M+H	Outer_Full	6.94	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+L	Outer_Full	7.77	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+L	Outer_Full	7.67	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+M	Outer_Full	7.47	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+M	Outer_Full	7.43	≤13	PASS
DC_12A_n 25A	15	5+10	CP-QPSK	M+H	Outer_Full	7.29	≤13	PASS
DC_12A_n 25A	15	5+10	CP-16QAM	M+H	Outer_Full	7.25	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+L	Outer_Full	7.54	≤13	PASS
DC_12A_n 25A	15	5+15	CP-16QAM	M+L	Outer_Full	7.55	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+M	Outer_Full	7.08	≤13	PASS
DC_12A_n 25A	15	5+15	CP-16QAM	M+M	Outer_Full	7.06	≤13	PASS
DC_12A_n 25A	15	5+15	CP-QPSK	M+H	Outer_Full	7.36	≤13	PASS

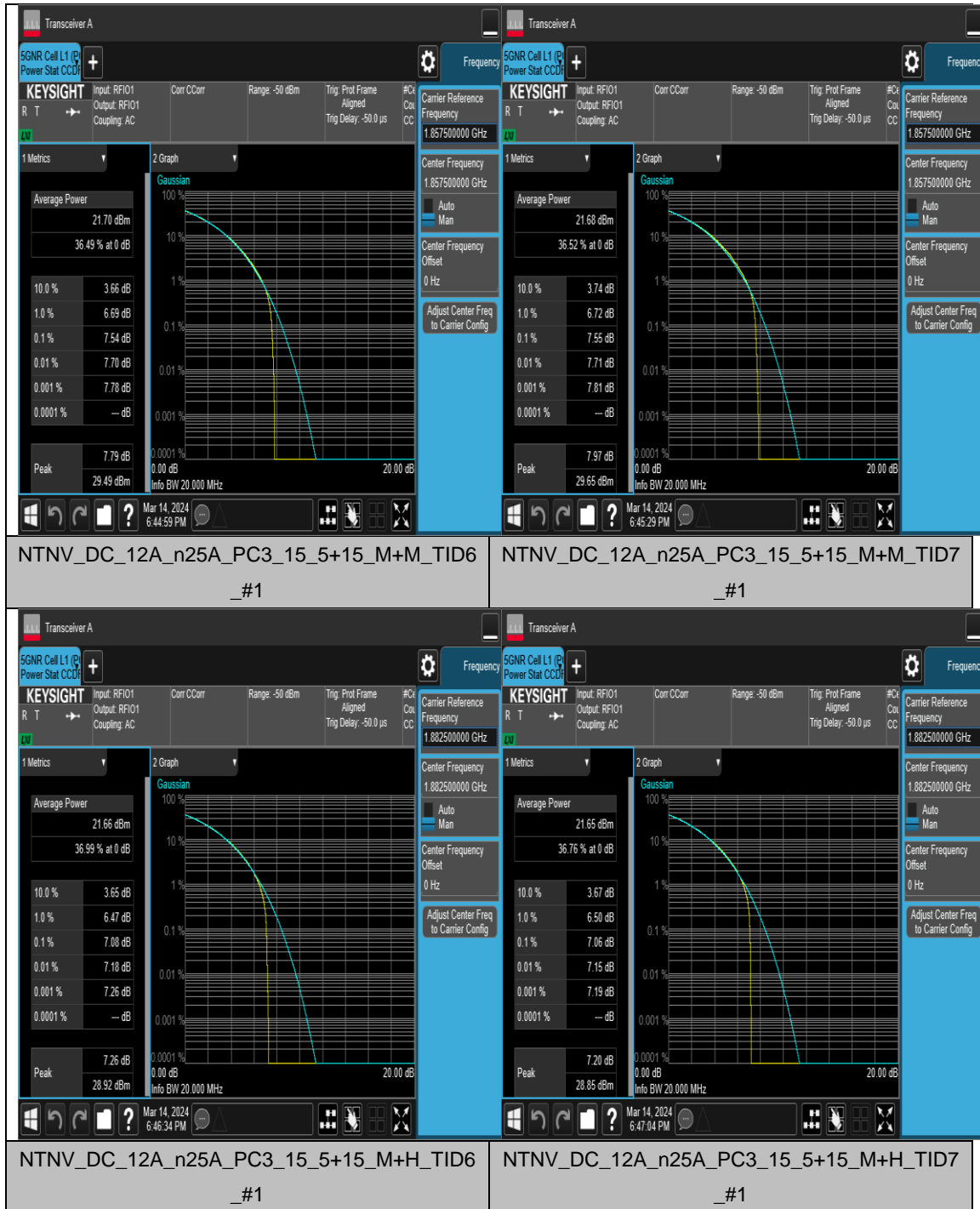
DC_12A_n 25A	15	5+15	CP-16QAM	M+H	Outer_Full	7.43	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+L	Outer_Full	7.29	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+L	Outer_Full	7.23	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+M	Outer_Full	7.60	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+M	Outer_Full	7.62	≤13	PASS
DC_12A_n 25A	15	5+20	CP-QPSK	M+H	Outer_Full	7.33	≤13	PASS
DC_12A_n 25A	15	5+20	CP-16QAM	M+H	Outer_Full	7.25	≤13	PASS

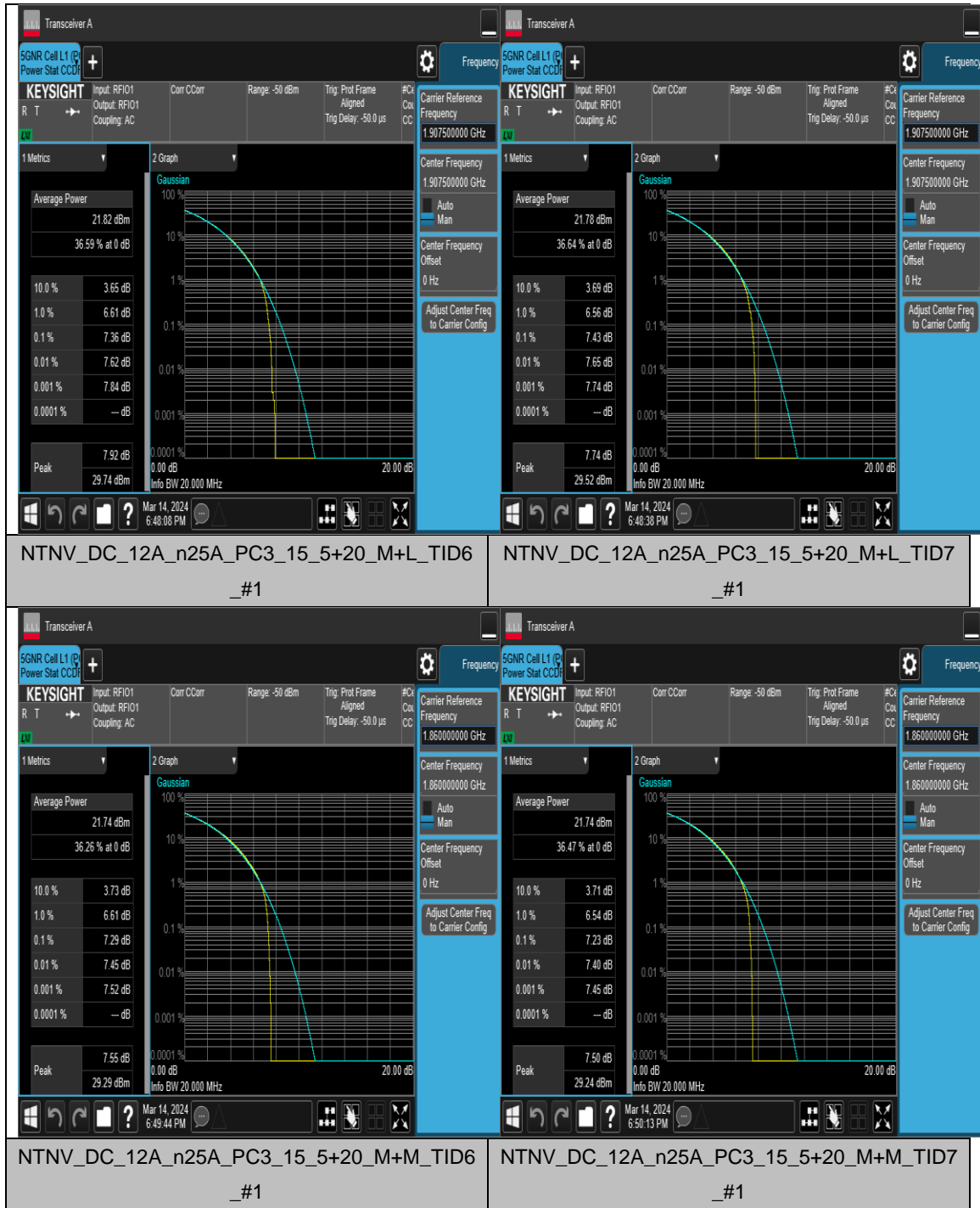
Test Graphs

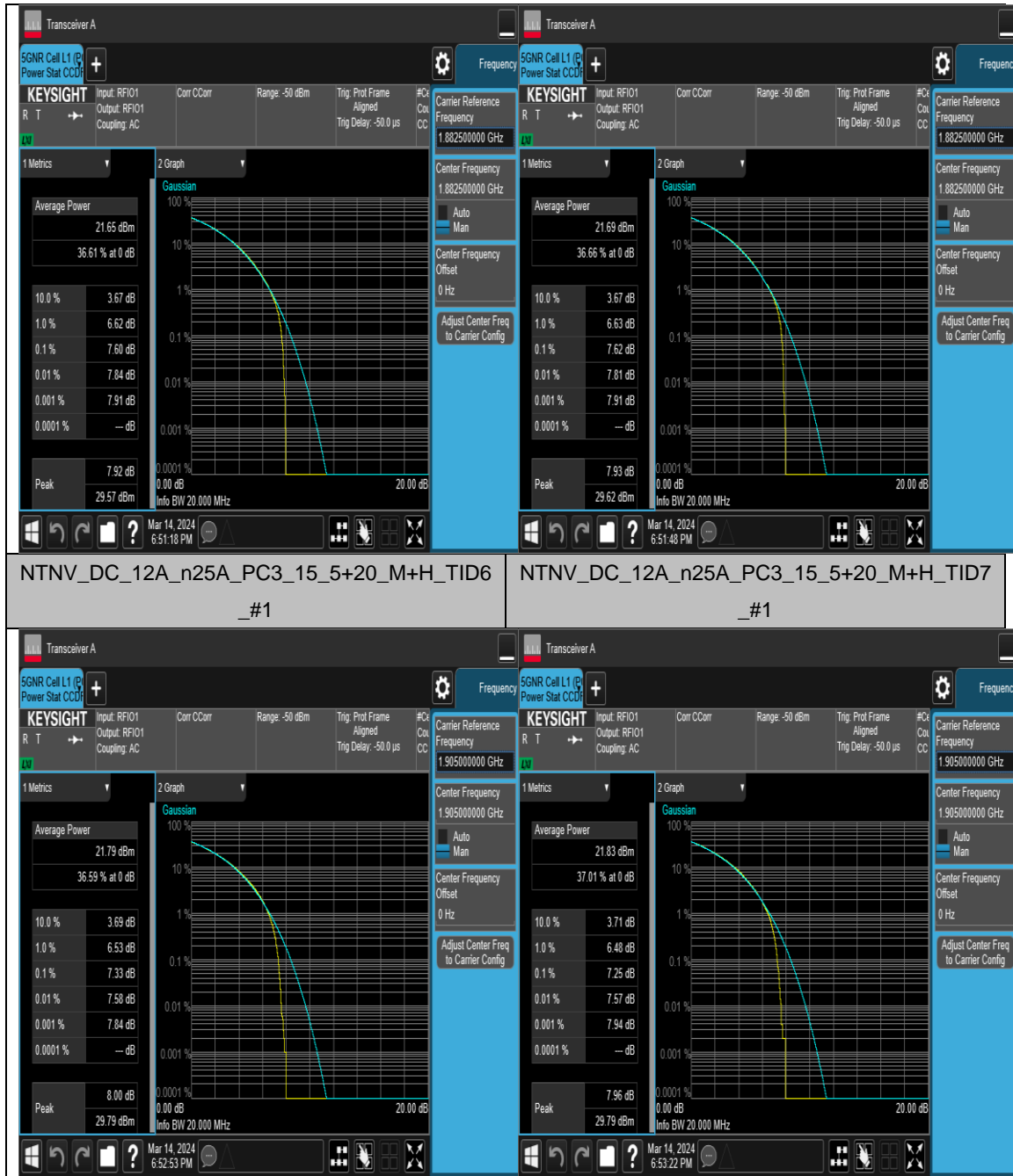








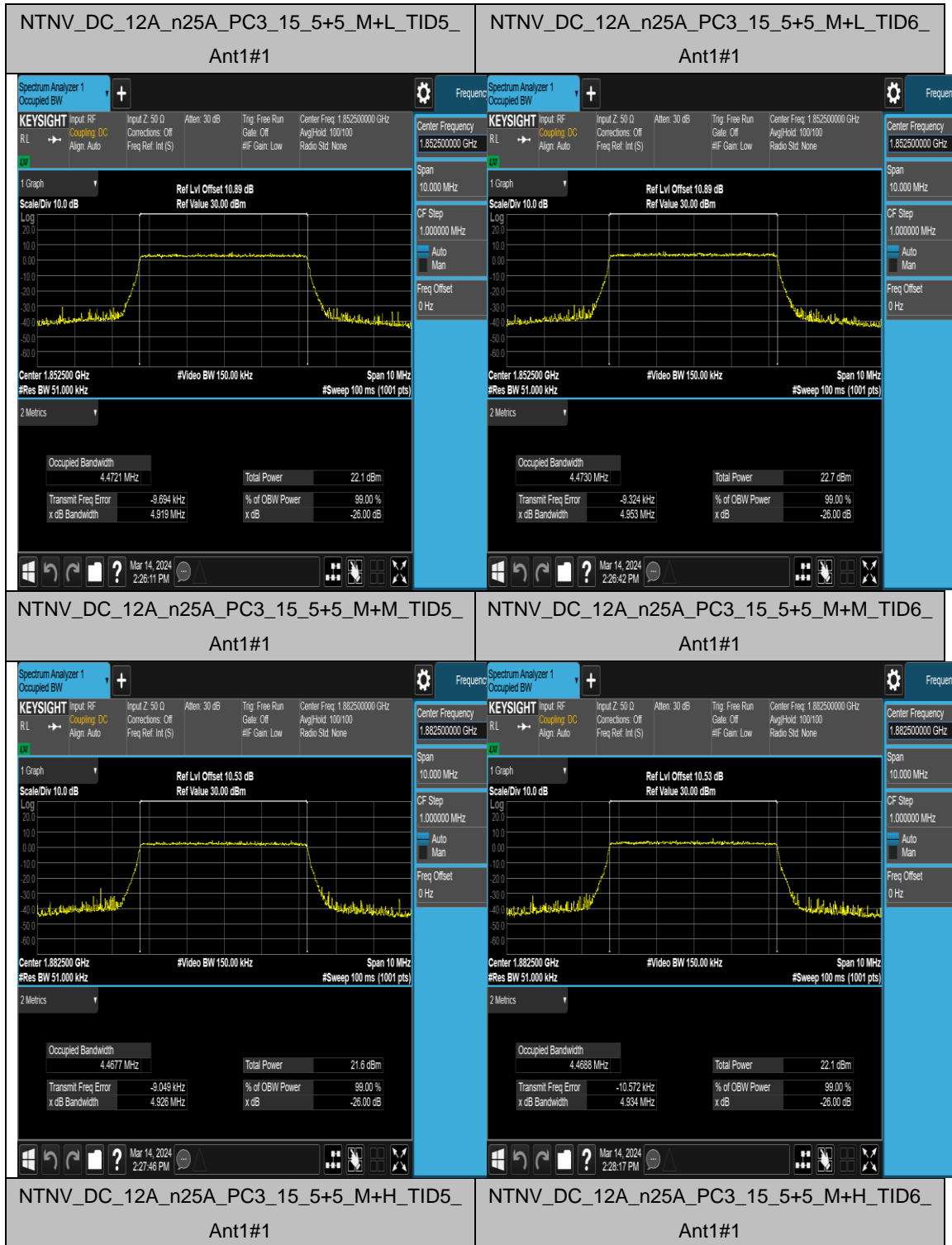


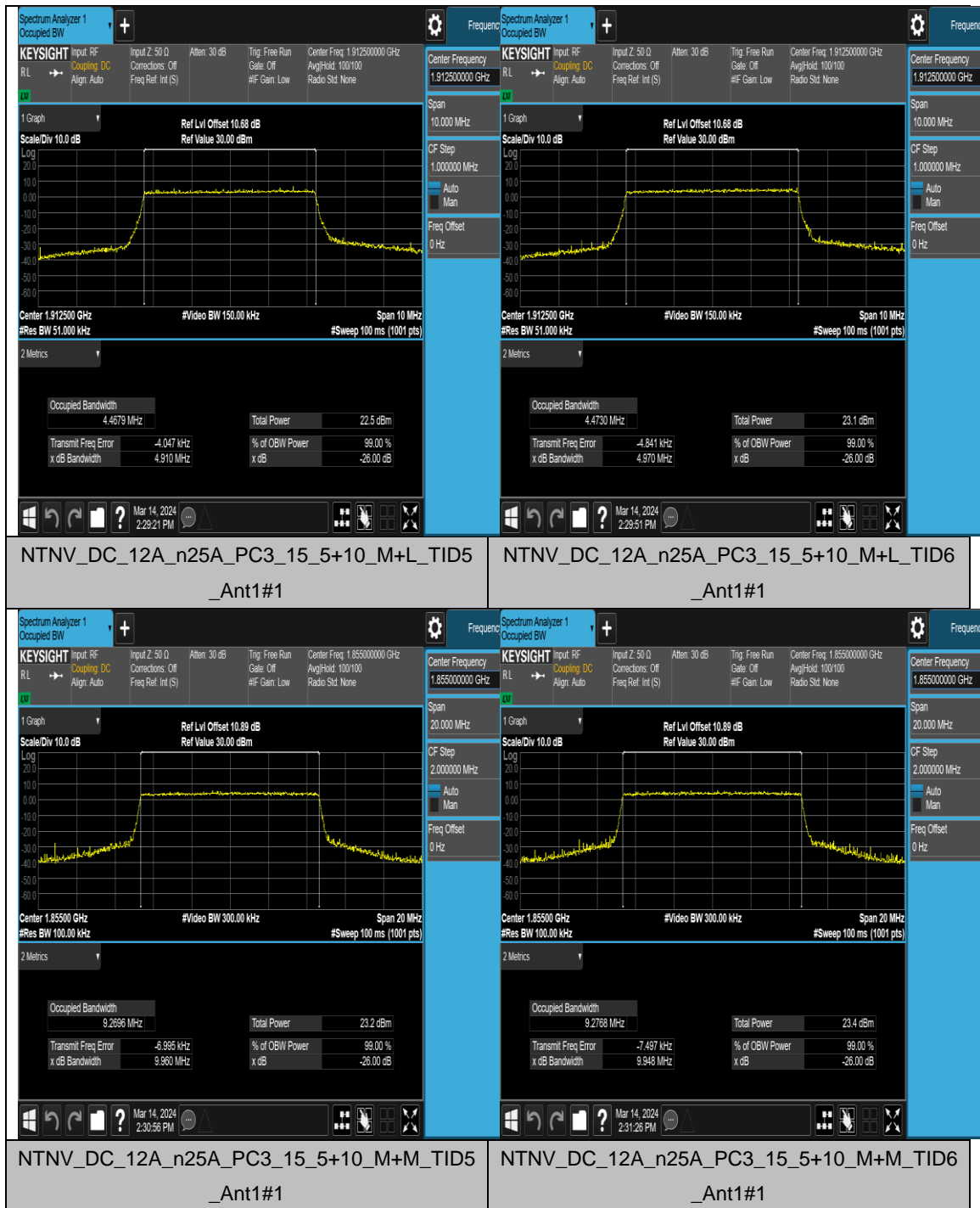


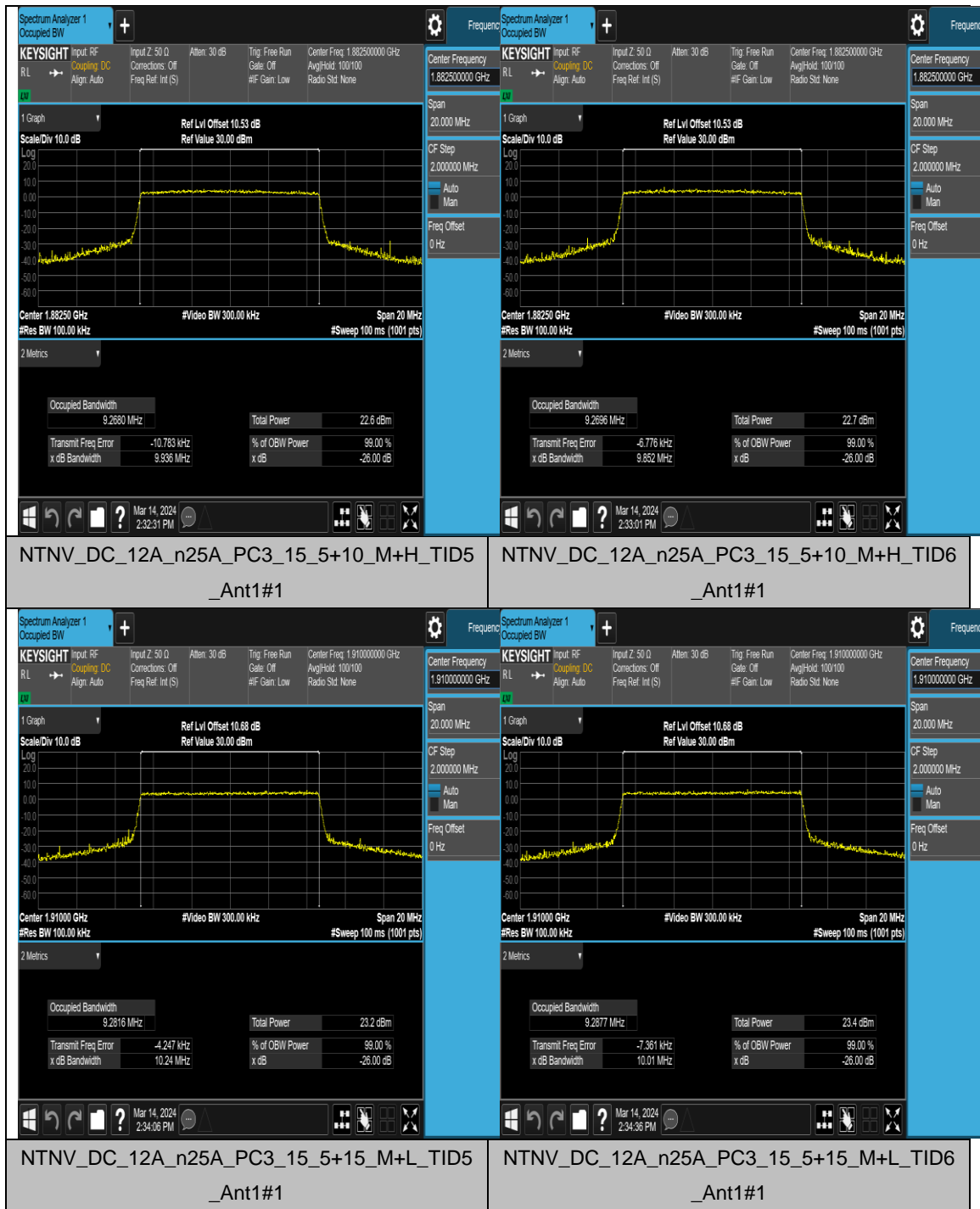
Appendix C: 26dB Bandwidth and Occupied Bandwidth for**NSA****Test Result**

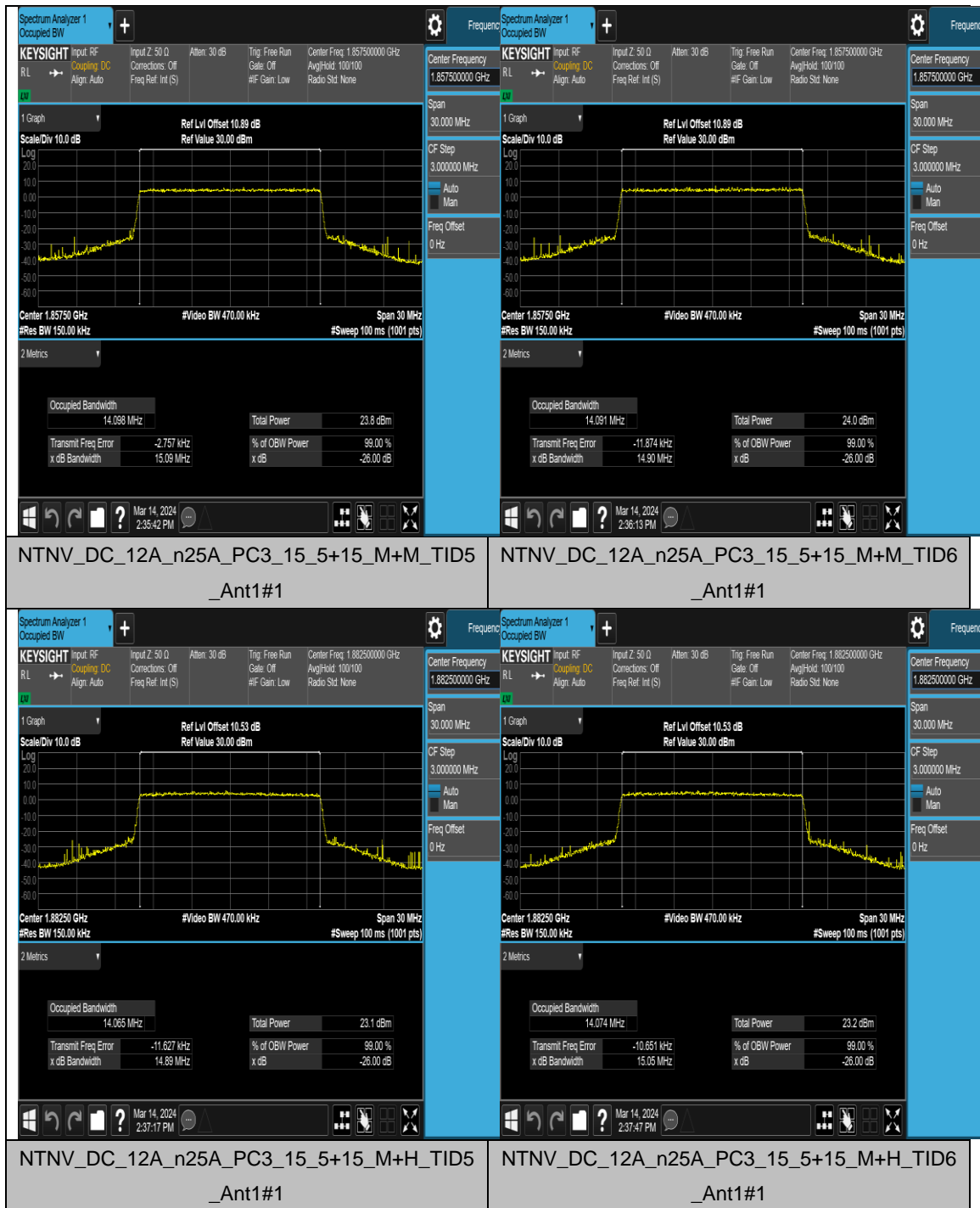
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4721	4.919	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4730	4.953	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4677	4.926	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4688	4.934	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4679	4.910	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4730	4.970	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2696	9.960	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2768	9.948	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2680	9.936	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2696	9.852	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2816	10.24	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2877	10.01	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	14.098	15.09	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	14.091	14.90	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	14.065	14.89	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	14.074	15.05	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	14.107	14.95	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	14.102	14.90	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	18.887	19.85	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	18.910	19.86	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	18.840	19.80	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	18.855	19.78	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	18.896	19.81	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	18.898	19.80	PASS

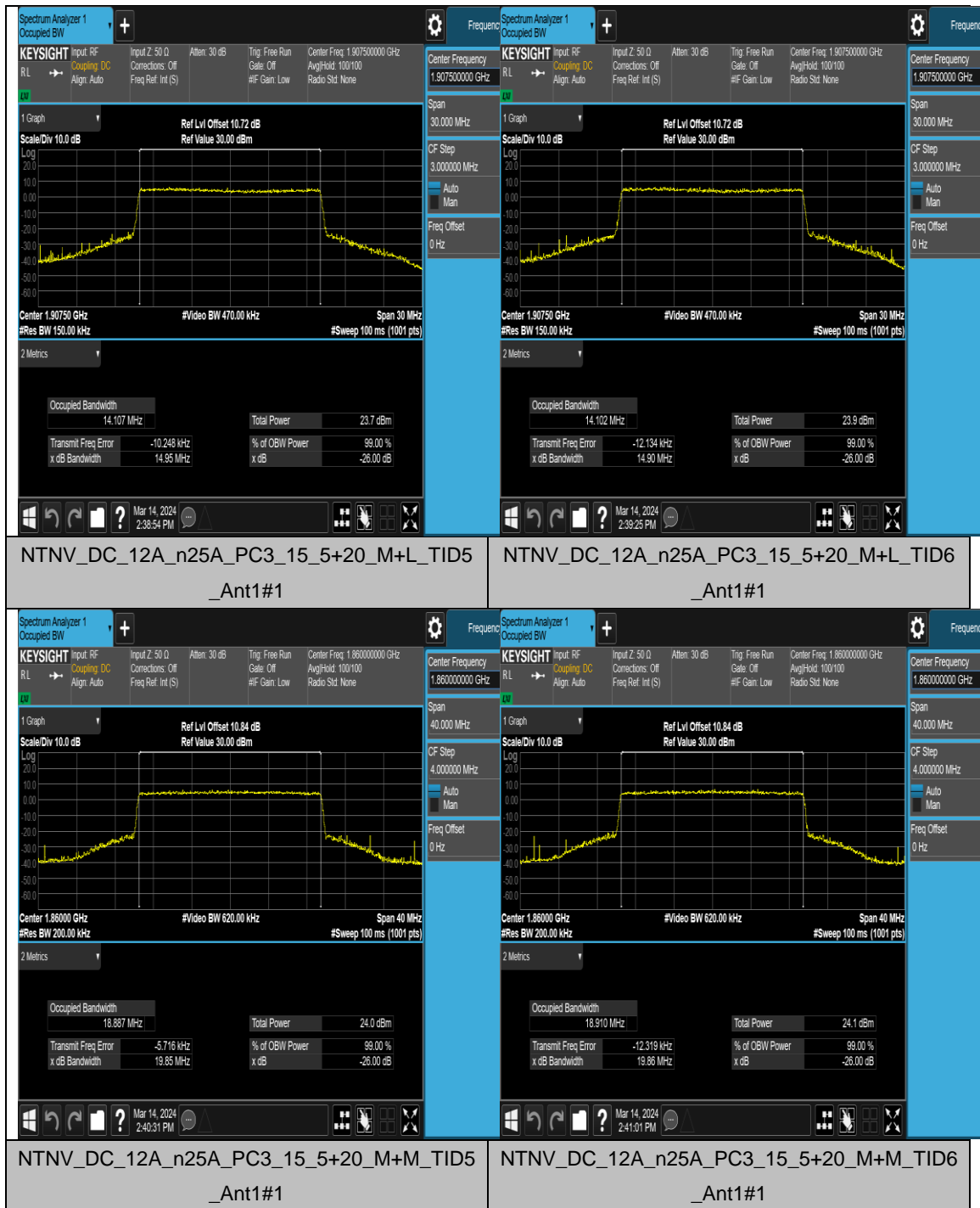
Test Graphs

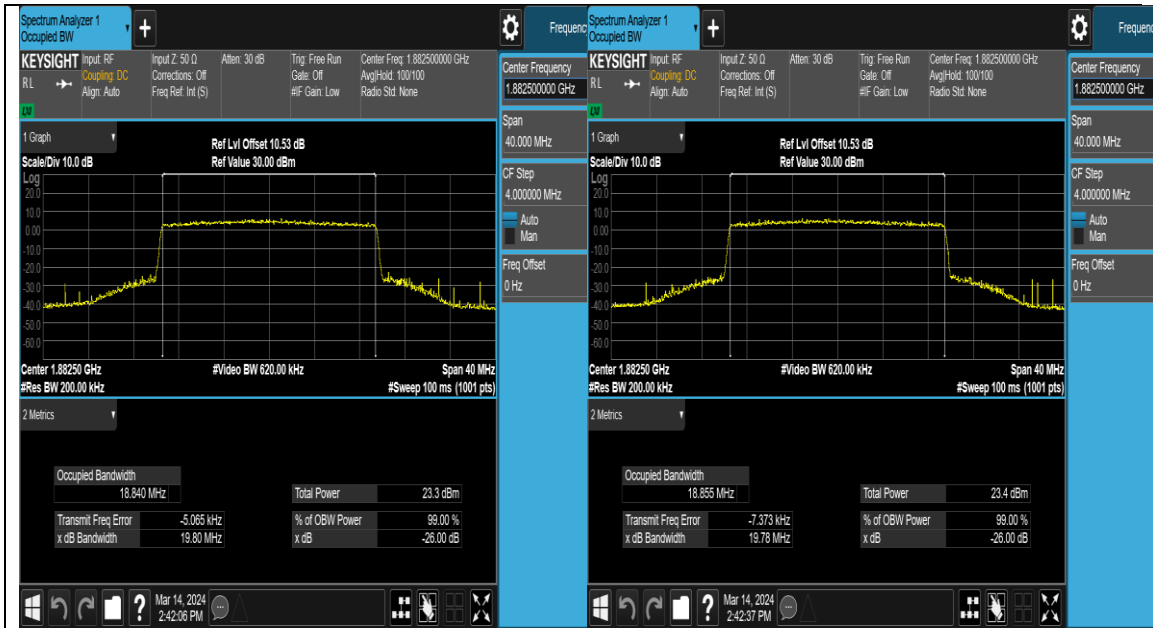






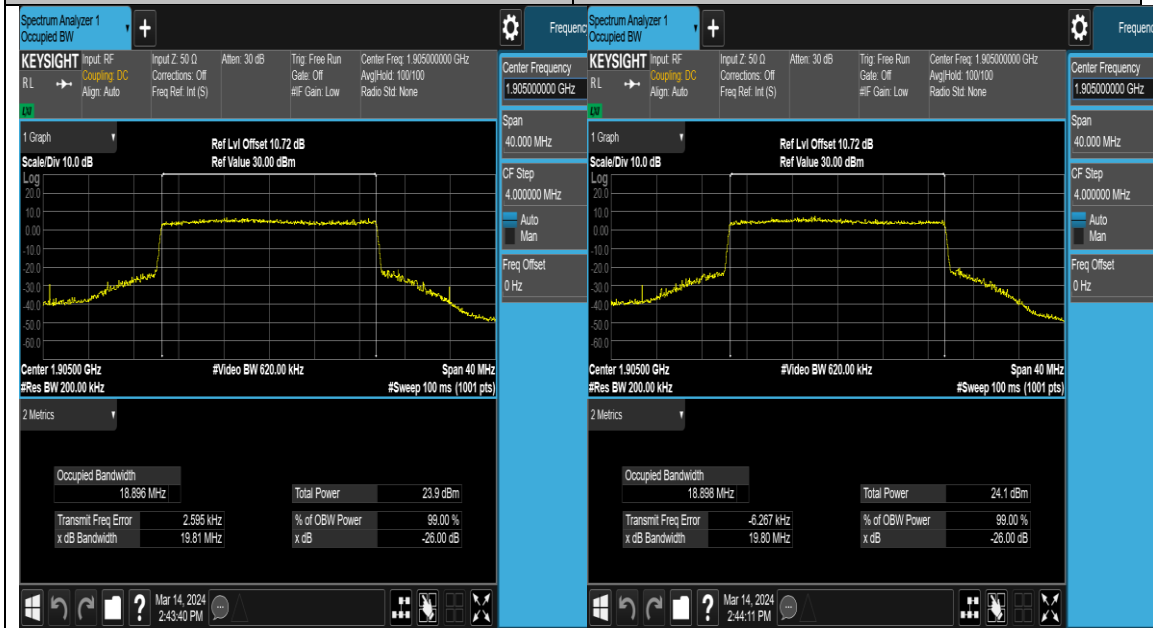






NTNV_DC_12A_n25A_PC3_15_5+20_M+H_TID5
_Ant1#1

NTNV_DC_12A_n25A_PC3_15_5+20_M+H_TID6
_Ant1#1



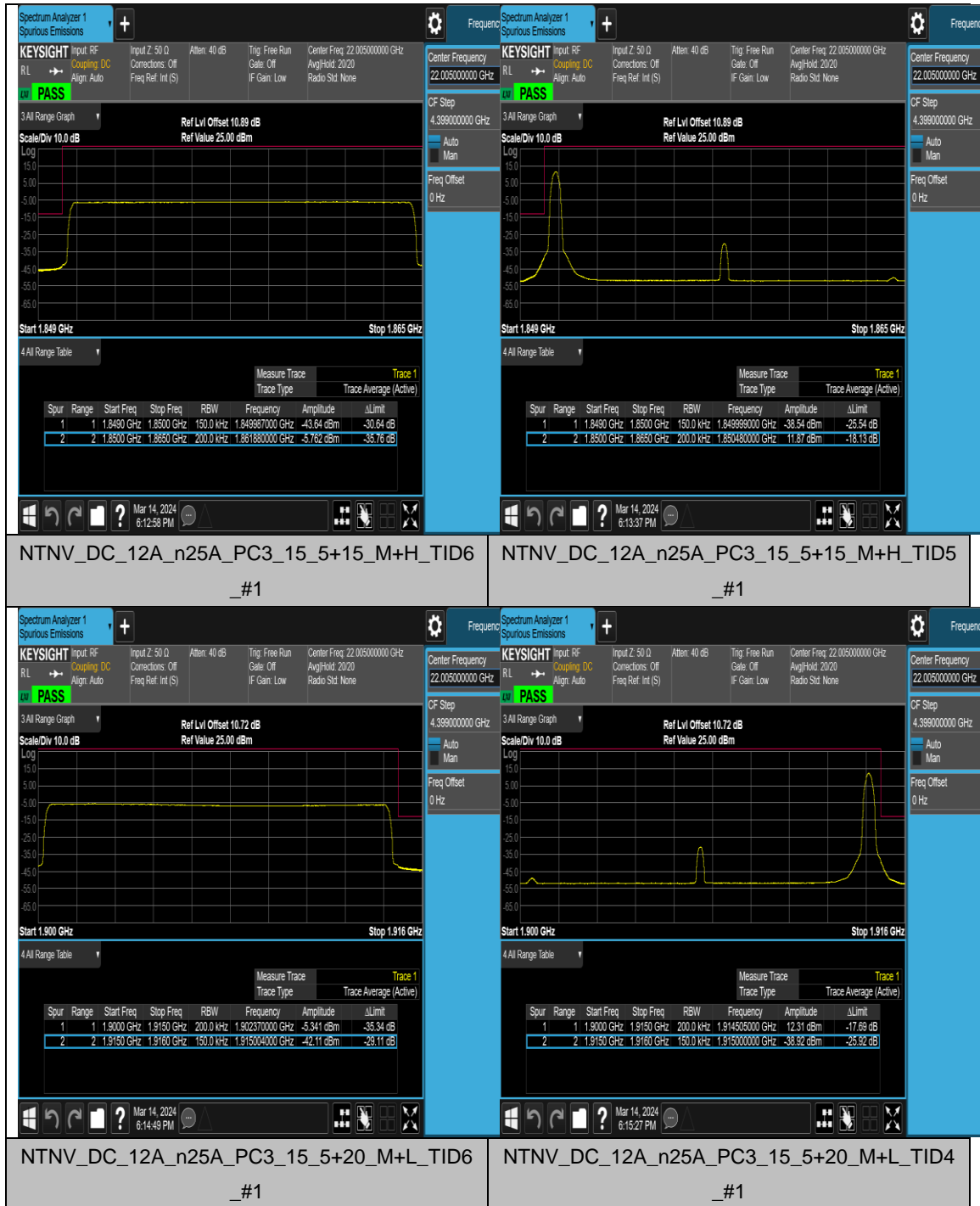
Appendix D: Band Edge for NSA**Test Result**

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA**Test Result**

Band	SC S	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-60.80	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-72.46	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-52.31	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-34.05	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-43.26	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-45.43	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.06	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.05	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.46	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-34.79	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.48	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.40	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.30	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.32	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.80	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.74	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.61	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.05	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-73.17	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-52.52	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-34.11	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-43.76	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-45.17	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.50	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.18	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.31	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-35.61	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.62	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.39	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.85	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.80	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.22	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.71	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.55	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.31	-13	PASS

DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.02	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-72.61	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-50.24	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-37.77	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-43.72	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-45.38	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.37	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.49	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.36	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.75	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.63	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.47	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.79	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.60	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-38.09	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.68	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.31	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-73.20	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-52.13	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-37.81	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-43.73	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-45.36	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.53	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.63	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.51	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.49	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.49	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.22	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.05	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.47	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.60	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.40	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.04	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-72.62	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-52.65	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-27.38	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-43.62	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-45.44	-13	PASS

DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.66	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.95	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.21	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.53	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.27	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.29	-43	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.84	-33	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.24	-23	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-35.58	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.67	-13	PASS
DC_12A_n25A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.35	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.37	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-73.24	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-50.51	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-27.07	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-43.75	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.68	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.88	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.04	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.52	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.21	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.02	-43	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.95	-33	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.09	-23	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-35.43	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.63	-13	PASS
DC_12A_n25A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.35	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.38	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-73.63	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-52.03	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-33.80	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-43.42	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-45.41	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.31	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.84	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-48.53	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.67	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.60	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.33	-13	PASS

DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.87	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.30	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-25.64	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.56	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.61	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-63.00	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-70.85	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-51.75	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-34.18	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-45.39	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.09	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.56	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.40	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.49	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.68	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.34	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.39	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.11	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-51.73	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.71	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.60	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.32	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.31	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-72.90	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-52.47	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-43.80	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-45.45	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.58	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.09	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.30	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.61	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.56	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.46	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.25	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.15	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-47.02	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.71	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.64	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.40	-13	PASS

DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.83	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-70.64	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-52.54	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-37.55	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-43.56	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-45.30	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-61.15	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-73.55	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.87	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.59	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.85	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.89	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.20	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.73	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.44	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.26	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.16	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-72.21	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-50.39	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-29.76	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-43.60	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-45.37	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.51	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.17	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.96	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.64	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.87	-43	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-69.69	-33	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-51.28	-23	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.75	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.63	-13	PASS
DC_12A_n25A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.37	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.91	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-72.30	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-50.62	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-29.53	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-43.44	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-45.41	-13	PASS

DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.56	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.60	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.05	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.84	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.47	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-61.09	-43	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.22	-33	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.41	-23	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.70	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.57	-13	PASS
DC_12A_n25A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.35	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-61.30	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-71.93	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-52.33	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-31.64	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-43.78	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.23	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.16	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.36	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.77	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.73	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.02	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.91	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.34	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.80	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.61	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.40	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-63.61	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-73.41	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-52.47	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-33.47	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-43.75	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-45.49	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-61.90	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.77	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-42.55	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.63	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.36	-13	PASS

DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.30	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.16	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.46	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.58	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.36	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.32	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.03	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-71.23	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-52.36	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-37.67	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-43.62	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-45.39	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.65	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.13	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.21	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.62	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.34	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.26	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.45	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.49	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.65	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.24	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-63.14	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-72.19	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-52.33	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-37.60	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-43.67	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-45.36	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.94	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.56	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-51.58	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.58	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.51	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.32	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.72	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.92	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.45	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.64	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.46	-13	PASS

DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-61.98	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-72.74	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-52.41	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-32.08	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.14	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.33	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.47	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.81	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.27	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.05	-43	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.69	-33	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.43	-23	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.56	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.69	-13	PASS
DC_12A_n25A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.47	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.11	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-74.02	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-52.39	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-32.41	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-43.60	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-45.40	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.81	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.54	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.62	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.76	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.55	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.28	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-61.38	-43	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-70.99	-33	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.62	-23	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.75	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.64	-13	PASS
DC_12A_n25A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.28	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.77	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-72.83	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-52.54	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-33.03	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-43.50	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-45.18	-13	PASS

DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.26	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.34	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.58	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.74	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.58	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.08	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.21	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.66	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.36	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.77	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.57	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.24	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.31	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-73.02	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-50.91	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-32.63	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-43.47	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-45.35	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.56	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.82	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.64	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.61	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.59	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.24	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.40	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.77	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.82	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.44	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.04	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-71.93	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-51.56	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-37.75	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.57	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-45.40	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.86	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.92	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.99	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.55	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.41	-13	PASS

DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.51	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.18	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.29	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.64	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.54	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-61.85	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-71.58	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-51.03	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-37.67	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-45.30	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.96	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.07	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.01	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.78	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.55	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.18	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.24	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.98	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.95	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.67	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.45	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.63	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-73.35	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-52.37	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-33.11	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-45.38	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.31	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.20	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.19	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.63	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.50	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.06	-43	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.25	-33	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.43	-23	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.73	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_12A_n25A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.41	-13	PASS

DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.43	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-73.48	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-50.29	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-32.66	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-43.68	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-45.48	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.67	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.49	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.56	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.67	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.78	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.47	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-61.36	-43	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.37	-33	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.42	-23	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.63	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.70	-13	PASS
DC_12A_n25A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.34	-13	PASS