

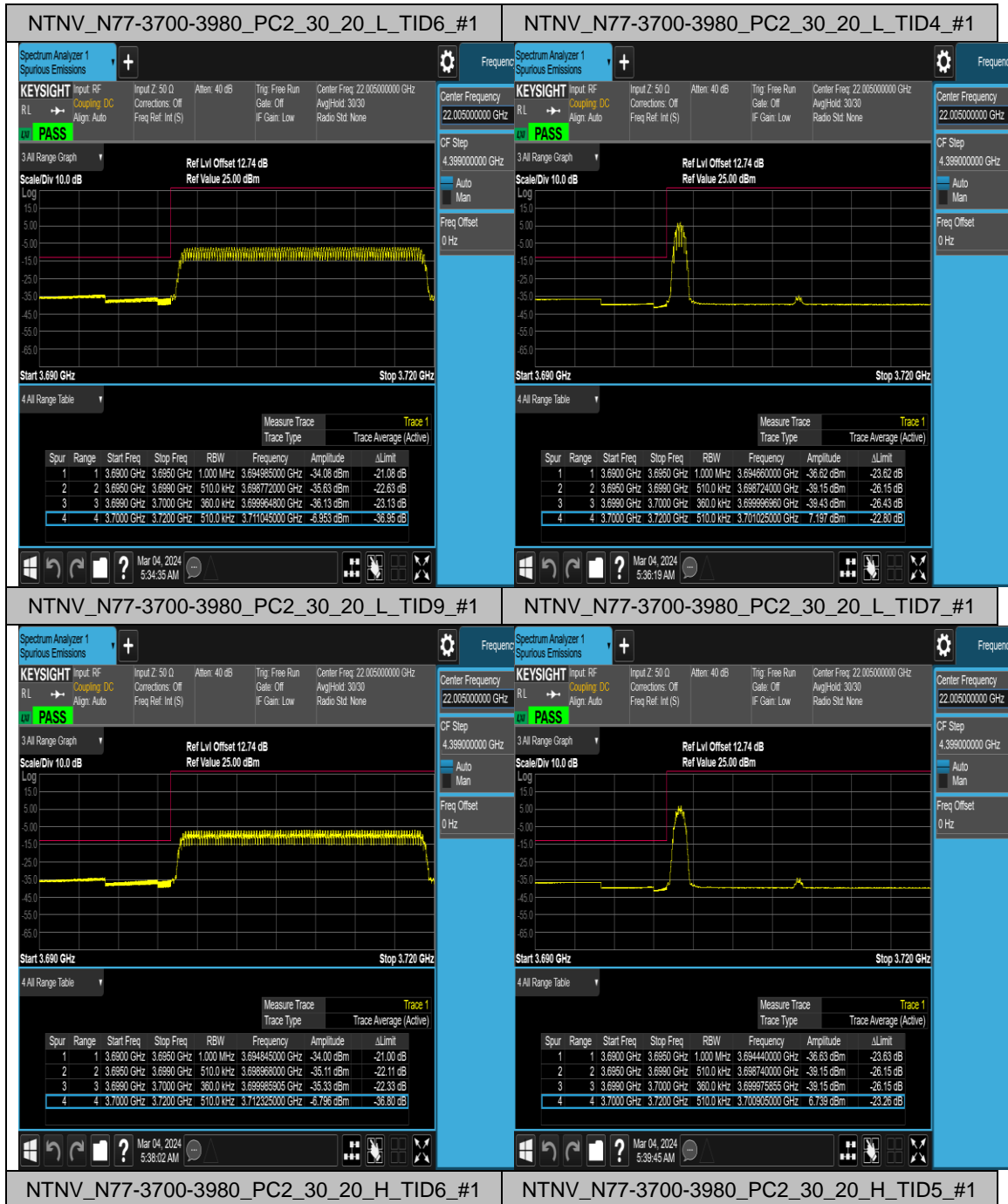
Appendix D: Band Edge for SA**Test Result**

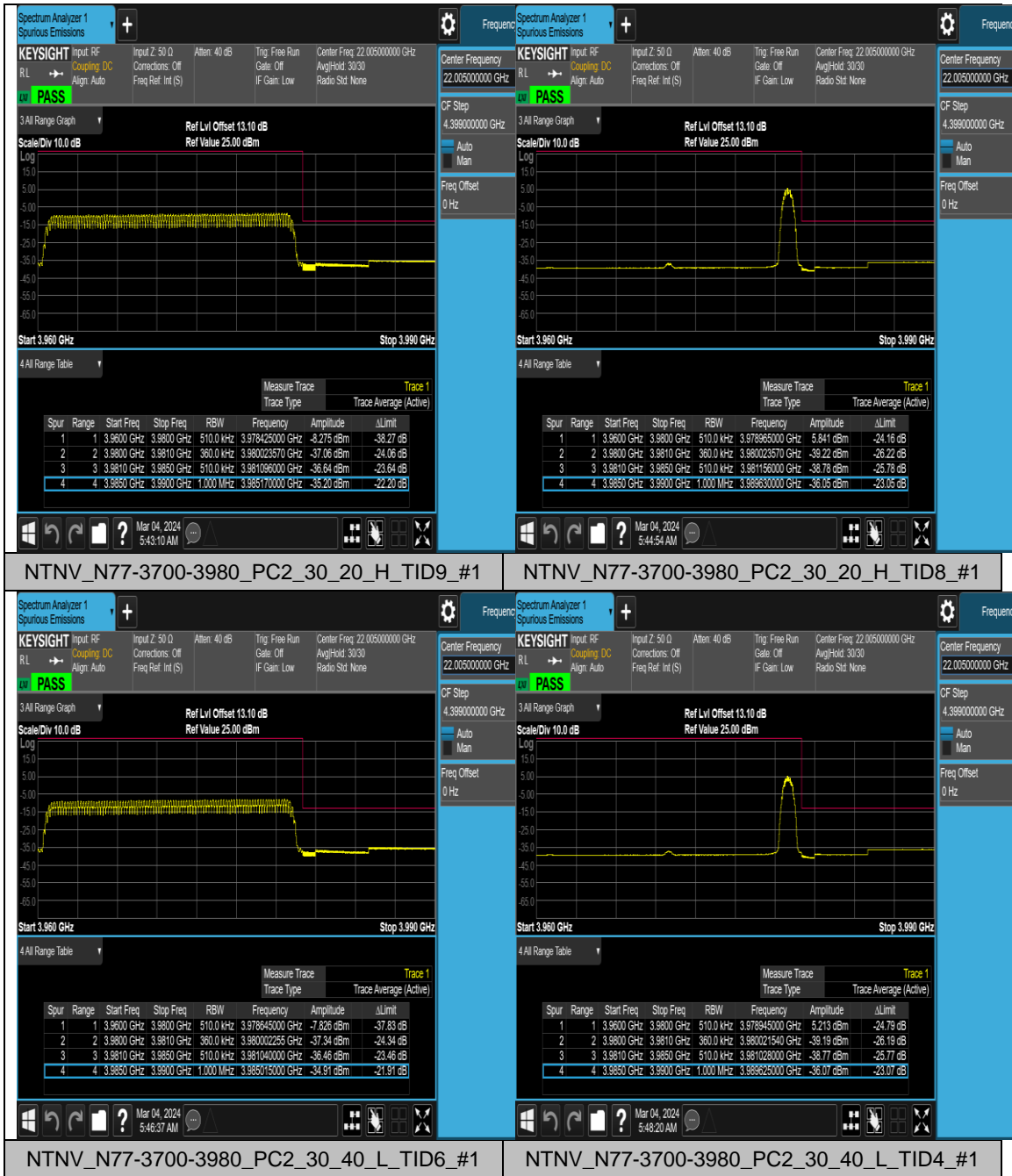
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
N77-3700-3980	30	20	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	20	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	20	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	20	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	20	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	20	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	20	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	20	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	40	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	40	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	40	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	40	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	40	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	40	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	40	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	40	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	50	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	50	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	50	CP-16QAM	L	Outer_Full	PASS

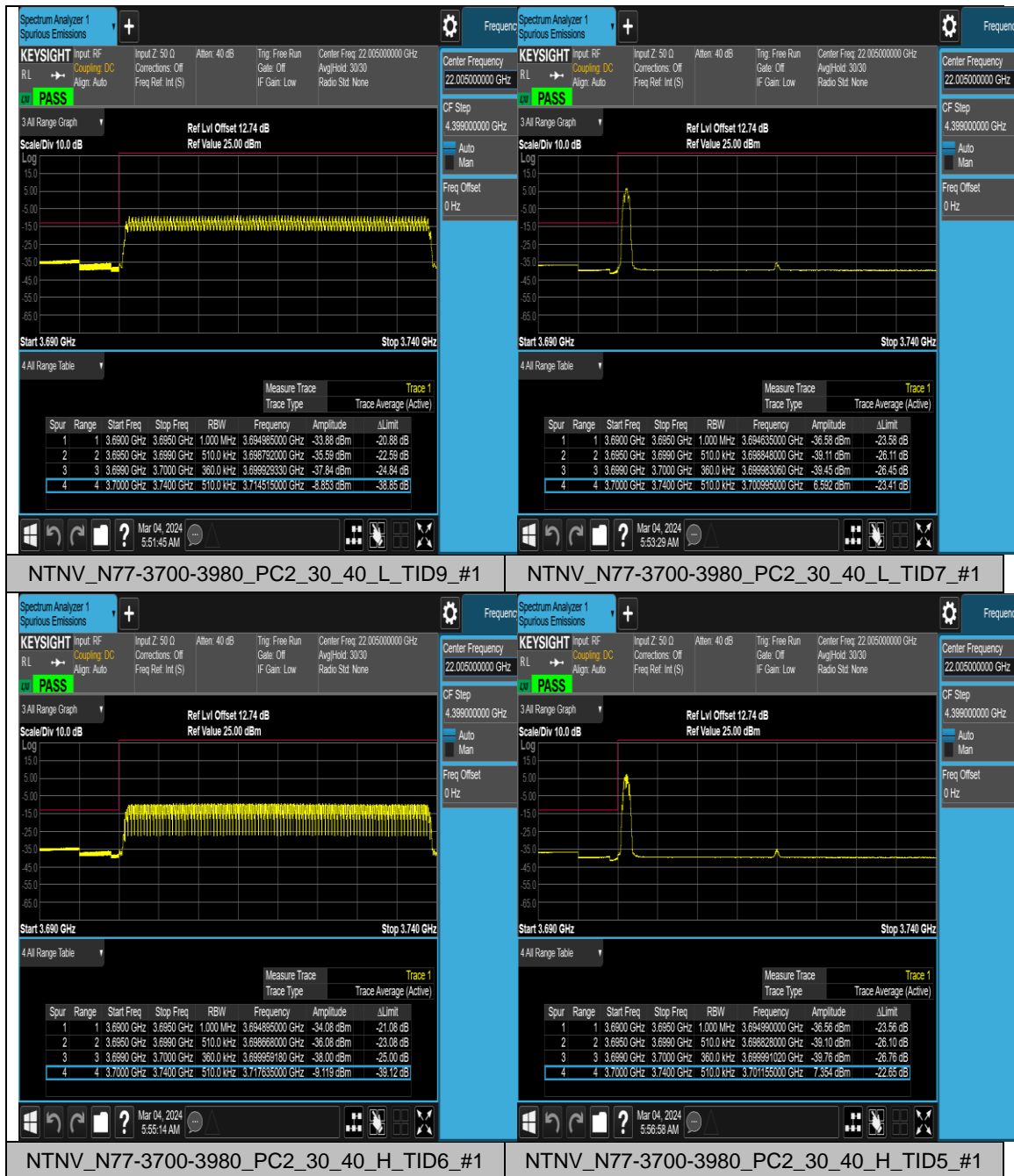
N77-3700-3980	30	50	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	50	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	50	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	50	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	50	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	60	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	60	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	60	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	60	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	60	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	60	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	60	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	60	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	80	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	80	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	80	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	80	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	80	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	80	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	80	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	80	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	90	CP-QPSK	L	Outer_Full	PASS

3980						
N77-3700-3980	30	90	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	90	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	90	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	90	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	90	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	90	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	90	CP-16QAM	H	Edge_1RB_Right	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	PASS
N77-3700-3980	30	100	CP-16QAM	L	Outer_Full	PASS
N77-3700-3980	30	100	CP-16QAM	L	Edge_1RB_Left	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	PASS
N77-3700-3980	30	100	CP-16QAM	H	Outer_Full	PASS
N77-3700-3980	30	100	CP-16QAM	H	Edge_1RB_Right	PASS

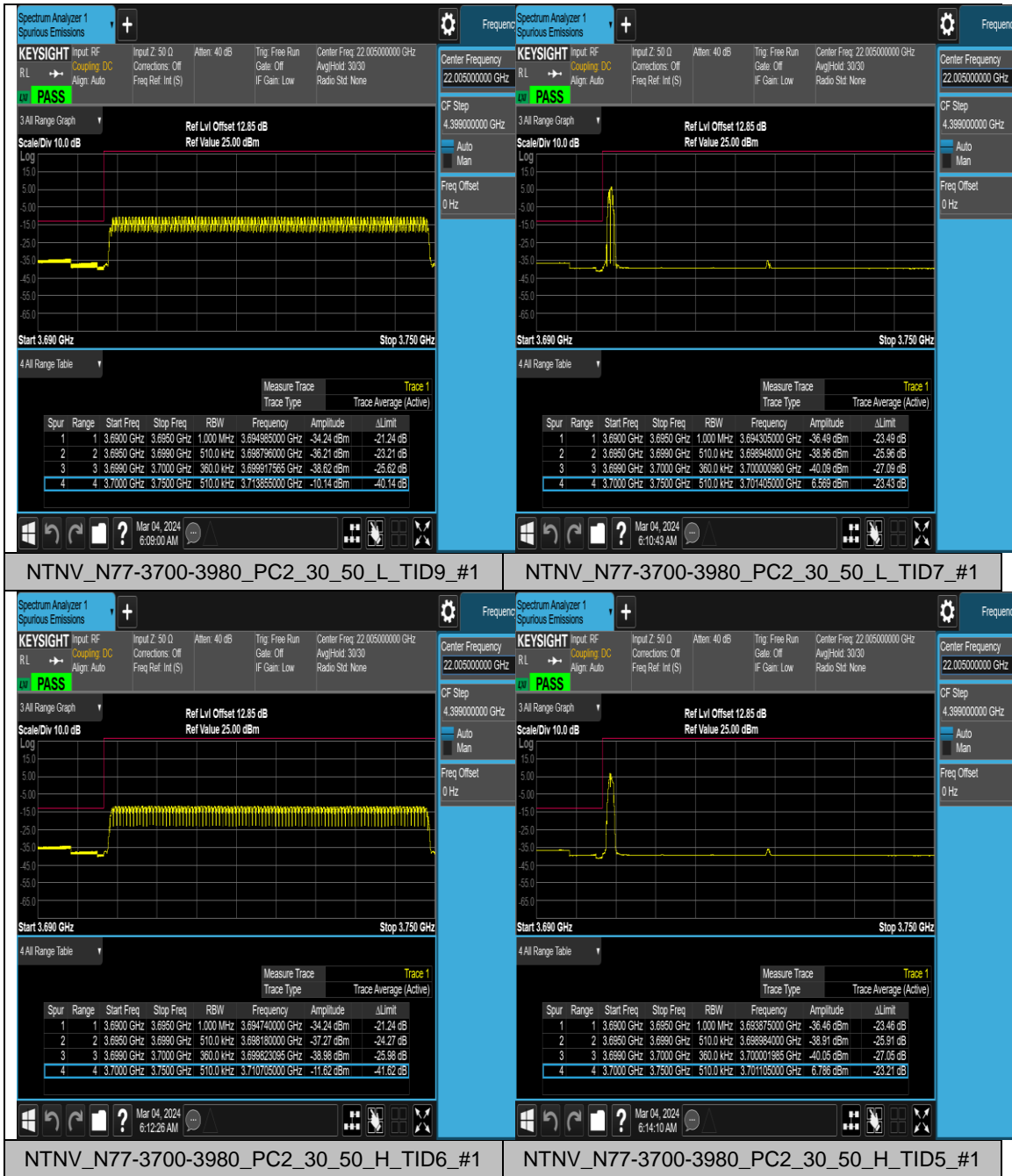
Test Graphs

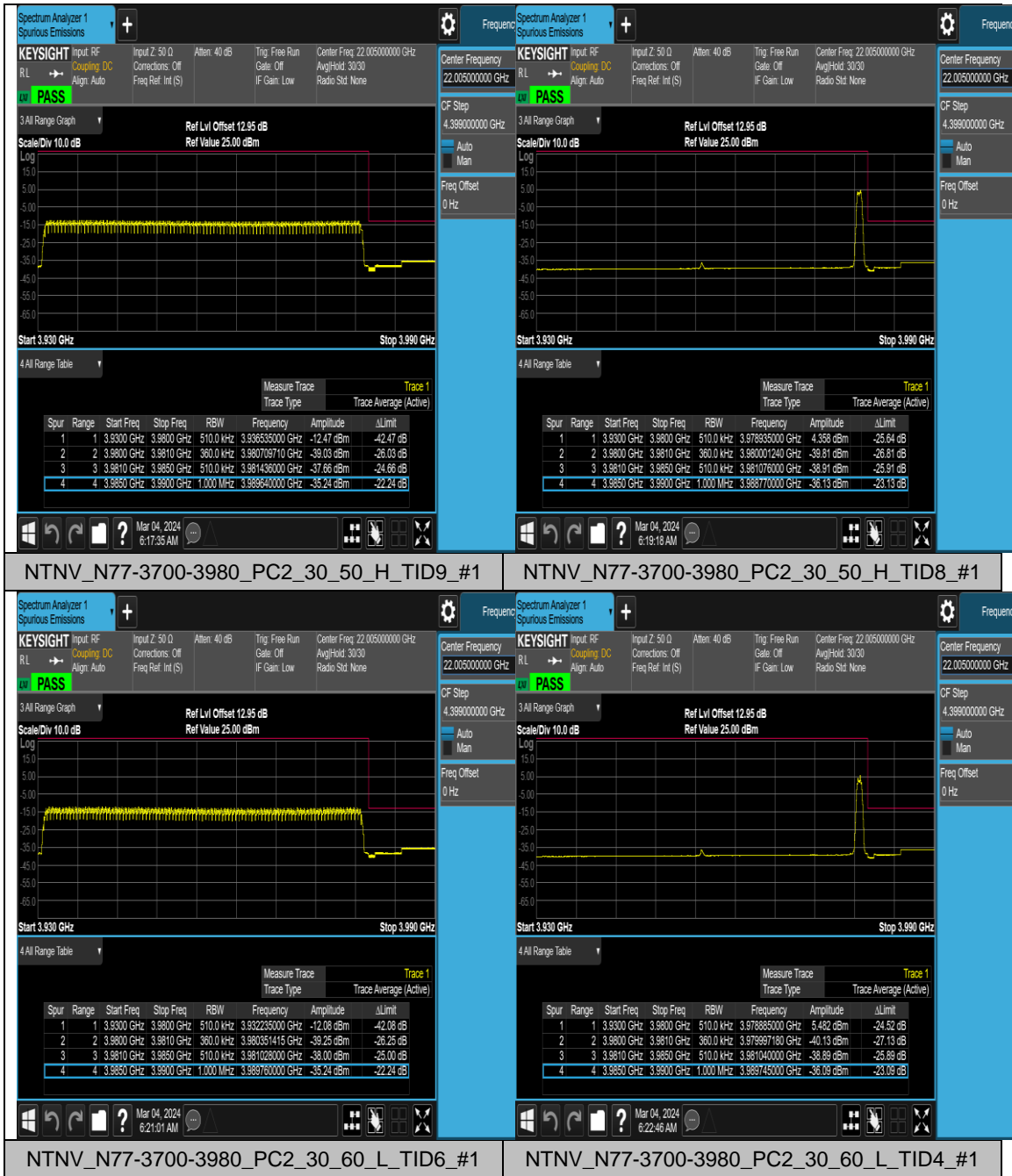


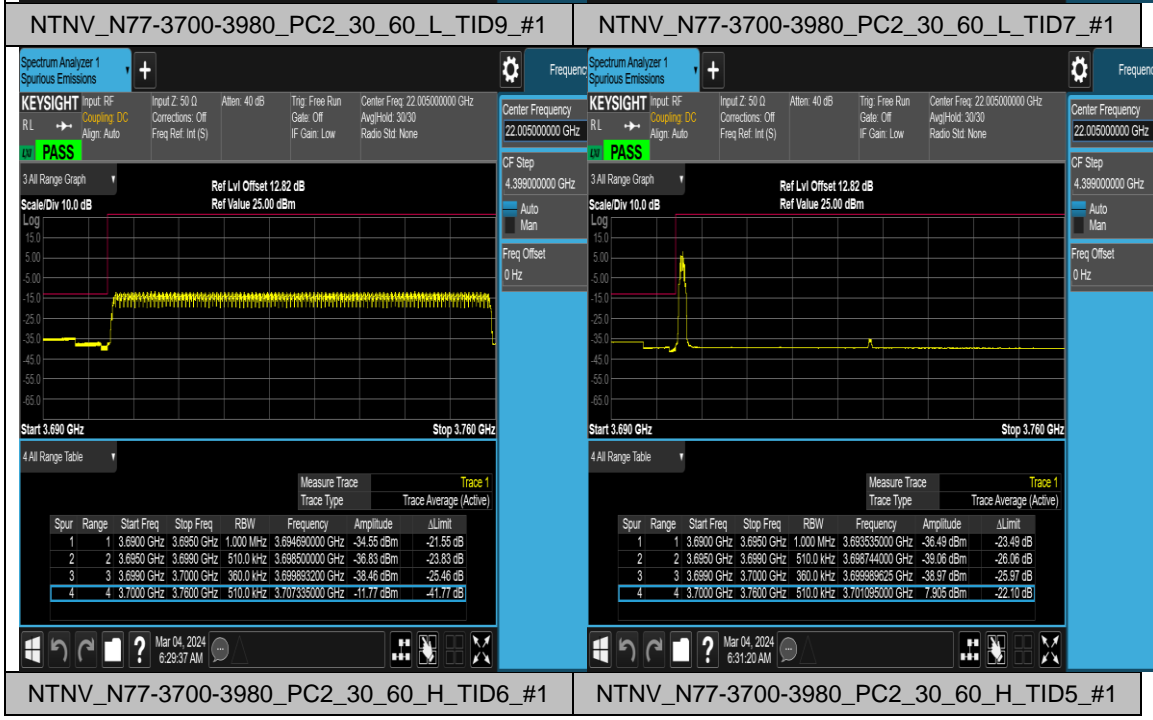
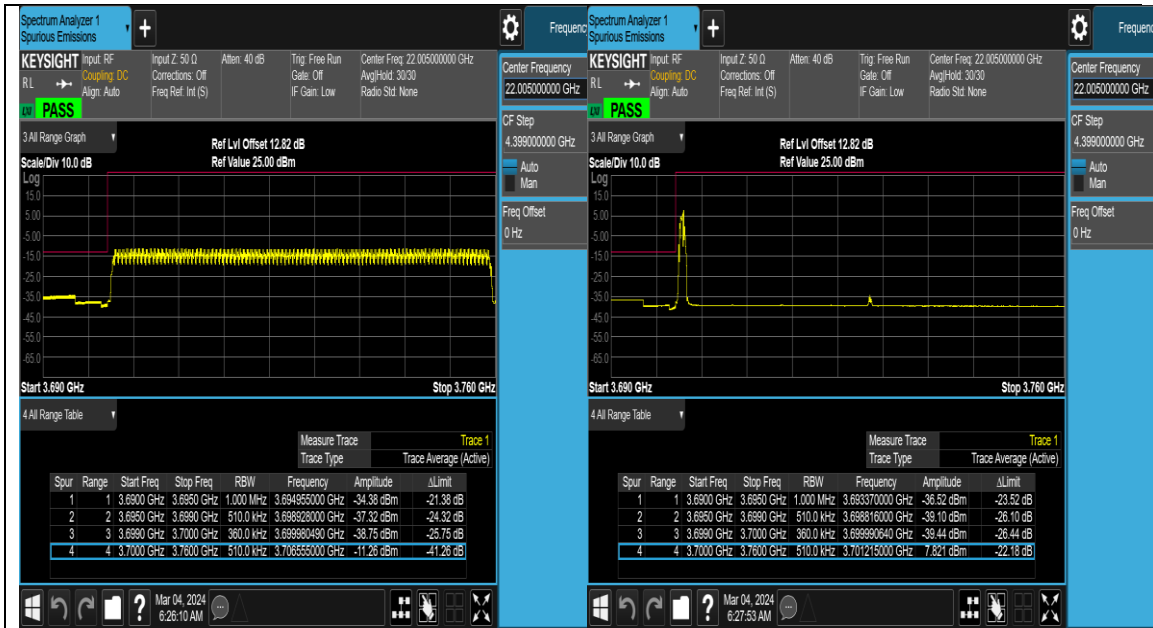


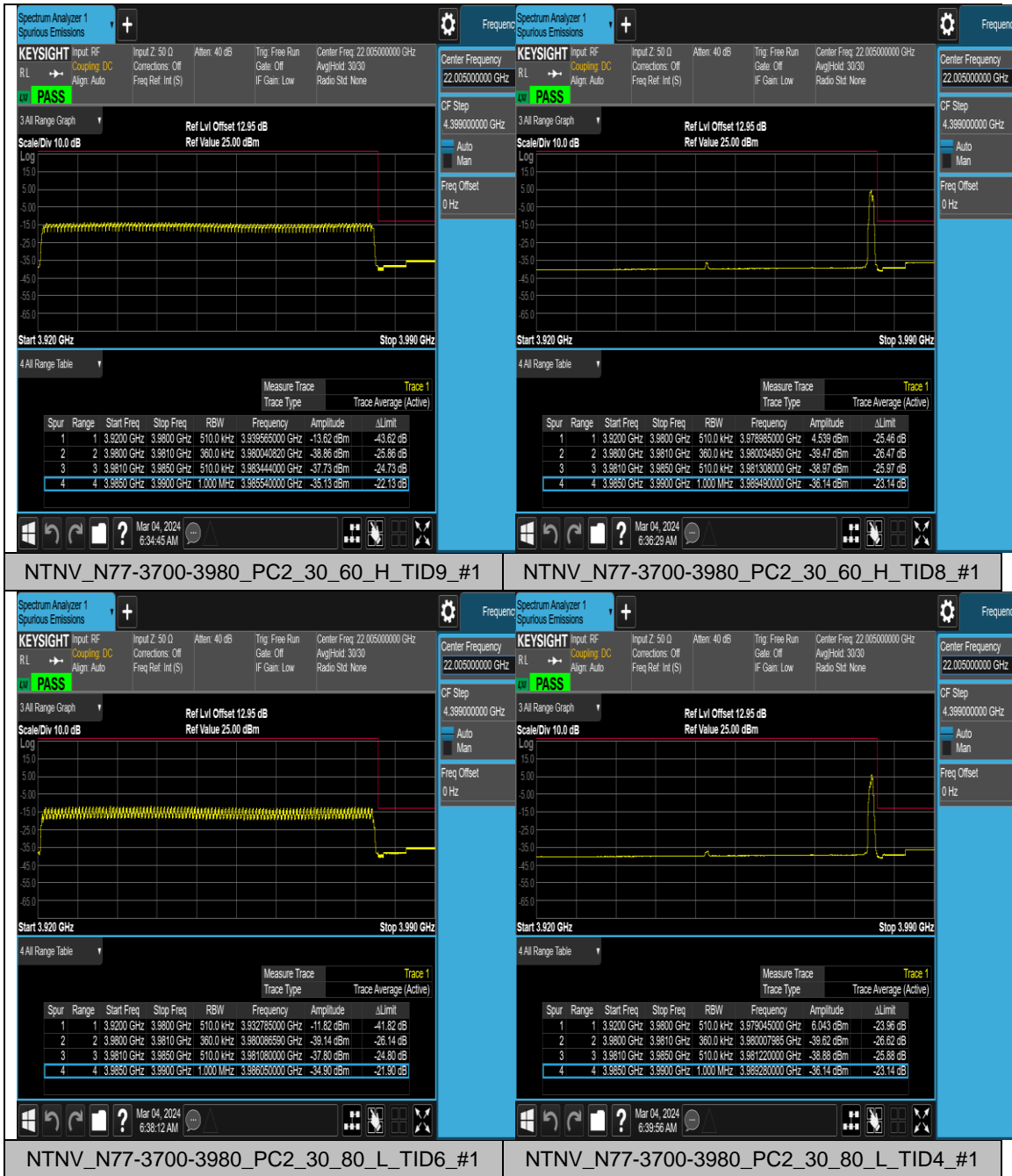


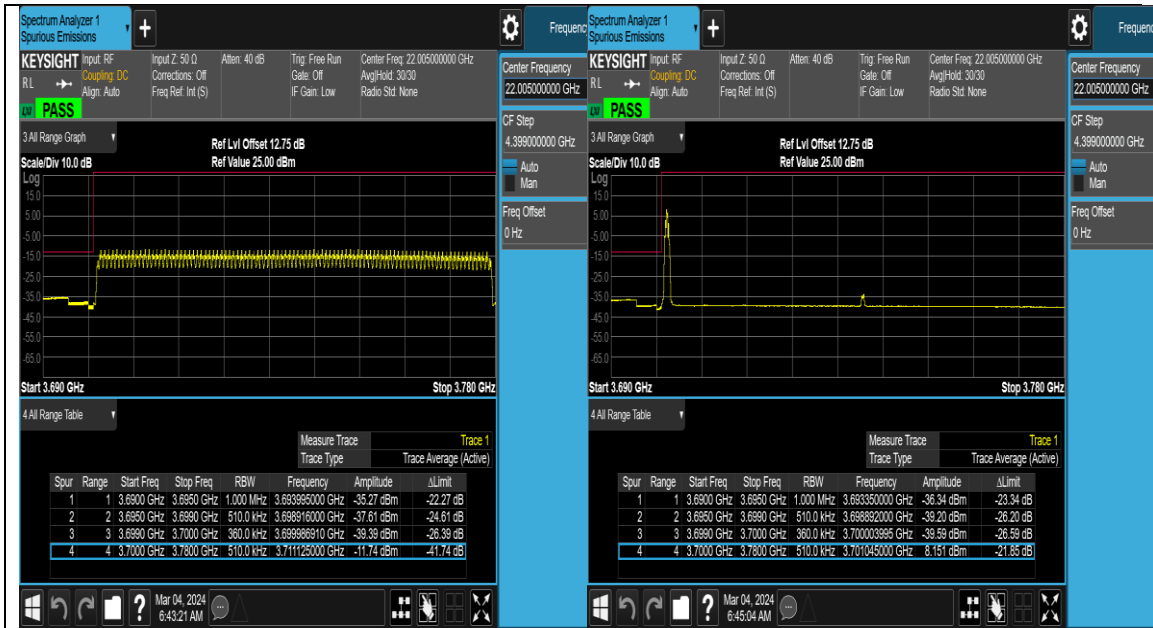




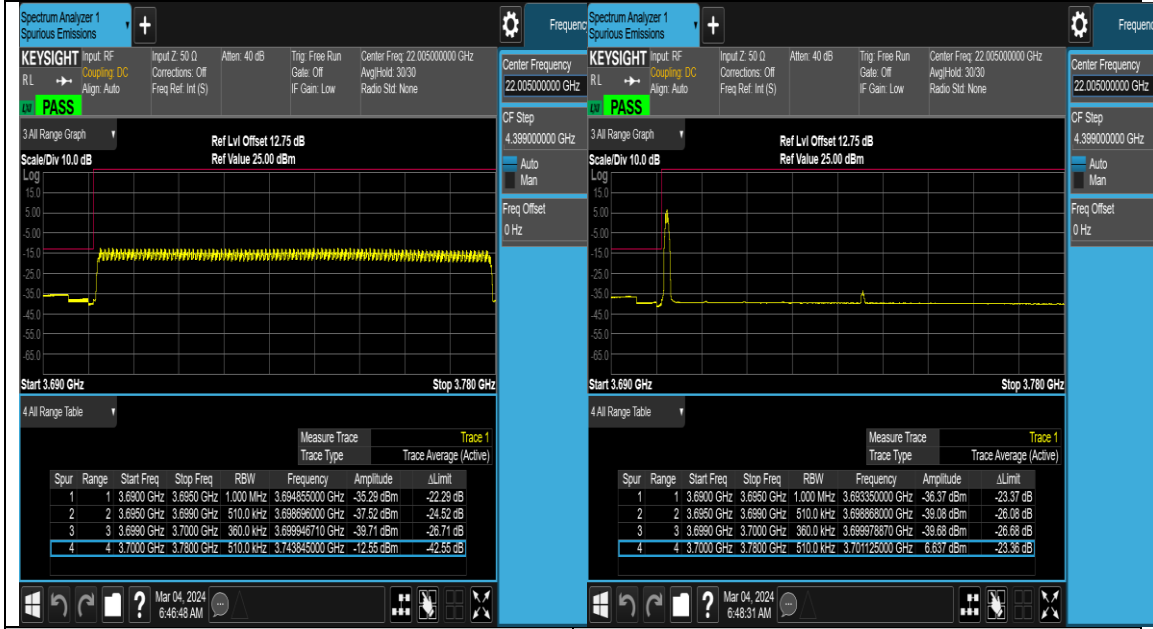








NTNV_N77-3700-3980_PC2_30_80_L_TID9_#1 NTVN_N77-3700-3980_PC2_30_80_L_TID7_#1



NTNV_N77-3700-3980_PC2_30_80_H_TID6_#1 NTVN_N77-3700-3980_PC2_30_80_H_TID5_#1



