

Appendix for 5A_n2A

Product Name: 5G CPE
Model No: S3

Appendix A: Average Power Output for SA

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	22.02	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Inner_Full	23.66	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.85	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	22.09	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	22.16	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Inner_Full	22.99	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.92	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.81	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	21.59	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Inner_Full	23.23	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.58	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.64	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	21.50	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Inner_Full	22.64	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.73	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.60	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	20.38	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Inner_Full	22.34	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.09	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.17	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	20.92	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Inner_Full	22.27	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.09	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	20.96	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	21.40	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Inner_Full	22.91	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.43	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.40	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	21.24	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Inner_Full	22.11	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	21.12	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.22	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	21.26	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Inner_Full	22.60	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.36	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.15	PC3	PASS

DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	21.13	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Inner_Full	22.14	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.32	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.59	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	21.62	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Inner_Full	22.83	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.31	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.33	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	21.36	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Inner_Full	22.36	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.16	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.23	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	21.32	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Inner_Full	22.71	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.31	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.52	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	21.61	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Inner_Full	22.37	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	21.28	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.41	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	21.20	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Inner_Full	22.85	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.31	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.19	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	21.27	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Inner_Full	22.15	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.28	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.31	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	21.37	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Inner_Full	22.96	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.06	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.33	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	21.26	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Inner_Full	22.28	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.28	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.27	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	21.41	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Inner_Full	22.88	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.47	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.56	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	21.49	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Inner_Full	22.52	PC3	PASS

DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.26	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	21.43	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	21.32	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Inner_Full	22.83	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.41	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.16	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	21.32	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Inner_Full	22.20	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.80	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.16	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	21.36	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Inner_Full	22.80	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.12	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.46	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	21.31	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Inner_Full	22.31	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.02	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.48	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

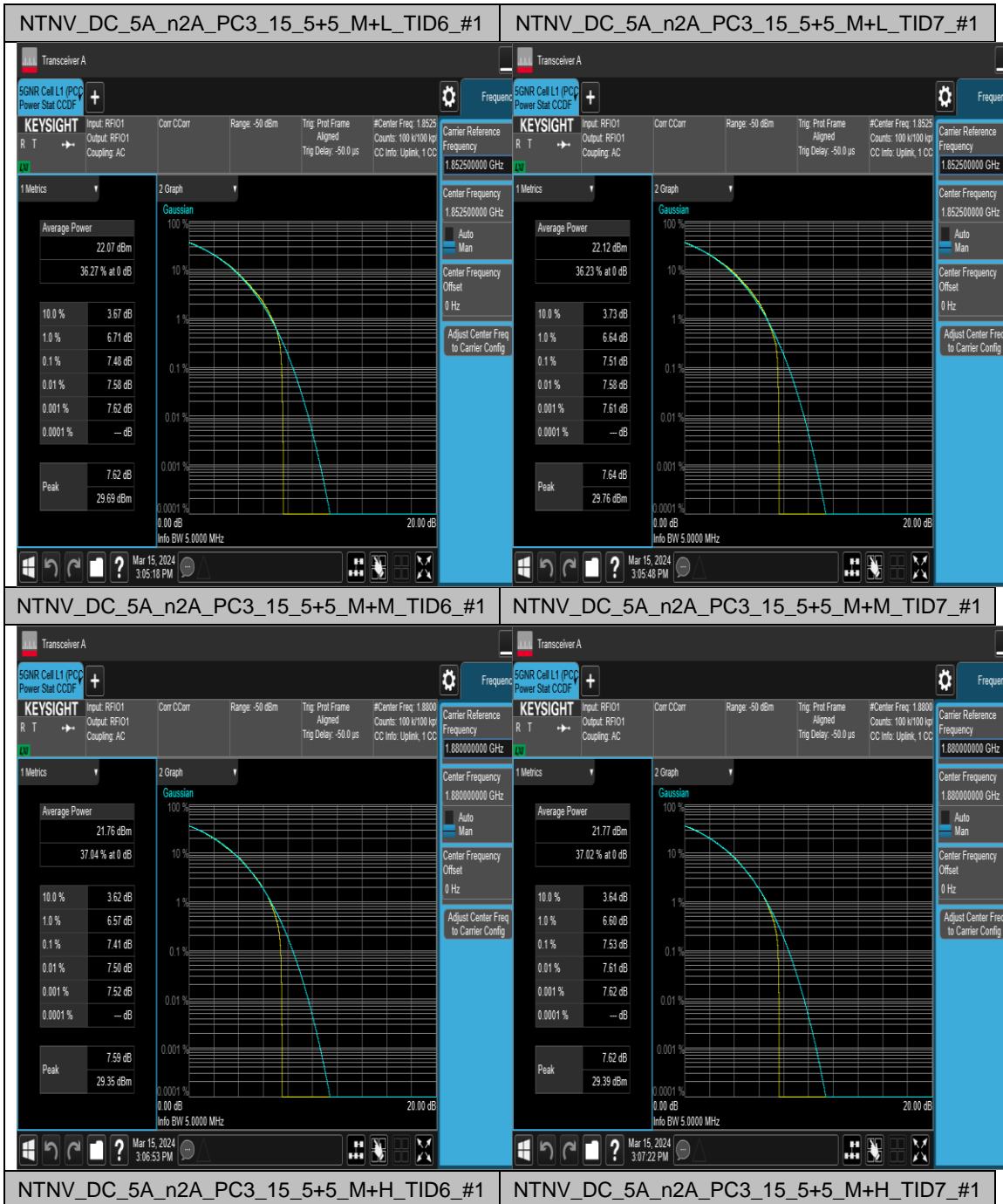
Peak-to-Average Ratio(CCDF)

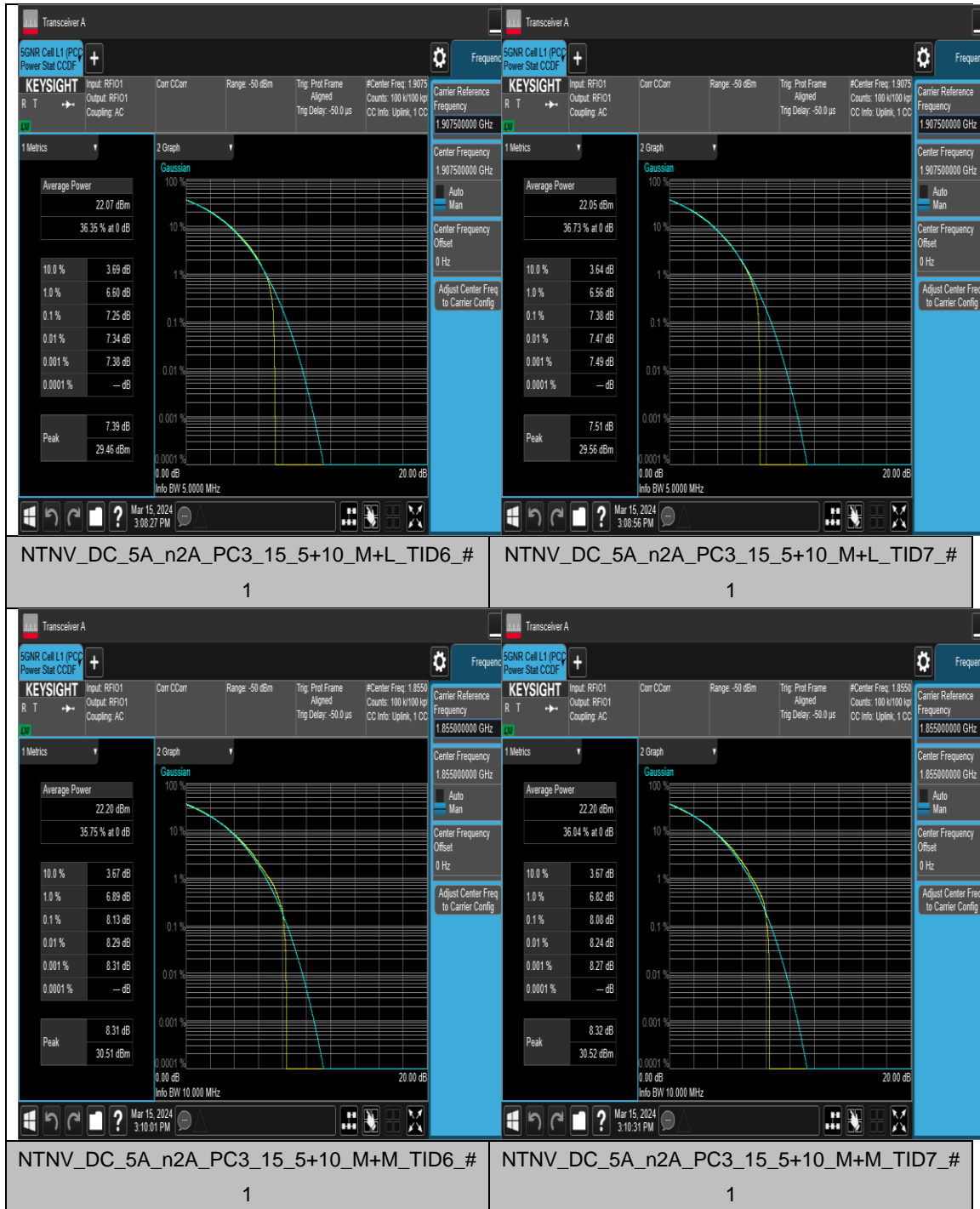
Test Result

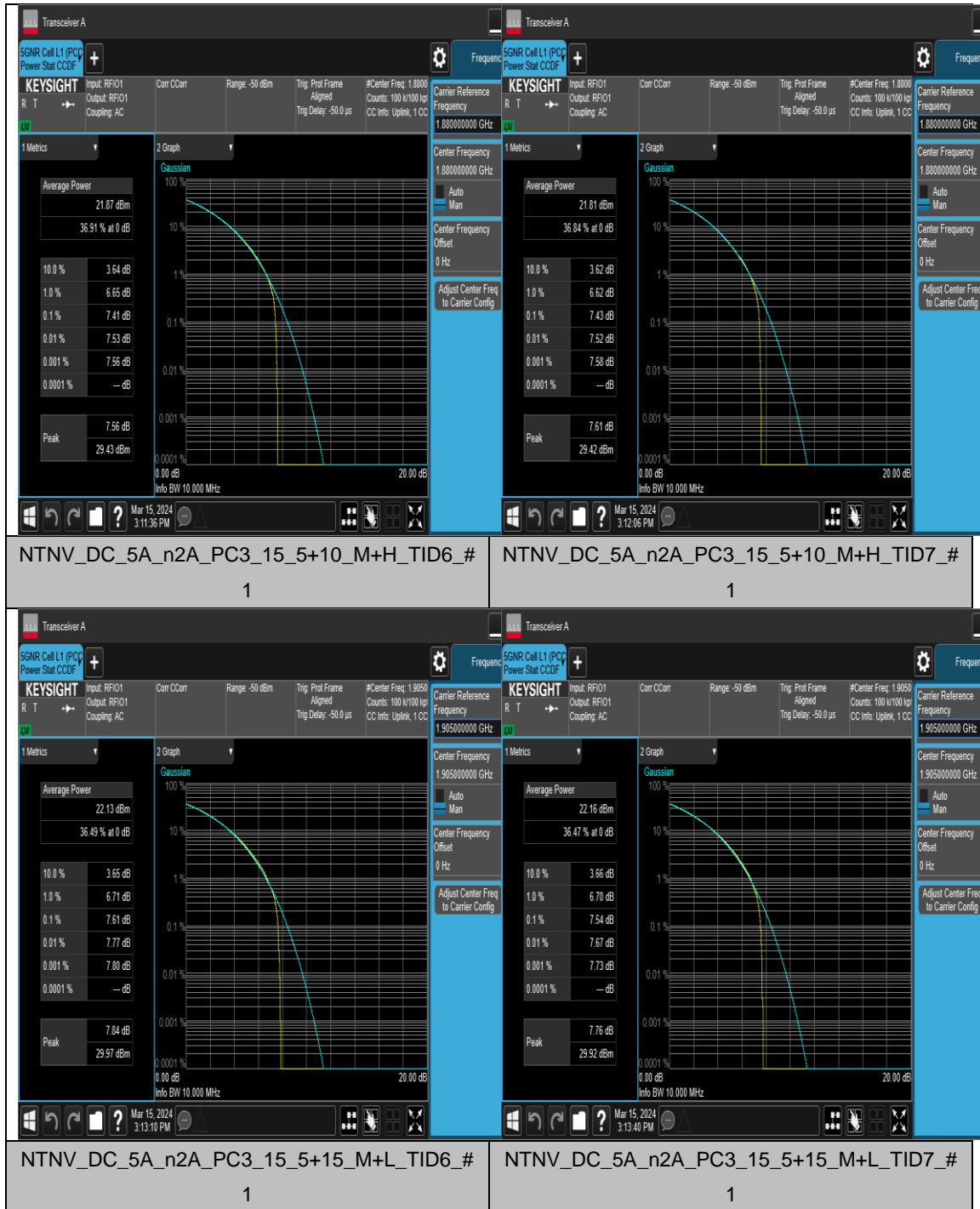
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n2 A	15	5+5	CP-QPSK	M+L	Outer_Full	7.48	≤13	PASS
DC_5A_n2 A	15	5+5	CP-16QAM	M+L	Outer_Full	7.51	≤13	PASS
DC_5A_n2 A	15	5+5	CP-QPSK	M+M	Outer_Full	7.41	≤13	PASS
DC_5A_n2 A	15	5+5	CP-16QAM	M+M	Outer_Full	7.53	≤13	PASS
DC_5A_n2 A	15	5+5	CP-QPSK	M+H	Outer_Full	7.25	≤13	PASS
DC_5A_n2 A	15	5+5	CP-16QAM	M+H	Outer_Full	7.38	≤13	PASS
DC_5A_n2 A	15	5+10	CP-QPSK	M+L	Outer_Full	8.13	≤13	PASS
DC_5A_n2 A	15	5+10	CP-16QAM	M+L	Outer_Full	8.08	≤13	PASS
DC_5A_n2 A	15	5+10	CP-QPSK	M+M	Outer_Full	7.41	≤13	PASS
DC_5A_n2 A	15	5+10	CP-16QAM	M+M	Outer_Full	7.43	≤13	PASS
DC_5A_n2 A	15	5+10	CP-QPSK	M+H	Outer_Full	7.61	≤13	PASS
DC_5A_n2 A	15	5+10	CP-16QAM	M+H	Outer_Full	7.54	≤13	PASS
DC_5A_n2 A	15	5+15	CP-QPSK	M+L	Outer_Full	7.26	≤13	PASS
DC_5A_n2 A	15	5+15	CP-16QAM	M+L	Outer_Full	7.29	≤13	PASS
DC_5A_n2 A	15	5+15	CP-QPSK	M+M	Outer_Full	6.97	≤13	PASS
DC_5A_n2 A	15	5+15	CP-16QAM	M+M	Outer_Full	7.79	≤13	PASS
DC_5A_n2 A	15	5+15	CP-QPSK	M+H	Outer_Full	7.63	≤13	PASS

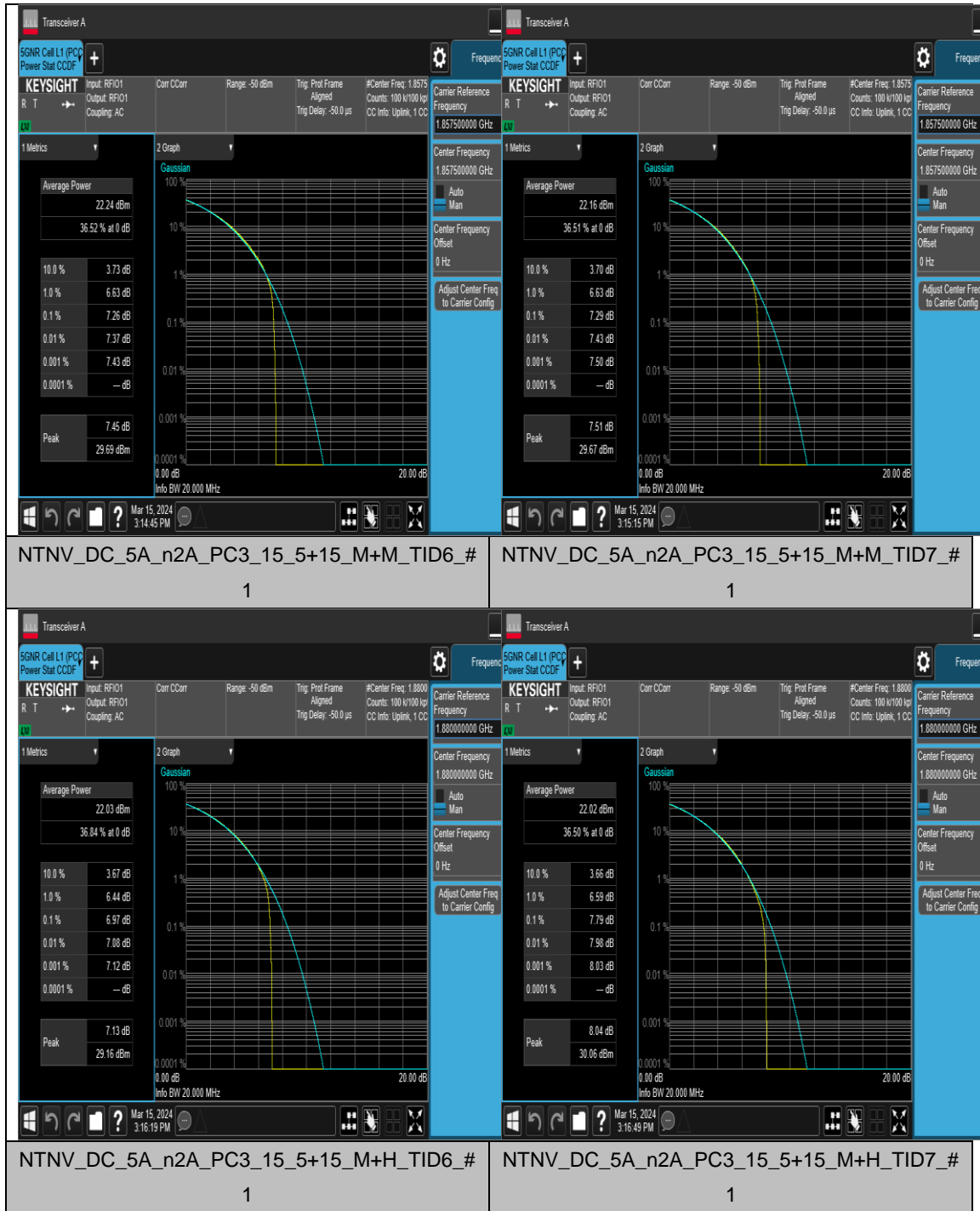
DC_5A_n2 A	15	5+15	CP-16QAM	M+H	Outer_Full	7.56	≤13	PASS
DC_5A_n2 A	15	5+20	CP-QPSK	M+L	Outer_Full	6.98	≤13	PASS
DC_5A_n2 A	15	5+20	CP-16QAM	M+L	Outer_Full	7.84	≤13	PASS
DC_5A_n2 A	15	5+20	CP-QPSK	M+M	Outer_Full	7.53	≤13	PASS
DC_5A_n2 A	15	5+20	CP-16QAM	M+M	Outer_Full	7.53	≤13	PASS
DC_5A_n2 A	15	5+20	CP-QPSK	M+H	Outer_Full	7.41	≤13	PASS
DC_5A_n2 A	15	5+20	CP-16QAM	M+H	Outer_Full	7.36	≤13	PASS

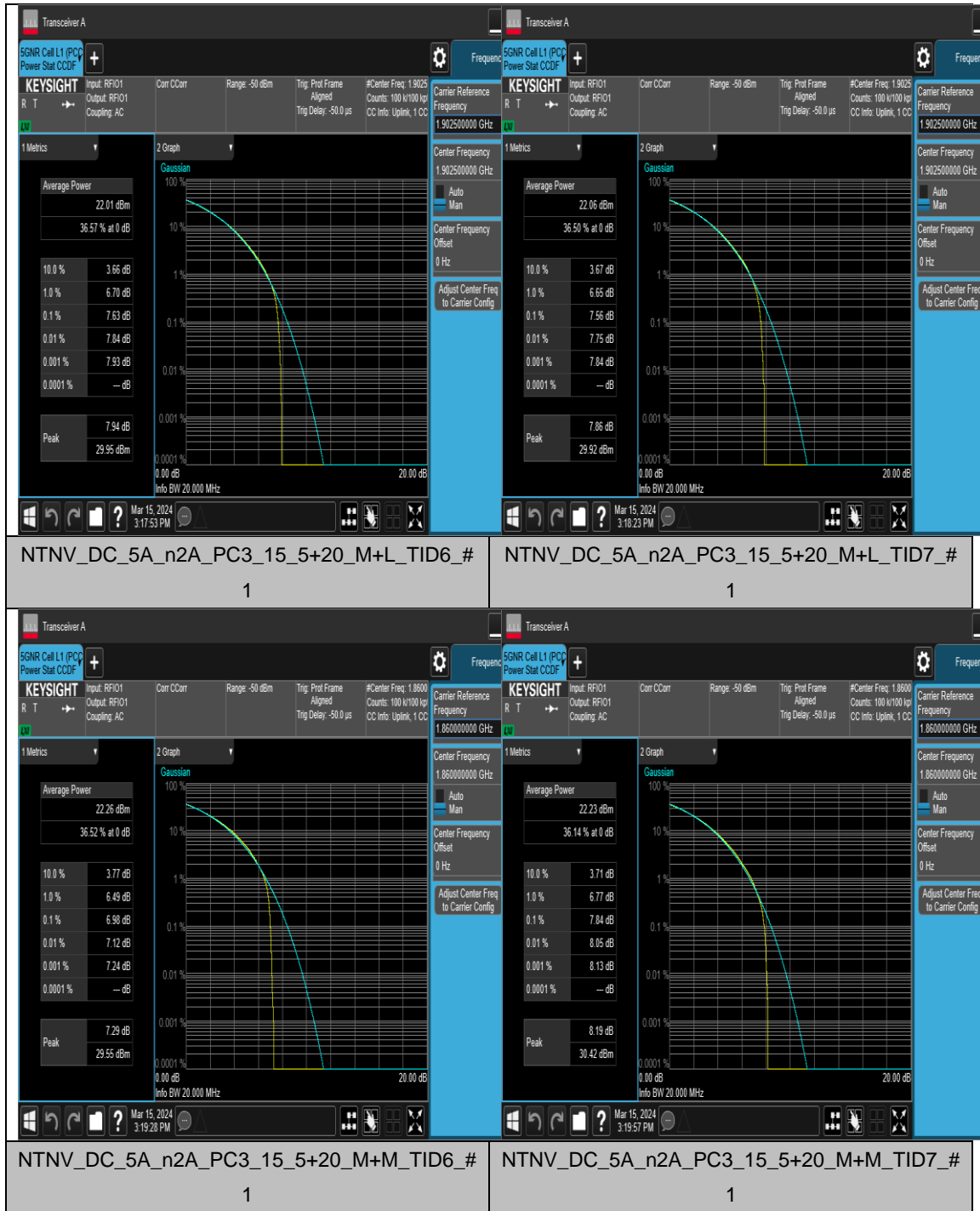
Test Graphs

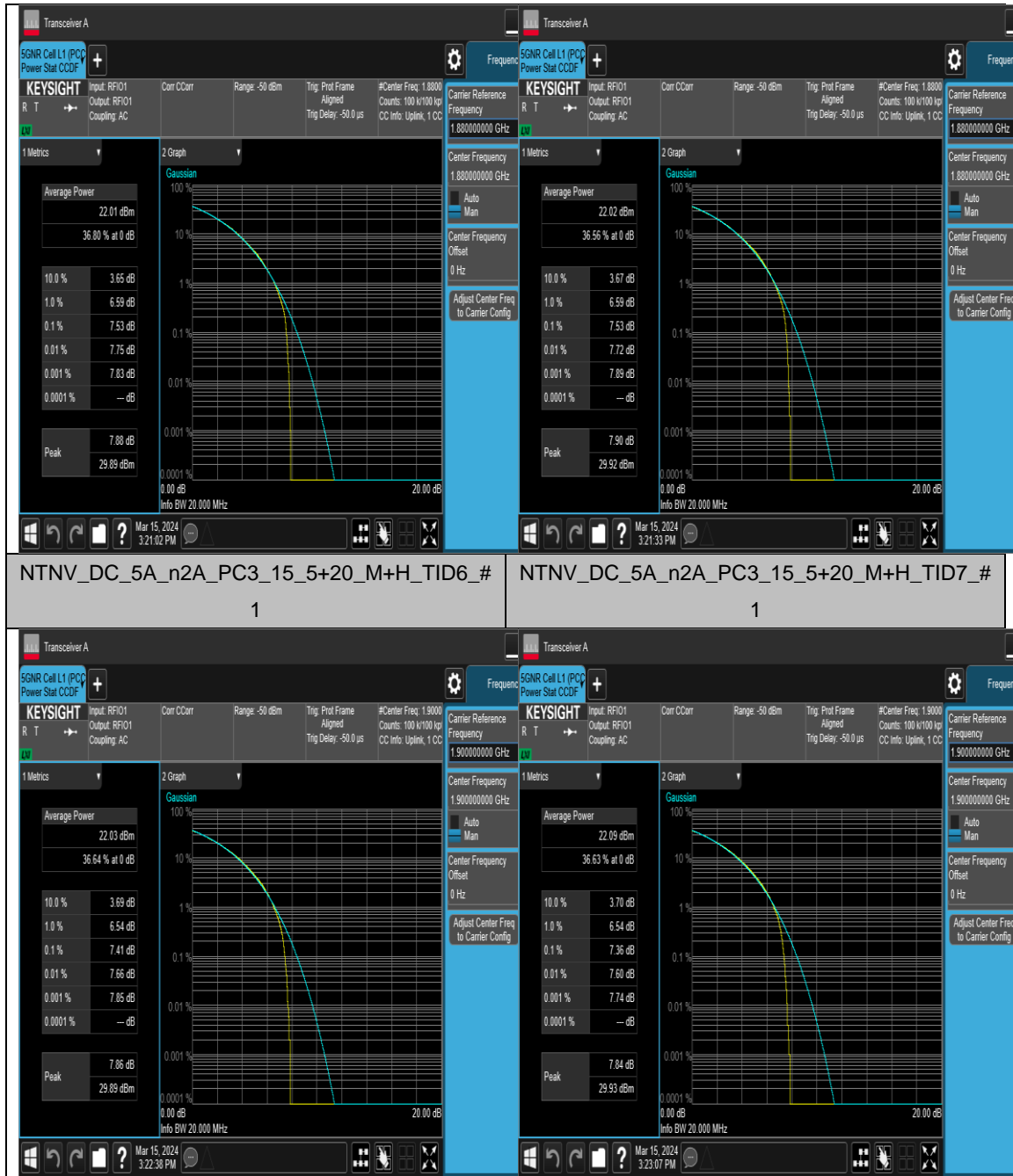








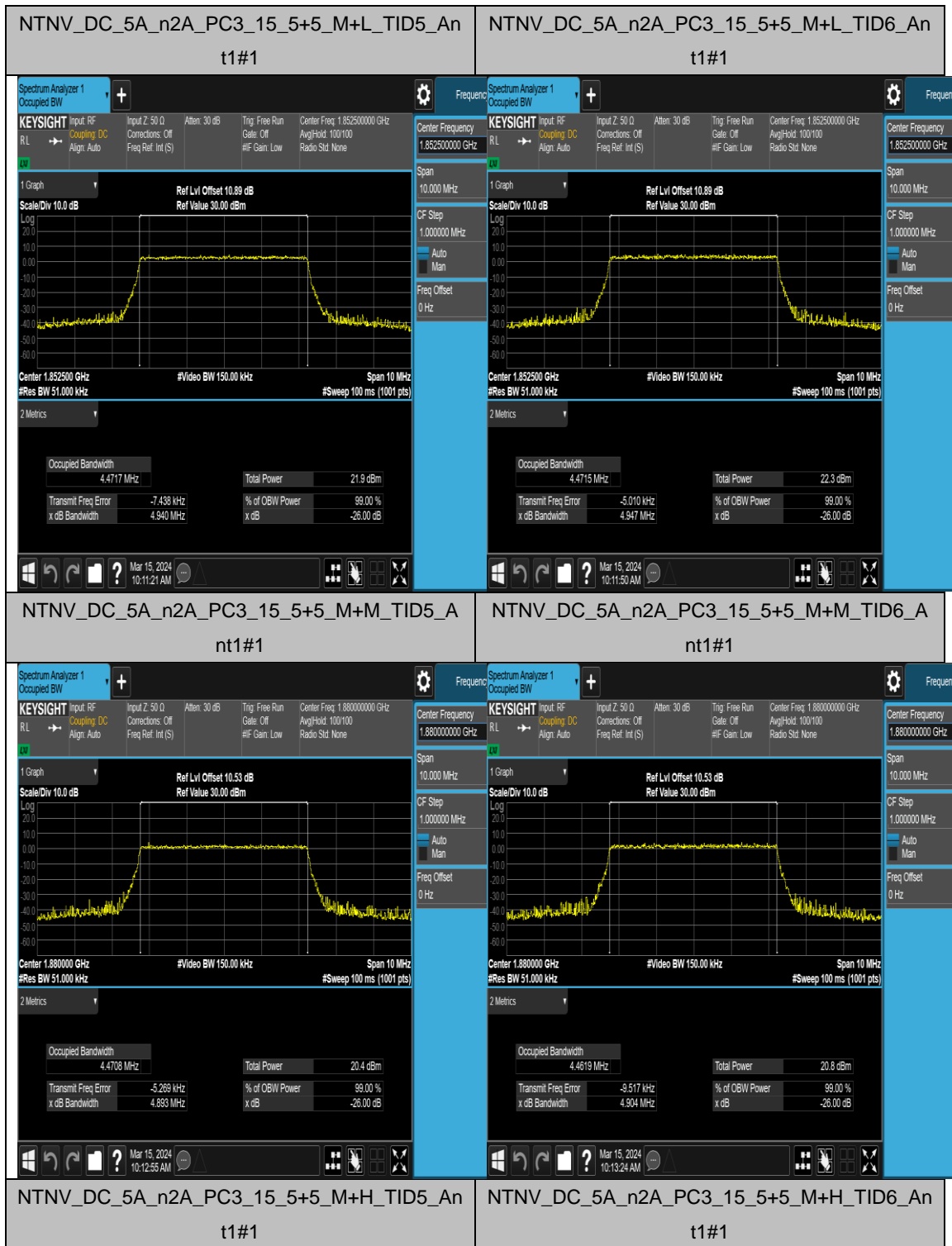


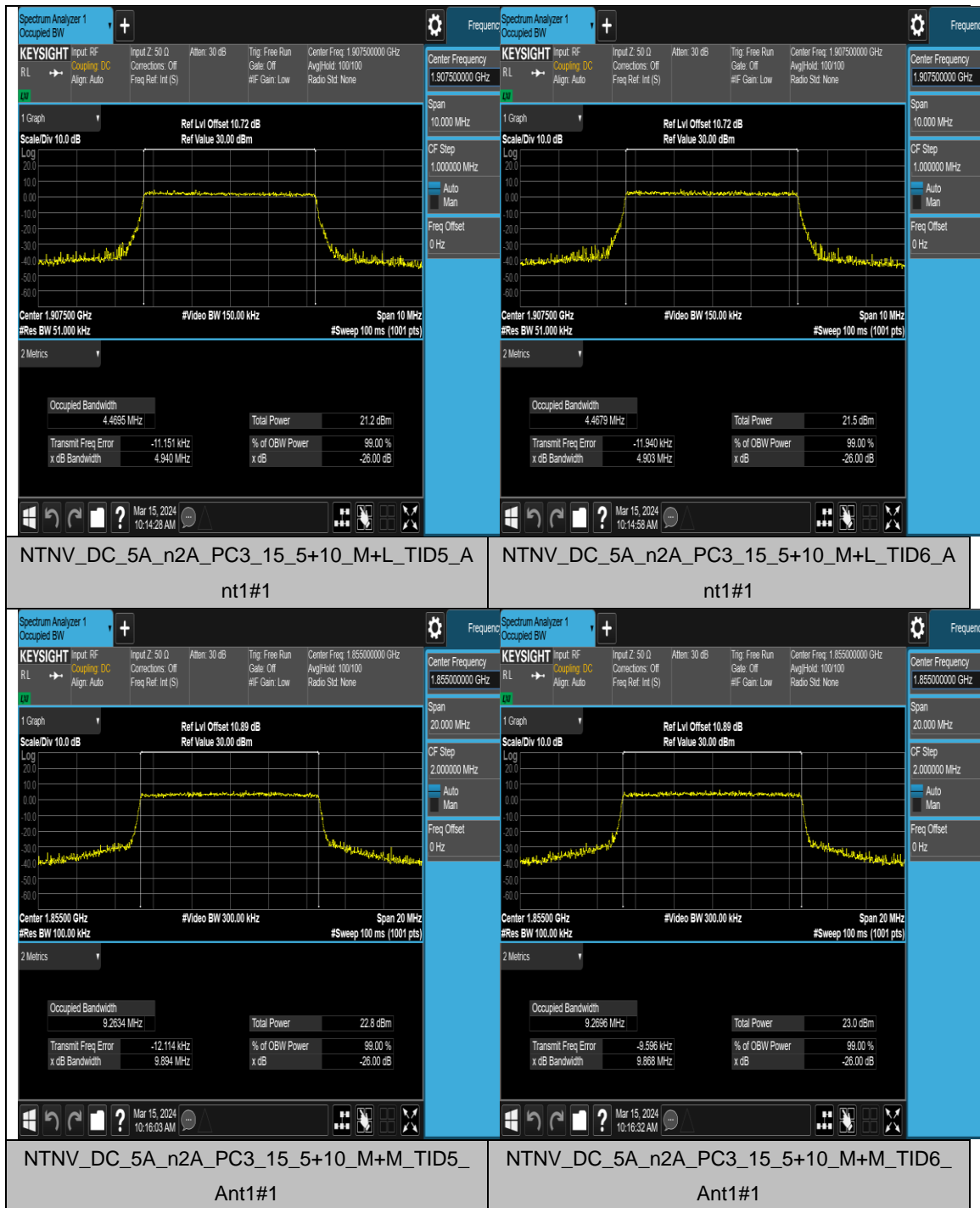


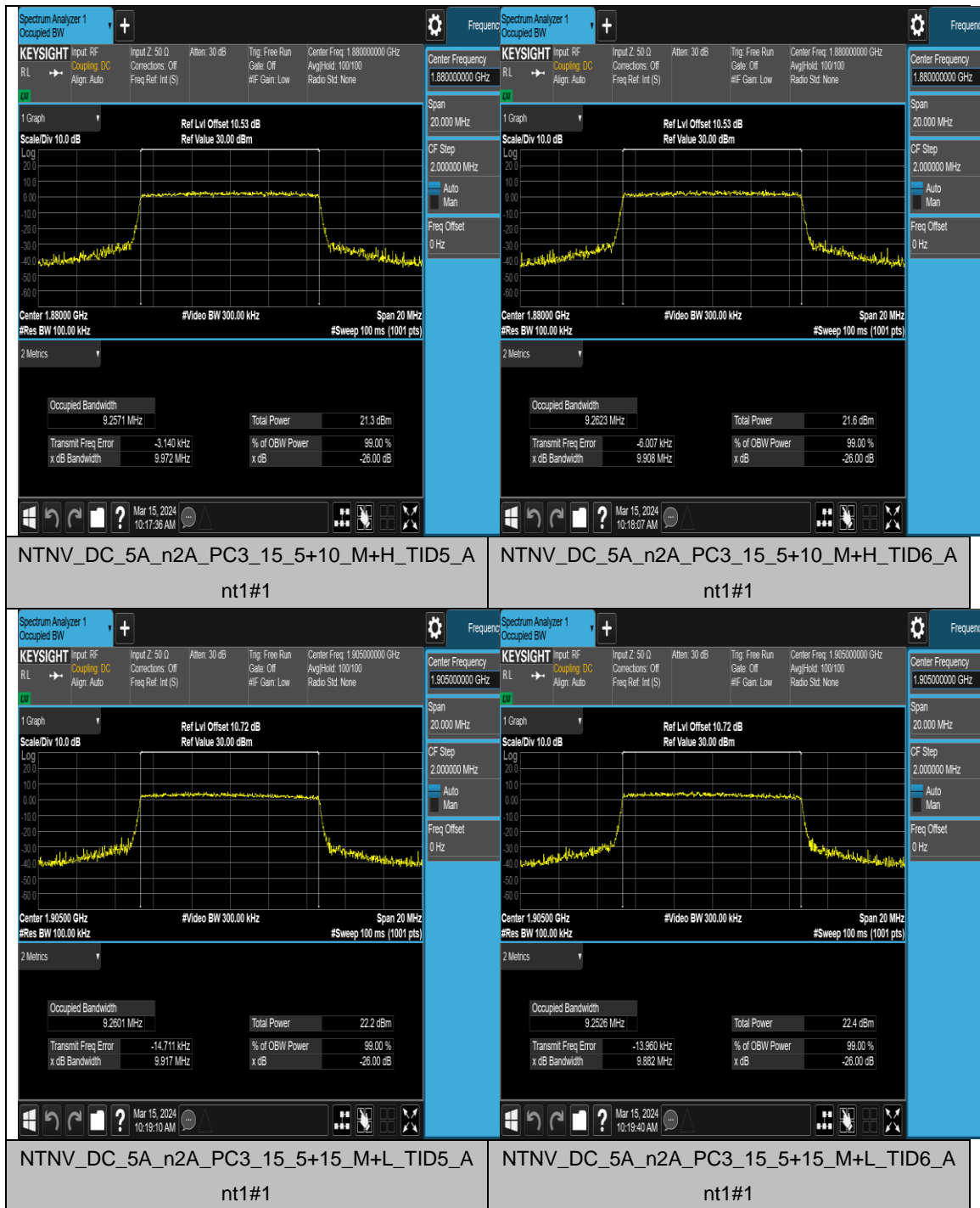
Appendix C: 26dB Bandwidth and Occupied Bandwidth for**NSA****Test Result**

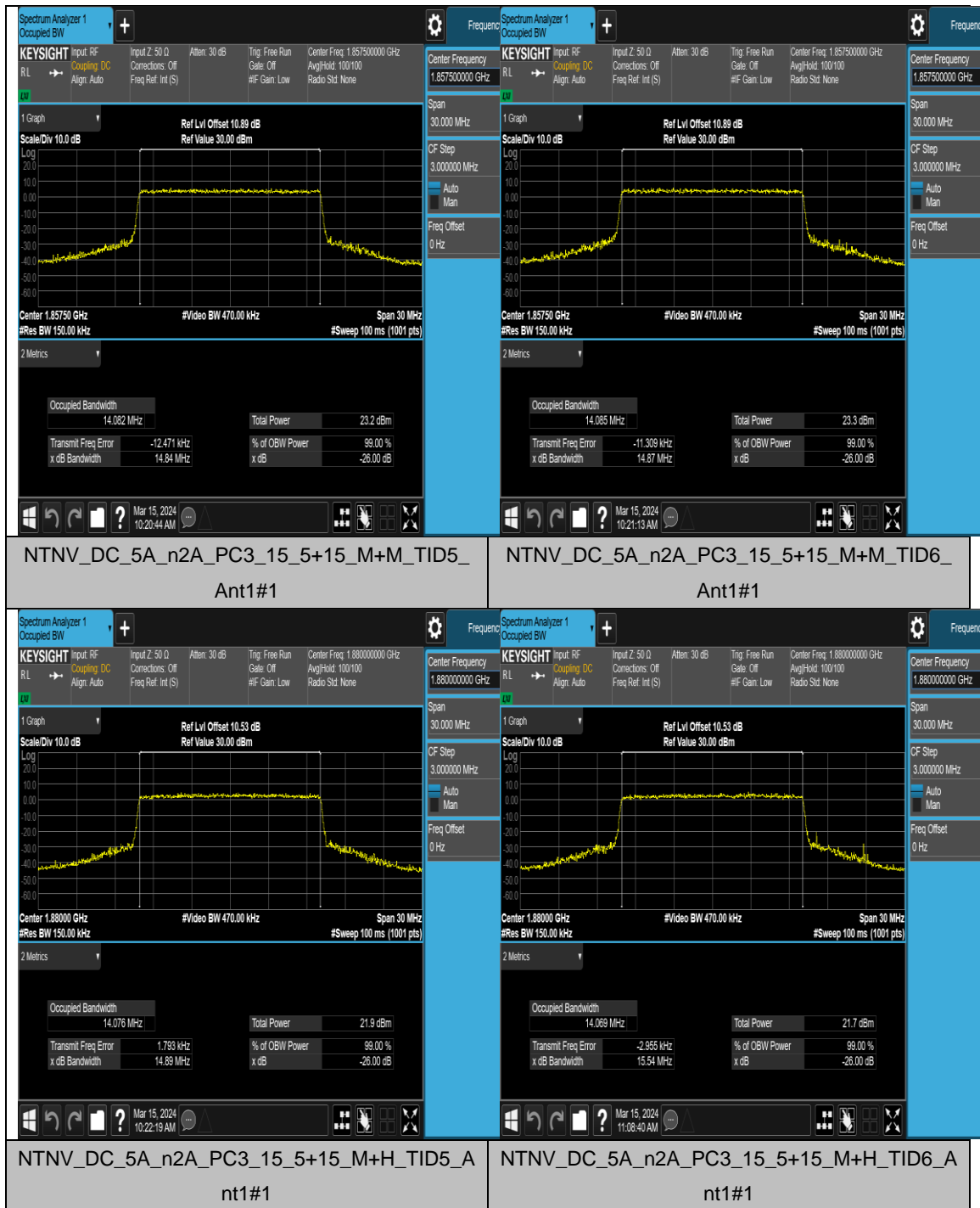
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4717	4.940	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4715	4.947	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4708	4.893	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4619	4.904	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4695	4.940	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4679	4.903	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2634	9.894	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2696	9.868	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2571	9.972	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2623	9.908	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2601	9.917	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2526	9.882	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	14.082	14.84	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	14.085	14.87	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	14.076	14.89	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	14.069	15.54	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	14.054	14.83	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	14.049	14.82	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	18.880	19.55	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	18.884	19.80	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	18.868	19.85	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	18.868	19.75	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	18.847	19.72	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	18.840	19.75	PASS

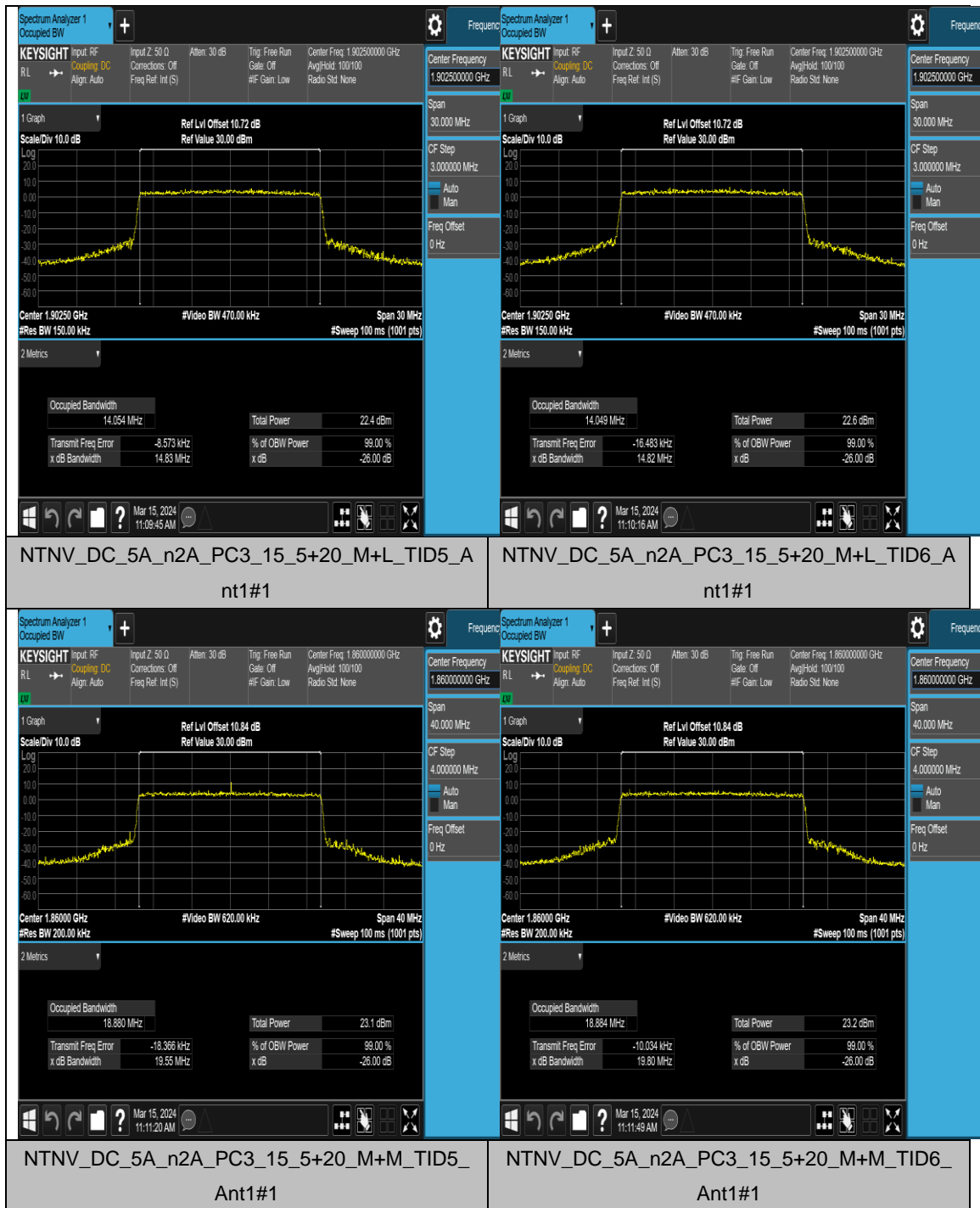
Test Graphs

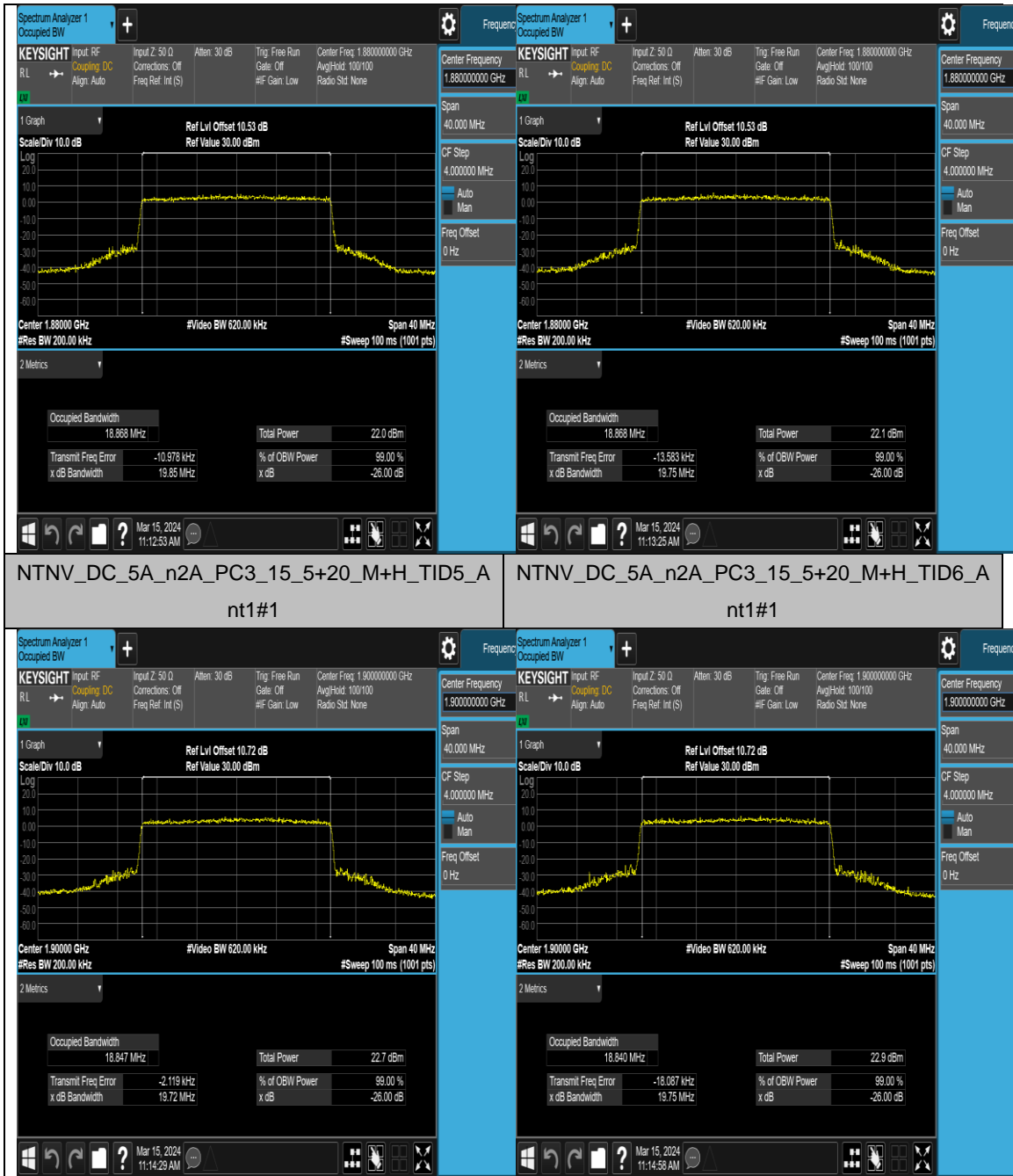








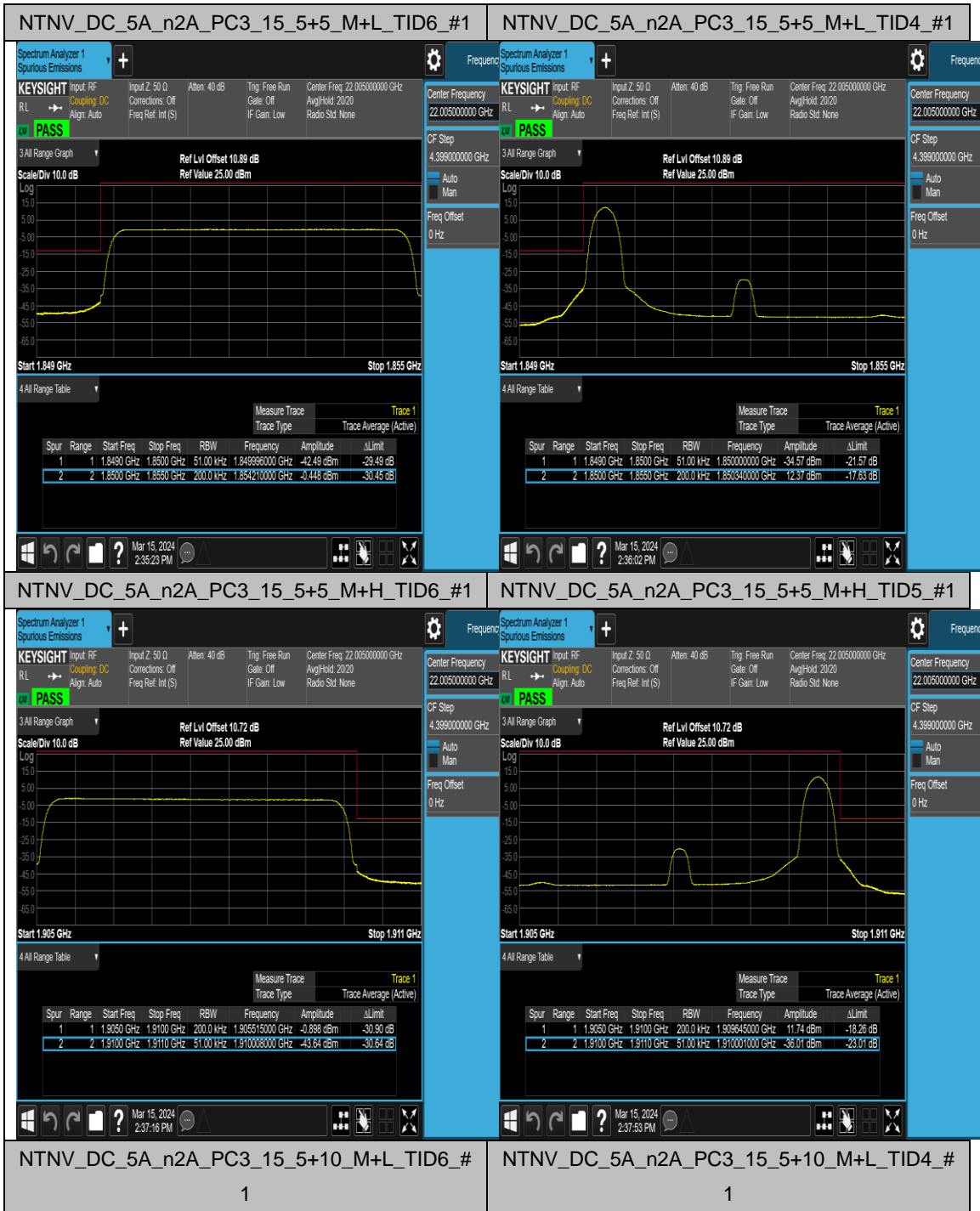


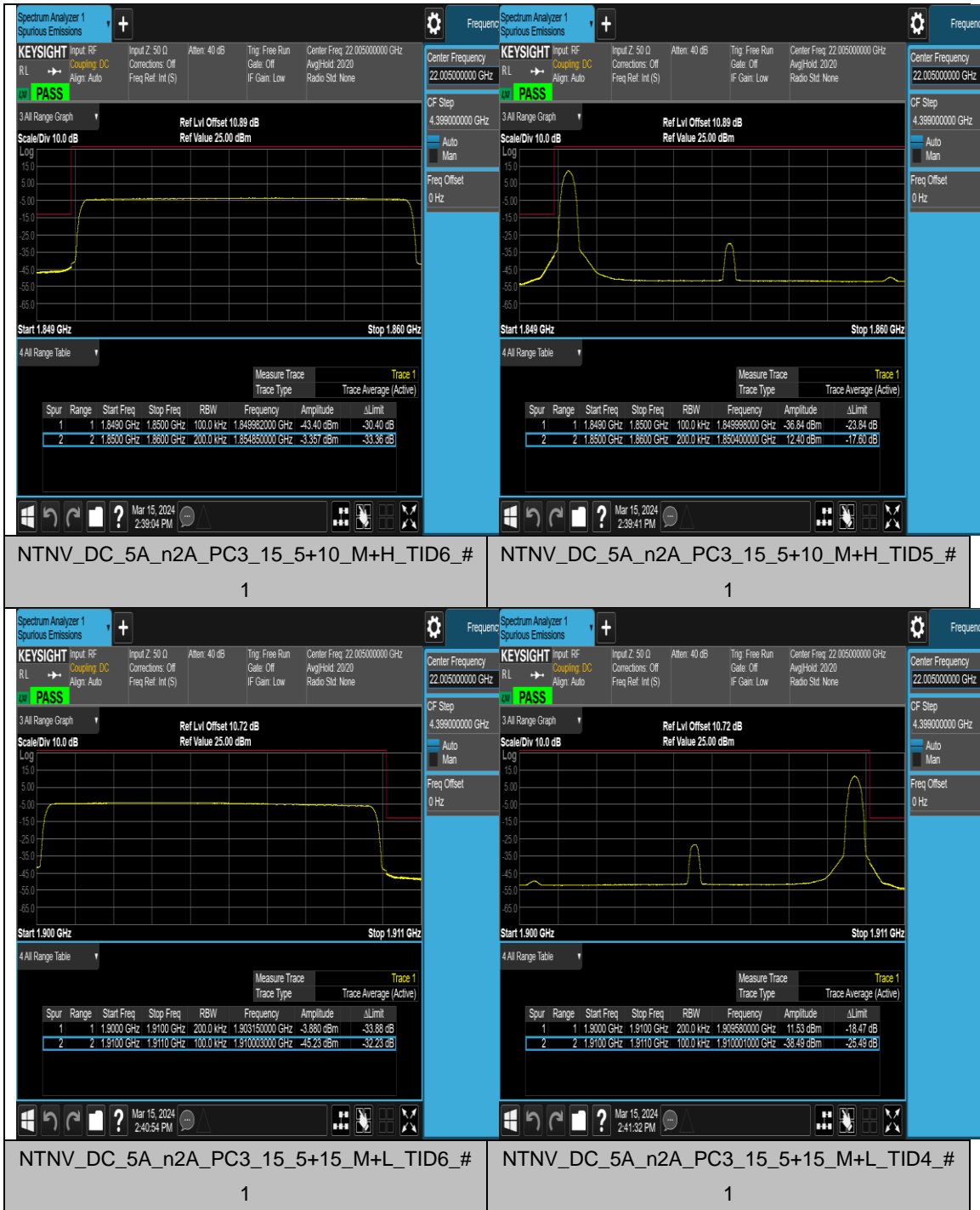


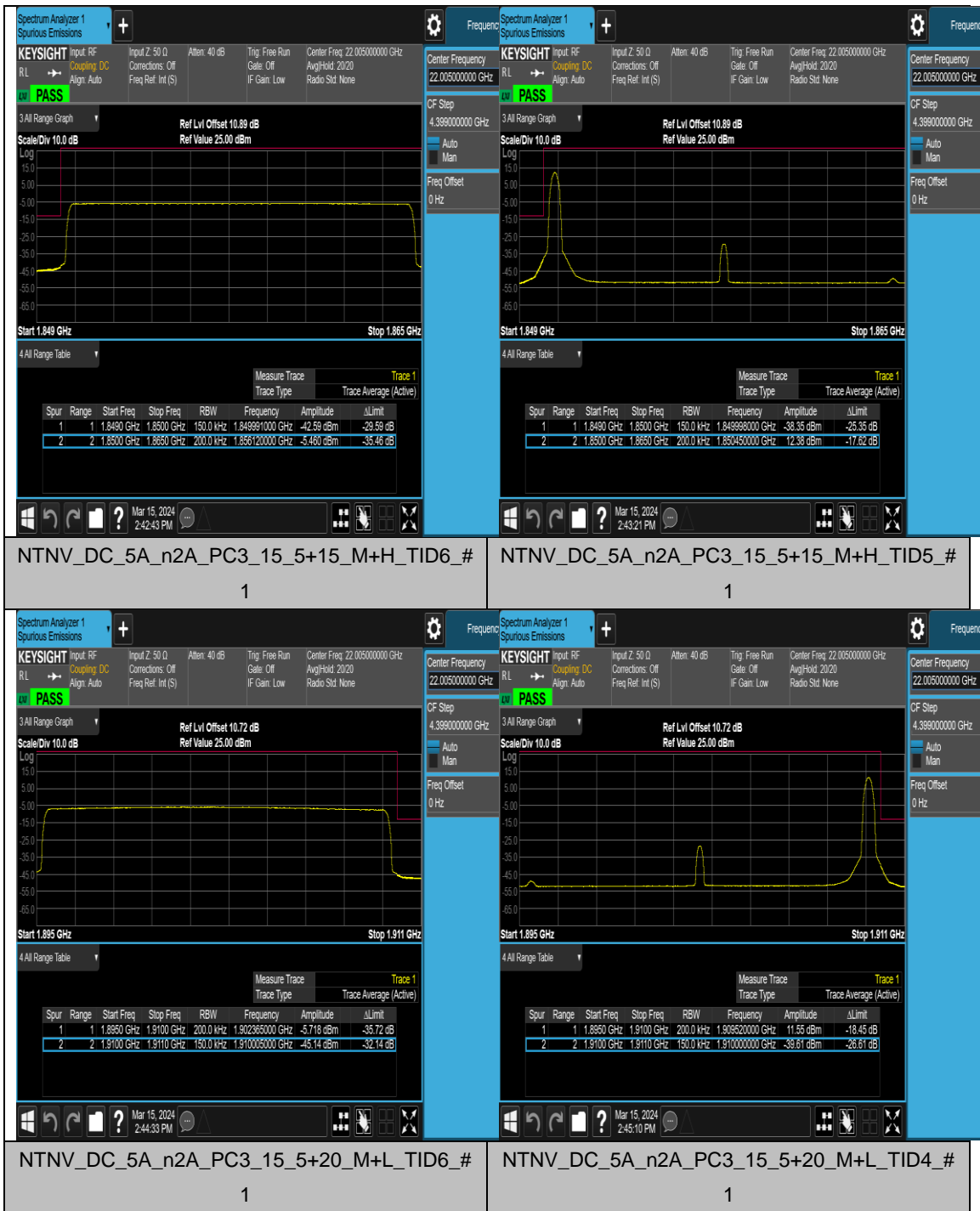
Appendix D: Band Edge for NSA**Test Result**

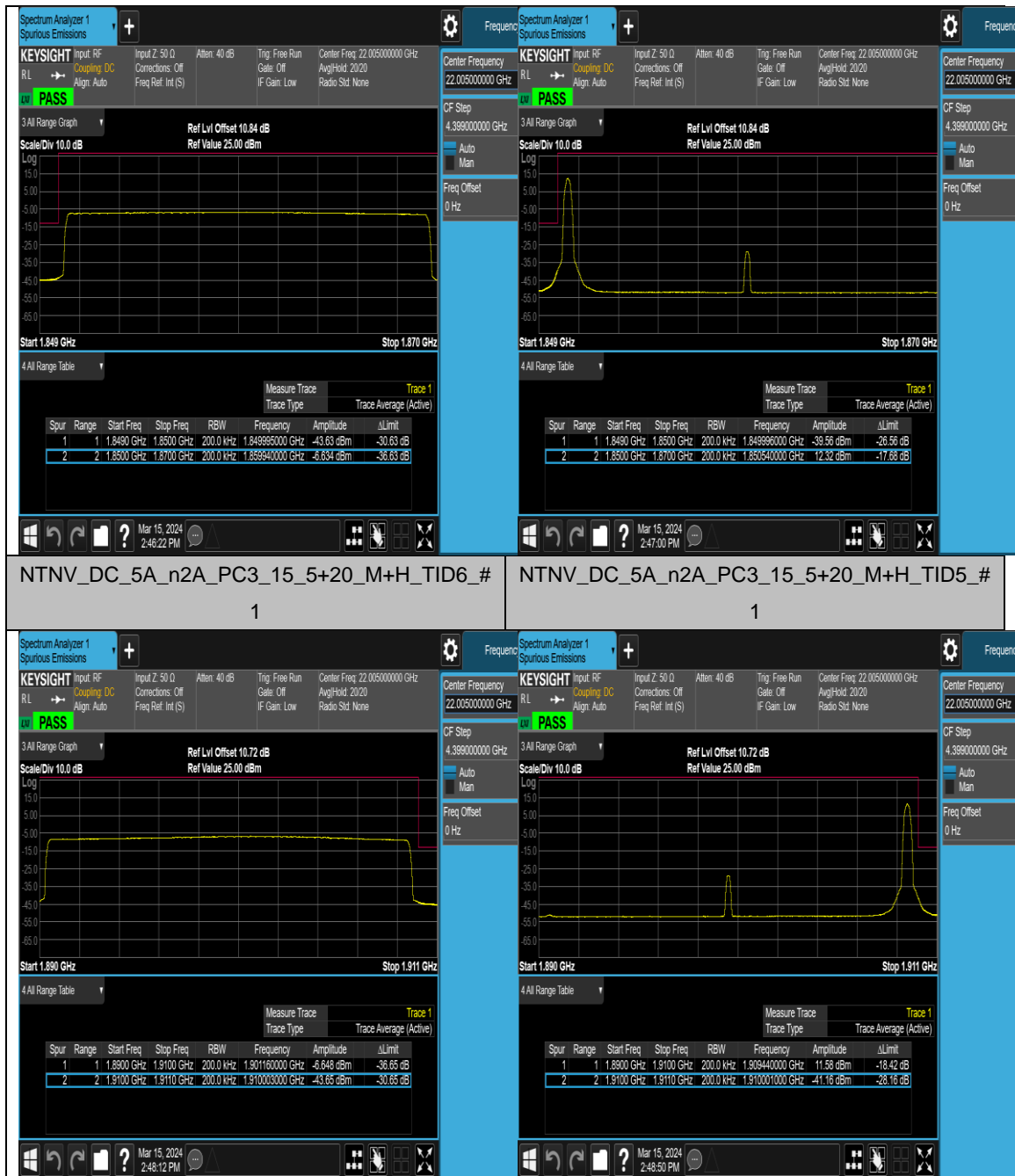
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-60.92	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-71.60	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-51.96	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-33.85	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-43.54	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-45.46	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.23	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-70.68	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-49.99	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.01	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.25	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.88	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.94	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.20	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.69	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.66	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.26	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.61	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-72.44	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-52.34	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-34.02	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-43.64	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-45.20	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-61.51	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.49	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-50.86	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-34.30	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.49	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.36	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.10	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-70.64	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.10	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.69	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.58	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.29	-13	PASS

DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.24	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-73.30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-52.46	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-37.65	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-43.66	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-45.30	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.12	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.13	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.38	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.61	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.45	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.27	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.89	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.63	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.22	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.68	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.32	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.21	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-71.84	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-51.75	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-37.70	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-43.60	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-45.32	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.18	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.51	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.26	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.49	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.37	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.95	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.62	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.02	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.57	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.66	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.19	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-61.31	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-72.99	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-50.54	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-34.83	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-43.44	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-45.11	-13	PASS

DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.11	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.27	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.88	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.57	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.17	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.03	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.19	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.44	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.59	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.36	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.26	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.72	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-71.93	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-52.48	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-35.02	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-43.67	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-45.39	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-63.18	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-71.67	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.61	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.64	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.52	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.13	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.29	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.18	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.38	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.58	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.41	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.67	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-72.46	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-52.41	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-32.76	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-43.44	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-45.38	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.02	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.39	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-50.26	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.64	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.52	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.32	-13	PASS

DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.09	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.53	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.20	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.53	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.22	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.25	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-72.46	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-52.12	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-32.99	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-45.32	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.07	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.11	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.27	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.52	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.54	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.40	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-63.26	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-73.51	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-51.83	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.70	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.44	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.39	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-63.04	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-72.84	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-52.10	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-37.54	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-45.35	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.44	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.79	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.50	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.55	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.30	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.80	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.19	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-50.91	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.67	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.64	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.32	-13	PASS

DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.49	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-71.65	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-52.37	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-37.59	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-43.56	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-45.38	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.32	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.18	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.37	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.69	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.46	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.98	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.67	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.11	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.49	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.55	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.45	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.12	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-72.92	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-52.22	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-32.61	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-45.38	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.66	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.58	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.12	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.59	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.38	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.17	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.81	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-50.86	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.61	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.61	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.33	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.19	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-72.01	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-52.61	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-34.96	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-45.24	-13	PASS

DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.31	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-71.41	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.57	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.57	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.60	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.31	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-63.05	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.28	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.52	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.54	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.62	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.33	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.14	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-71.73	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-51.52	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-34.07	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-43.61	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-45.09	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.87	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-70.48	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.40	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.51	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.97	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.63	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.02	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.65	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.65	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.24	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.74	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-72.20	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-51.62	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-33.75	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-45.26	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.11	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-71.36	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-51.26	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.53	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.43	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.33	-13	PASS

DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-61.51	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.29	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.24	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.53	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.64	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.29	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.86	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-72.39	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-52.57	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-37.73	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-43.58	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-45.35	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-61.99	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.63	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.21	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.37	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.03	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.10	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.13	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.67	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.58	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.20	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-61.81	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-73.54	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-52.31	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-37.64	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-43.65	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-45.25	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-60.97	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.79	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-50.63	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.55	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.43	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.28	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.93	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.00	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.69	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.19	-13	PASS

DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.13	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-73.97	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-52.07	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-34.11	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-43.61	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-45.34	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.50	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.73	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.13	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.49	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.74	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.20	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-60.97	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.76	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.31	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.67	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.60	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.32	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.37	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-73.17	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-51.59	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-33.36	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-43.67	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-45.19	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.92	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-71.22	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.22	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.63	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.41	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.02	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.91	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.28	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.56	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.33	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.75	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-73.31	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-51.98	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-32.67	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-43.58	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-45.22	-13	PASS

DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.34	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.15	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.97	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.55	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.67	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.31	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.32	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.59	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.16	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.63	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.67	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.34	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.88	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-72.92	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-51.34	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-31.88	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-43.60	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-45.34	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.41	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.02	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.35	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.48	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.52	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.35	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.29	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.24	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.40	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.66	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.66	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.39	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.10	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-73.32	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-52.30	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-37.67	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.42	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-45.30	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.06	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.11	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.15	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.68	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.65	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.31	-13	PASS

DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.78	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.68	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.96	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.61	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.65	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.29	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.39	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-71.68	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-52.18	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-37.62	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-43.54	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-45.31	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.39	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.33	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.55	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.68	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.58	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.35	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.31	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.68	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.11	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.57	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.64	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.39	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.26	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-73.02	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-52.07	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-33.33	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-43.60	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-45.24	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.40	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.89	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.28	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.48	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.33	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.47	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.53	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.20	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.66	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.51	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.14	-13	PASS

DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.16	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-71.92	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-52.23	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-33.32	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-43.55	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-45.28	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-60.74	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.14	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-51.45	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.56	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.59	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.29	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-61.96	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.73	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.12	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.54	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.61	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.35	-13	PASS