Appendix for 2A_n71A

Product Name: 5G CPE
Model No: S3

Appendix A: Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	22.07	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Inner_Full	23.04	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	22.01	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	21.46	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	22.02	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Inner_Full	22.47	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	22.07	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.30	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	22.20	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Inner_Full	23.63	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	22.14	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	22.13	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Inner_Full	23.05	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.80	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	22.02	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	22.09	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Inner_Full	23.62	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	22.01	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	22.13	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	22.07	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Inner_Full	23.11	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	22.65	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	22.25	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	21.63	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Inner_Full	22.34	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.88	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.73	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	21.55	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Inner_Full	21.89	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	22.36	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.58	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	22.17	PC3	PASS

DC_2A_n71A	15	5+10	CP-QPSK	M+M	Inner_Full	23.69	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	22.25	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	22.12	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Inner_Full	23.07	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.66	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	22.09	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	22.17	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Inner_Full	23.73	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	22.17	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.99	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	22.29	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Inner_Full	23.14	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	22.17	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	22.20	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	21.96	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Inner_Full	22.49	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	22.09	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	22.01	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Inner_Full	22.00	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.98	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	22.27	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Inner_Full	23.68	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.85	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.98	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	22.21	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Inner_Full	23.09	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.82	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	22.02	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	22.23	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Inner_Full	23.45	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.84	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	22.29	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Inner_Full	23.00	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	22.11	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	22.02	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Inner_Full	23.42	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.93	PC3	PASS

DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.93	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	22.07	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Inner_Full	22.83	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.87	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	22.19	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	22.14	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Inner_Full	23.60	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	22.07	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	22.23	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Inner_Full	23.23	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.89	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	22.03	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	22.19	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Inner_Full	23.34	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.89	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	22.22	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Inner_Full	22.86	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.79	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.89	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

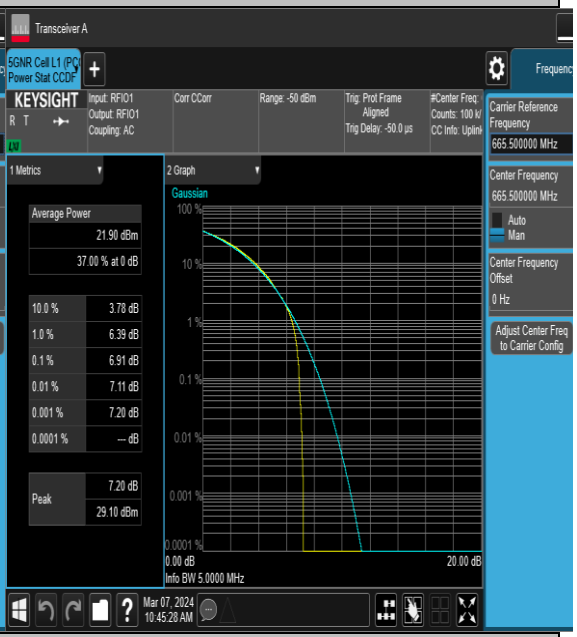
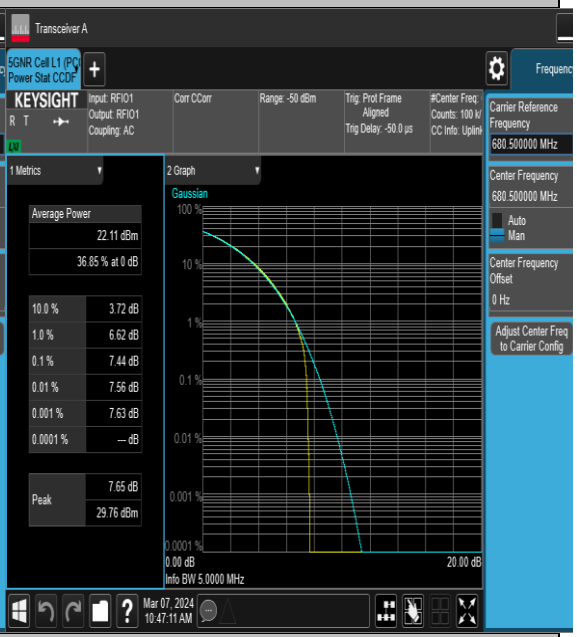
Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	6.77	≤13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	6.91	≤13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	7.36	≤13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	7.44	≤13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	7.27	≤13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	7.36	≤13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	7.21	≤13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	7.22	≤13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	7.92	≤13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	7.82	≤13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	7.45	≤13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	7.39	≤13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	7.18	≤13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	7.18	≤13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	7.37	≤13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	7.31	≤13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	7.64	≤13	PASS

DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	7.65	≤13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	7.44	≤13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	7.44	≤13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	7.52	≤13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	7.52	≤13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	7.67	≤13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	7.61	≤13	PASS

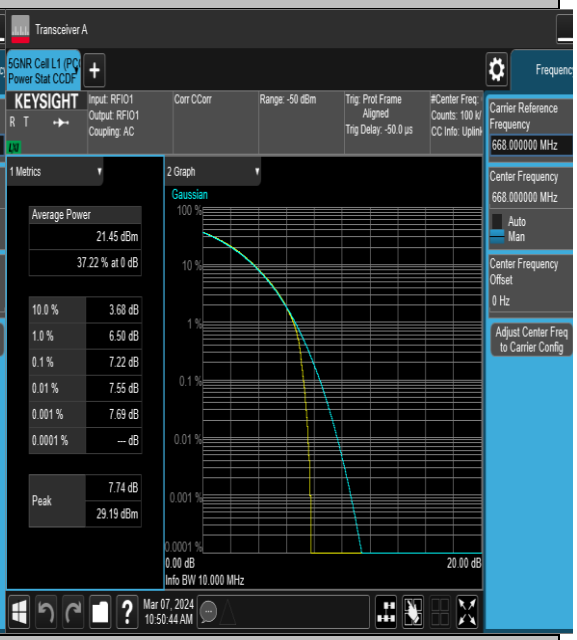
Test Graphs

NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID7_#1
	
NTNV_DC_2A_n71A_PC3_15_5+5_M+M_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+M_TID7_#1
	
NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID7_#1

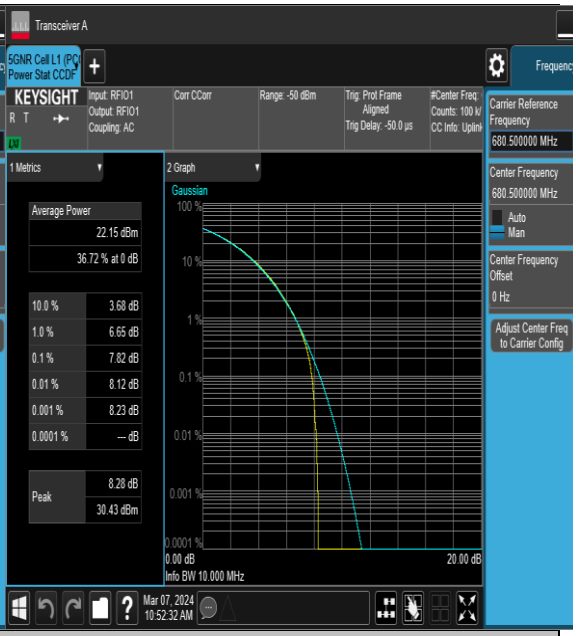
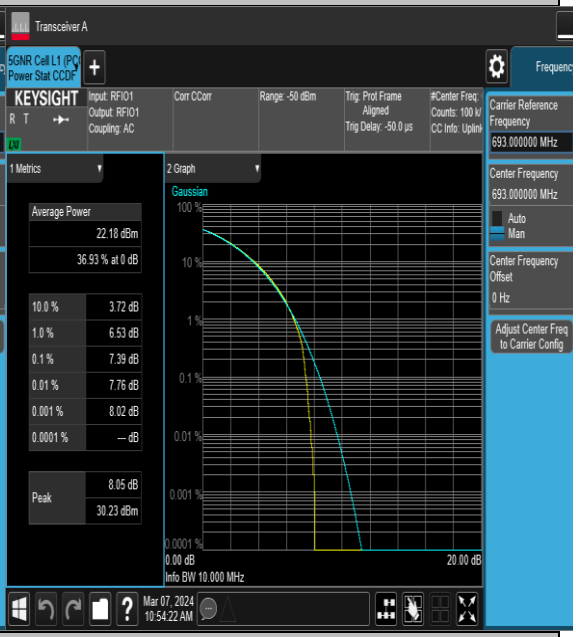
NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID6_#1

NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID7_#1

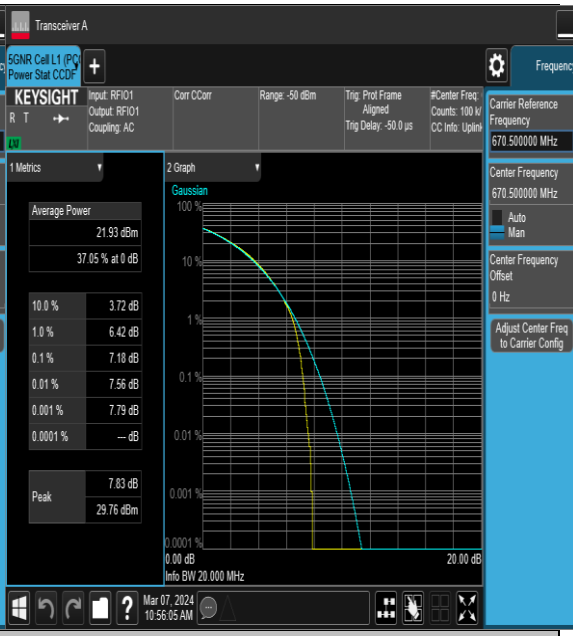
NTNV_DC_2A_n71A_PC3_15_5+10_M+M_TID6_#1



NTNV_DC_2A_n71A_PC3_15_5+10_M+M_TID7_#1

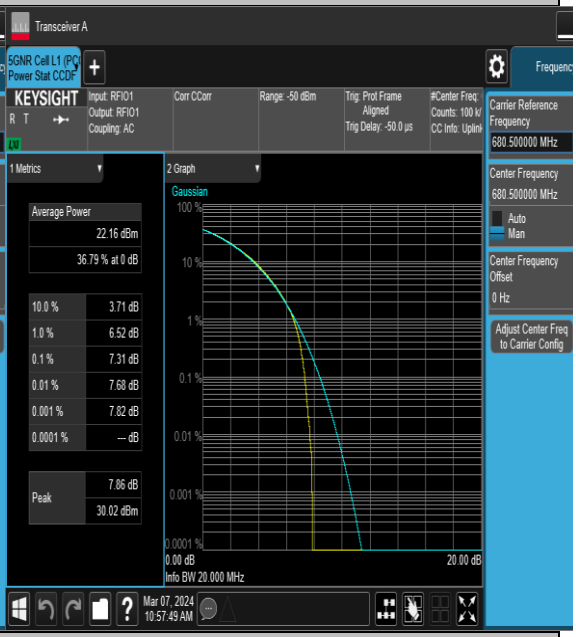
	
NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID7_#1
	
NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID7_#1

NTNV_DC_2A_n71A_PC3_15_5+15_M+M_TID6_#1



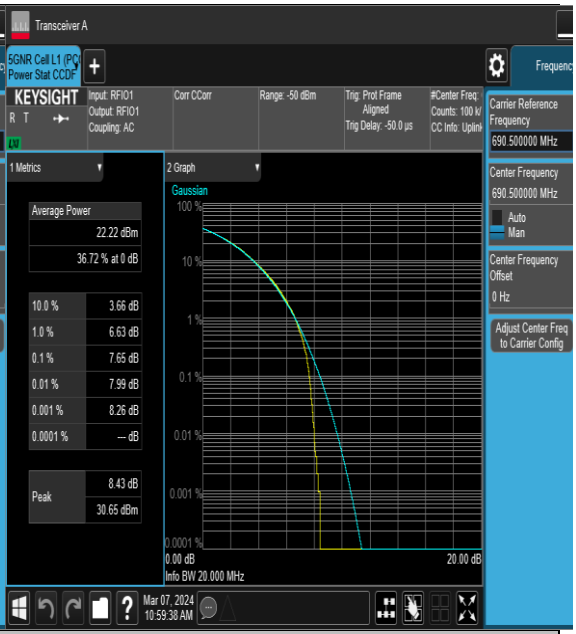
NTNV_DC_2A_n71A_PC3_15_5+15_M+M_TID7_#1

NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID6_#1



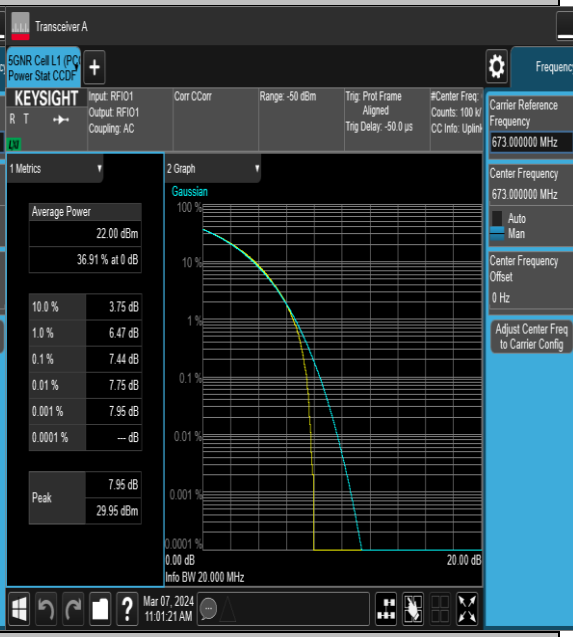
NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID7_#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID6_#1



NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID7_#1

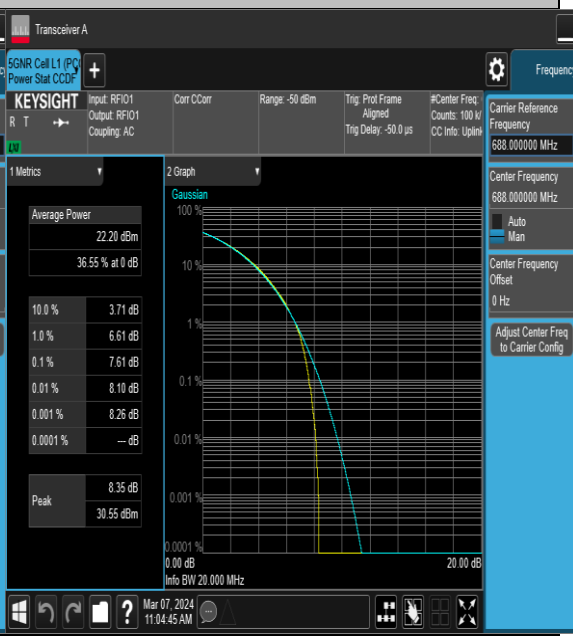
NTNV_DC_2A_n71A_PC3_15_5+20_M+M_TID6_#1



NTNV_DC_2A_n71A_PC3_15_5+20_M+M_TID7_#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID6_#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID7_#1

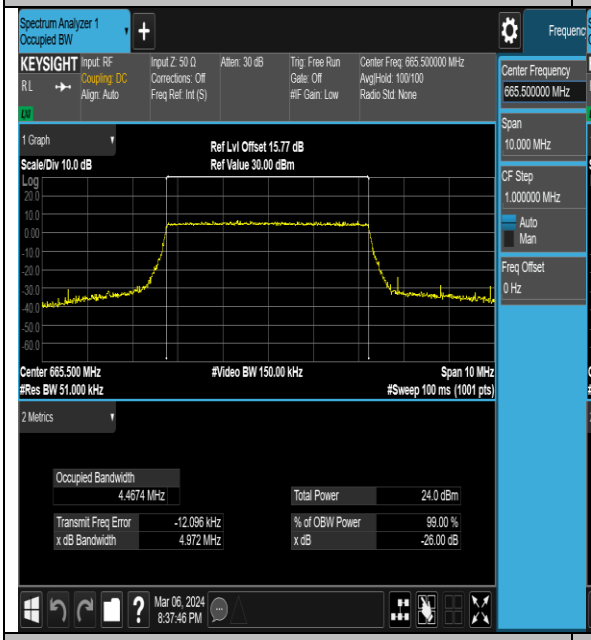
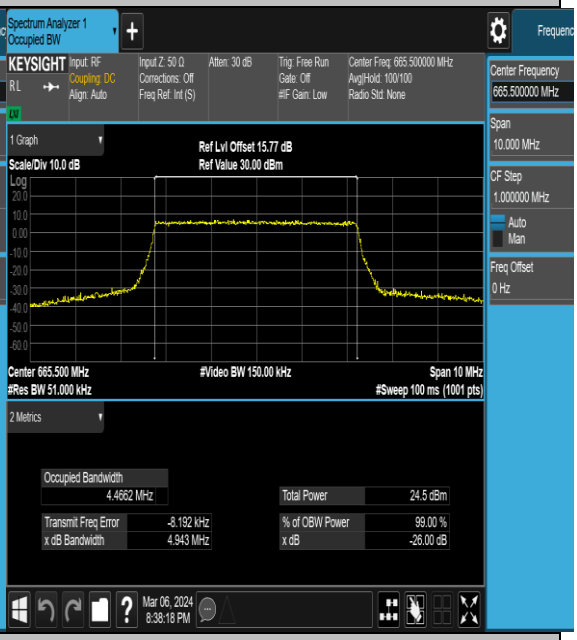
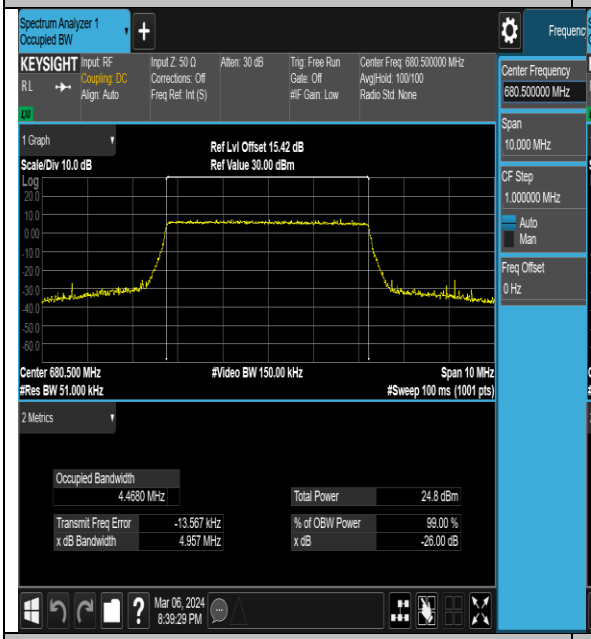
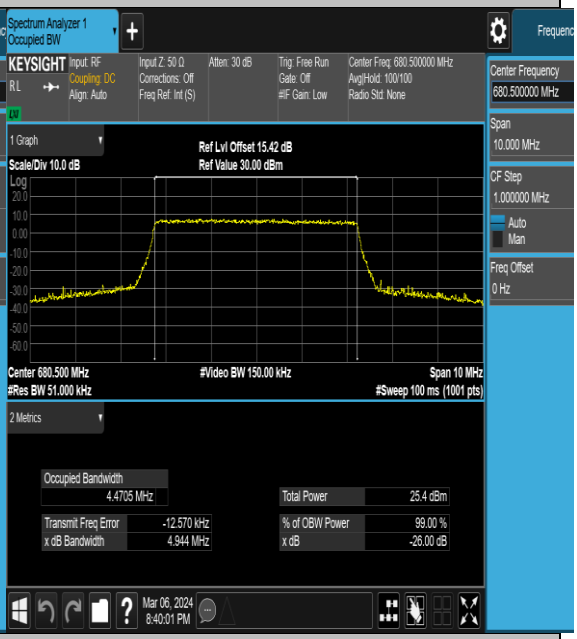


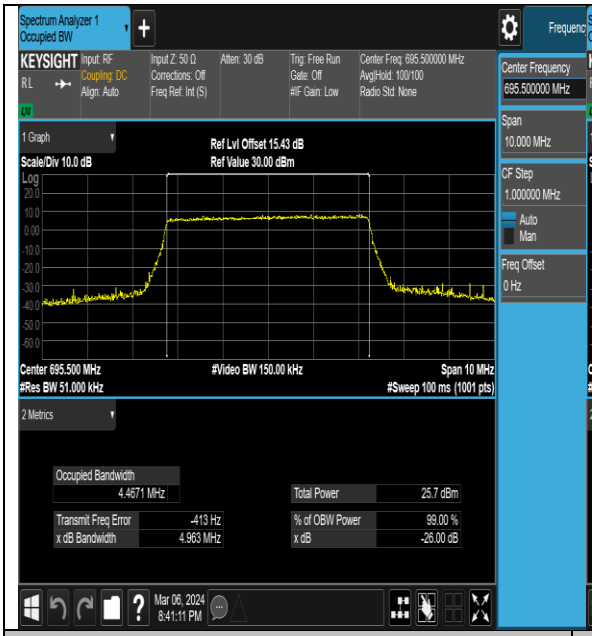
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

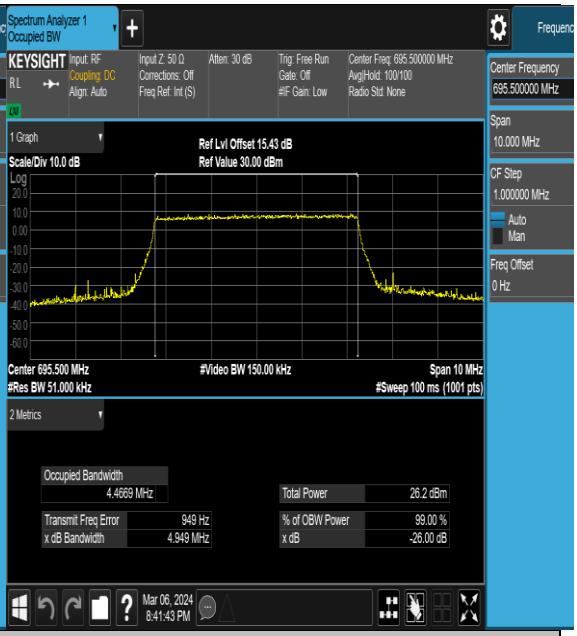
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4674	4.972	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4662	4.943	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4680	4.957	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4705	4.944	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4671	4.963	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4669	4.949	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2833	9.982	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2987	9.972	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2530	9.917	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2587	9.891	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2623	9.929	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2646	9.915	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	14.122	14.86	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	14.117	14.88	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	14.040	14.87	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	14.043	14.92	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	14.132	14.90	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	14.139	14.86	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	18.871	19.80	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	18.893	19.74	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	18.799	19.73	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	18.806	19.74	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	18.955	22.06	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	18.971	19.82	PASS

Test Graphs

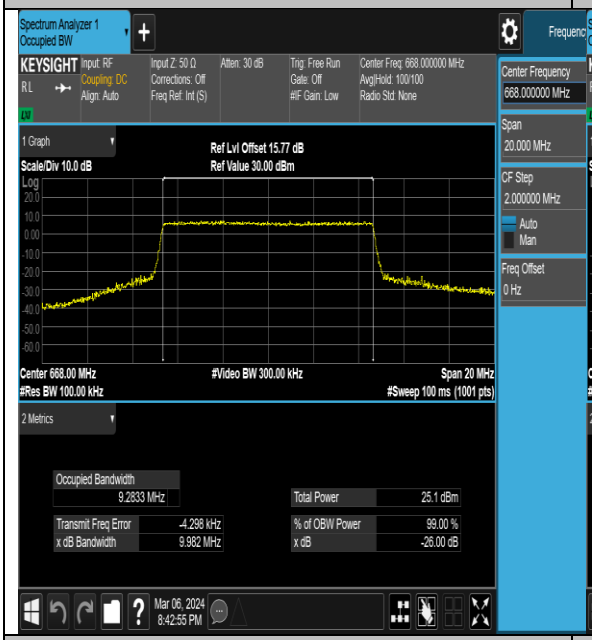
NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID5_Ant1#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID6_Ant1#1
	
NTNV_DC_2A_n71A_PC3_15_5+5_M+M_TID5_Ant1#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+M_TID6_Ant1#1
	
NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID5_Ant1#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID6_Ant1#1



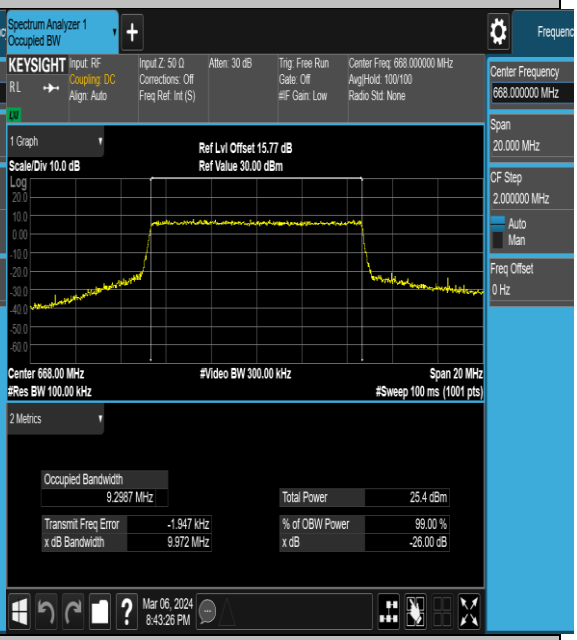
NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID5_Ant1#1



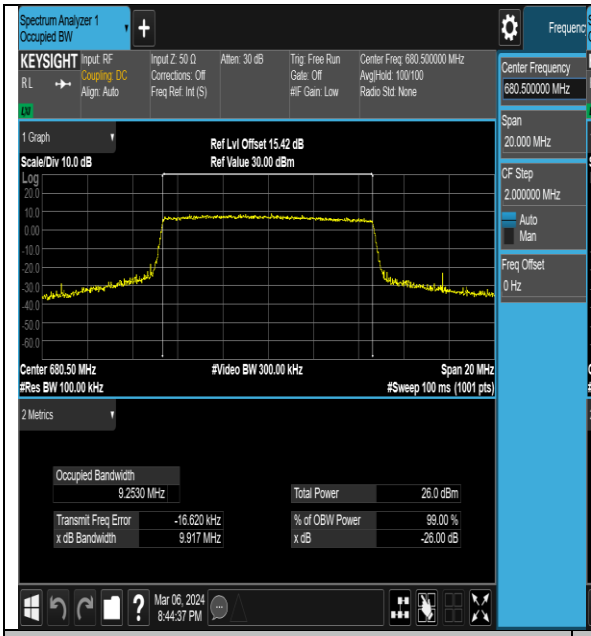
NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID6_Ant1#1



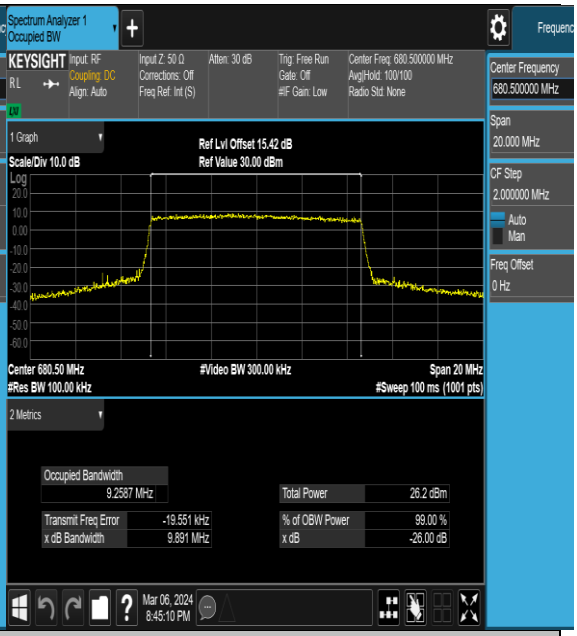
NTNV_DC_2A_n71A_PC3_15_5+10_M+M_TID5_Ant1#1



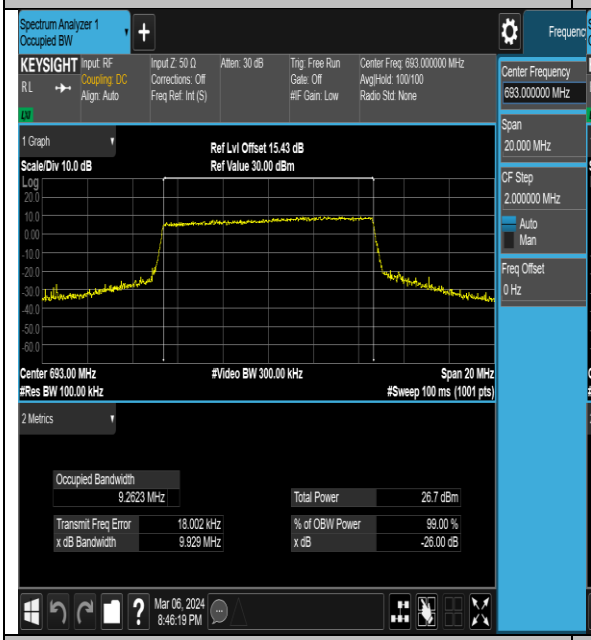
NTNV_DC_2A_n71A_PC3_15_5+10_M+M_TID6_Ant1#1



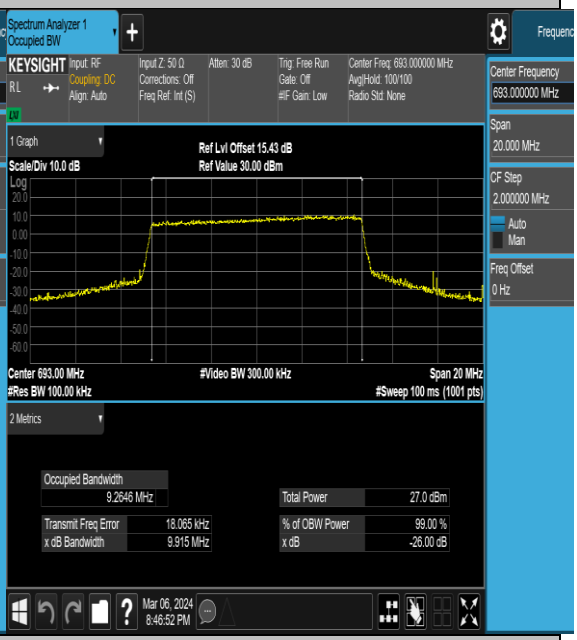
NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID5_Ant1#1



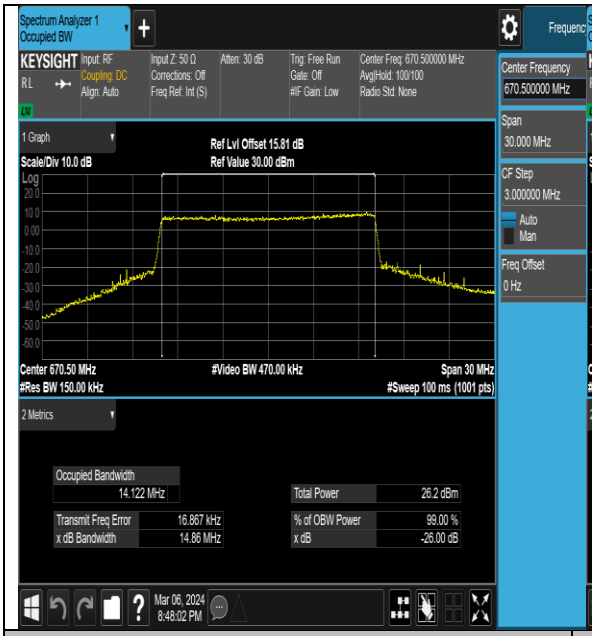
NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID6_Ant1#1



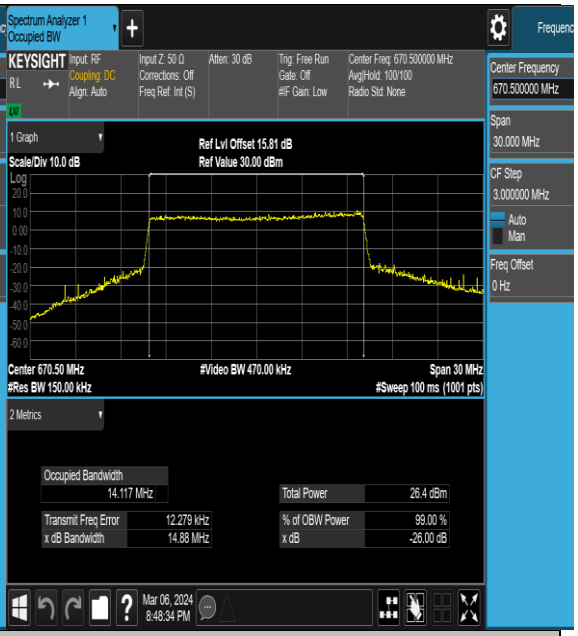
NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID5_Ant1#1



NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID6_Ant1#1

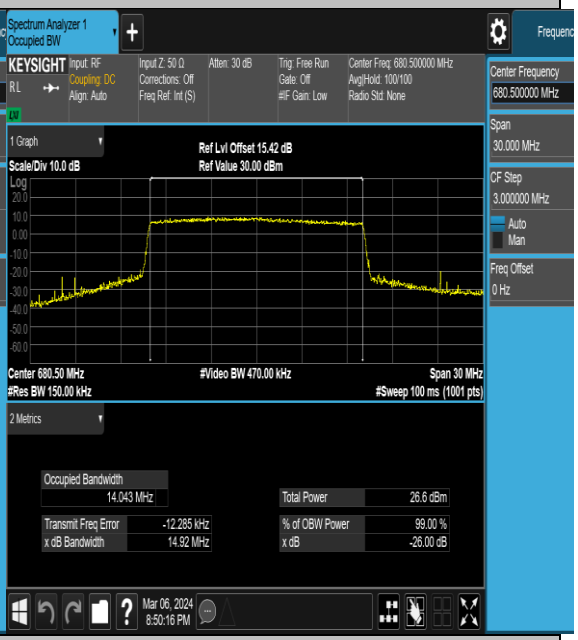


NTNV_DC_2A_n71A_PC3_15_5+15_M+M_TID5_Ant1#1



NTNV_DC_2A_n71A_PC3_15_5+15_M+M_TID6_Ant1#1

NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID5_Ant1#1

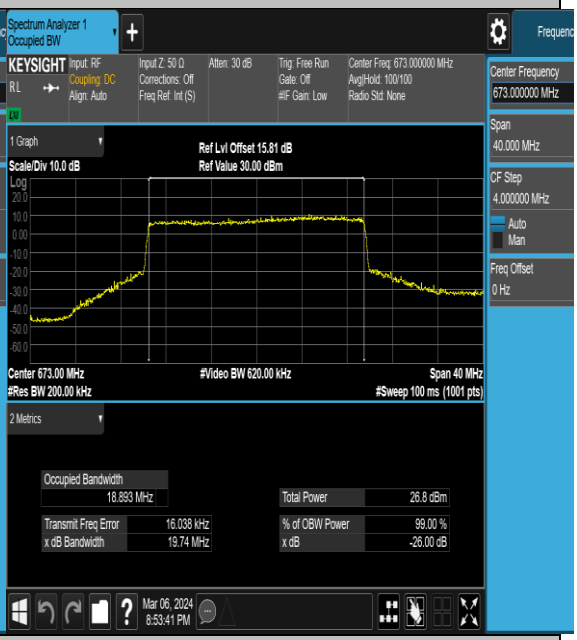


NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID6_Ant1#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID5_Ant1#1

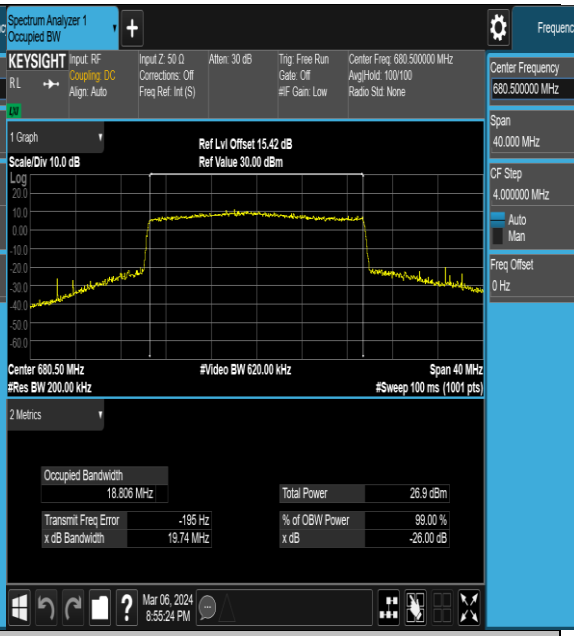
NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID6_Ant1#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+M_TID5_Ant1#1

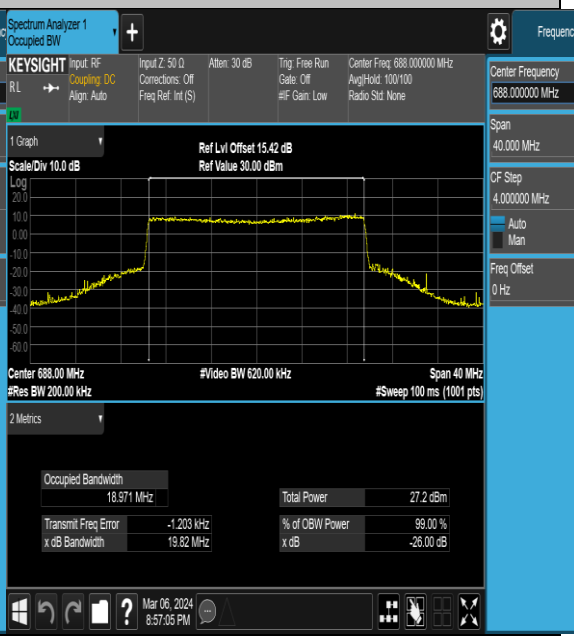


NTNV_DC_2A_n71A_PC3_15_5+20_M+M_TID6_Ant1#1

NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID5_Ant1#1



NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID6_Ant1#1

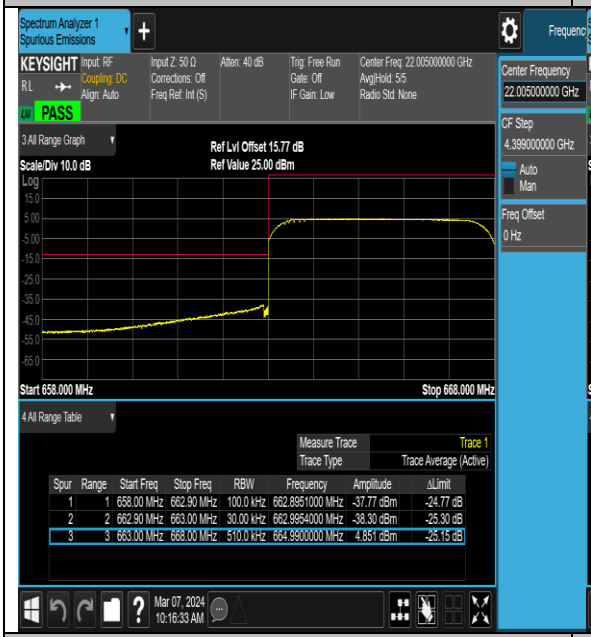
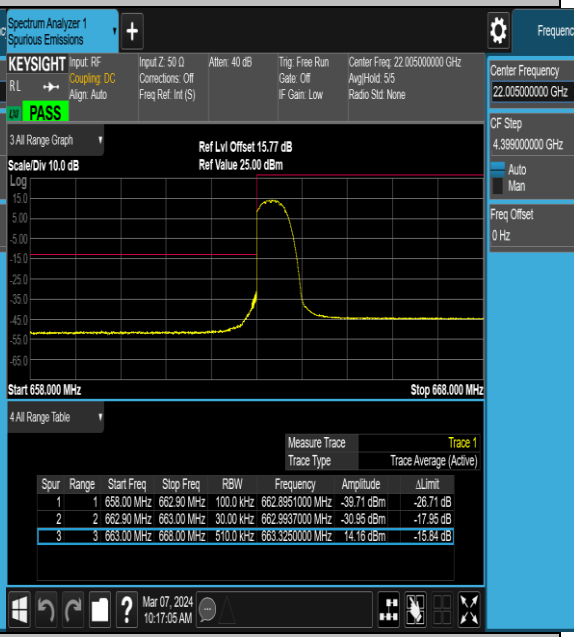
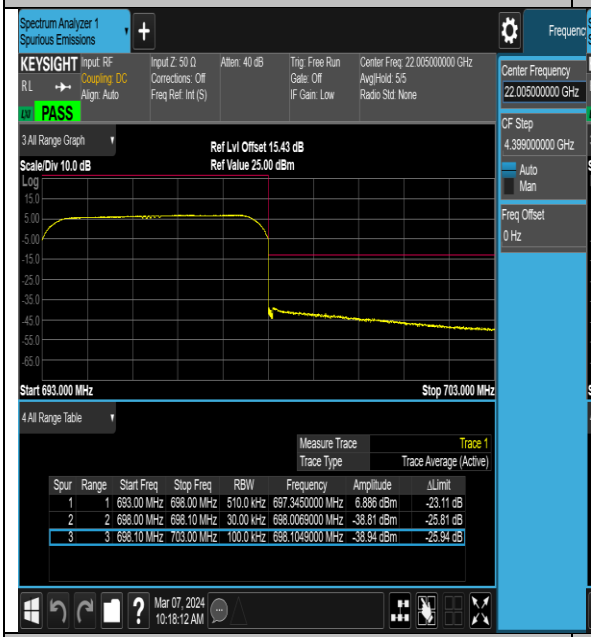
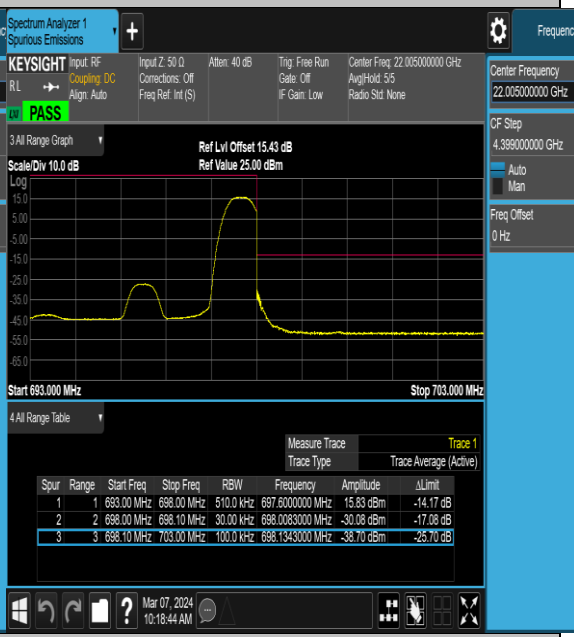


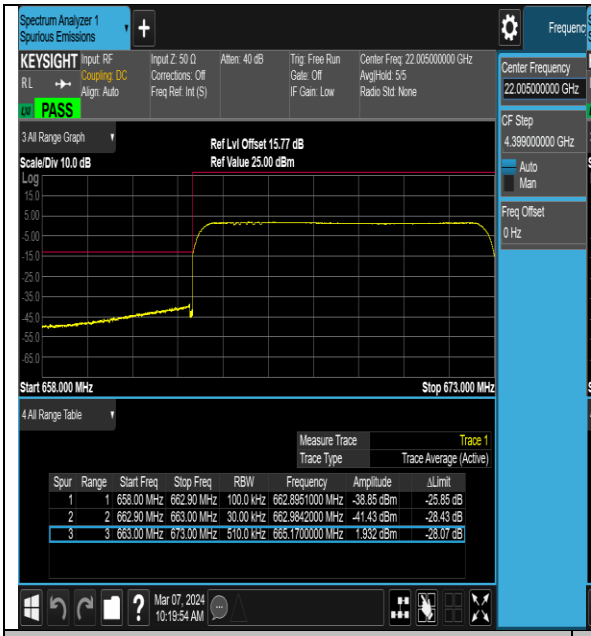
Appendix D: Band Edge for NSA

Test Result

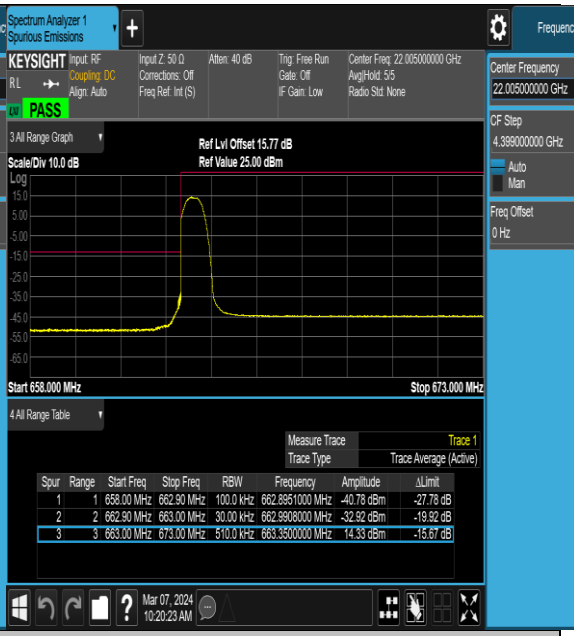
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

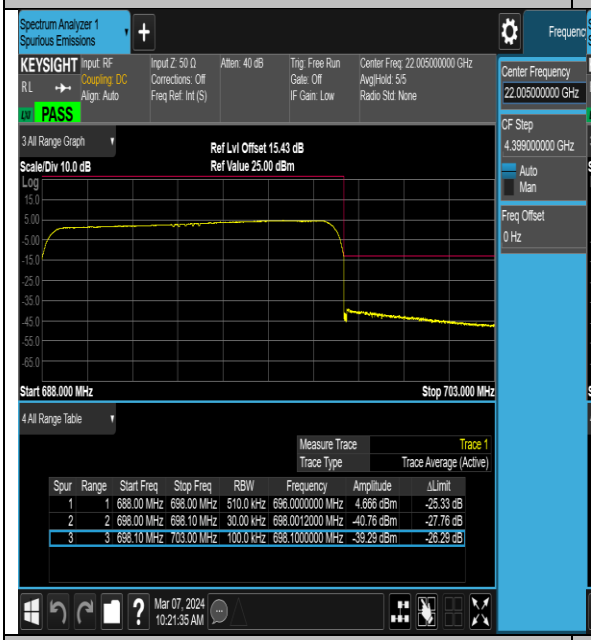
NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+L_TID4_#1
	
NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+5_M+H_TID5_#1
	
NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID6_#1	NTNV_DC_2A_n71A_PC3_15_5+10_M+L_TID4_#1



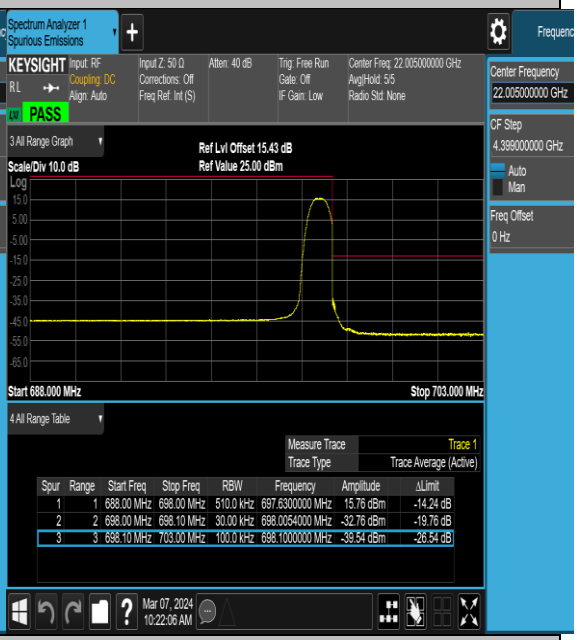
NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID6_#1



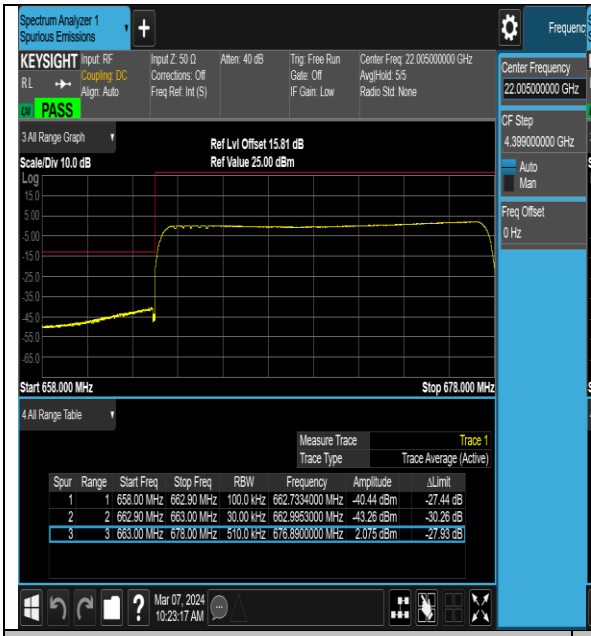
NTNV_DC_2A_n71A_PC3_15_5+10_M+H_TID5_#1



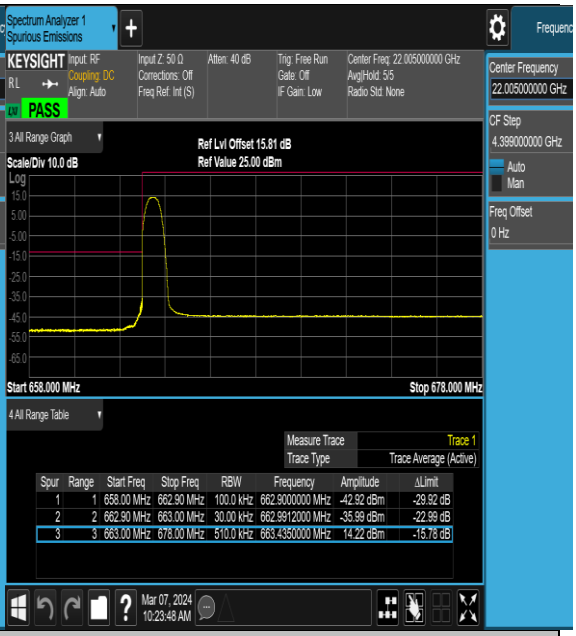
NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID6_#1



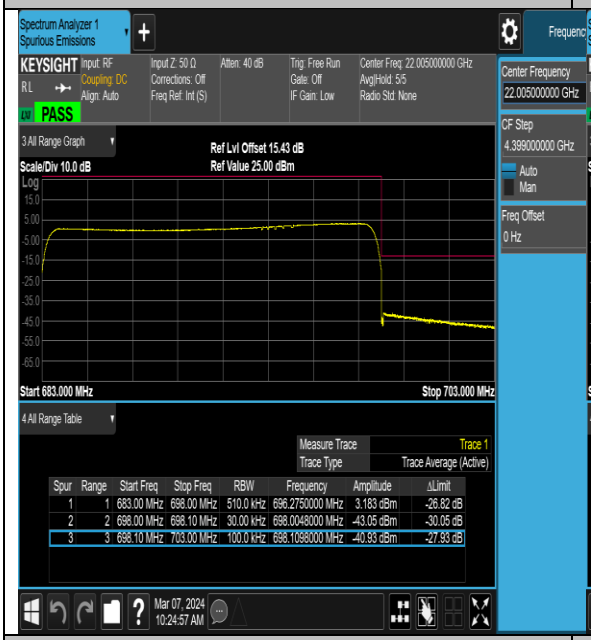
NTNV_DC_2A_n71A_PC3_15_5+15_M+L_TID4_#1



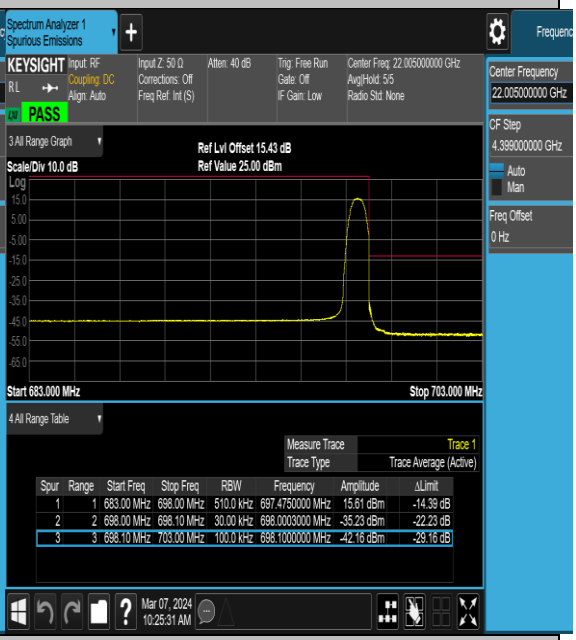
NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID6_#1



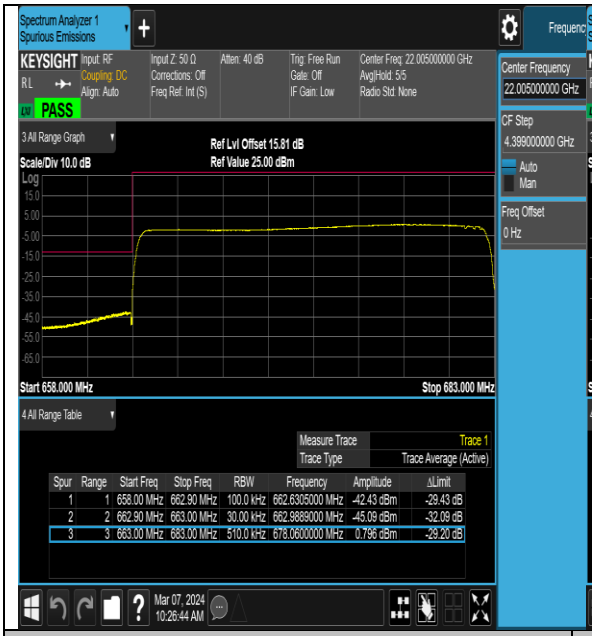
NTNV_DC_2A_n71A_PC3_15_5+15_M+H_TID5_#1



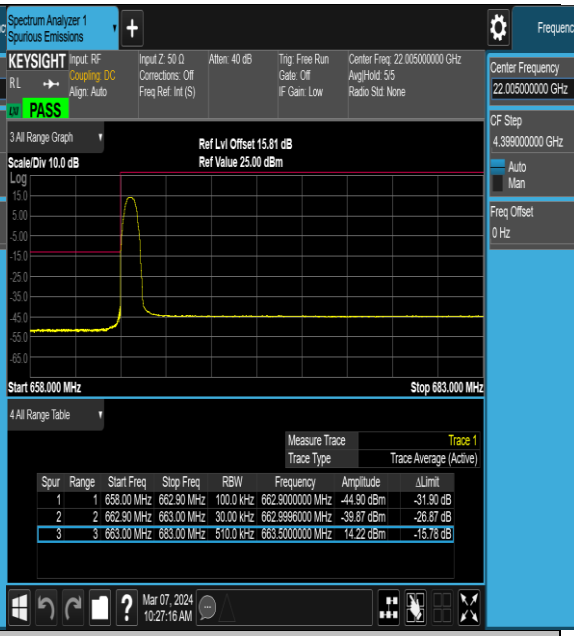
NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID6_#1



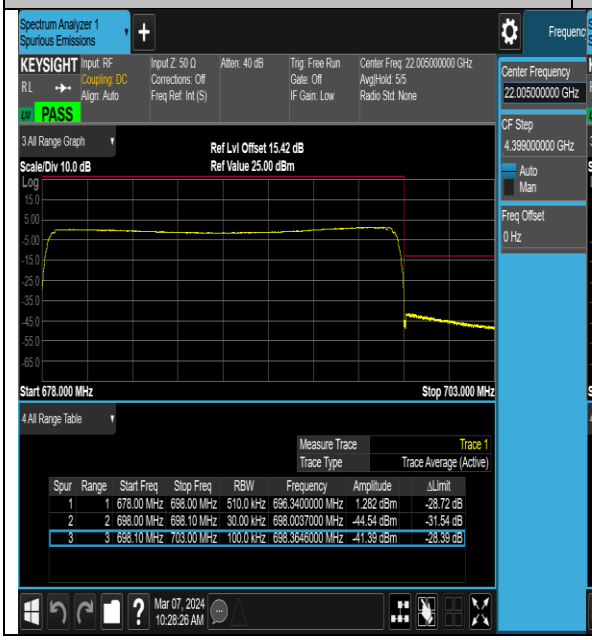
NTNV_DC_2A_n71A_PC3_15_5+20_M+L_TID4_#1

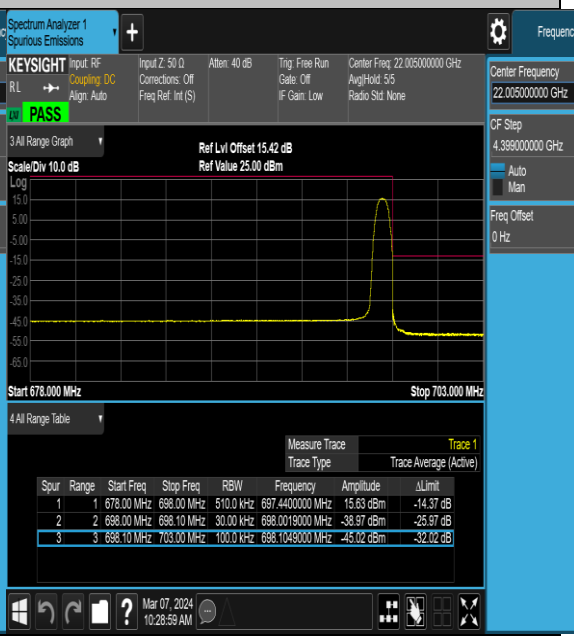


NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID6_#1



NTNV_DC_2A_n71A_PC3_15_5+20_M+H_TID5_#1





Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-54.36	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-65.14	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-45.82	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-29.90	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-37.18	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-38.67	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.83	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.63	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-45.57	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-29.45	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-37.15	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-38.46	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.47	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.93	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-45.53	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-29.44	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-36.84	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-38.40	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-55.58	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-65.23	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-45.38	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-29.52	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-36.99	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-38.42	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.90	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.71	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-45.47	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-29.50	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-37.13	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-38.49	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.96	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-65.16	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-45.66	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-29.47	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-37.14	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-38.38	-13	PASS

DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-53.12	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-65.82	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-45.54	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-29.50	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-37.07	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-38.58	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.98	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.88	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-45.25	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-29.51	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-37.04	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-38.56	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.39	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.71	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-45.69	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-29.49	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-37.10	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-38.57	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-54.40	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-66.34	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-45.63	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-29.51	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-36.98	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-38.52	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-54.96	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.96	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-45.56	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-29.55	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-37.03	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-38.52	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.53	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.48	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-45.49	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-29.43	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-36.97	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-38.43	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-54.51	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-64.79	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-45.42	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-29.93	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-36.99	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-38.66	-13	PASS

DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.86	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.45	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-45.62	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-29.54	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-37.16	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-38.54	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.83	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.14	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-45.69	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-29.53	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-36.98	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-38.58	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-55.58	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-65.85	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-45.32	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-29.52	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-37.11	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-38.53	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.40	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.51	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-45.76	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-29.45	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-37.09	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-38.33	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.66	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-66.58	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-45.22	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-29.57	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-37.10	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-38.29	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-55.05	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-64.22	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-45.53	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-29.50	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-36.90	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-38.43	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.97	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.40	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-45.77	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-29.55	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-37.18	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-38.52	-13	PASS

DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.68	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.33	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-45.66	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-29.55	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-37.11	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-38.45	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-55.31	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-66.68	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-45.60	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-29.44	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-37.00	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-38.47	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.83	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-63.87	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-45.34	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-29.52	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-37.07	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-38.31	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.92	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.52	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-45.27	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-29.50	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-37.15	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-38.50	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-55.19	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-65.74	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-45.67	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-29.51	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-37.06	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-38.45	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.20	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.86	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-45.68	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-29.56	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-37.07	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-38.49	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.76	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-63.99	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-45.65	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-29.56	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-37.04	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-38.50	-13	PASS

DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-54.90	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-66.38	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-45.26	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-29.53	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-37.01	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-38.41	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.93	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.14	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-45.48	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-29.50	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-37.17	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-38.46	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.32	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.38	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-45.75	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-29.55	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-36.97	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-38.61	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-55.40	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-64.20	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-44.27	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-29.89	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-37.20	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-38.47	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.55	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.42	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-45.47	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-29.95	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-37.13	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-38.64	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.82	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.79	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-45.51	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-29.80	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-37.13	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-38.36	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-54.85	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-65.15	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-42.64	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-29.87	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-37.25	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-38.69	-13	PASS

DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.36	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-66.73	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-45.72	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-29.92	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-37.25	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-38.67	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-55.01	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.68	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-45.40	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-29.75	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-37.22	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-38.69	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-55.76	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-65.60	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-45.67	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-29.78	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-37.05	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-38.66	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.76	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.68	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-45.44	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-29.87	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-37.24	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-38.74	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.77	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.54	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-45.37	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-29.82	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-37.23	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-38.52	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-55.35	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-65.50	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-45.59	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-29.93	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-37.21	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-38.55	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-55.07	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.25	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-45.46	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-29.90	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-37.05	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-38.47	-13	PASS

DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-56.14	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-67.28	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-45.49	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-29.79	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-37.01	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-38.49	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-54.64	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-63.64	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-45.45	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-29.80	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-37.22	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-38.30	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.47	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.54	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-45.44	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-29.83	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-37.27	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-38.61	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.43	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.77	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-45.40	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-29.73	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-37.13	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-38.39	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-55.30	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-65.86	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-45.09	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-29.74	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-36.91	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-38.41	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-55.52	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-66.21	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-45.45	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-29.76	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-37.20	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-38.43	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.54	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-65.64	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-45.43	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-29.86	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-37.19	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-38.59	-13	PASS

DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-56.60	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-65.45	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-43.93	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-29.79	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-37.03	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-38.55	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.74	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.56	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-45.56	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-29.84	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-37.10	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-38.53	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.48	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.95	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-45.71	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-29.70	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-37.12	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-38.44	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-55.65	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-64.87	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-43.44	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-29.68	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-37.19	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-38.47	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-55.20	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-65.78	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-45.30	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-29.77	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-36.95	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-38.45	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-54.85	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-65.79	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-45.57	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-29.79	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-37.07	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-38.36	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-55.76	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-64.35	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-45.70	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-29.77	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-37.22	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-38.45	-13	PASS

DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.81	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.16	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-45.29	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-29.77	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-37.18	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-38.44	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.78	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.60	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-45.17	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-29.77	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-37.17	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-38.49	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-55.50	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-66.08	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-45.34	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-29.75	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-36.86	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-38.42	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-54.52	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-65.68	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-45.30	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-29.79	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-37.12	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-38.41	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-54.06	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-66.25	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-45.47	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-29.65	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-36.99	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-38.24	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-54.58	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-66.05	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-45.14	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-29.69	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-37.06	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-38.42	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.45	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.73	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-45.39	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-29.72	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-37.08	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-38.48	-13	PASS

DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.91	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.59	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-45.60	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-29.72	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-37.18	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-38.31	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-56.08	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-65.68	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-45.21	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-29.71	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-37.11	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-38.43	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-54.57	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-65.96	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-45.48	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-29.70	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-37.08	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-38.33	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-55.37	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-66.12	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-45.35	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-29.72	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-37.04	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-38.35	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-55.40	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-66.05	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-43.89	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-29.65	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-37.06	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-38.44	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.05	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-64.88	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-45.38	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-29.78	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-37.03	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-38.28	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.72	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-64.81	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-45.55	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-29.66	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-37.04	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-38.46	-13	PASS

DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-55.48	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-65.60	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-44.02	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-29.73	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-37.03	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-38.53	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-54.83	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-64.61	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-45.04	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-29.67	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-36.94	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-38.31	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-55.07	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-64.22	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-45.05	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-29.68	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-37.09	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-38.42	-13	PASS