

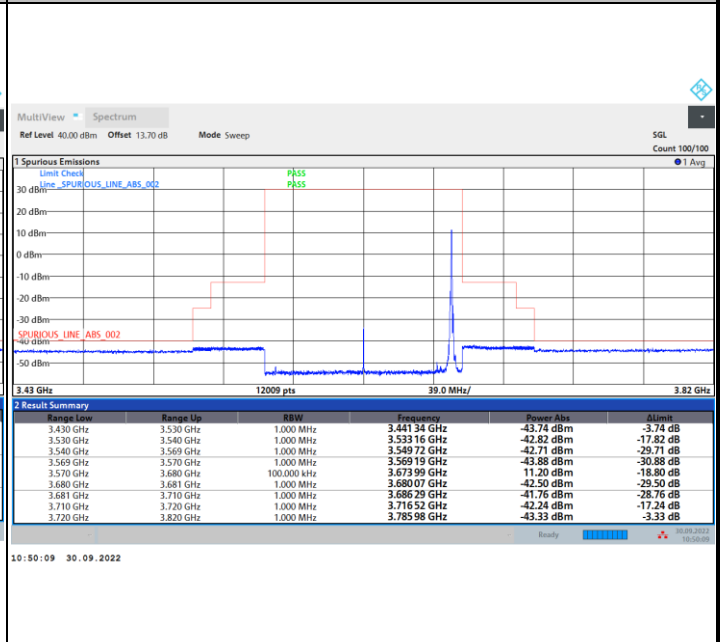
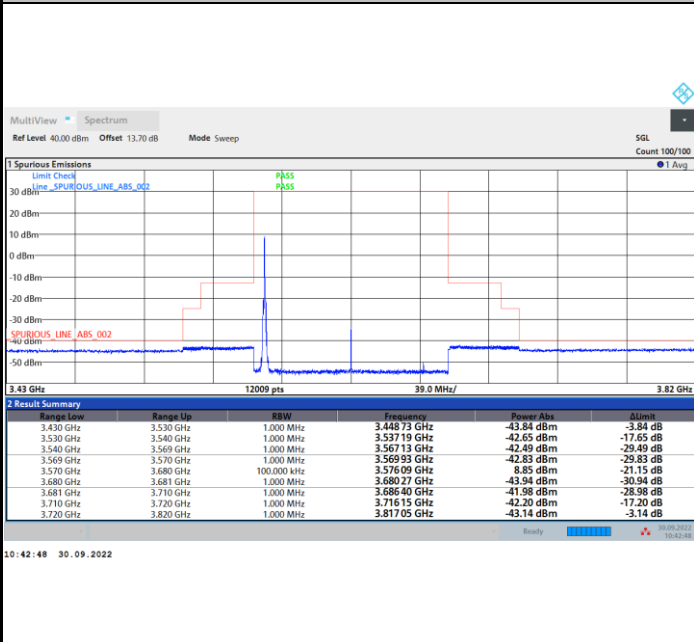


FR1 N77 / 100MHz / DFT-S OFDM / QPSK

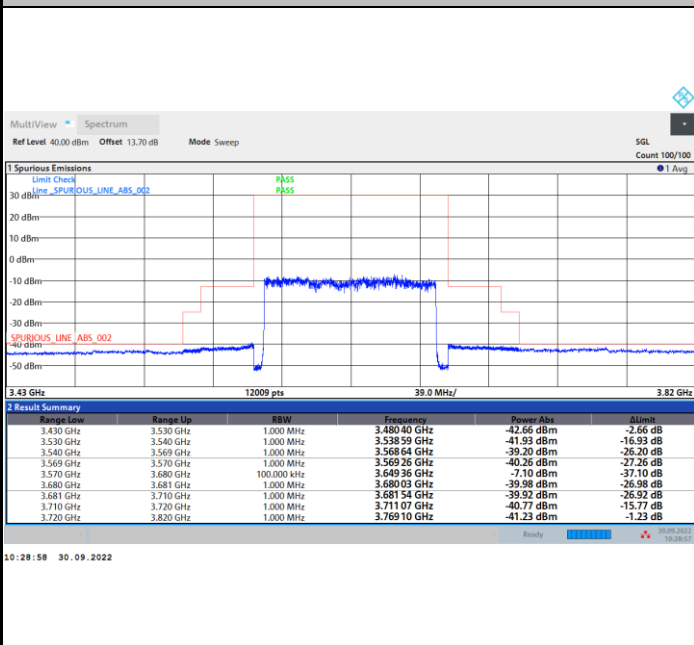
Middle Channel

1RB0

1RBmax



Full RB



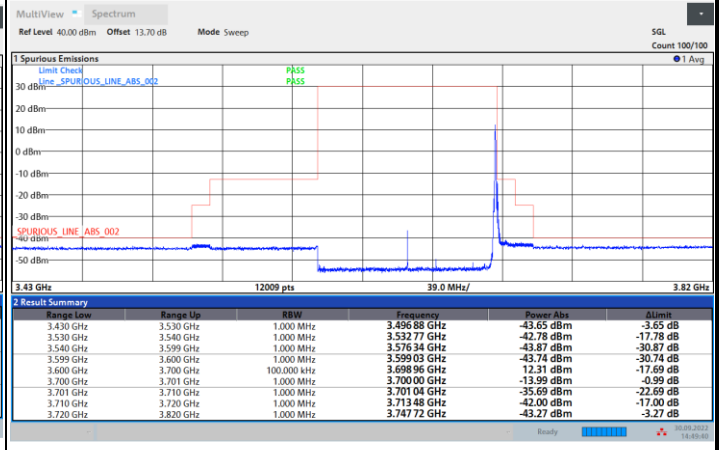
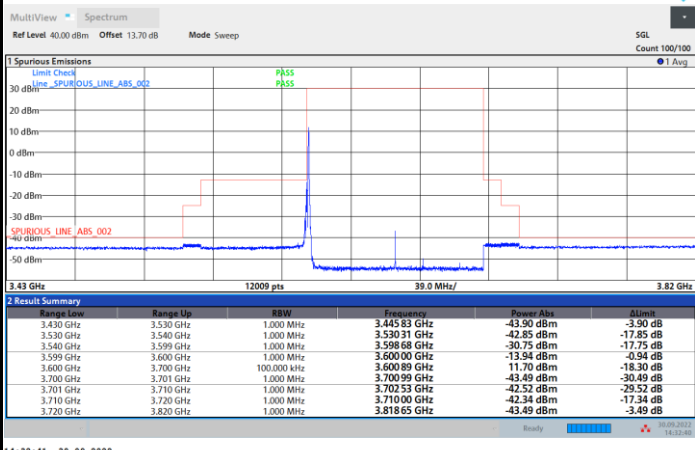


FR1 N77 / 100MHz / DFT-S OFDM / QPSK

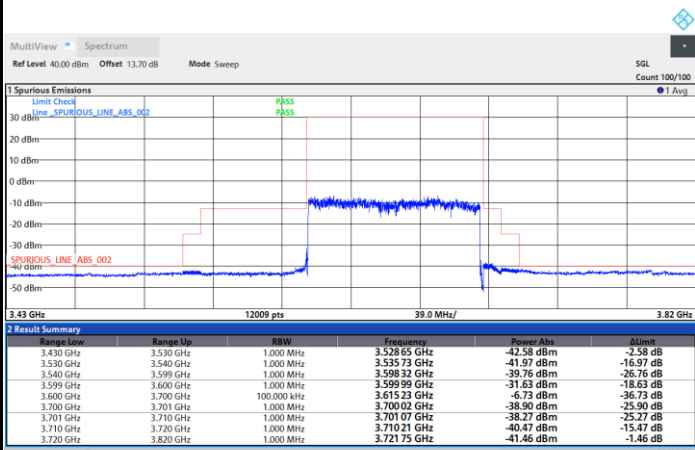
Highest Channel

1RB0

1RBmax



Full RB



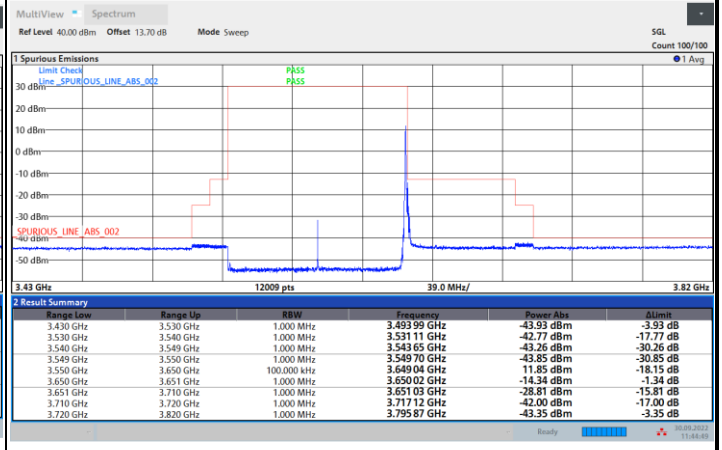
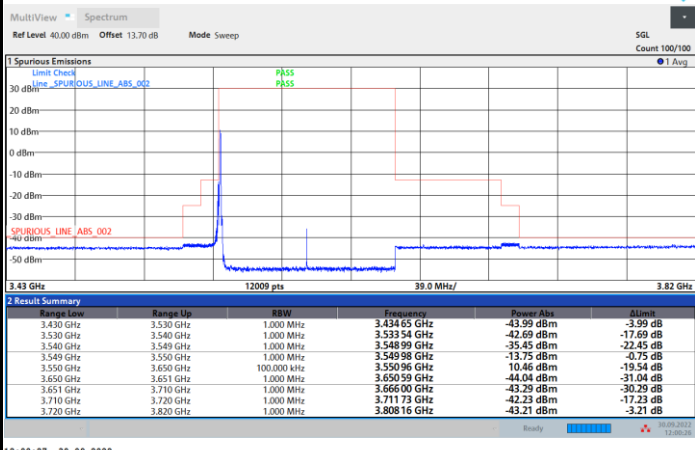


FR1 N77 / 100MHz / DFT-S OFDM / 16QAM

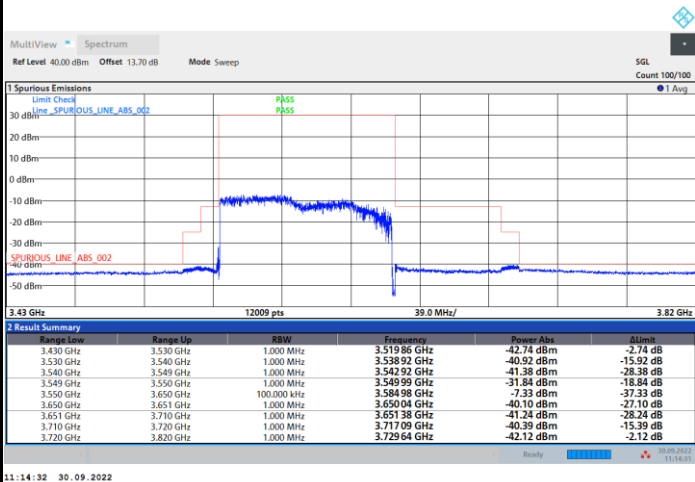
Lowest Channel

1RB0

1RBmax



Full RB



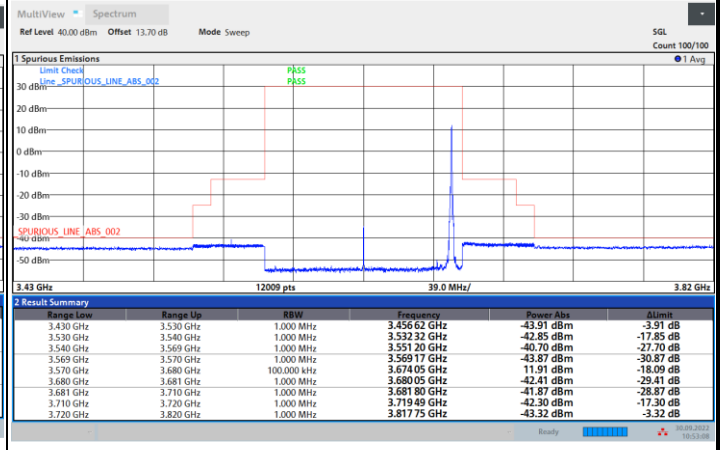
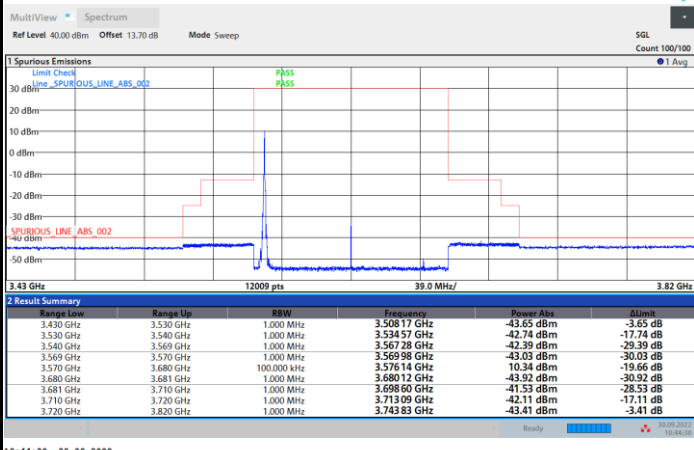


FR1 N77 / 100MHz / DFT-S OFDM / 16QAM

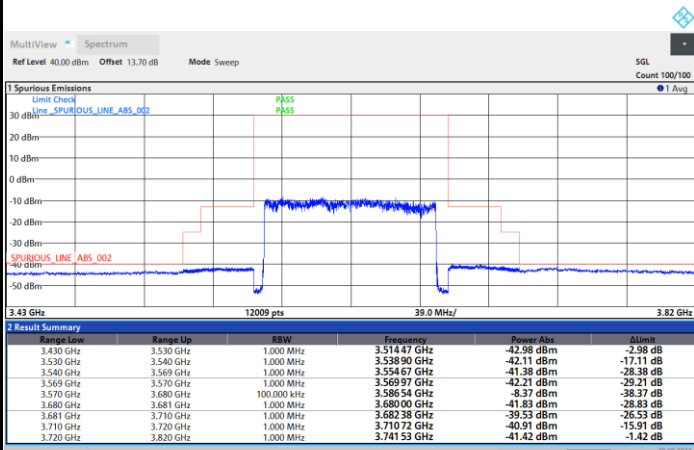
Middle Channel

1RB0

1RBmax



Full RB



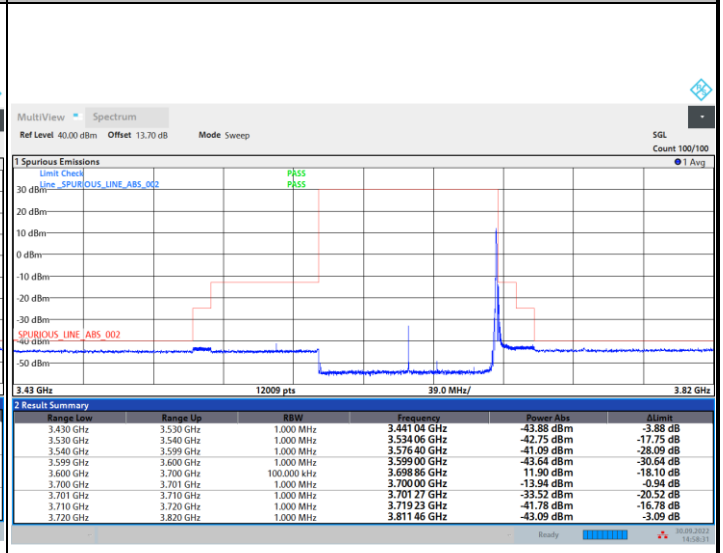
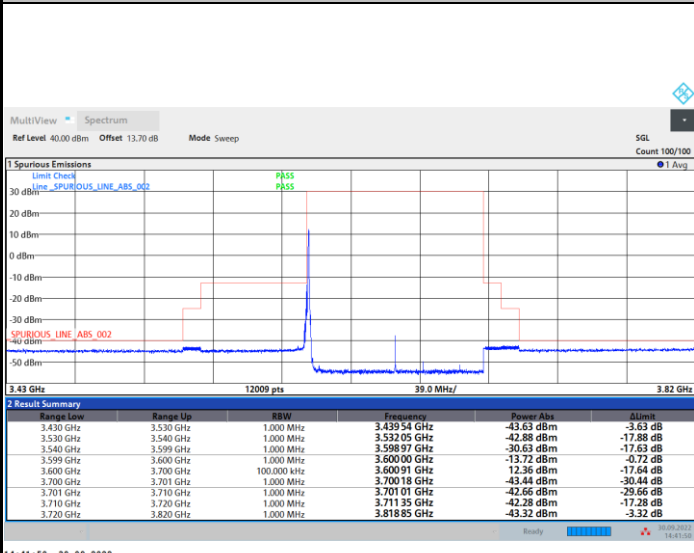


FR1 N77 / 100MHz / DFT-S OFDM / 16QAM

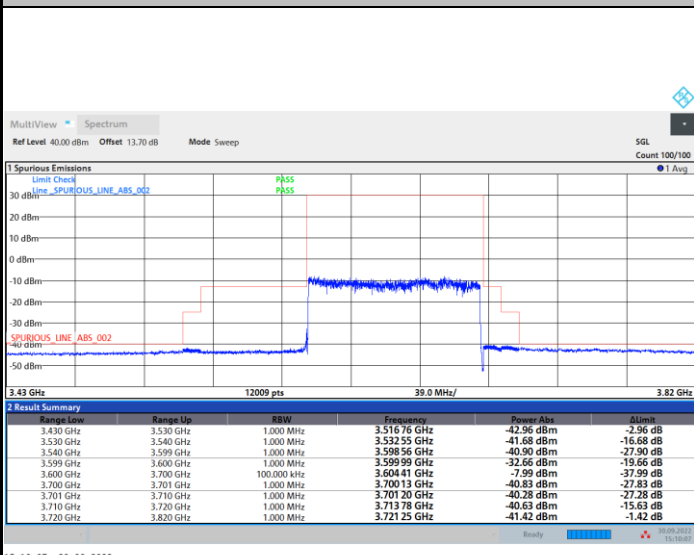
Highest Channel

1RB0

1RBmax



Full RB



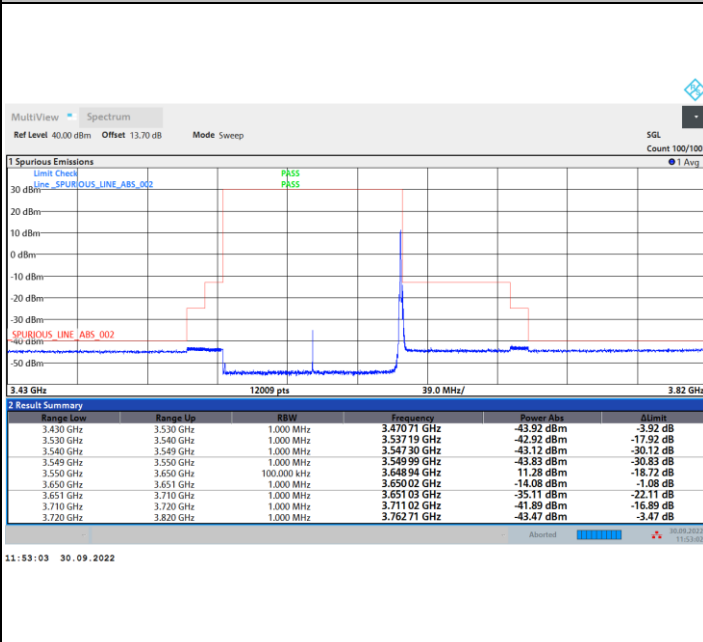
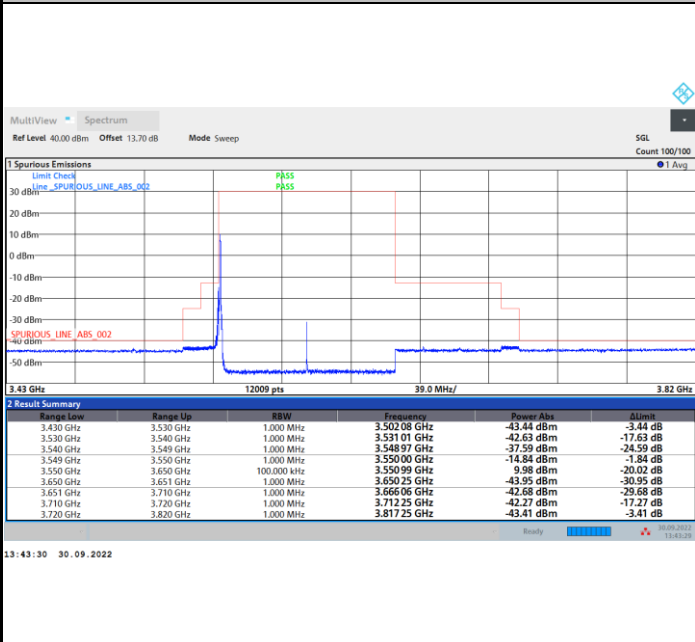


FR1 N77 / 100MHz / DFT-S OFDM / 64QAM

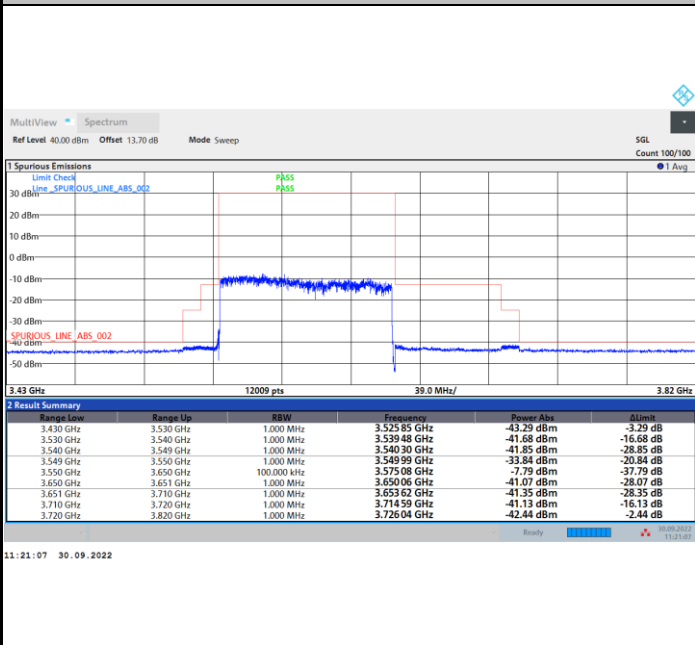
Lowest Channel

1RB0

1RBmax



Full RB



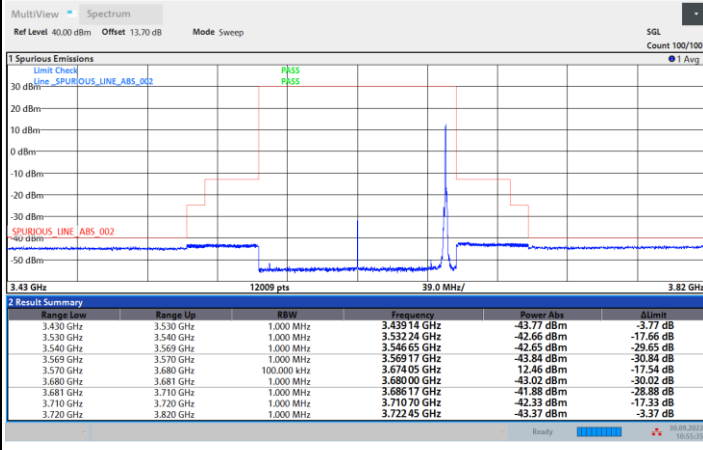
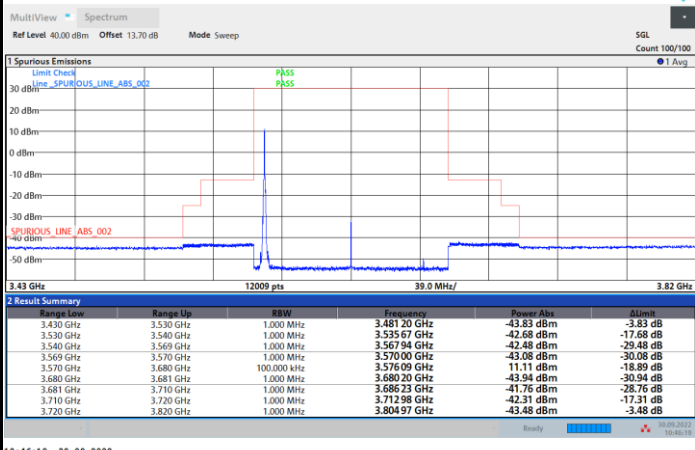


FR1 N77 / 100MHz / DFT-S OFDM / 64QAM

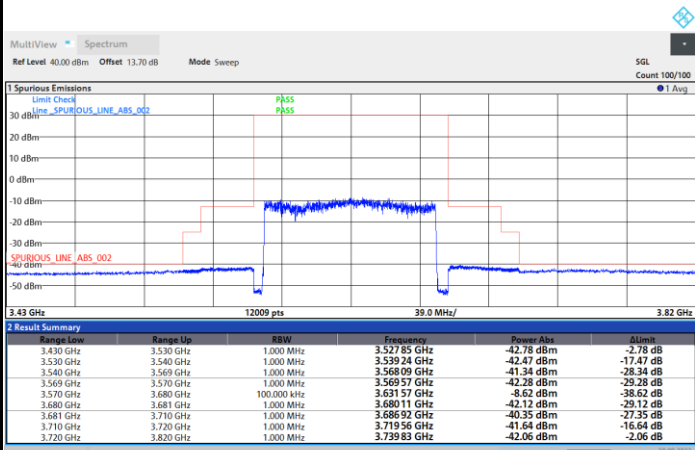
Middle Channel

1RB0

1RBmax



Full RB



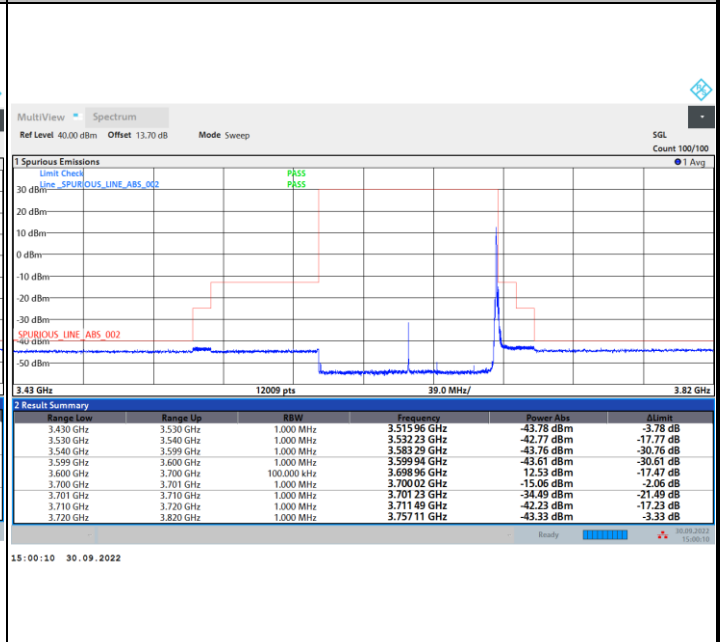
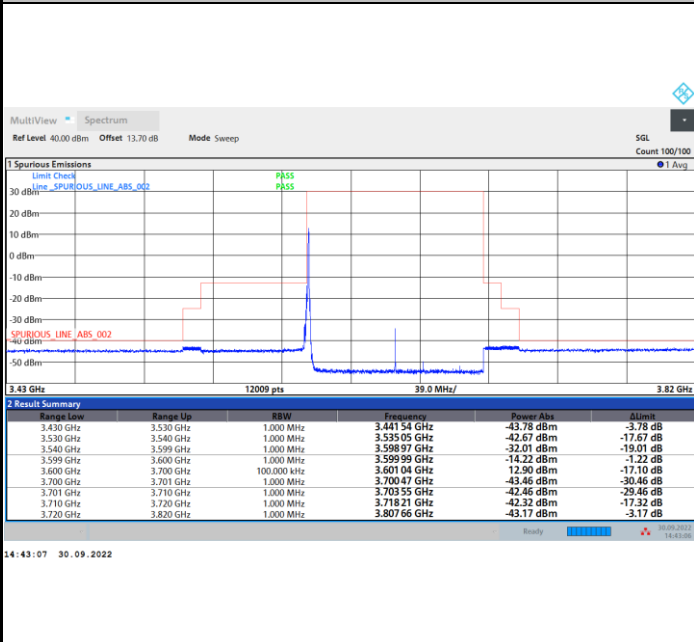


FR1 N77 / 100MHz / DFT-S OFDM / 64QAM

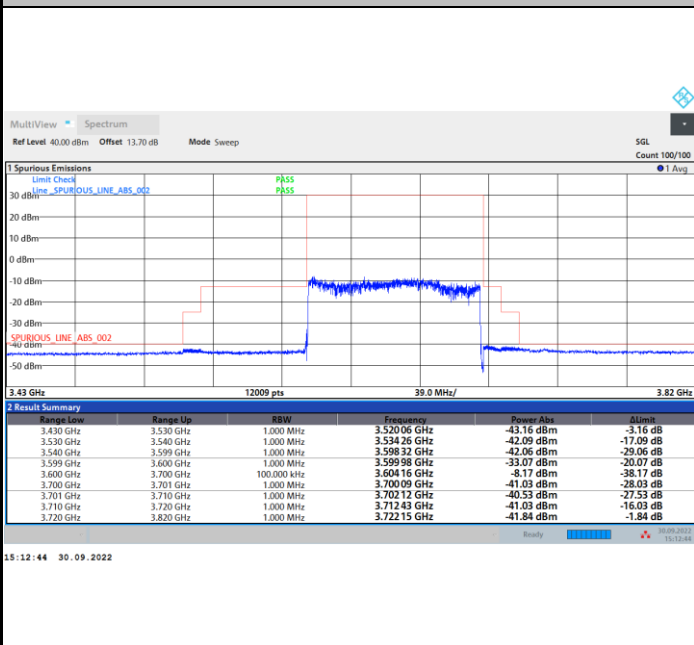
Highest Channel

1RB0

1RBmax



Full RB





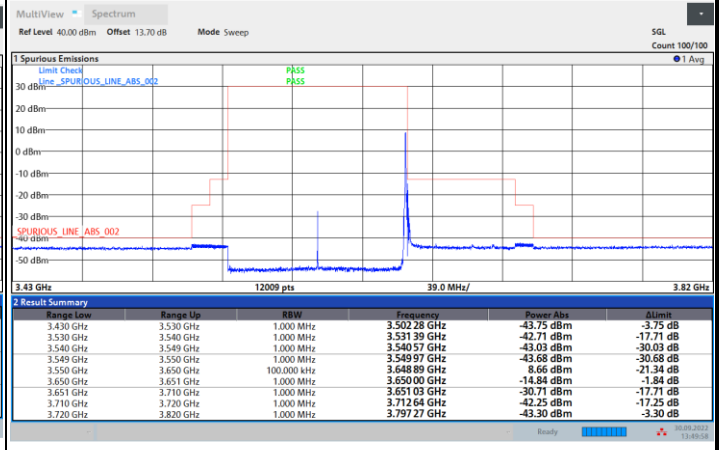
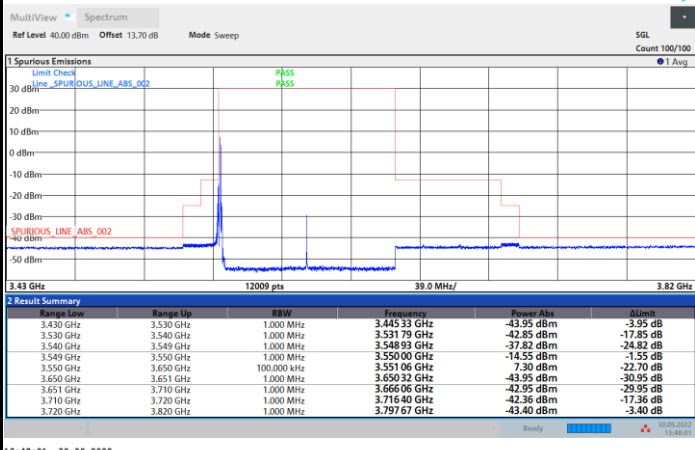


FR1 N77 / 100MHz / DFT-S OFDM / 256QAM

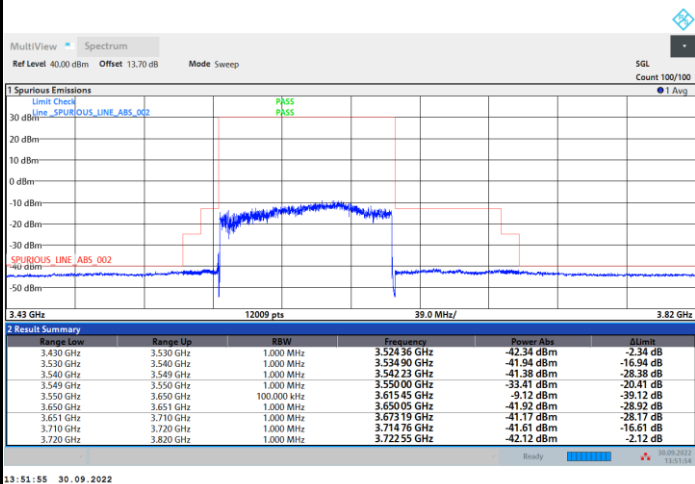
Lowest Channel

1RB0

1RBmax



Full RB



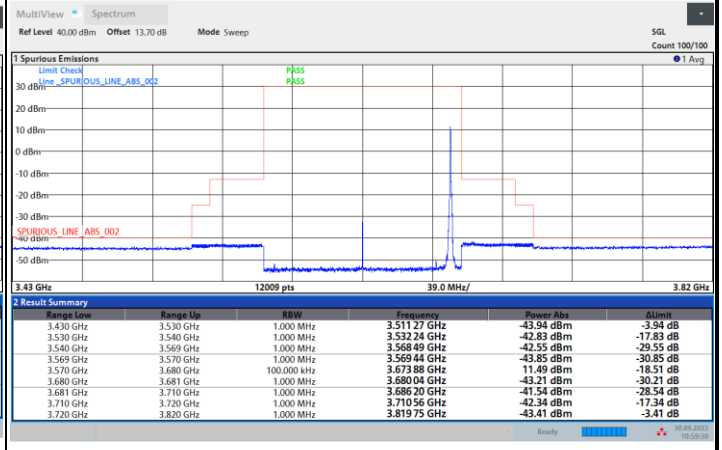
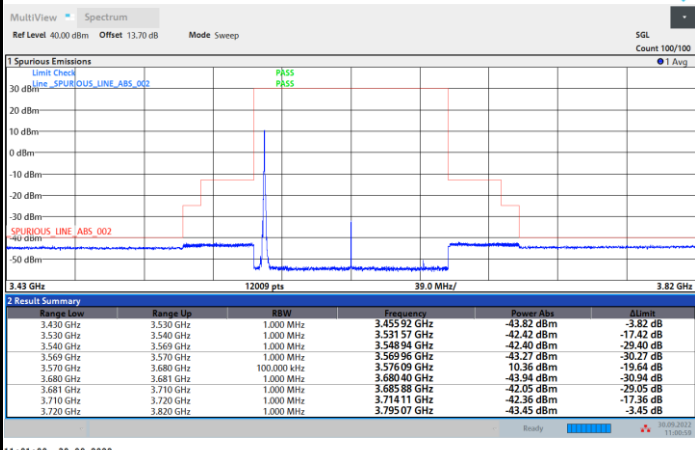


FR1 N77 / 100MHz / DFT-S OFDM / 256QAM

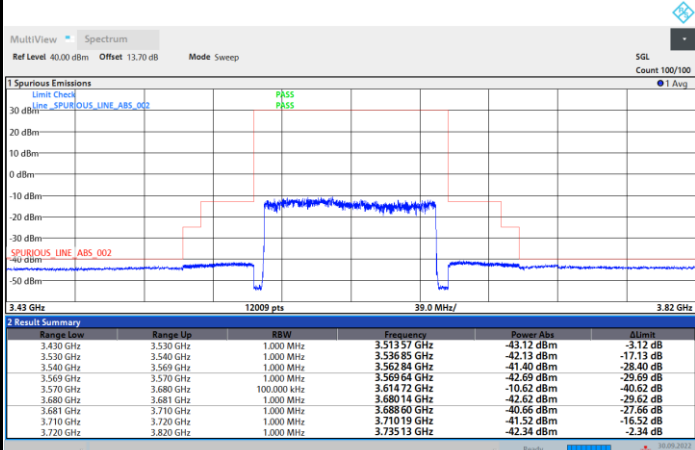
Middle Channel

1RB0

1RBmax



Full RB



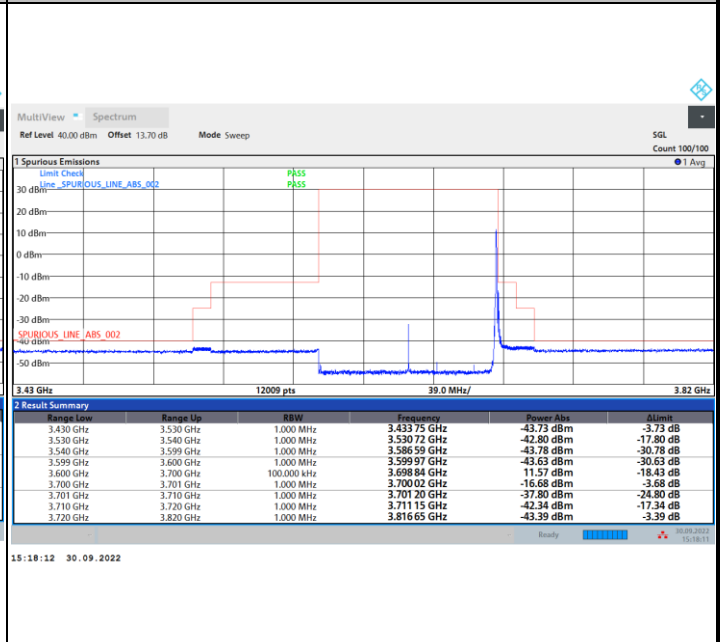
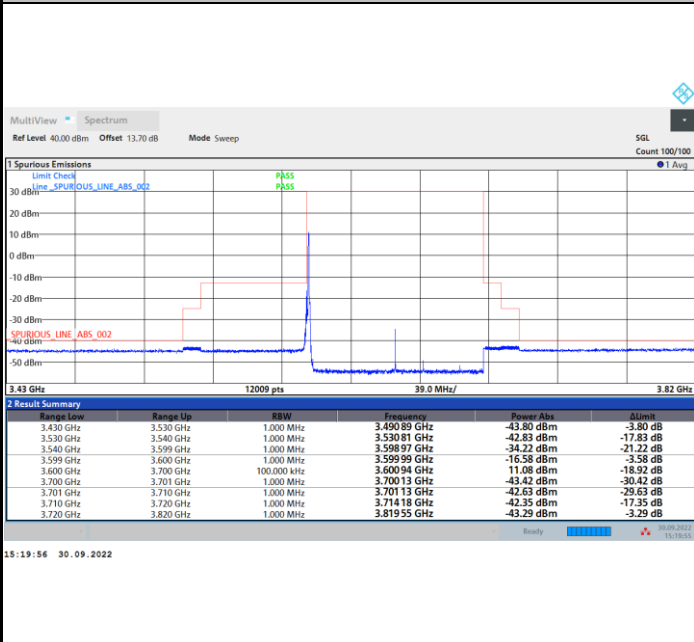


FR1 N77 / 100MHz / DFT-S OFDM / 256QAM

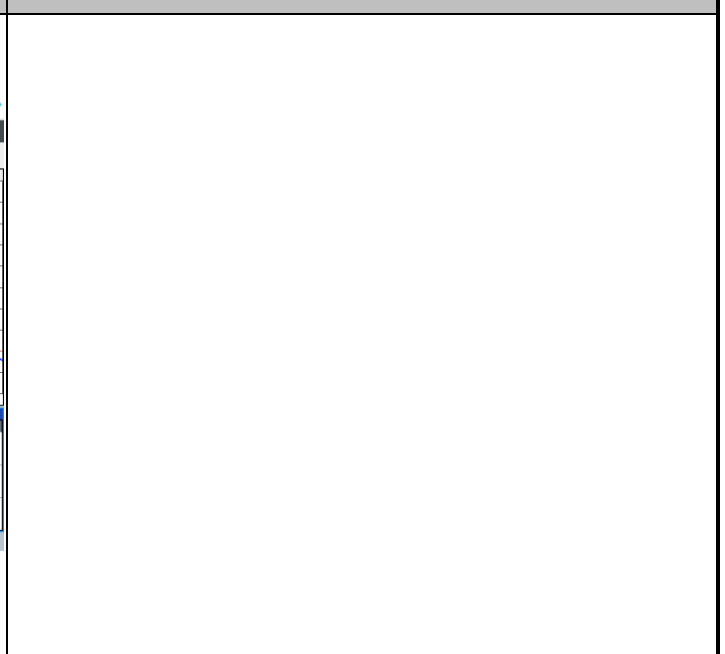
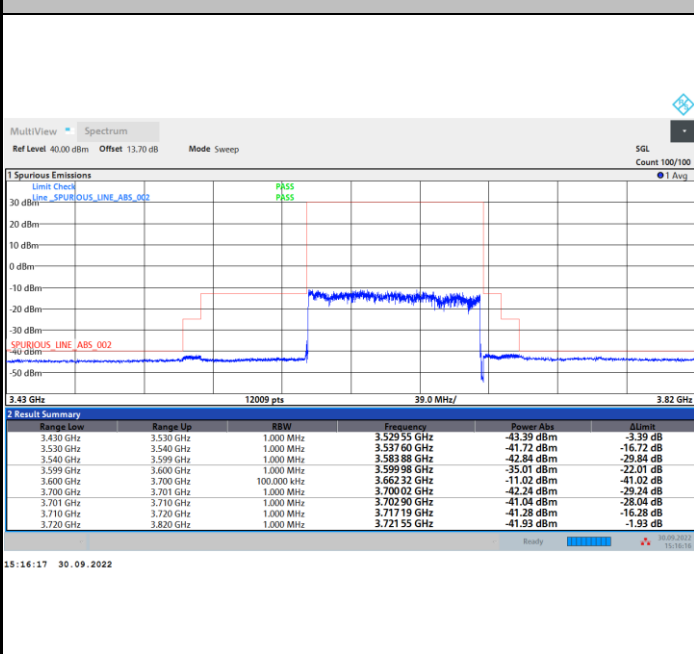
Highest Channel

1RB0

1RBmax



Full RB

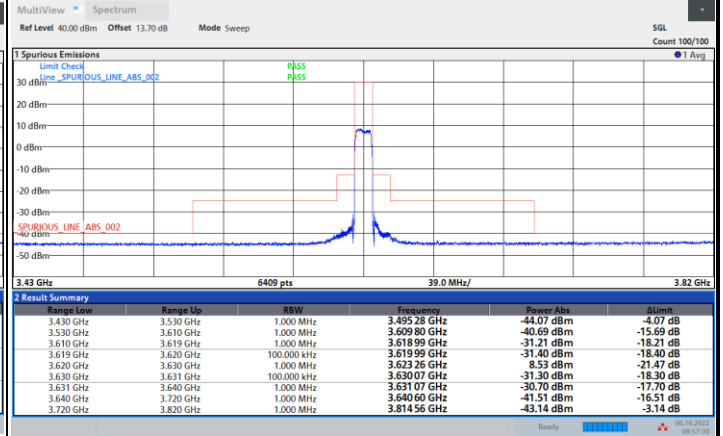
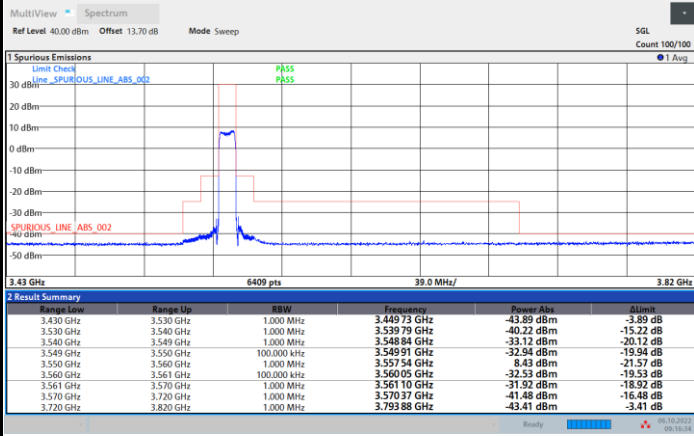




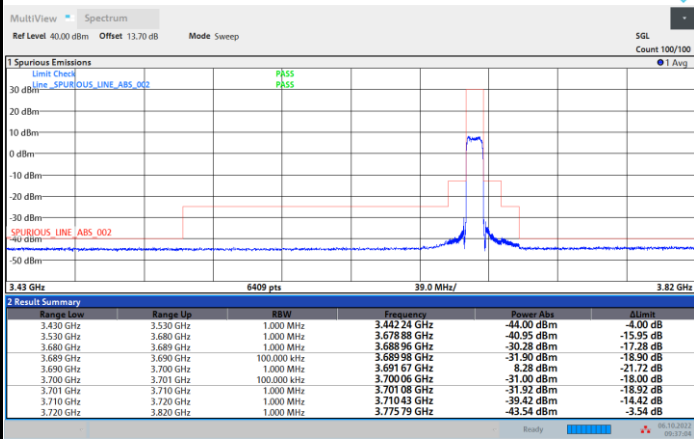
FR1 N77 / 10MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel



Highest Channel

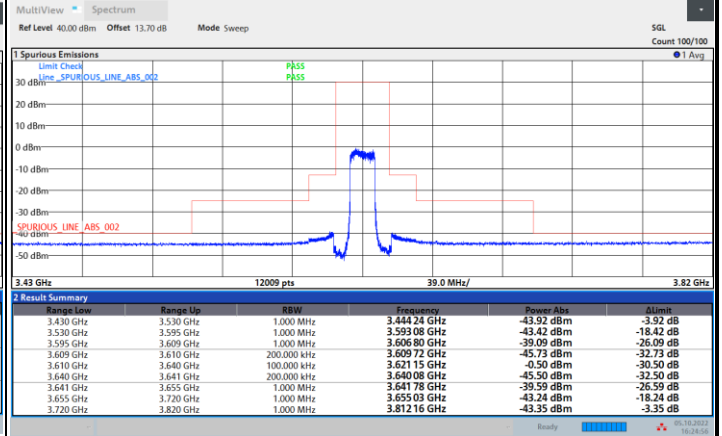
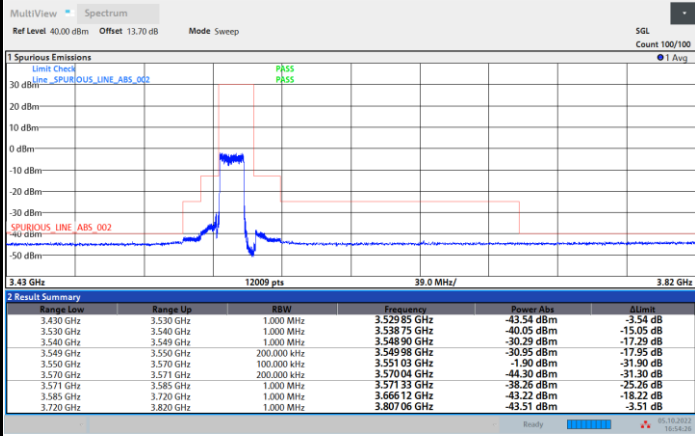




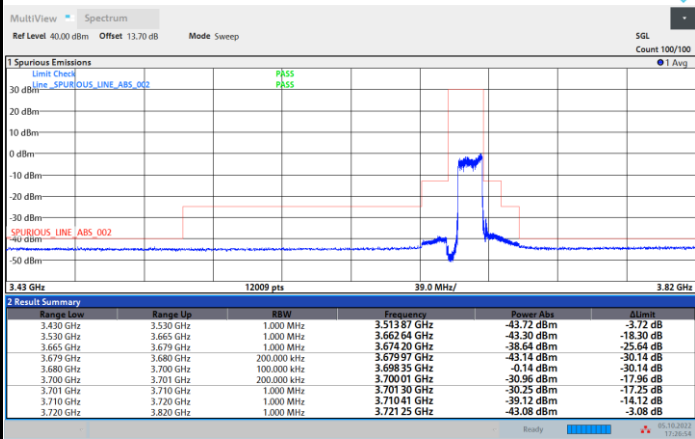
FR1 N77 / 15MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel



Highest Channel

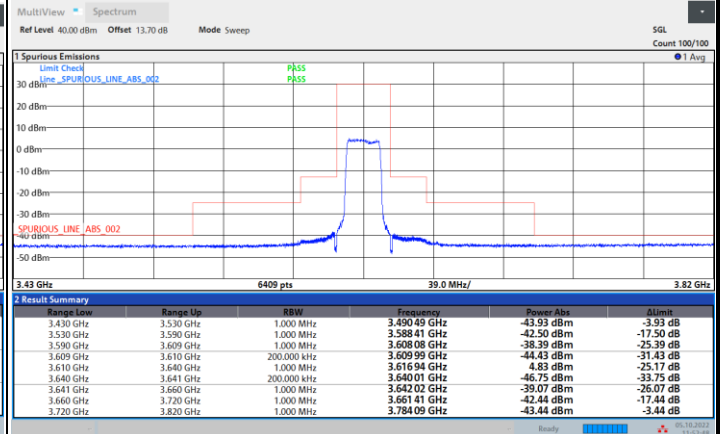
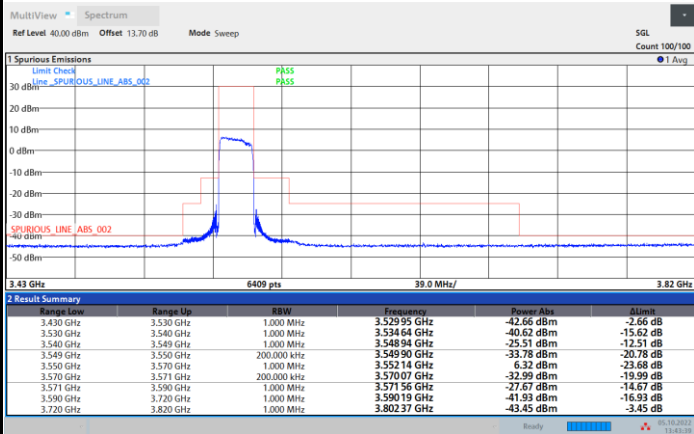




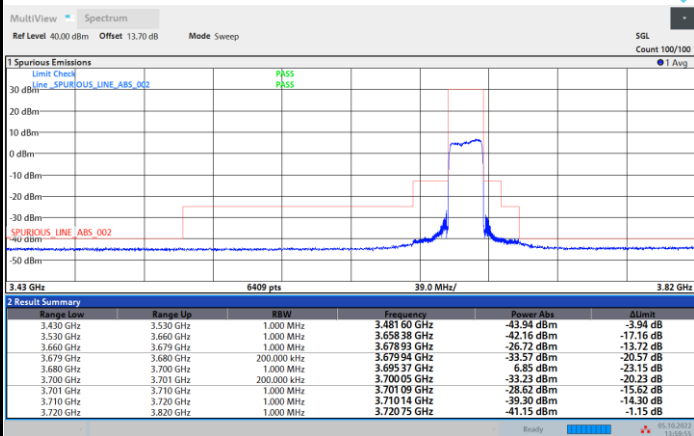
FR1 N77 / 20MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel



Highest Channel

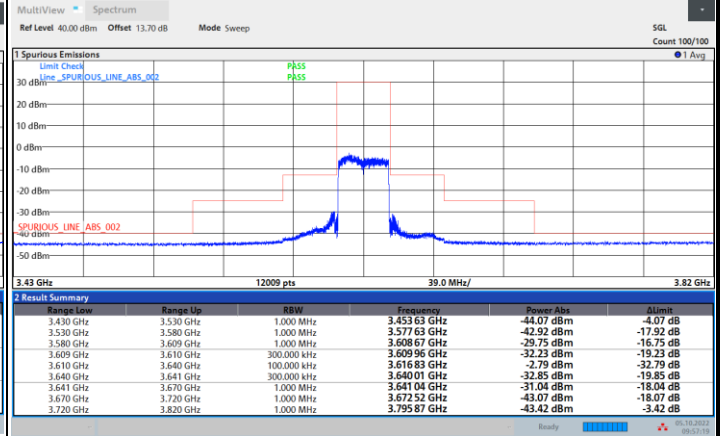
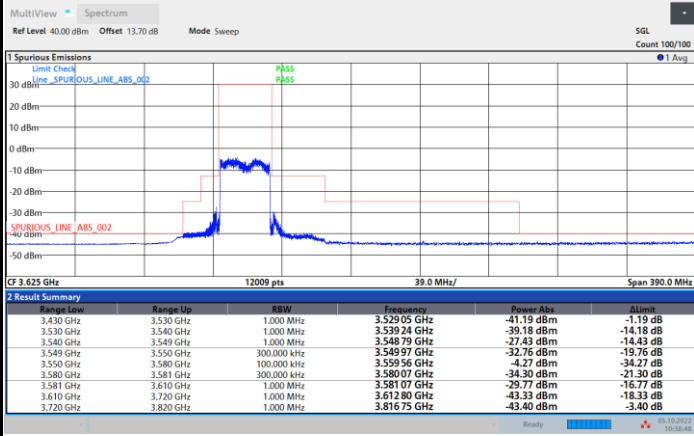




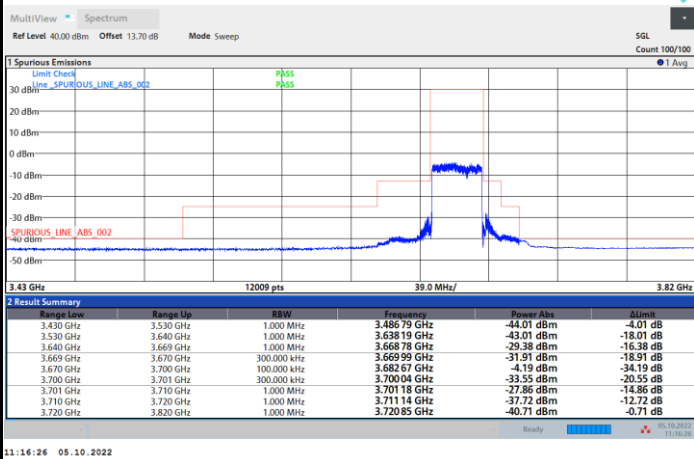
FR1 N77 / 30MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel



Highest Channel

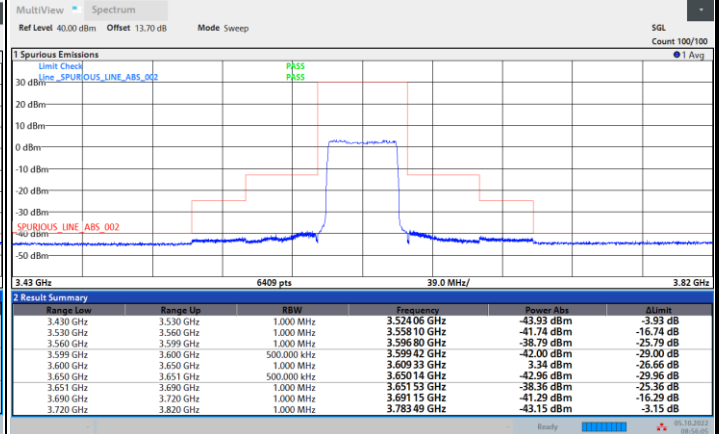
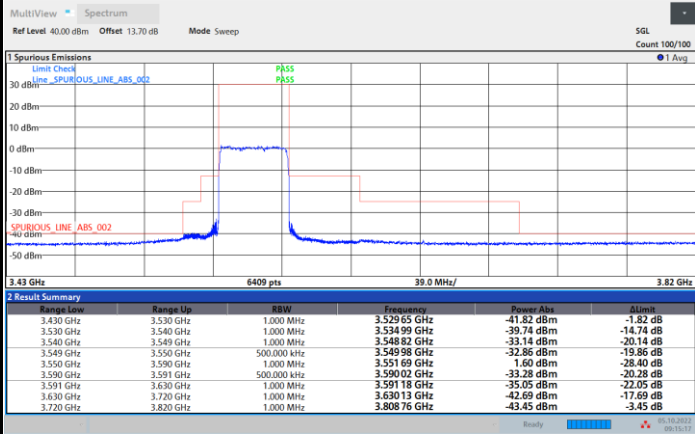




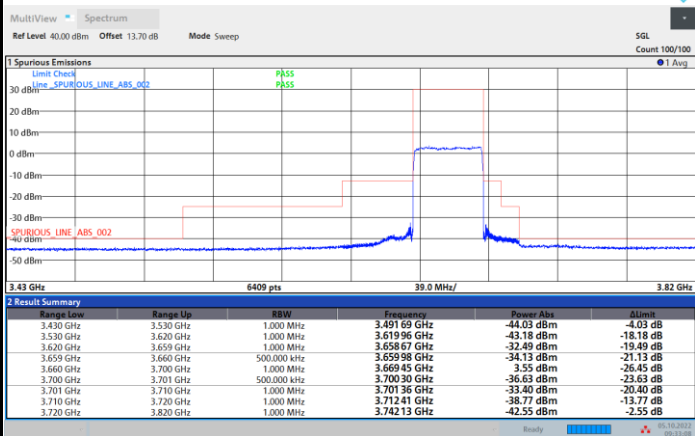
FR1 N77 / 40MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel



Highest Channel



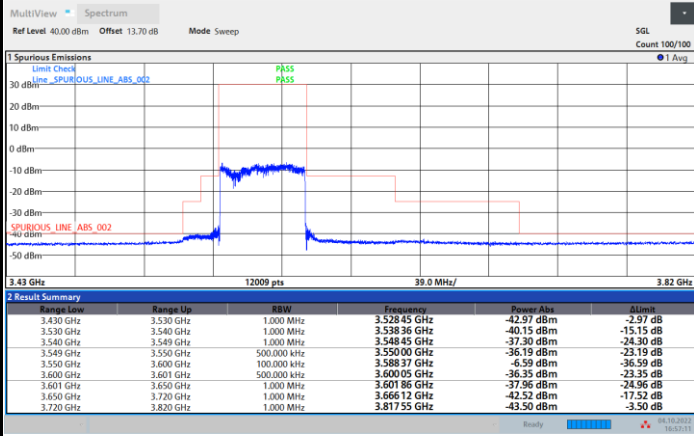




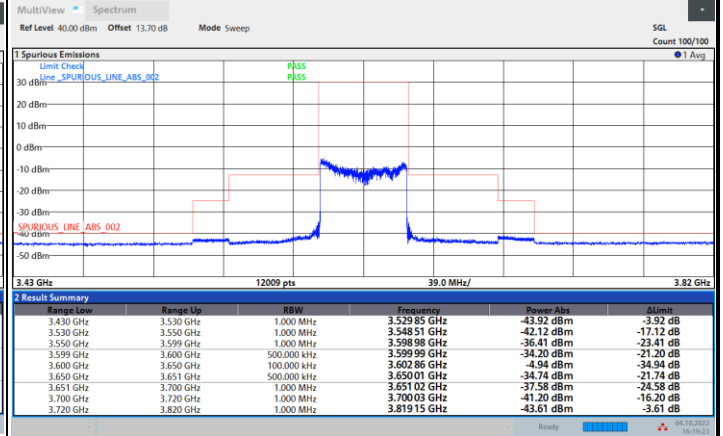
FR1 N77 / 50MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel

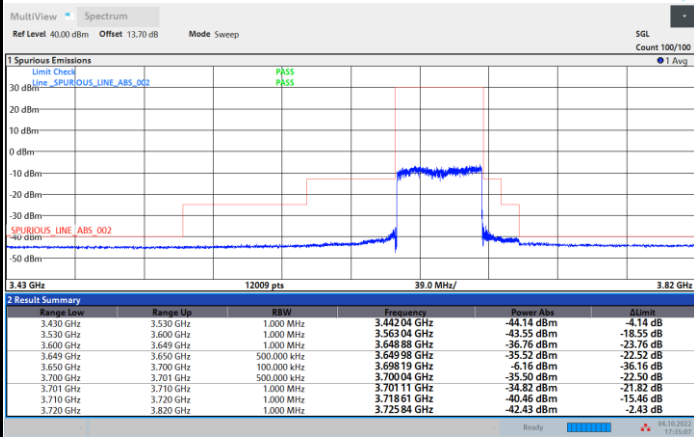


16:57:11 04.10.2022



16:19:23 04.10.2022

Highest Channel



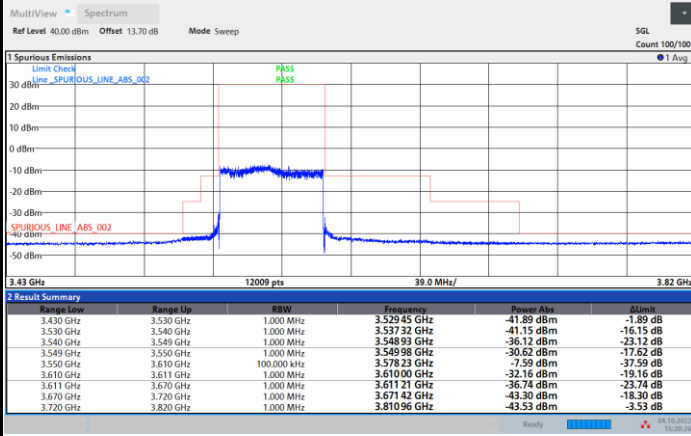
17:35:07 04.10.2022



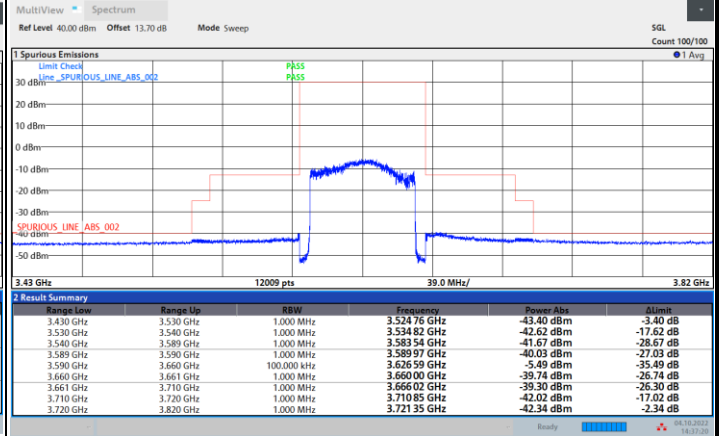
FR1 N77 / 60MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel

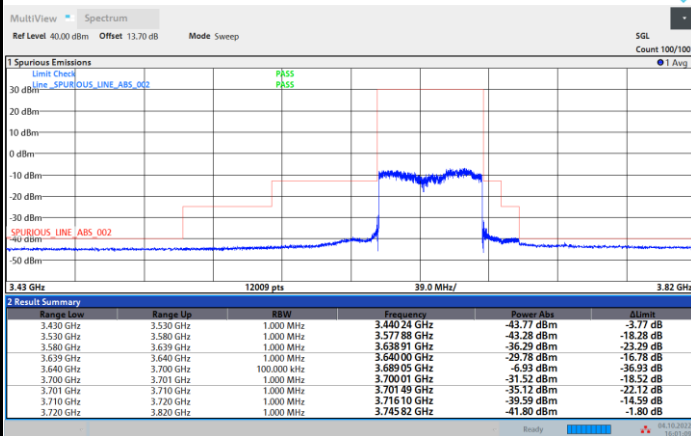


15:20:26 04.10.2022



14:37:21 04.10.2022

Highest Channel



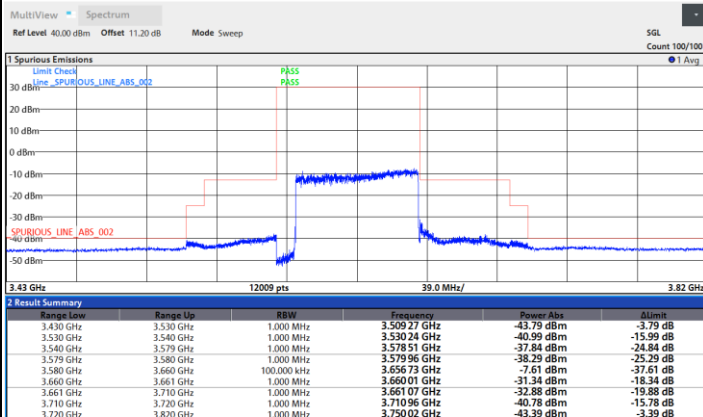
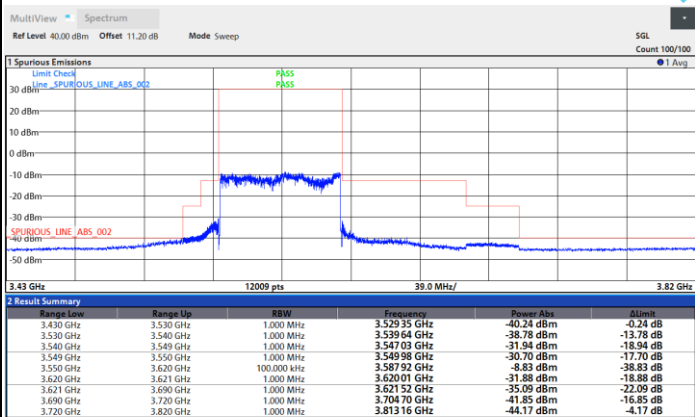
16:01:10 04.10.2022



FR1 N77 / 70MHz / CP OFDM / QPSK / Full RB

Lowest Channel

Middle Channel

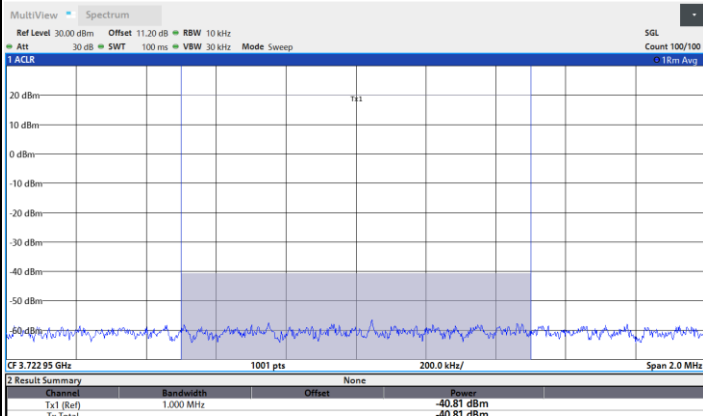
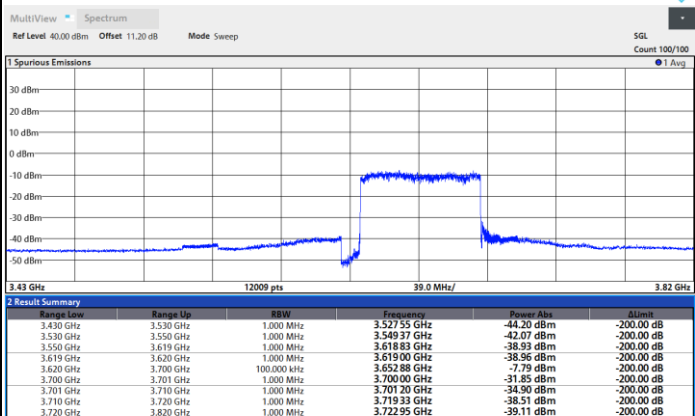


20:45:34 20.10.2022

22:37:44 20.10.2022

Highest Channel

Power Limit -40dBm > -40.81 dBm Pass



22:55:52 20.10.2022

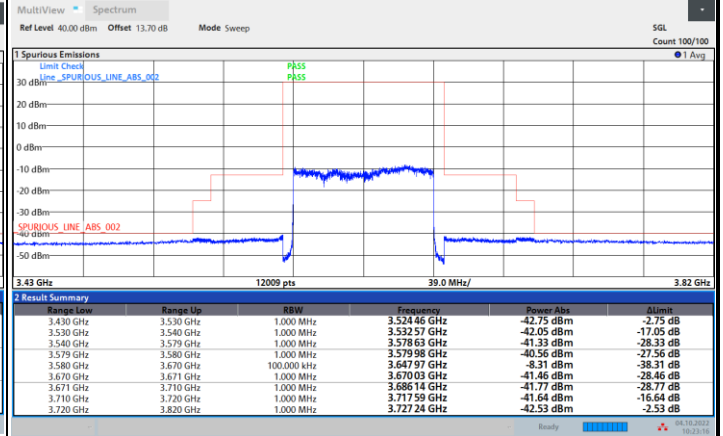
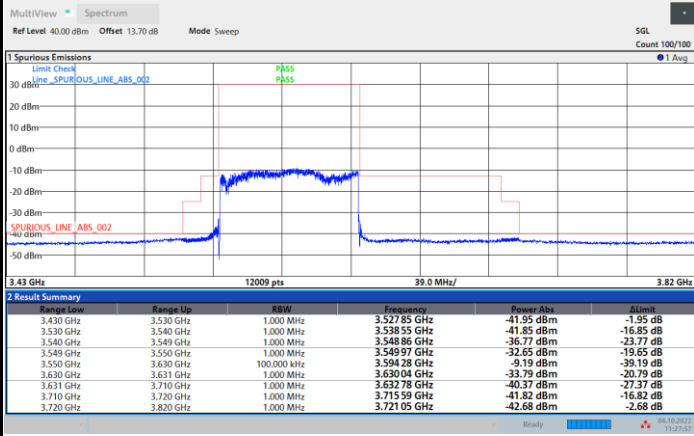
22:56:52 20.10.2022



FR1 N77 / 80MHz / CP OFDM / QPSK / Full RB

Lowest Channel

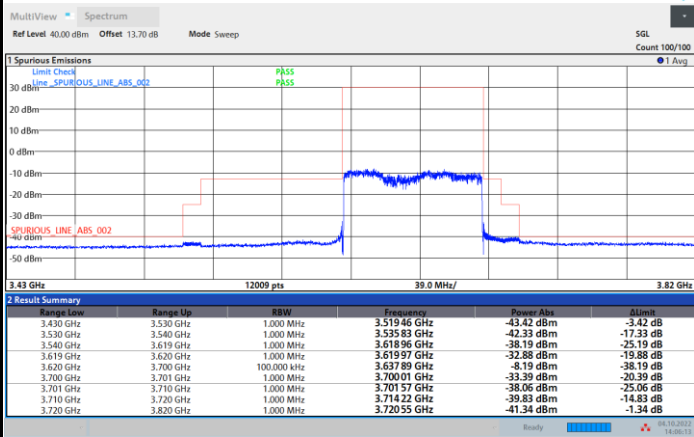
Middle Channel



11:27:58 04.10.2022

10:23:16 04.10.2022

Highest Channel



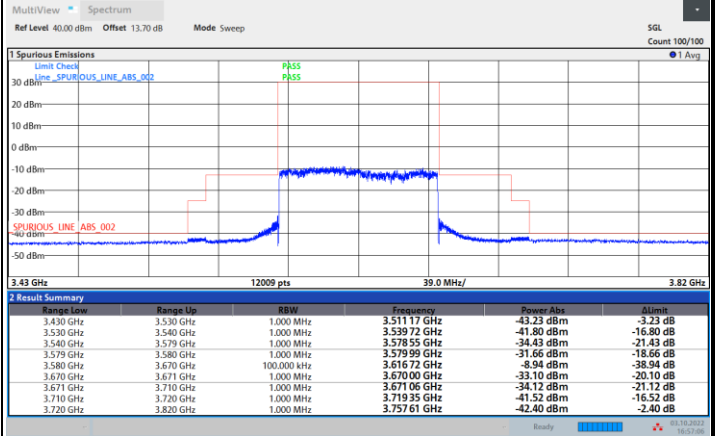
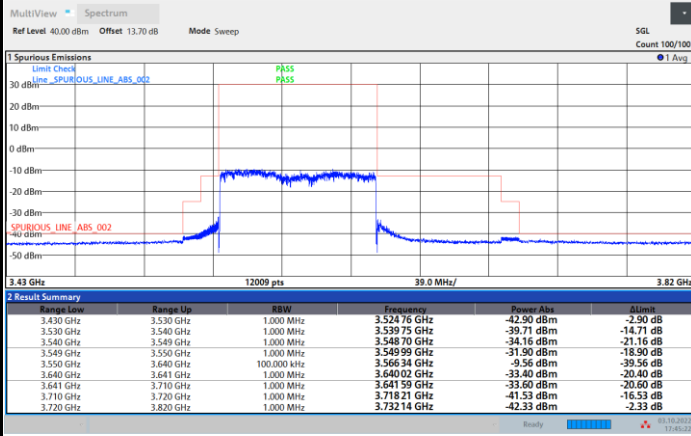
14:06:13 04.10.2022



FR1 N77 / 90MHz / CP OFDM / QPSK / Full RB

Lowest Channel

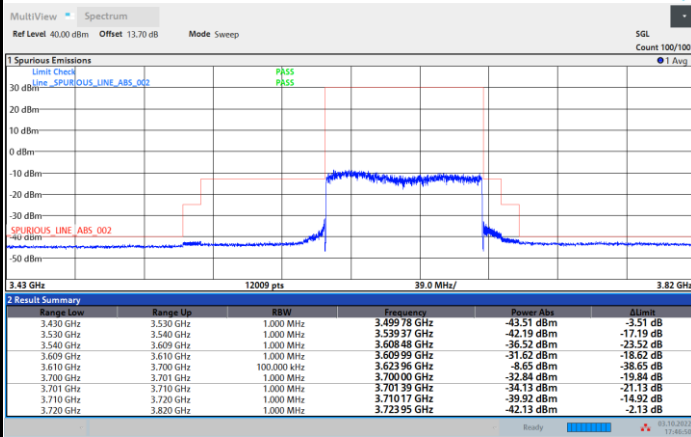
Middle Channel



17:45:24 03.10.2022

16:57:07 03.10.2022

Highest Channel



17:46:51 03.10.2022