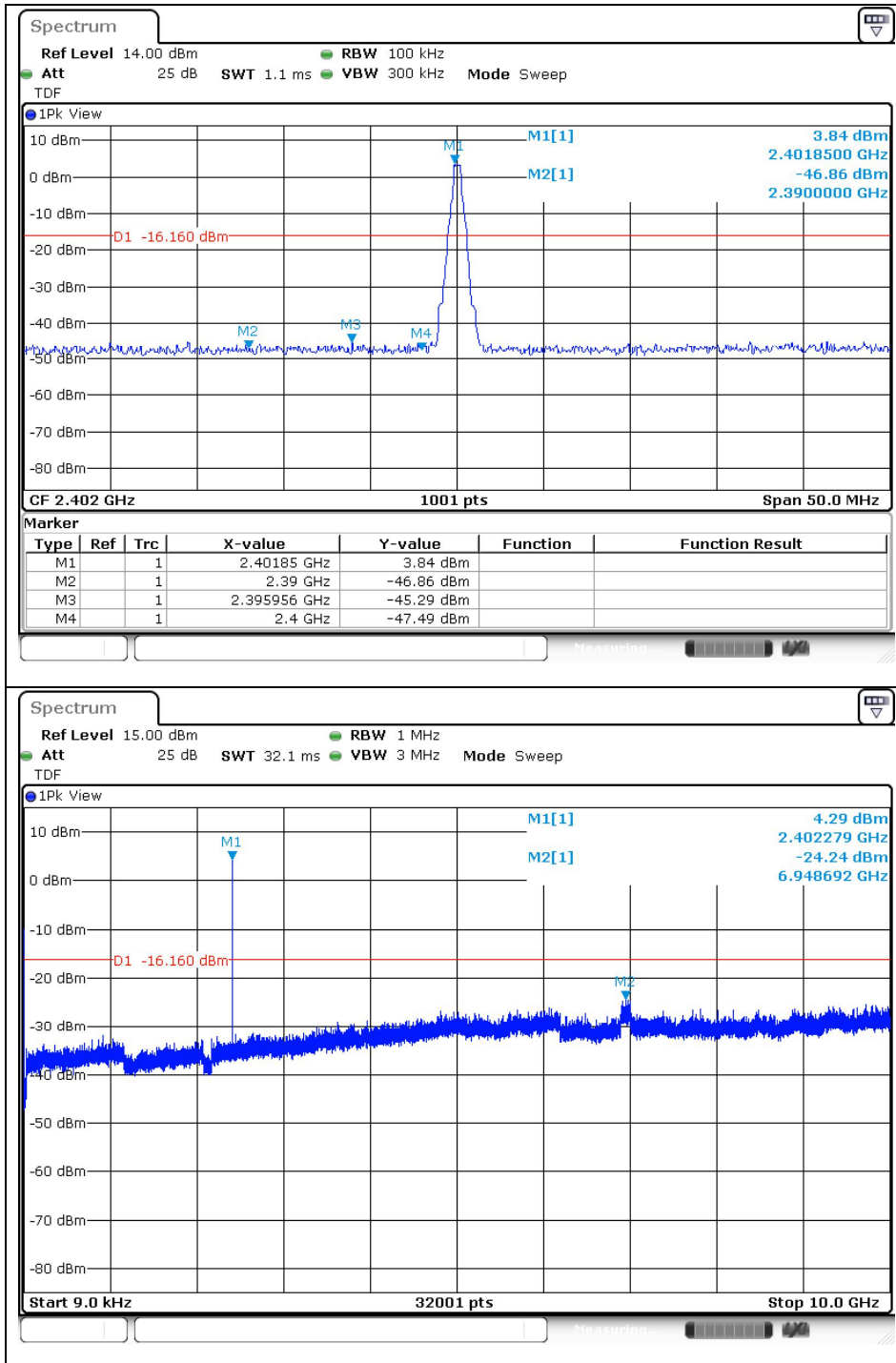


## 2.4.4. Plot of Conducted Spurious Emissions

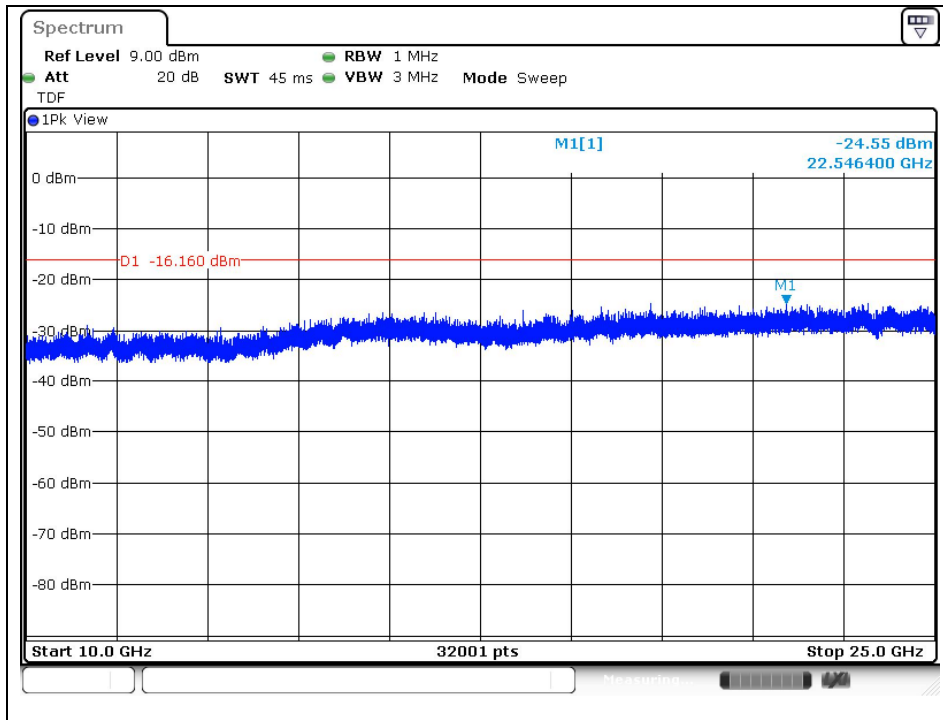
Test voltage: DC 12 V

Operating Mode: GFSK (1 Mbps)

Low channel

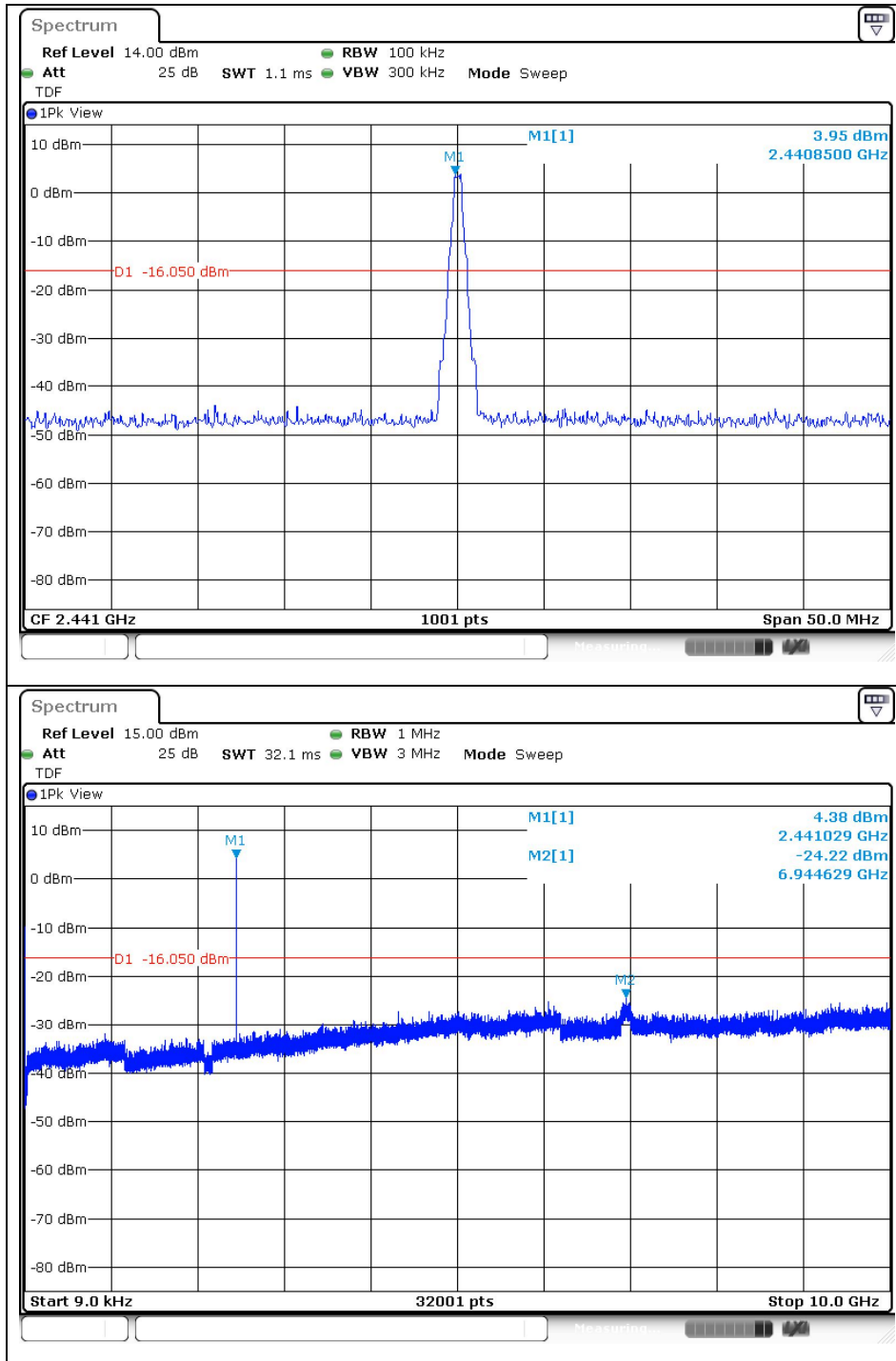


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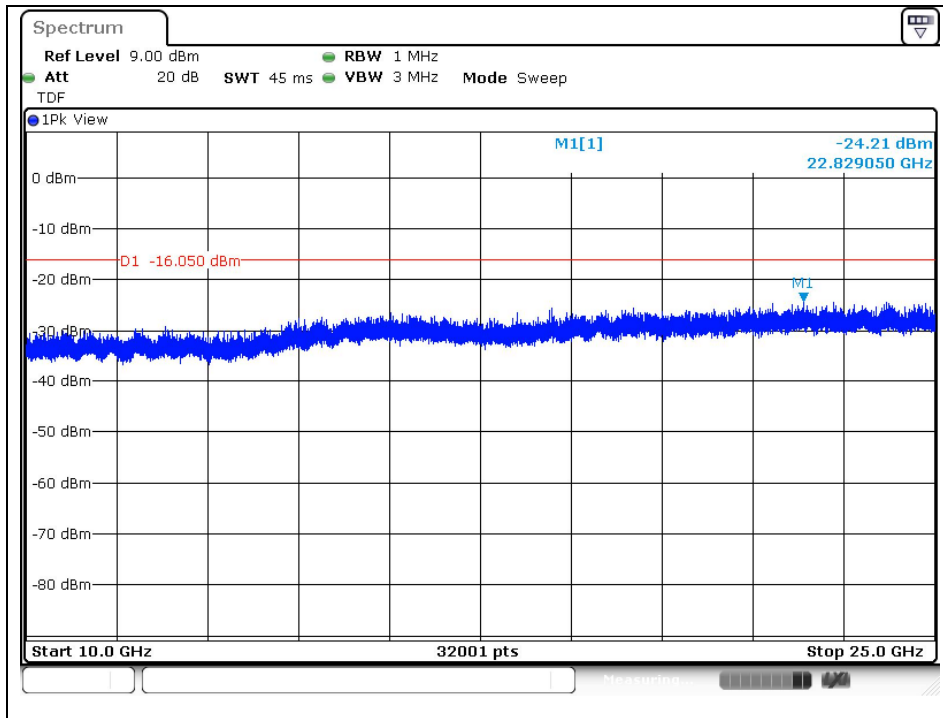


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Middle channel

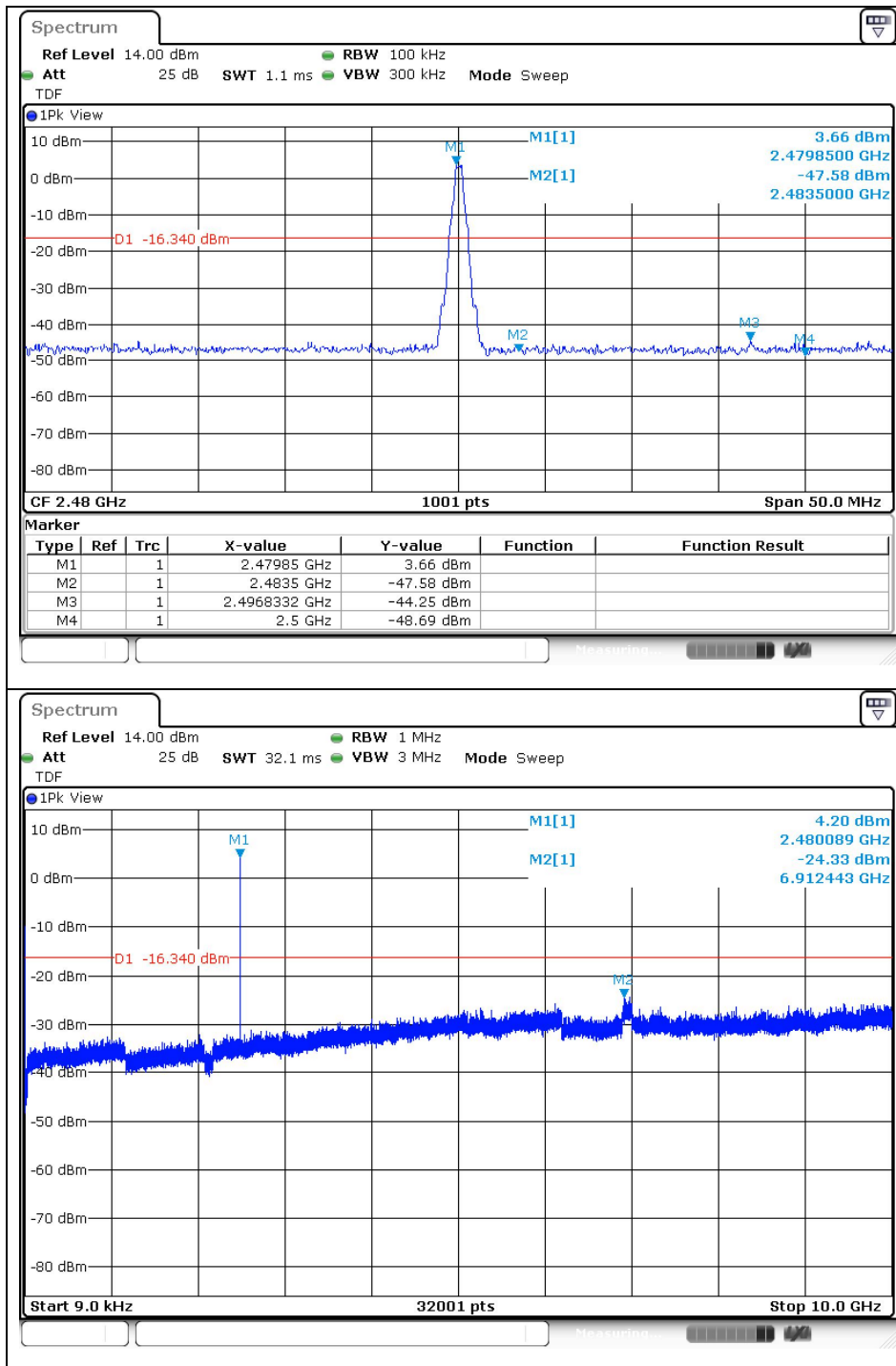


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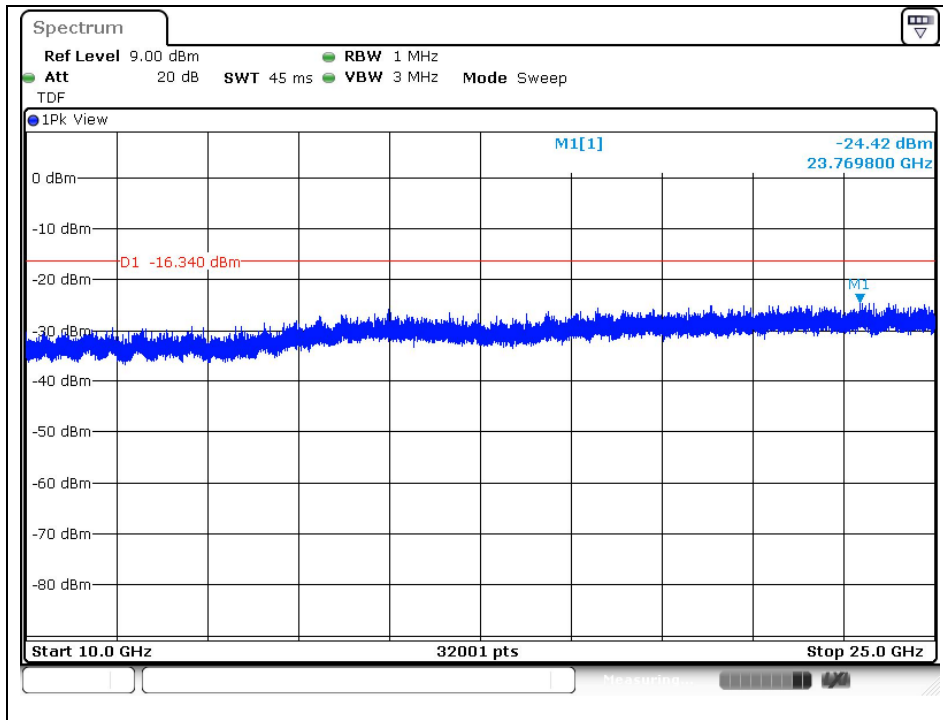


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High channel



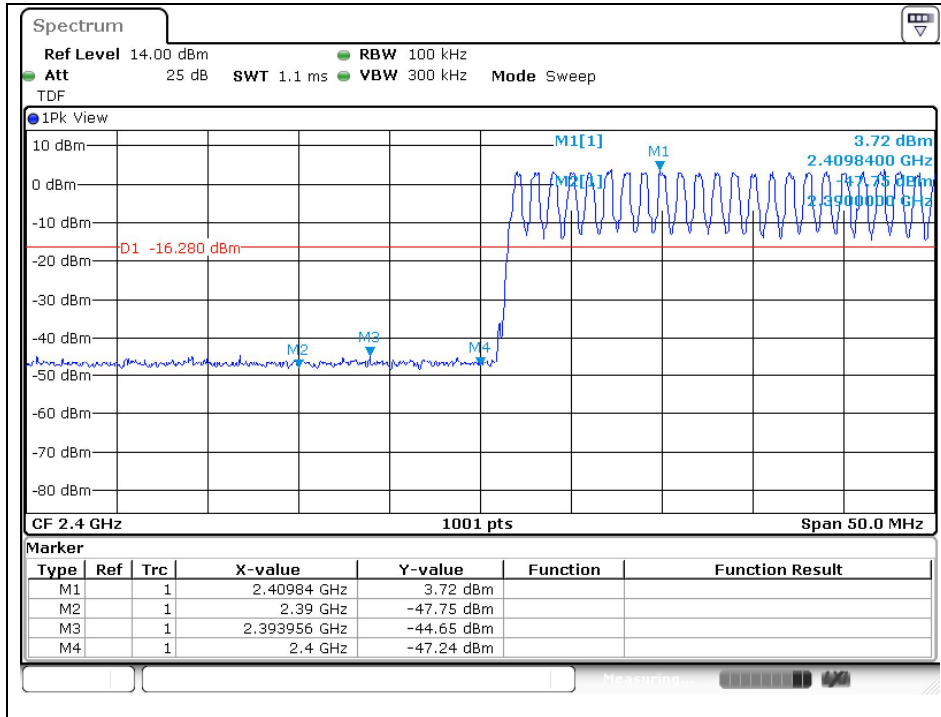
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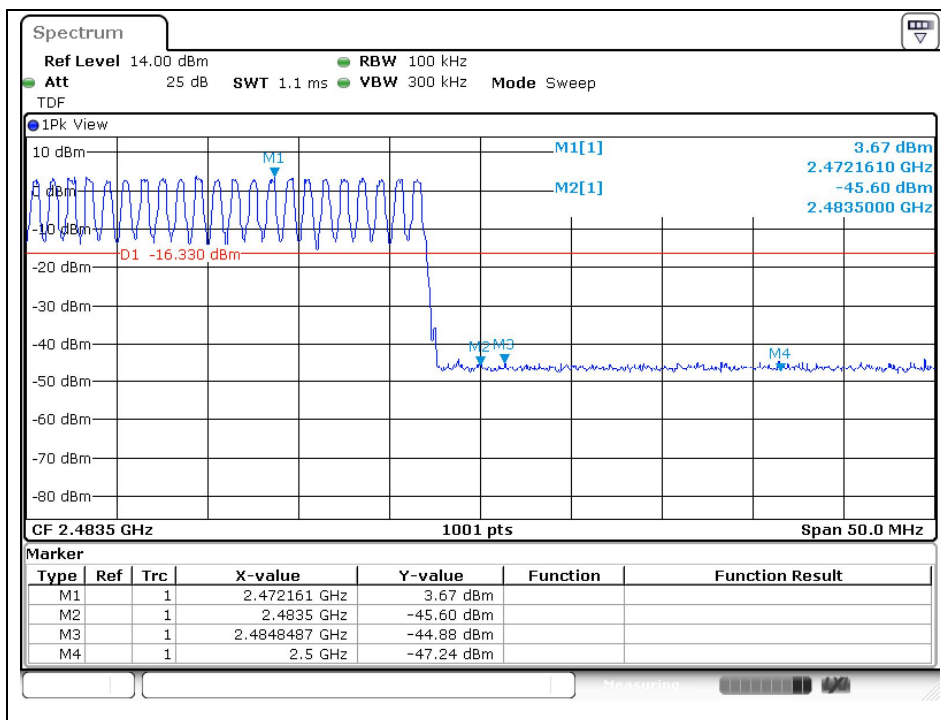
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Band edge compliance with hopping enabled

Low channel



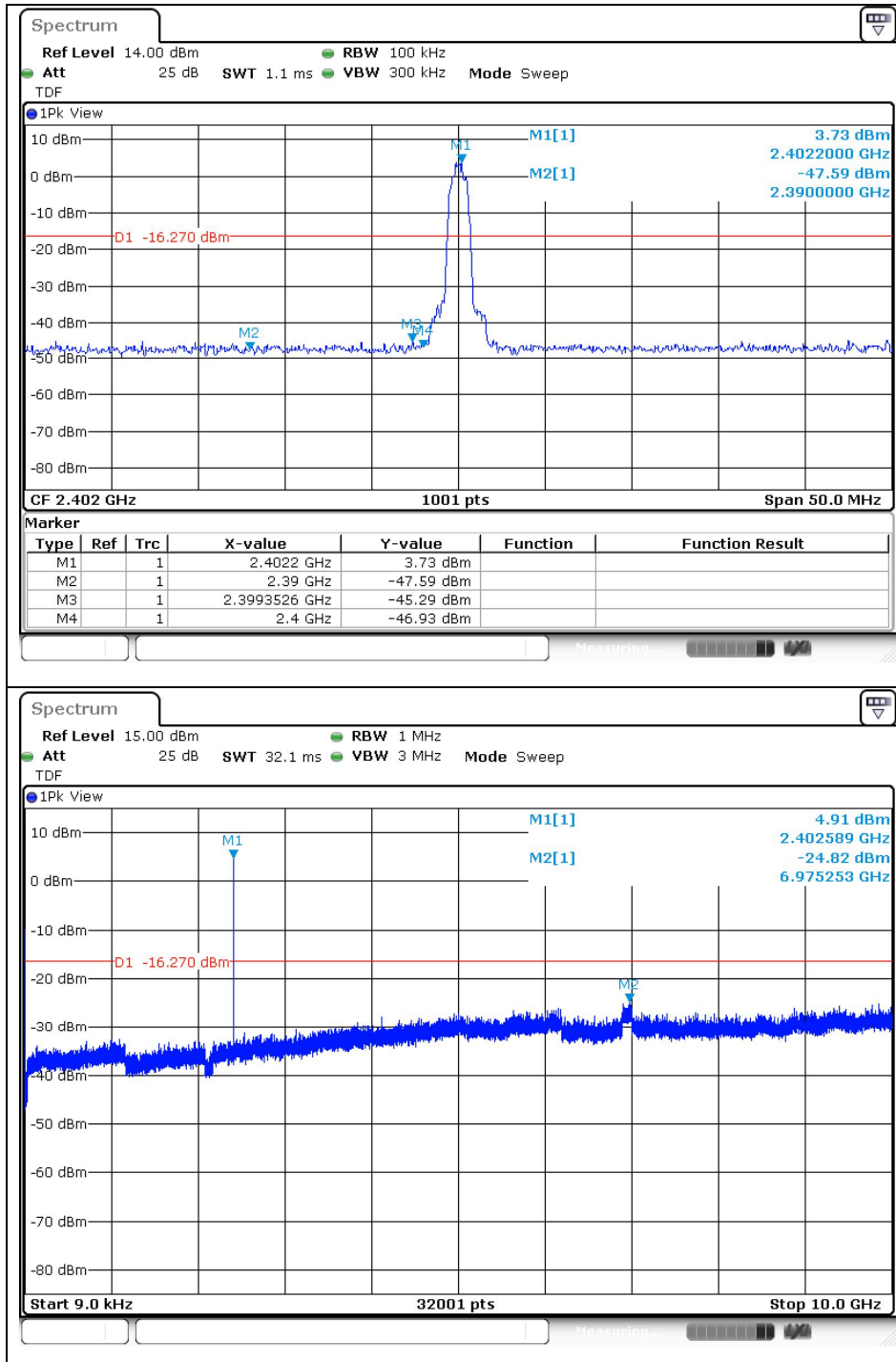
High channel



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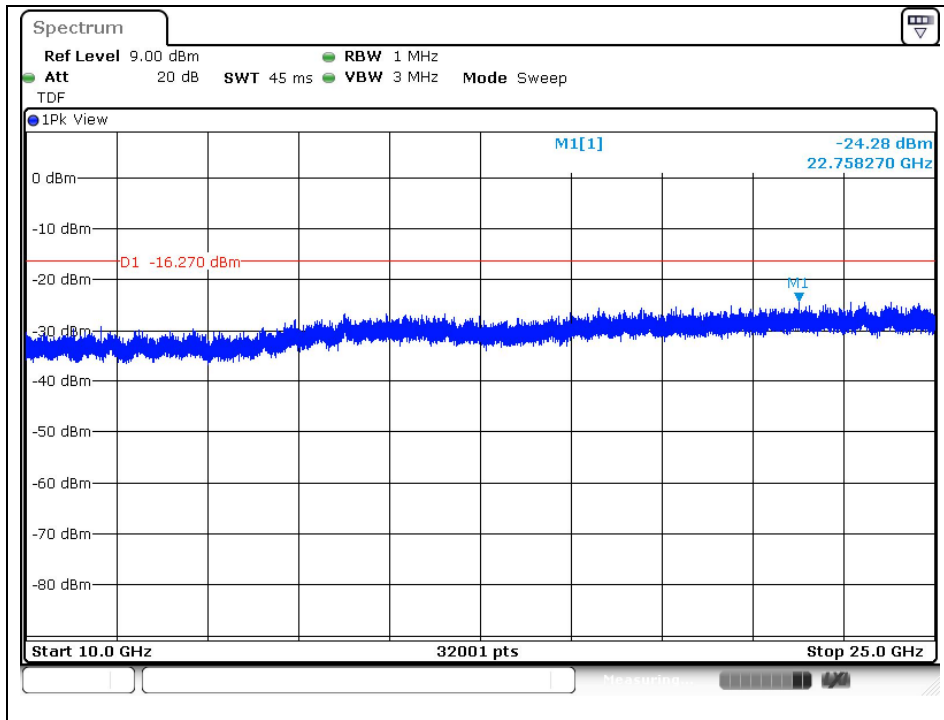
## Operating Mode: 8DPSK (3 Mbps)

Low channel



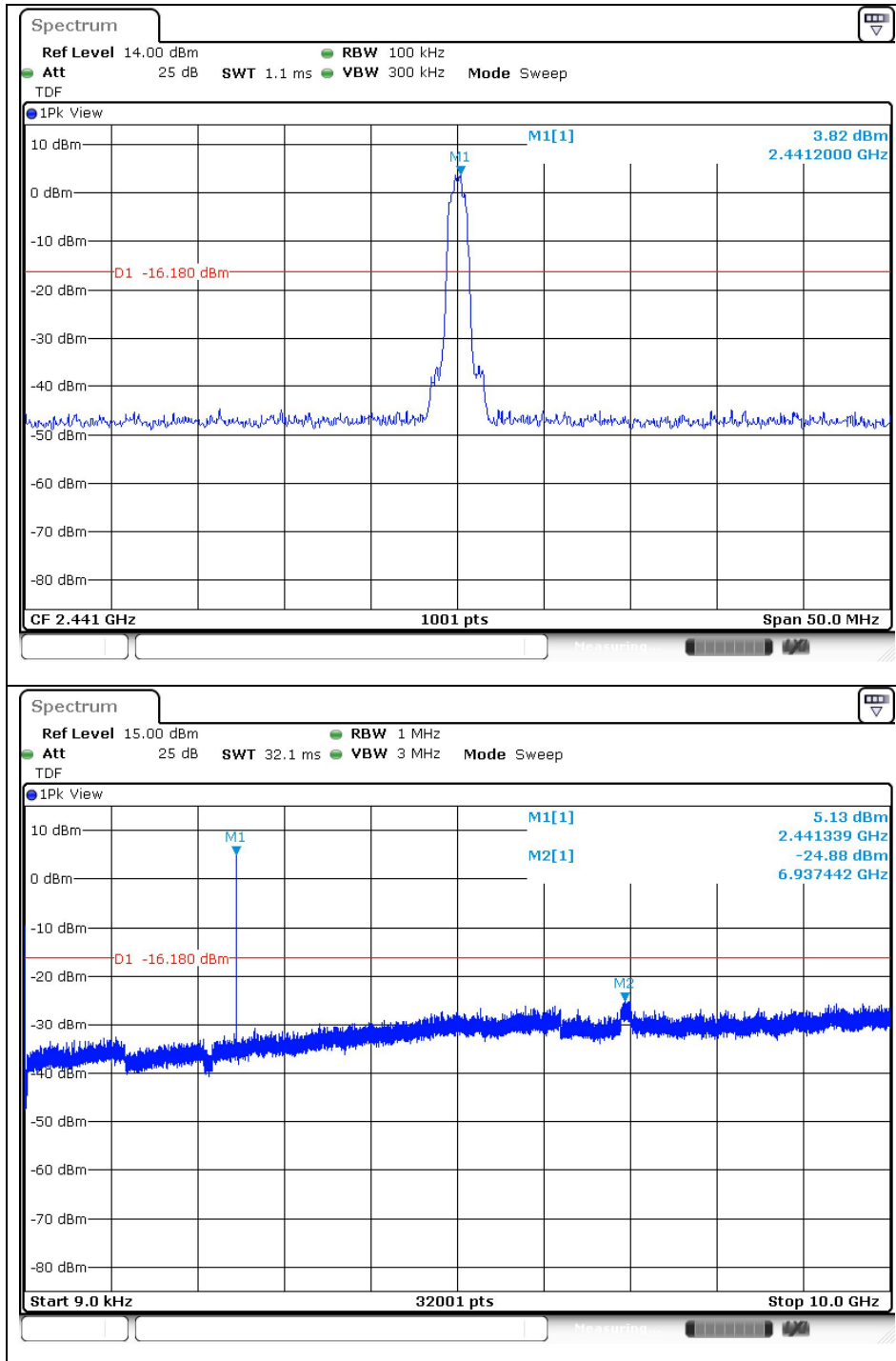
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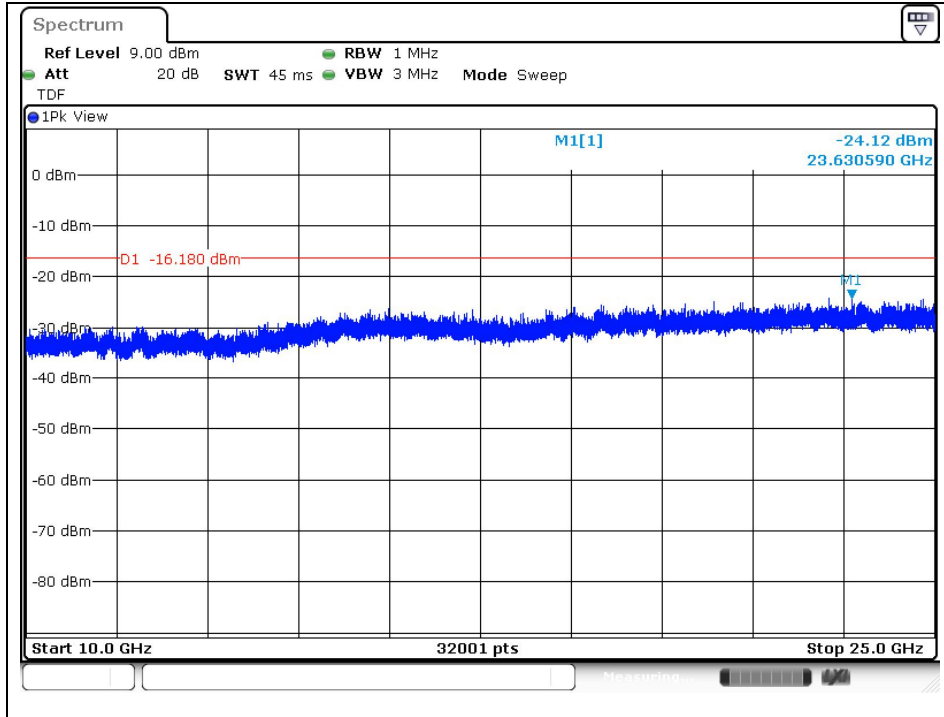


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Middle channel

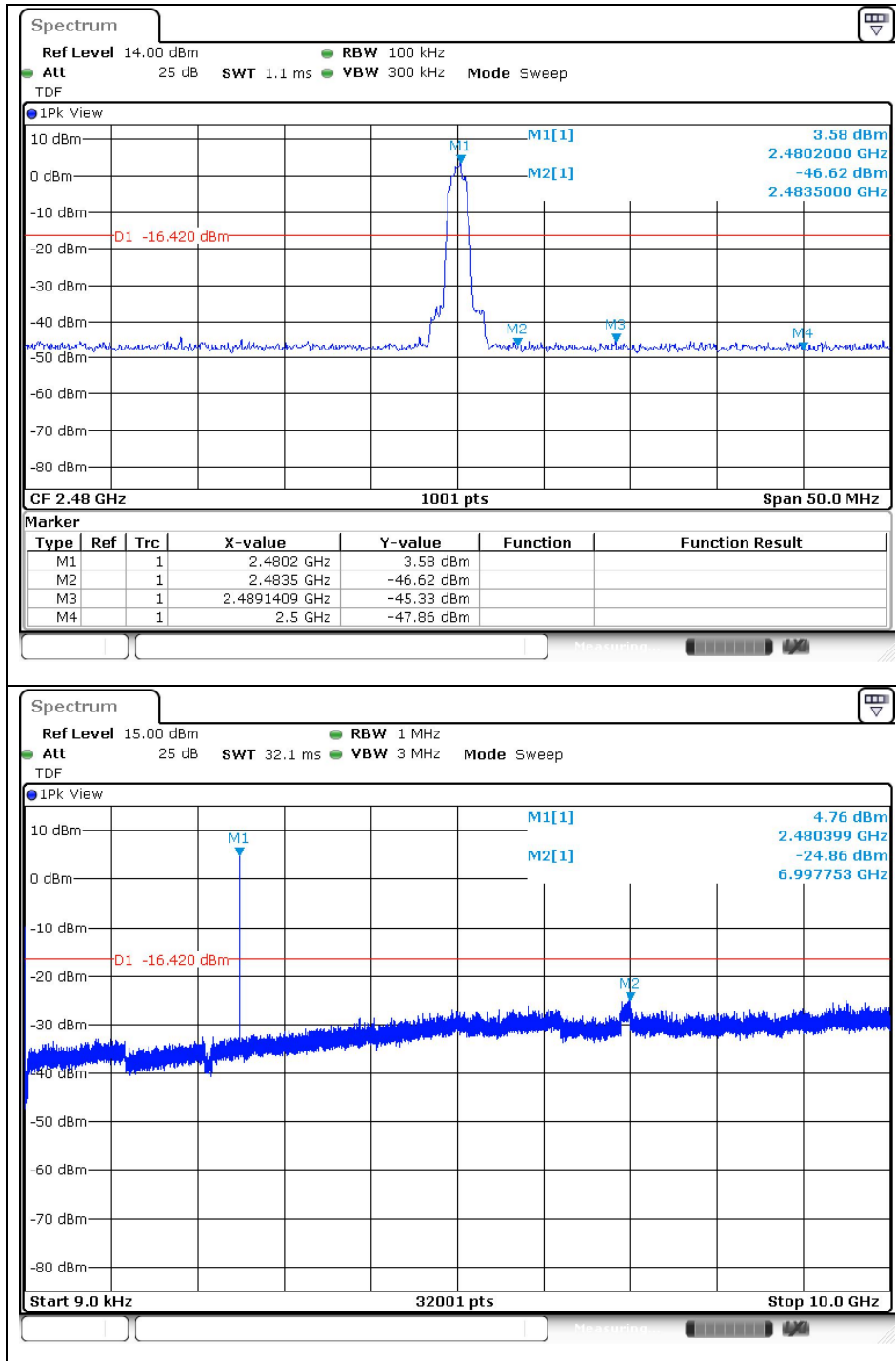


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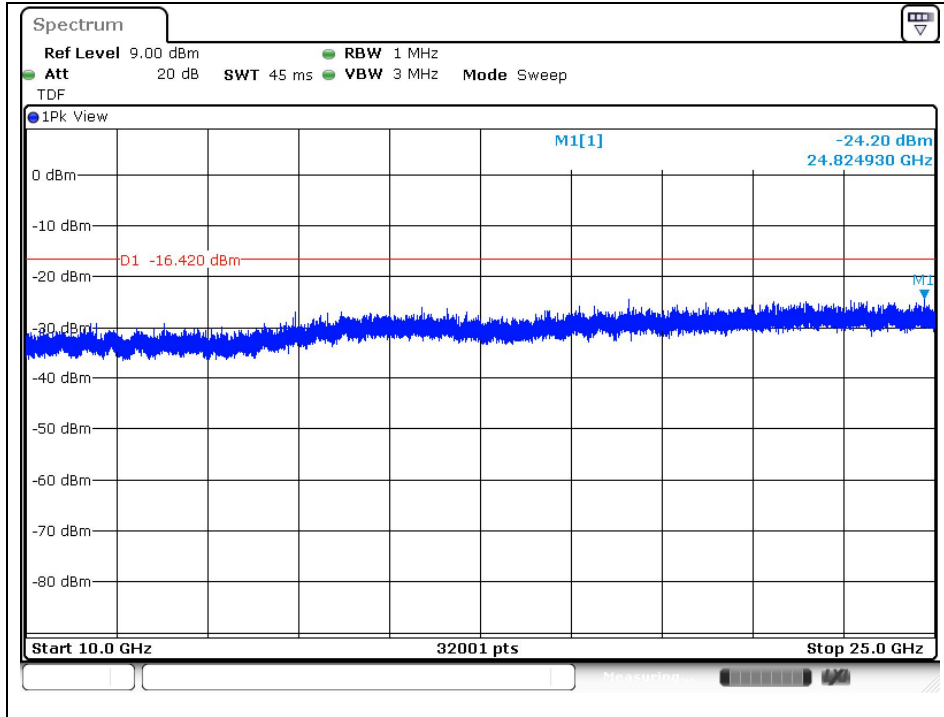


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High channel



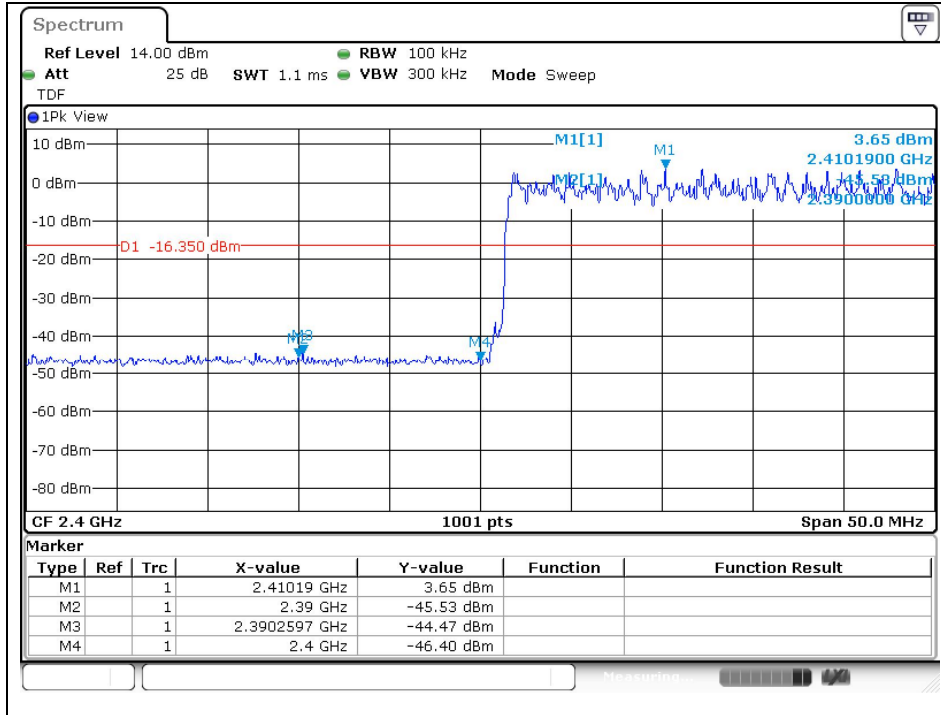
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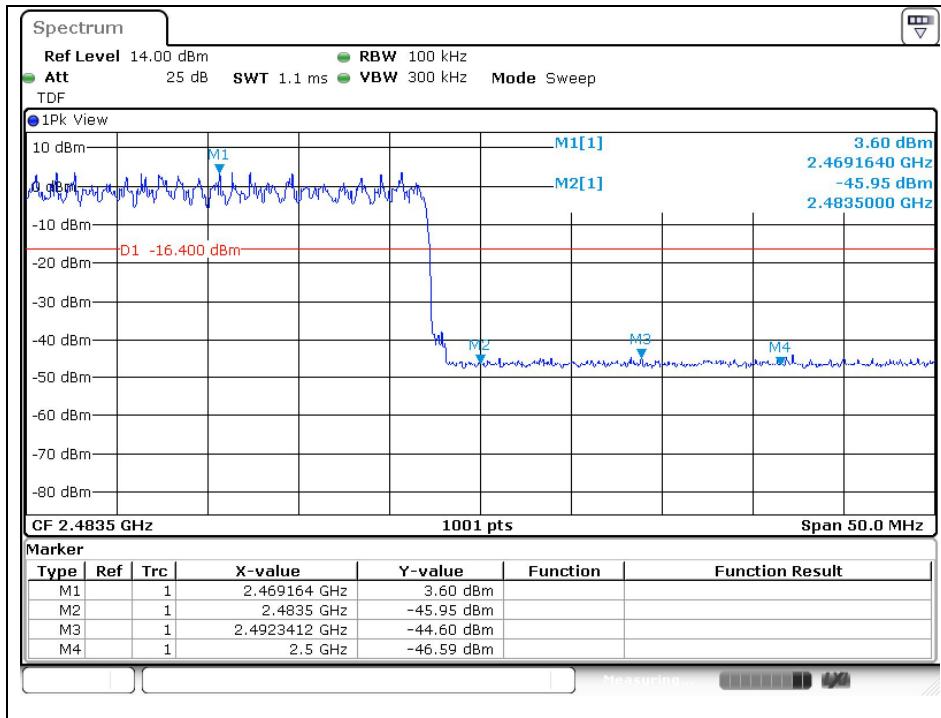
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Band edge compliance with hopping enabled

Low channel



High channel

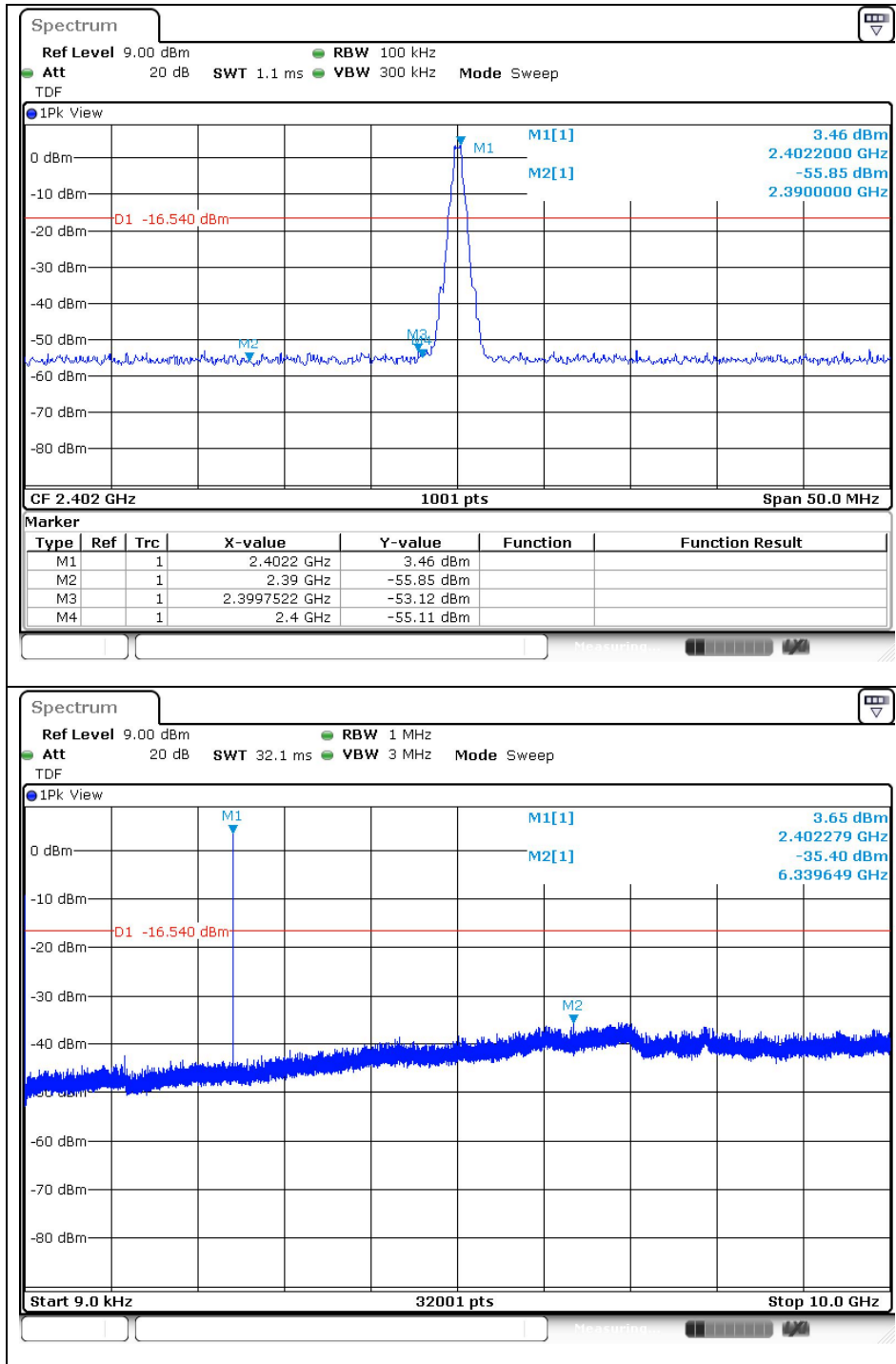


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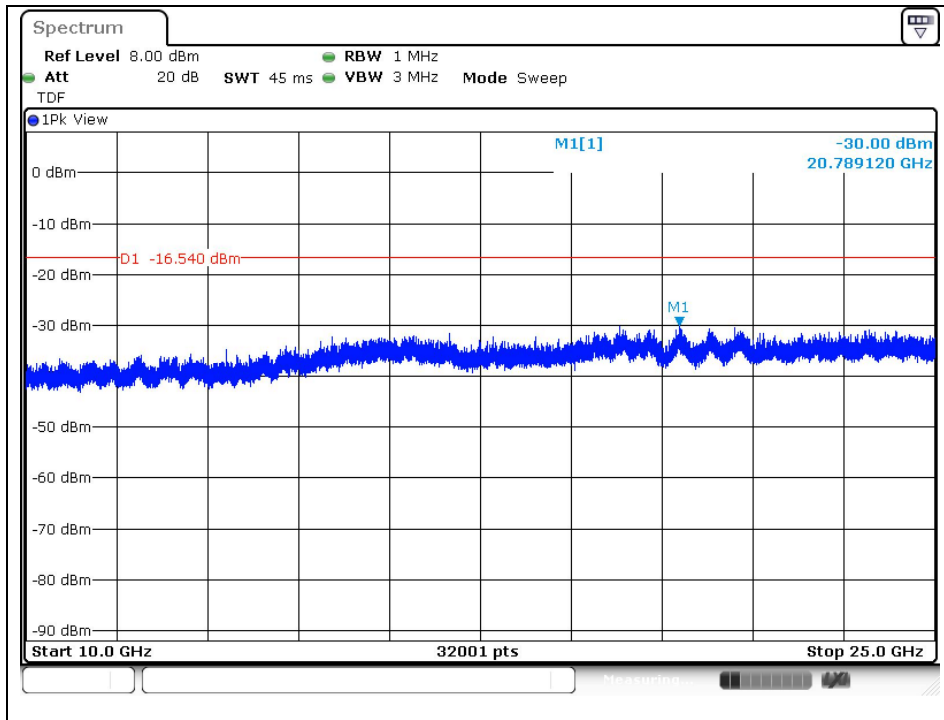
Test voltage: DC 24 V

Operating Mode: GFSK (1 Mbps)

Low channel



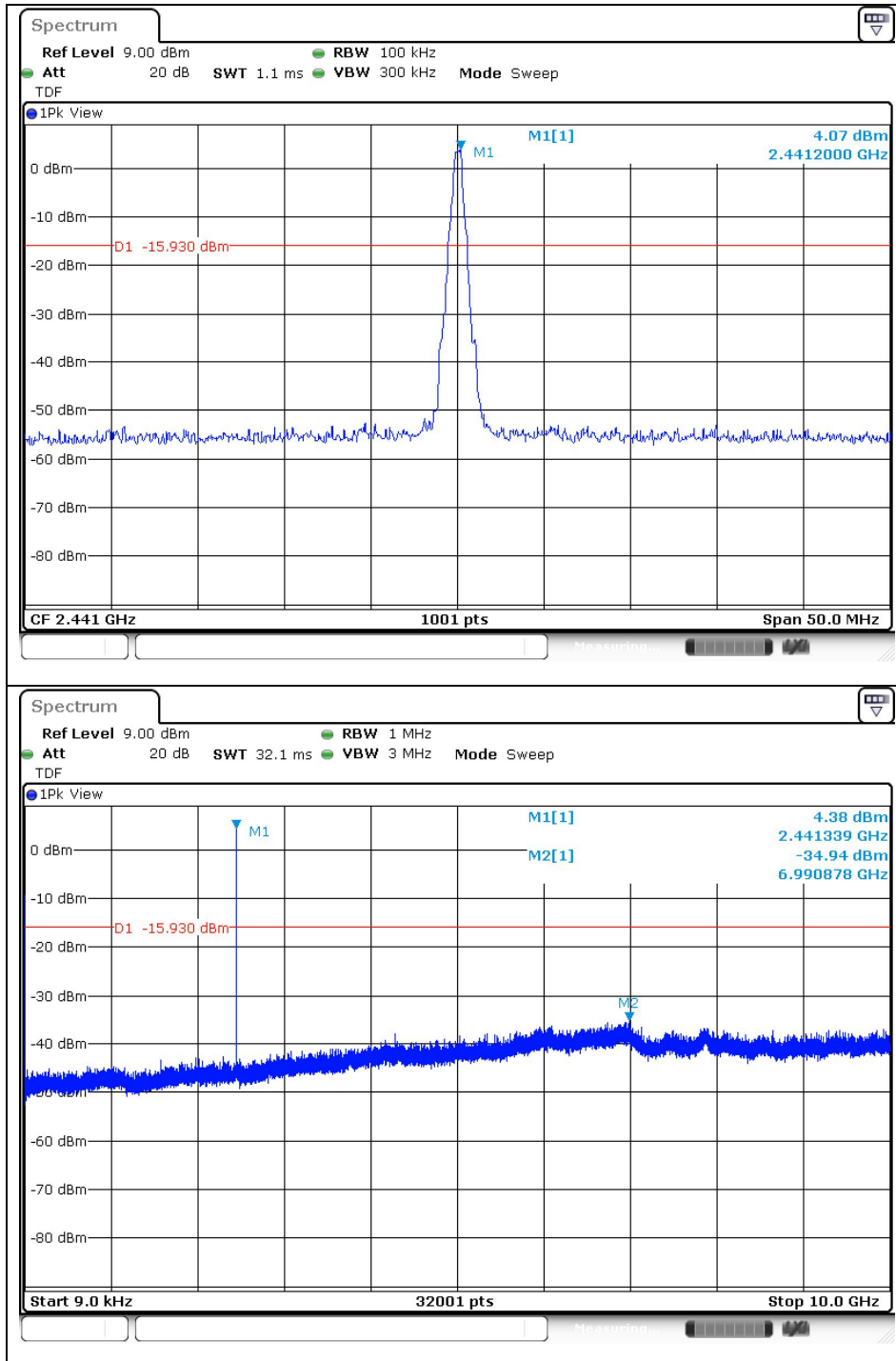
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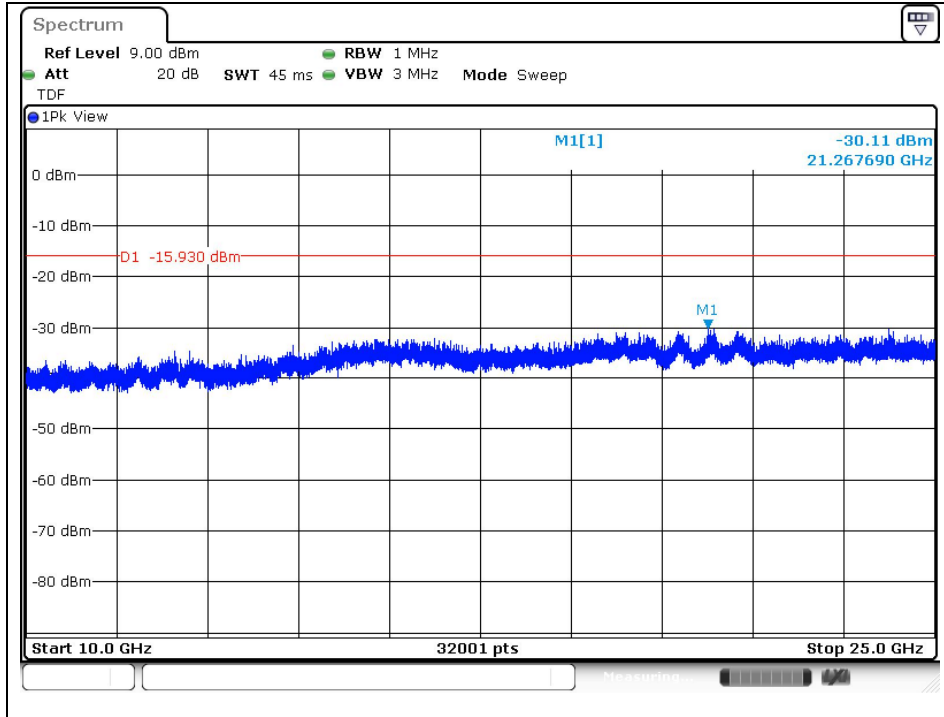
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Middle channel

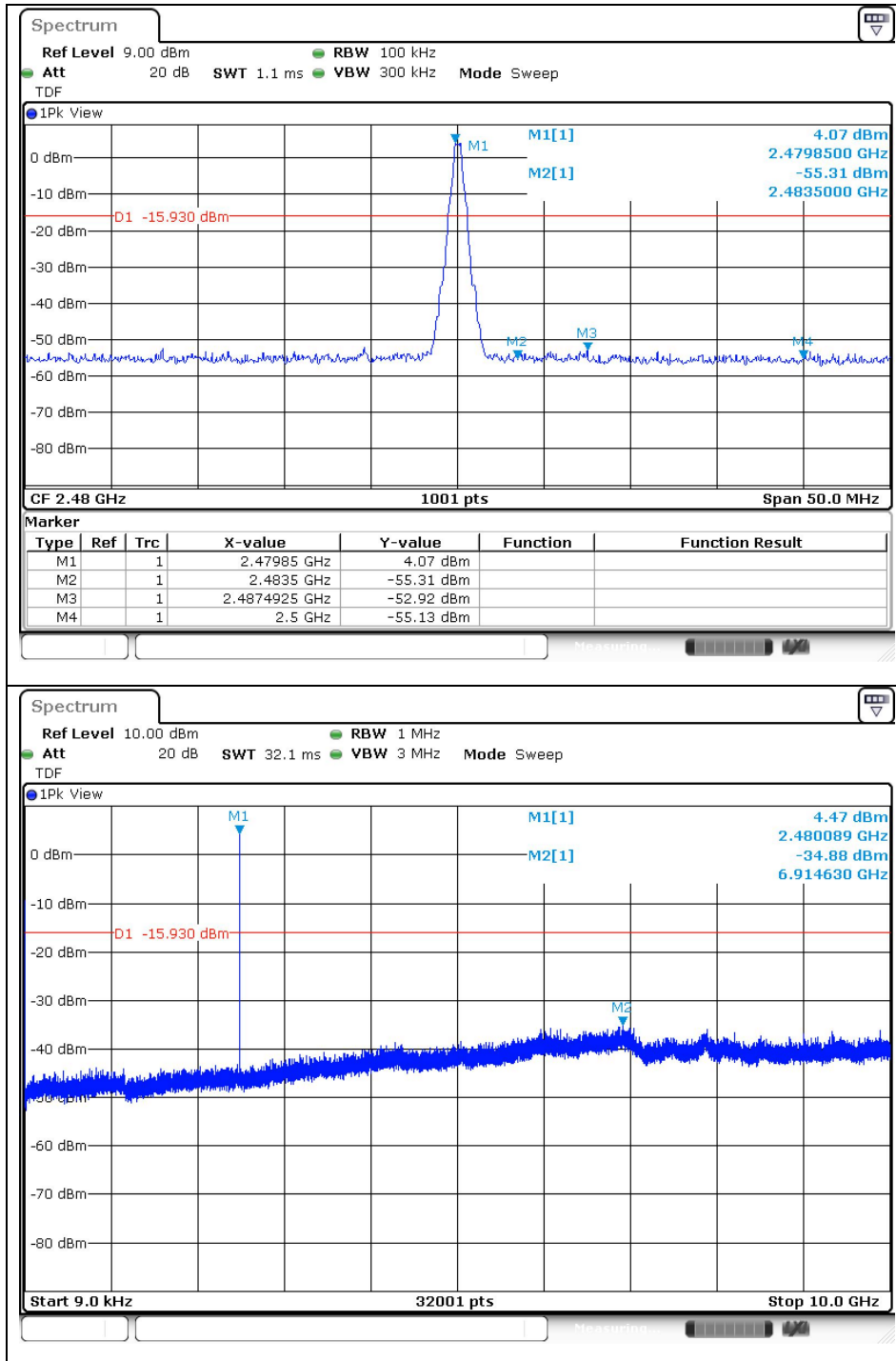


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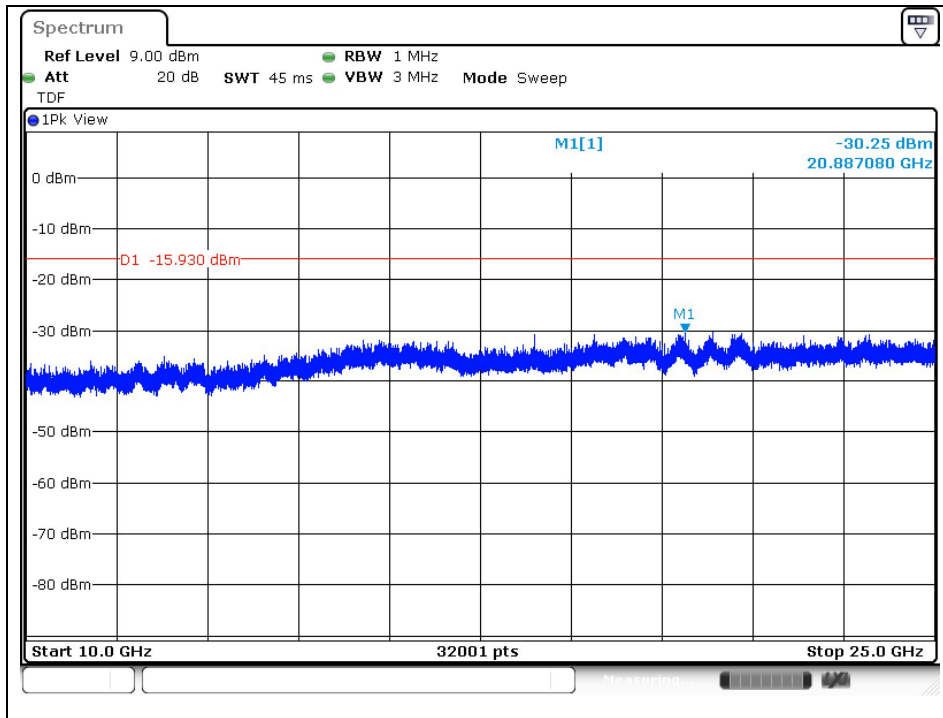


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High channel



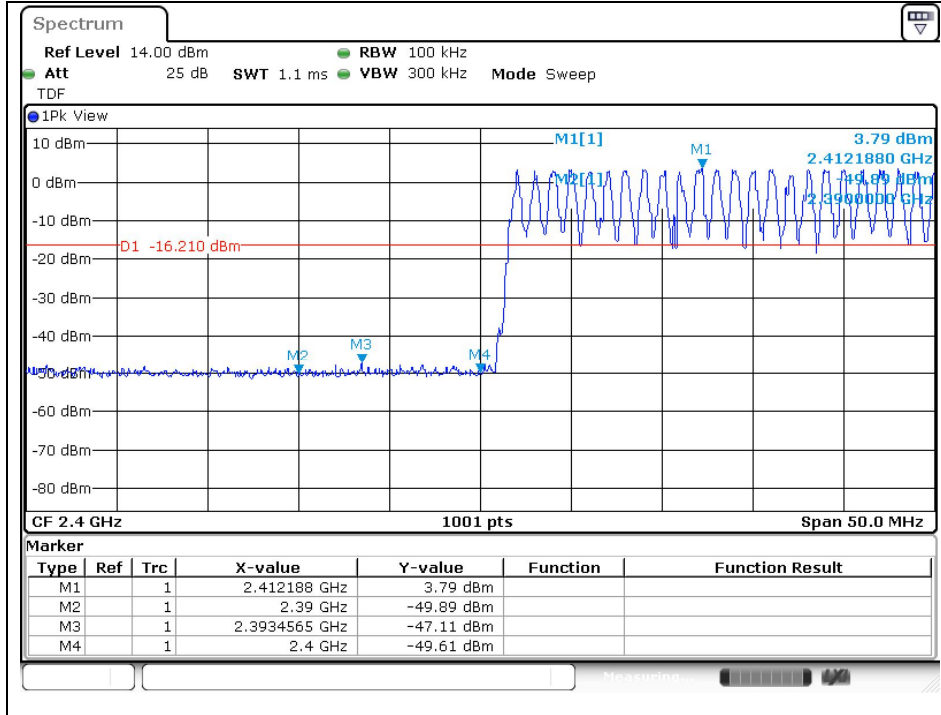
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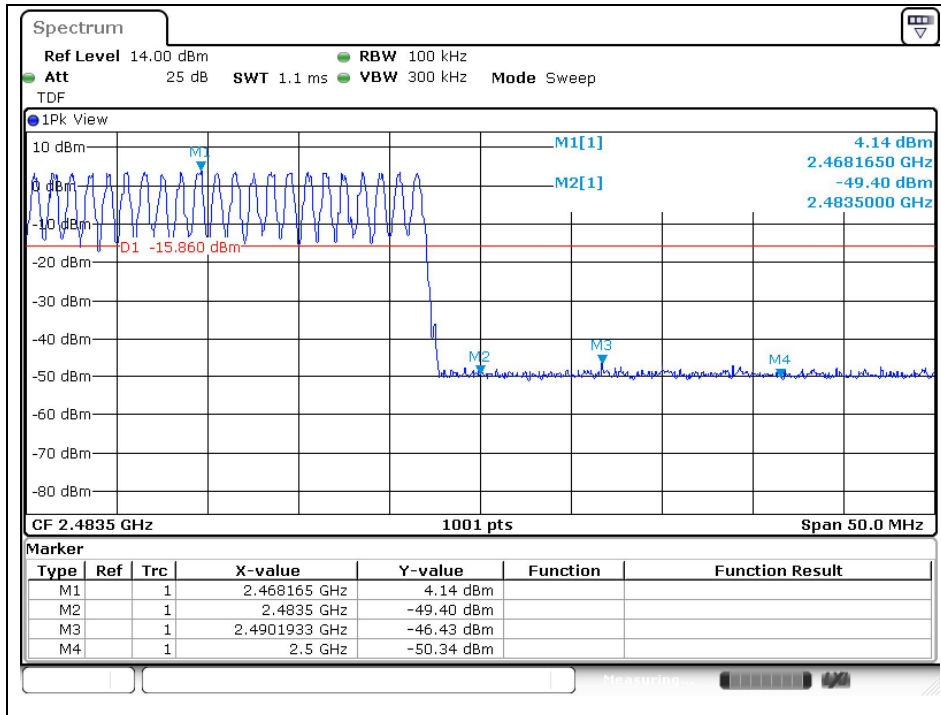
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Band edge compliance with hopping enabled

Low channel



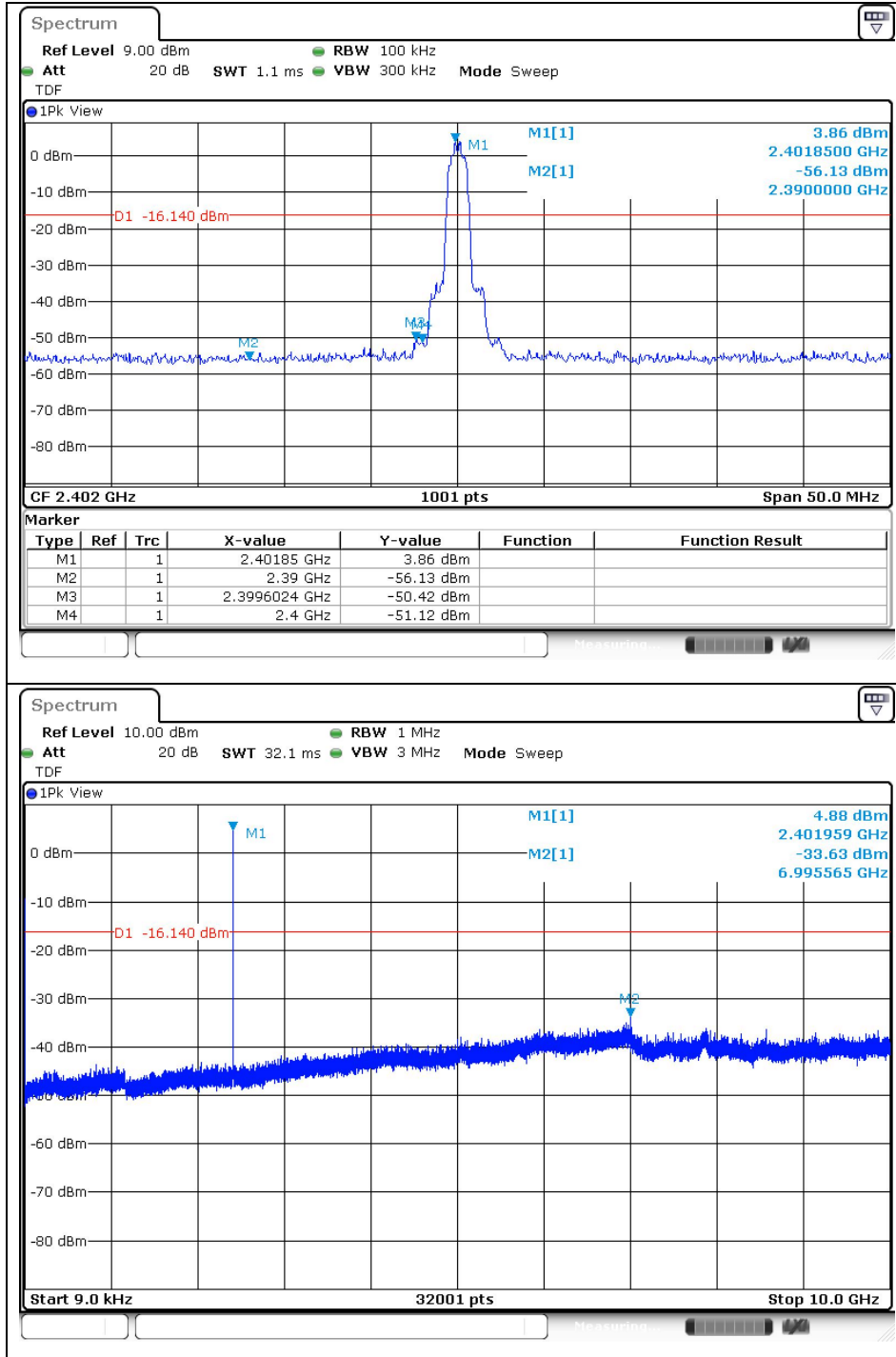
High channel



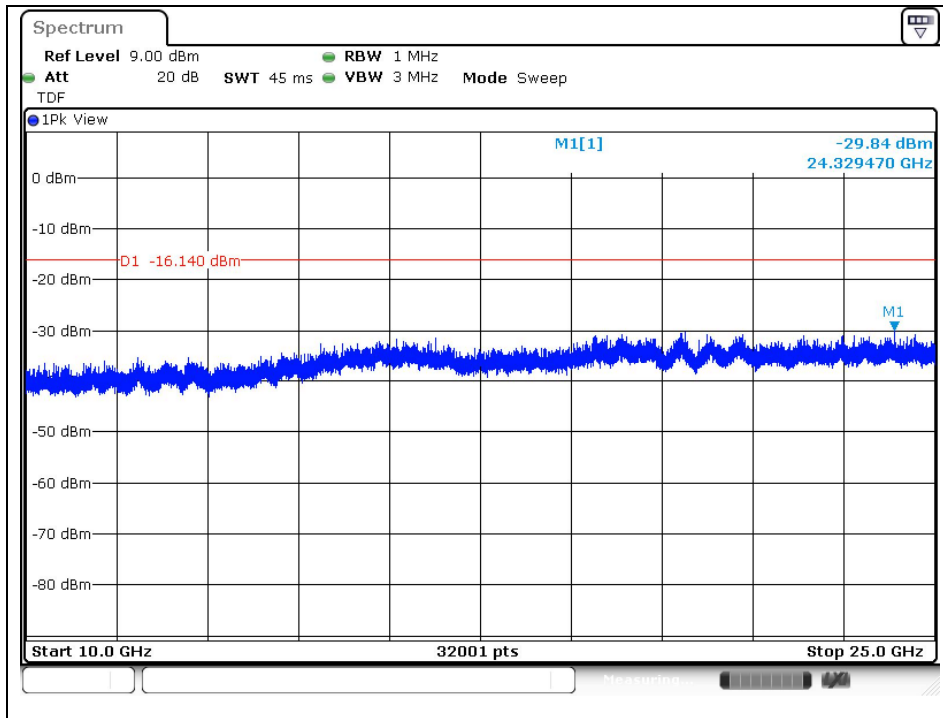
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## Operating Mode: 8DPSK (3 Mbps)

Low channel

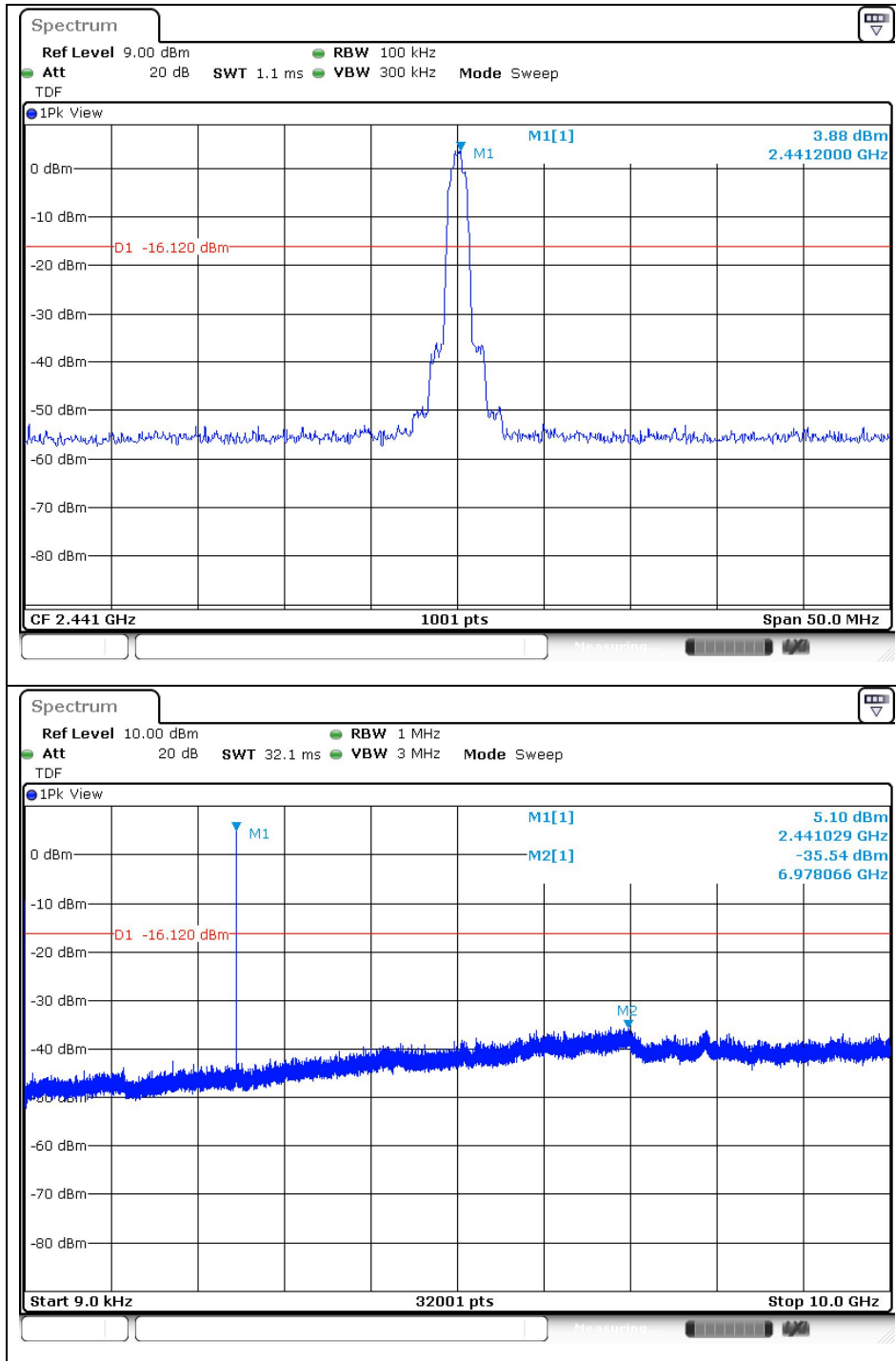


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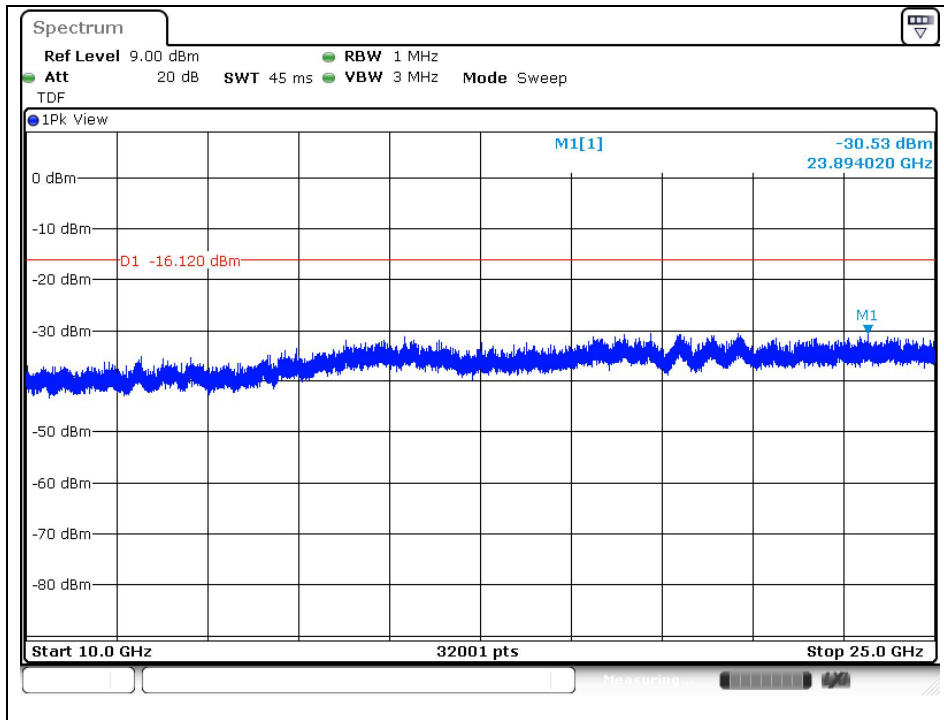
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Middle channel



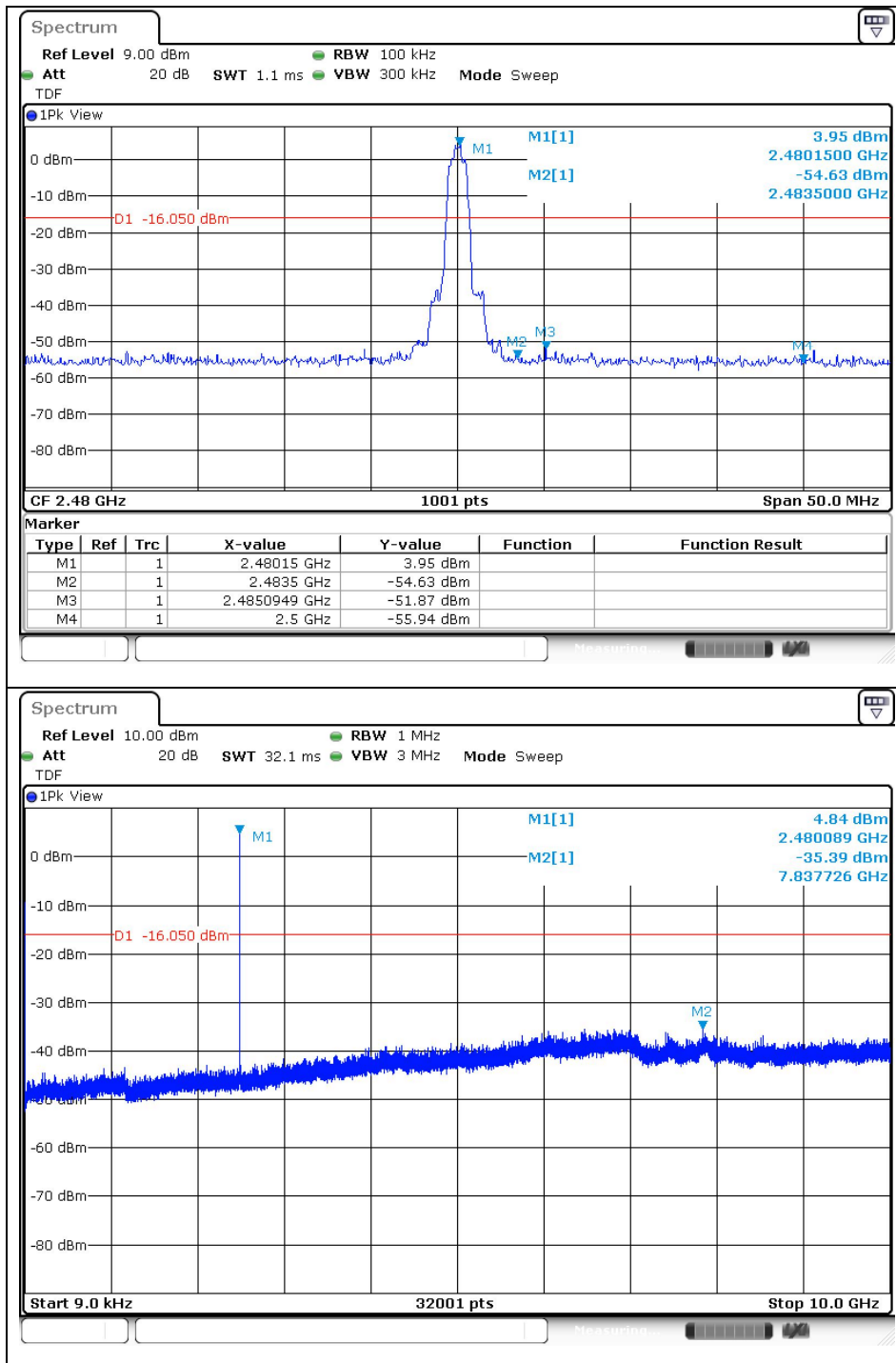
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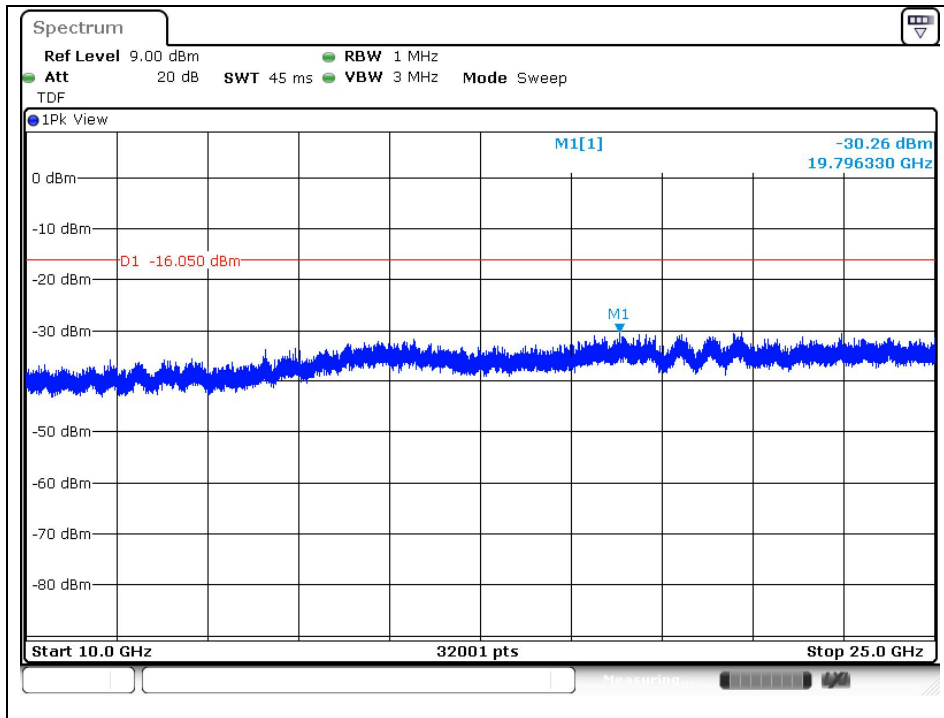


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High channel



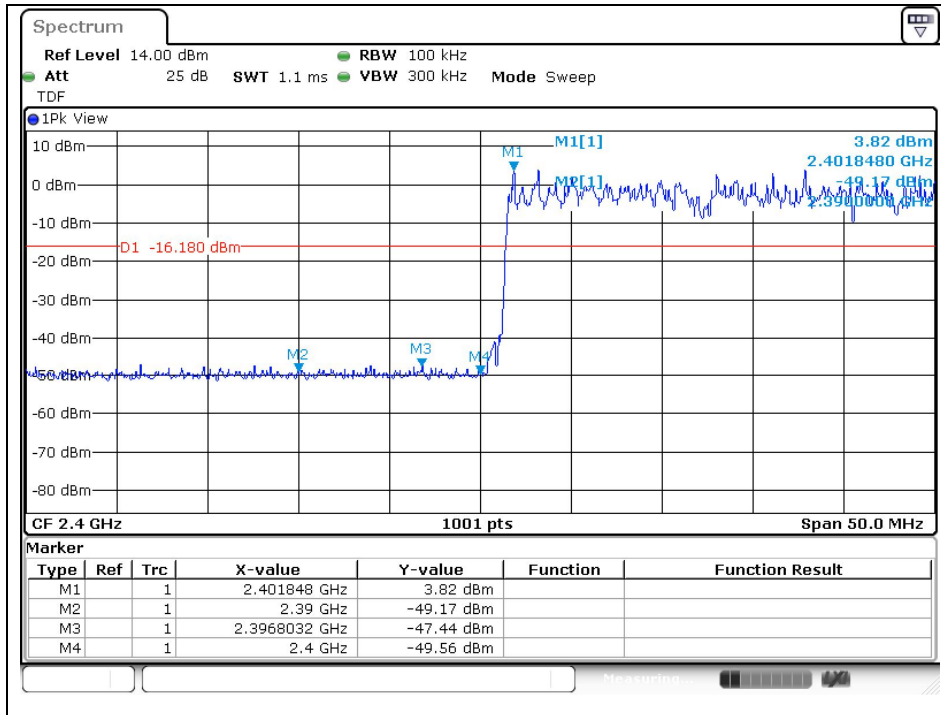
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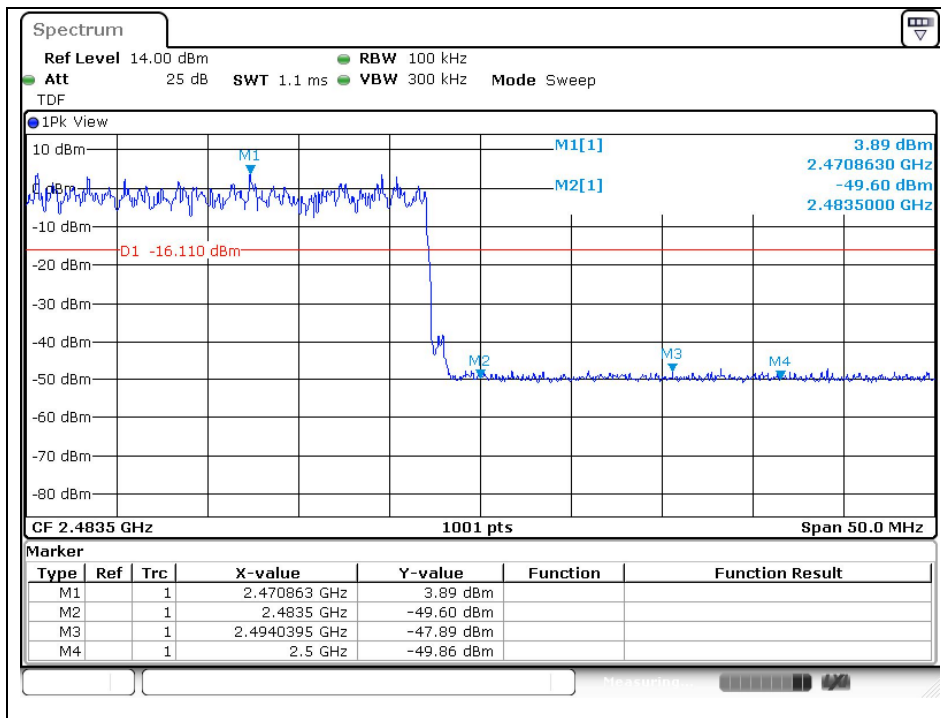
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## Band edge compliance with hopping enabled

### Low channel



### High channel



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