

Appendix B1: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	10.18	<=24	PASS
	Ant2	5180	10.50	<=24	PASS
	Ant1	5200	10.19	<=24	PASS
	Ant2	5200	10.53	<=24	PASS
	Ant1	5240	9.98	<=24	PASS
	Ant2	5240	10.60	<=24	PASS
	Ant1	5260	10.12	<=24	PASS
	Ant2	5260	9.78	<=24	PASS
	Ant1	5280	10.13	<=24	PASS
	Ant2	5280	9.77	<=24	PASS
	Ant1	5320	10.21	<=24	PASS
	Ant2	5320	9.76	<=24	PASS
	Ant1	5500	10.07	<=24	PASS
	Ant2	5500	10.24	<=24	PASS
	Ant1	5580	9.95	<=24	PASS
	Ant2	5580	9.71	<=24	PASS
	Ant1	5700	10.65	<=24	PASS
	Ant2	5700	10.15	<=24	PASS
	Ant1	5745	10.54	<=30	PASS
	Ant2	5745	10.28	<=30	PASS
	Ant1	5785	10.06	<=30	PASS
	Ant2	5785	9.94	<=30	PASS
	Ant1	5825	10.03	<=30	PASS
	Ant2	5825	9.87	<=30	PASS
11N20SISO	Ant1	5180	9.07	<=24	PASS
	Ant2	5180	8.88	<=24	PASS
	Ant1	5200	9.08	<=24	PASS
	Ant2	5200	8.94	<=24	PASS
	Ant1	5240	9.00	<=24	PASS
	Ant2	5240	8.81	<=24	PASS
	Ant1	5260	8.95	<=24	PASS
	Ant2	5260	9.09	<=24	PASS
	Ant1	5280	9.07	<=24	PASS
	Ant2	5280	8.99	<=24	PASS
	Ant1	5320	9.03	<=24	PASS
	Ant2	5320	9.08	<=24	PASS
	Ant1	5500	8.87	<=24	PASS
	Ant2	5500	9.44	<=24	PASS
	Ant1	5580	8.96	<=24	PASS
	Ant2	5580	8.81	<=24	PASS

	Ant1	5700	9.27	<=24	PASS
	Ant2	5700	9.47	<=24	PASS
	Ant1	5745	9.35	<=30	PASS
	Ant2	5745	9.30	<=30	PASS
	Ant1	5785	9.00	<=30	PASS
	Ant2	5785	8.91	<=30	PASS
	Ant1	5825	8.55	<=30	PASS
	Ant2	5825	8.83	<=30	PASS
11N40SISO	Ant1	5190	8.11	<=24	PASS
	Ant2	5190	7.86	<=24	PASS
	Ant1	5230	7.97	<=24	PASS
	Ant2	5230	7.93	<=24	PASS
	Ant1	5270	8.12	<=24	PASS
	Ant2	5270	7.98	<=24	PASS
	Ant1	5310	8.11	<=24	PASS
	Ant2	5310	8.03	<=24	PASS
	Ant1	5510	7.86	<=24	PASS
	Ant2	5510	8.29	<=24	PASS
	Ant1	5550	7.81	<=24	PASS
	Ant2	5550	8.10	<=24	PASS
	Ant1	5670	8.54	<=24	PASS
	Ant2	5670	8.48	<=24	PASS
	Ant1	5755	8.22	<=30	PASS
	Ant2	5755	8.32	<=30	PASS
	Ant1	5795	7.66	<=30	PASS
	Ant2	5795	7.84	<=30	PASS
11AC20SISO	Ant1	5180	8.54	<=24	PASS
	Ant2	5180	7.86	<=24	PASS
	Ant1	5200	8.49	<=24	PASS
	Ant2	5200	7.93	<=24	PASS
	Ant1	5240	7.97	<=24	PASS
	Ant2	5240	7.93	<=24	PASS
	Ant1	5260	8.30	<=24	PASS
	Ant2	5260	8.18	<=24	PASS
	Ant1	5280	8.36	<=24	PASS
	Ant2	5280	8.21	<=24	PASS
	Ant1	5320	8.56	<=24	PASS
	Ant2	5320	8.15	<=24	PASS
	Ant1	5500	8.26	<=24	PASS
	Ant2	5500	8.53	<=24	PASS
	Ant1	5580	7.84	<=24	PASS
	Ant2	5580	7.68	<=24	PASS
	Ant1	5700	7.94	<=24	PASS
	Ant2	5700	8.24	<=24	PASS
Ant1	5745	8.27	<=30	PASS	

	Ant2	5745	8.07	<=30	PASS
	Ant1	5785	7.70	<=30	PASS
	Ant2	5785	7.84	<=30	PASS
	Ant1	5825	7.67	<=30	PASS
	Ant2	5825	8.00	<=30	PASS
11AC40SISO	Ant1	5190	7.82	<=24	PASS
	Ant2	5190	8.31	<=24	PASS
	Ant1	5230	8.19	<=24	PASS
	Ant2	5230	8.33	<=24	PASS
	Ant1	5270	8.20	<=24	PASS
	Ant2	5270	7.84	<=24	PASS
	Ant1	5310	8.36	<=24	PASS
	Ant2	5310	7.87	<=24	PASS
	Ant1	5510	8.54	<=24	PASS
	Ant2	5510	8.64	<=24	PASS
	Ant1	5550	7.86	<=24	PASS
	Ant2	5550	7.98	<=24	PASS
	Ant1	5670	8.40	<=24	PASS
	Ant2	5670	8.34	<=24	PASS
	Ant1	5755	7.99	<=30	PASS
	Ant2	5755	8.34	<=30	PASS
	Ant1	5795	9.97	<=30	PASS
	Ant2	5795	8.05	<=30	PASS
	11AC80SISO	Ant1	5210	7.95	<=24
Ant2		5210	8.07	<=24	PASS
Ant1		5290	8.07	<=24	PASS
Ant2		5290	7.78	<=24	PASS
Ant1		5530	7.91	<=24	PASS
Ant2		5530	8.09	<=24	PASS
Ant1		5610	8.23	<=24	PASS
Ant2		5610	8.09	<=24	PASS
Ant1		5775	8.38	<=30	PASS
Ant2		5775	8.31	<=30	PASS
11N20MIMO	Ant1	5180	9.09	<=24	PASS
	Ant2	5180	8.21	<=24	PASS
	total	5180	11.7	<=24	PASS
	Ant1	5200	9.47	<=24	PASS
	Ant2	5200	8.66	<=24	PASS
	total	5200	12.1	<=24	PASS
	Ant1	5240	9.59	<=24	PASS
	Ant2	5240	8.83	<=24	PASS
	total	5240	12.2	<=24	PASS
	Ant1	5260	8.86	<=24	PASS
	Ant2	5260	8.61	<=24	PASS
	total	5260	11.7	<=24	PASS

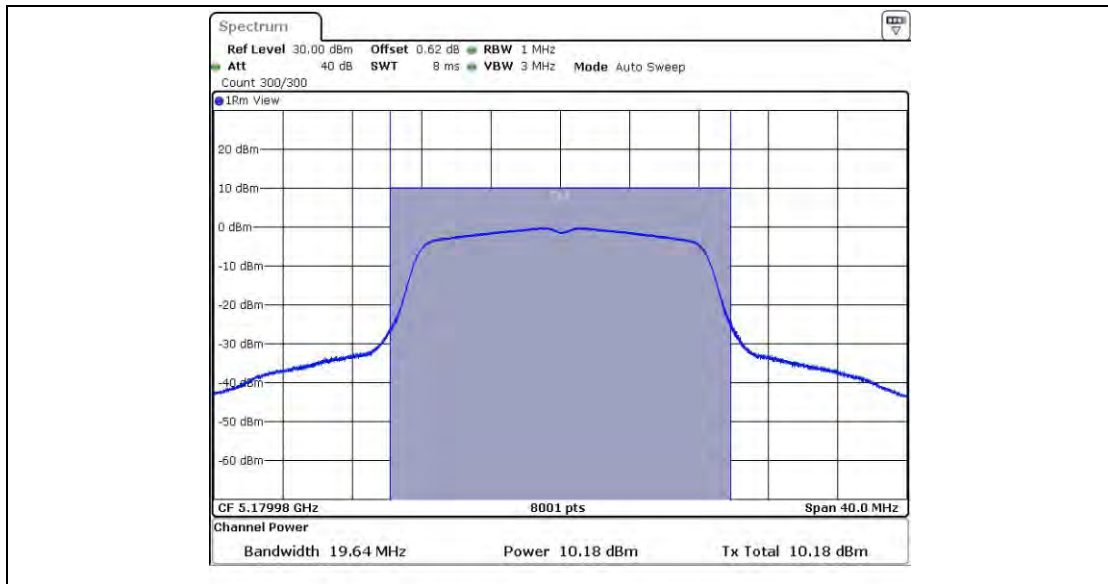
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	Ant2	5320	8.58	<=24	PASS
	total	5320	12.0	<=24	PASS
	Ant1	5500	8.81	<=24	PASS
	Ant2	5500	9.95	<=24	PASS
	total	5500	12.4	<=24	PASS
	Ant1	5580	9.11	<=24	PASS
	Ant2	5580	8.87	<=24	PASS
	total	5580	12.0	<=24	PASS
	Ant1	5700	9.40	<=24	PASS
	Ant2	5700	8.49	<=24	PASS
	total	5700	12.0	<=24	PASS
	Ant1	5745	9.40	<=30	PASS
	Ant2	5745	8.40	<=30	PASS
	total	5745	11.9	<=30	PASS
	Ant1	5785	8.94	<=30	PASS
	Ant2	5785	8.85	<=30	PASS
	total	5785	11.9	<=30	PASS
	Ant1	5825	9.28	<=30	PASS
	Ant2	5825	8.83	<=30	PASS
	total	5825	12.1	<=30	PASS
11N40MIMO	Ant1	5190	7.78	<=24	PASS
	Ant2	5190	8.86	<=24	PASS
	total	5190	11.4	<=24	PASS
	Ant1	5230	8.71	<=24	PASS
	Ant2	5230	7.60	<=24	PASS
	total	5230	11.2	<=24	PASS
	Ant1	5270	8.18	<=24	PASS
	Ant2	5270	8.75	<=24	PASS
	total	5270	11.5	<=24	PASS
	Ant1	5310	8.77	<=24	PASS
	Ant2	5310	7.83	<=24	PASS
	total	5310	11.3	<=24	PASS
	Ant1	5510	8.37	<=24	PASS
	Ant2	5510	8.40	<=24	PASS
	total	5510	11.4	<=24	PASS
	Ant1	5550	7.99	<=24	PASS
	Ant2	5550	7.67	<=24	PASS
	total	5550	10.8	<=24	PASS
	Ant1	5670	8.35	<=24	PASS
	Ant2	5670	8.37	<=24	PASS
total	5670	11.4	<=24	PASS	

	Ant1	5755	8.69	<=30	PASS
	Ant2	5755	7.19	<=30	PASS
	total	5755	11.0	<=30	PASS
	Ant1	5795	8.21	<=30	PASS
	Ant2	5795	8.47	<=30	PASS
	total	5795	11.4	<=30	PASS
11AC20MIMO	Ant1	5180	6.46	<=24	PASS
	Ant2	5180	7.89	<=24	PASS
	total	5180	10.2	<=24	PASS
	Ant1	5200	7.77	<=24	PASS
	Ant2	5200	6.34	<=24	PASS
	total	5200	10.1	<=24	PASS
	Ant1	5240	6.38	<=24	PASS
	Ant2	5240	7.46	<=24	PASS
	total	5240	10.0	<=24	PASS
	Ant1	5260	7.52	<=24	PASS
	Ant2	5260	6.50	<=24	PASS
	total	5260	10.1	<=24	PASS
	Ant1	5280	6.58	<=24	PASS
	Ant2	5280	7.65	<=24	PASS
	total	5280	10.2	<=24	PASS
	Ant1	5320	7.76	<=24	PASS
	Ant2	5320	6.76	<=24	PASS
	total	5320	10.3	<=24	PASS
	Ant1	5500	7.71	<=24	PASS
	Ant2	5500	7.15	<=24	PASS
	total	5500	10.4	<=24	PASS
	Ant1	5580	6.21	<=24	PASS
	Ant2	5580	7.36	<=24	PASS
	total	5580	9.8	<=24	PASS
	Ant1	5700	8.24	<=24	PASS
	Ant2	5700	6.94	<=24	PASS
	total	5700	10.6	<=24	PASS
	Ant1	5745	6.60	<=30	PASS
	Ant2	5745	7.74	<=30	PASS
	total	5745	10.2	<=30	PASS
	Ant1	5785	7.48	<=30	PASS
	Ant2	5785	6.22	<=30	PASS
	total	5785	9.9	<=30	PASS
Ant1	5825	5.33	<=30	PASS	
Ant2	5825	8.07	<=30	PASS	
total	5825	9.9	<=30	PASS	
11AC40MIMO	Ant1	5190	7.27	<=24	PASS
	Ant2	5190	6.10	<=24	PASS
	total	5190	9.7	<=24	PASS

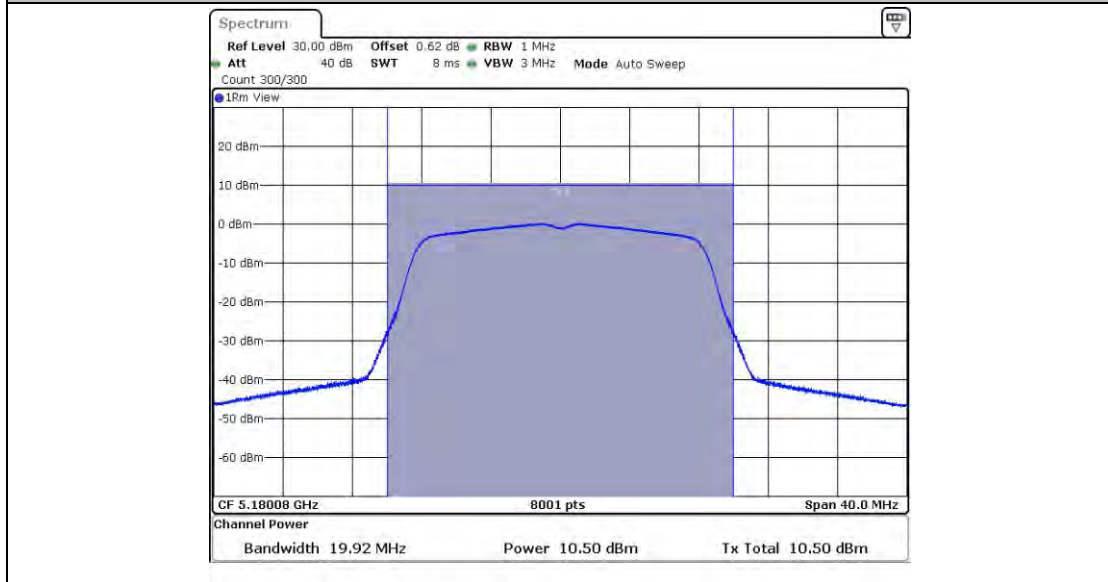
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	total	5230	9.9	<=24	PASS
	Ant1	5270	7.35	<=24	PASS
	Ant2	5270	6.40	<=24	PASS
	total	5270	9.9	<=24	PASS
	Ant1	5310	6.32	<=24	PASS
	Ant2	5310	7.43	<=24	PASS
	total	5310	9.9	<=24	PASS
	Ant1	5510	6.60	<=24	PASS
	Ant2	5510	6.69	<=24	PASS
	total	5510	9.7	<=24	PASS
	Ant1	5550	7.32	<=24	PASS
	Ant2	5550	7.03	<=24	PASS
	total	5550	10.2	<=24	PASS
	Ant1	5670	7.17	<=24	PASS
	Ant2	5670	7.29	<=24	PASS
	total	5670	10.2	<=24	PASS
	Ant1	5755	6.51	<=30	PASS
	Ant2	5755	7.81	<=30	PASS
	total	5755	10.2	<=30	PASS
	Ant1	5795	6.74	<=30	PASS
	Ant2	5795	7.82	<=30	PASS
	total	5795	10.3	<=30	PASS
11AC80MIMO	Ant1	5210	6.40	<=24	PASS
	Ant2	5210	7.70	<=24	PASS
	total	5210	10.1	<=24	PASS
	Ant1	5290	7.64	<=24	PASS
	Ant2	5290	6.42	<=24	PASS
	total	5290	10.1	<=24	PASS
	Ant1	5530	7.09	<=24	PASS
	Ant2	5530	7.07	<=24	PASS
	total	5530	10.1	<=24	PASS
	Ant1	5610	7.23	<=24	PASS
	Ant2	5610	7.22	<=24	PASS
	total	5610	10.2	<=24	PASS
	Ant1	5775	7.03	<=30	PASS
	Ant2	5775	7.19	<=30	PASS
	total	5775	10.1	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.

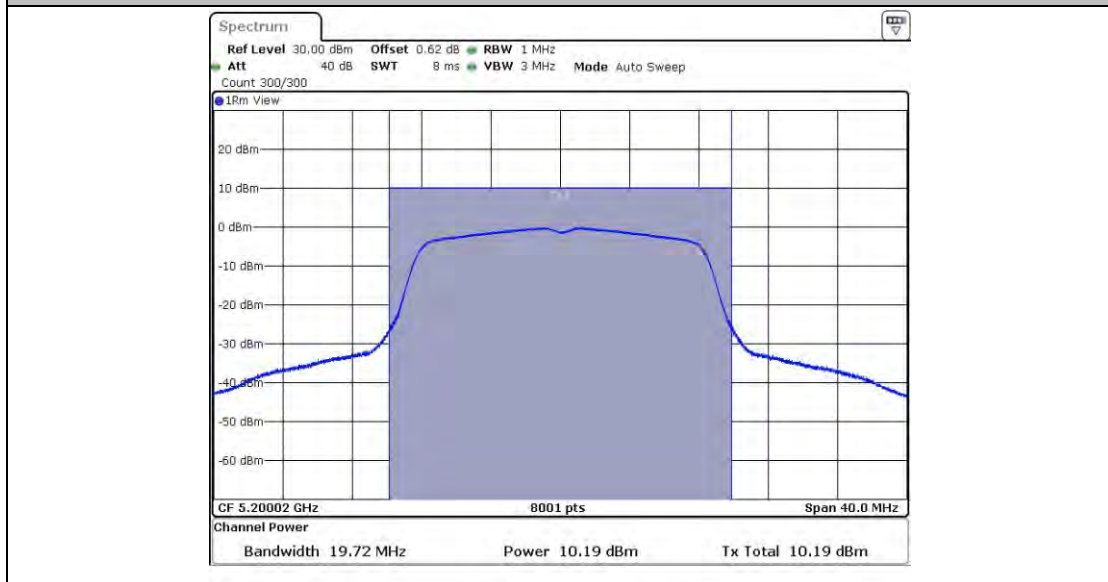
Test Graphs



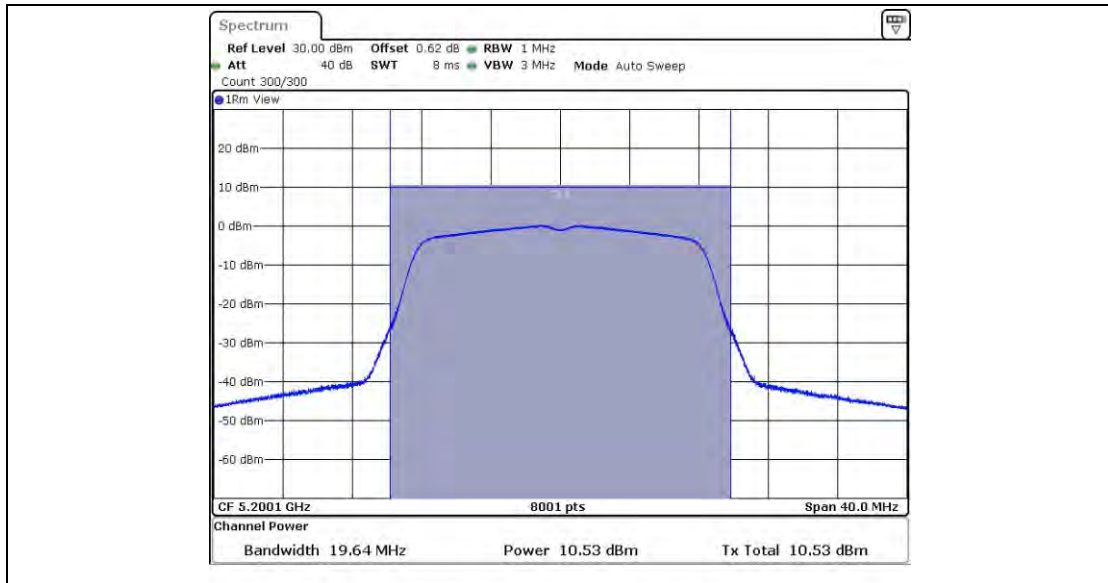
11A_Ant1_5180



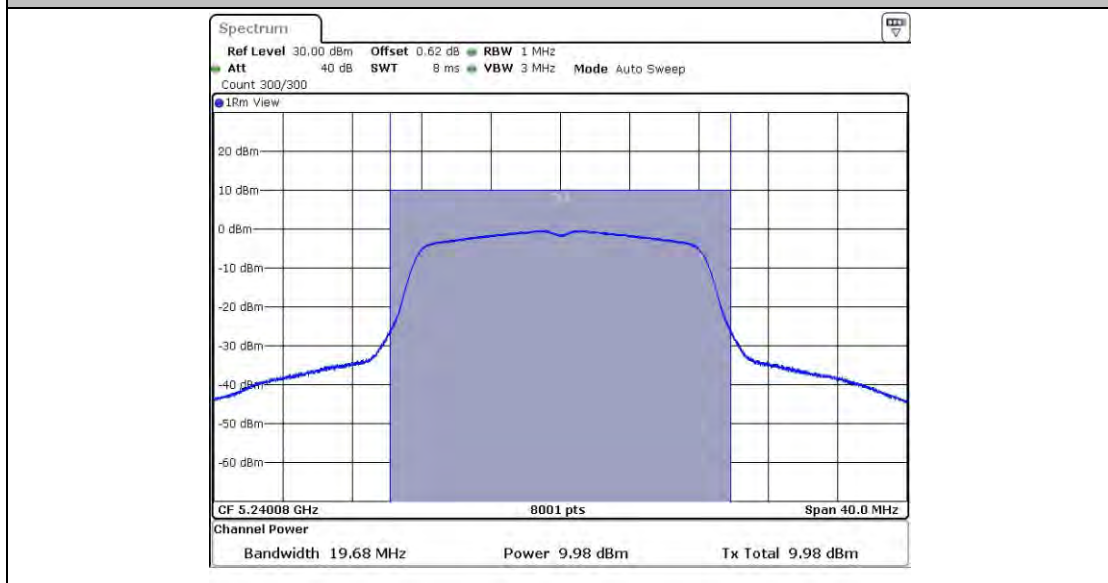
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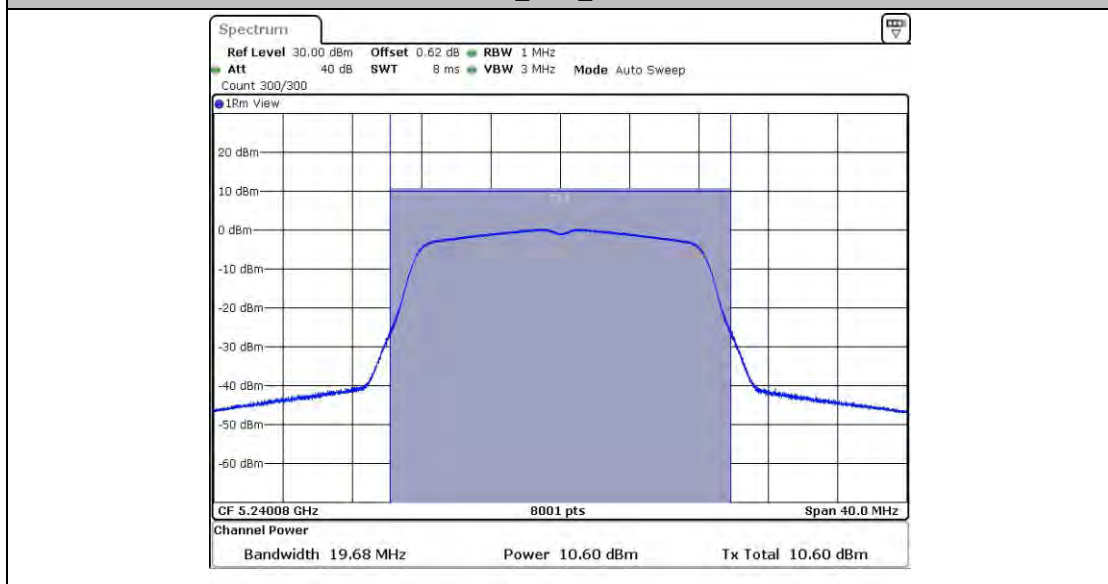
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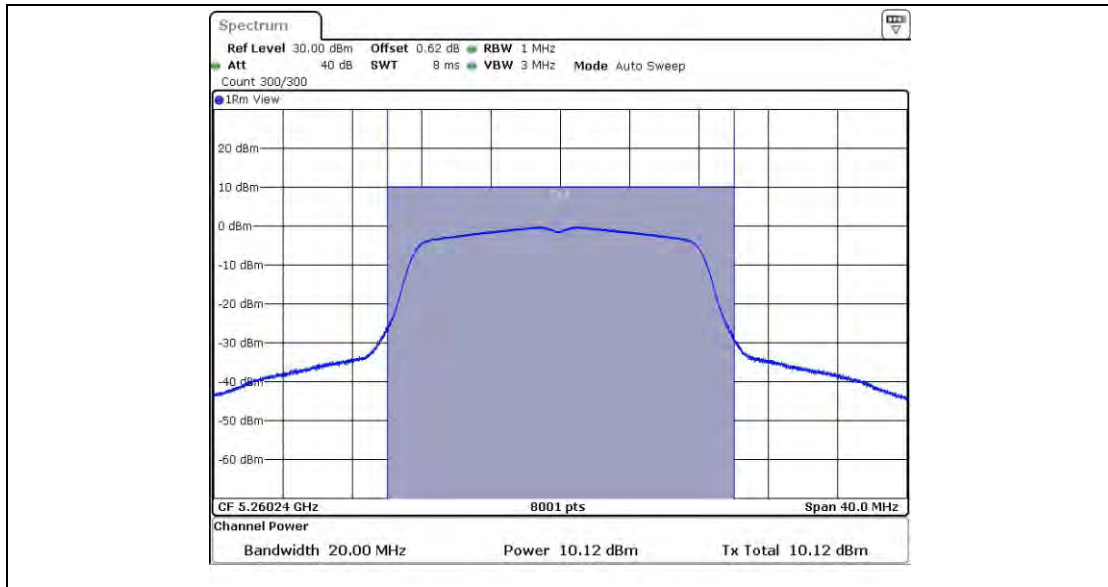
11A_Ant2_5200



11A_Ant1_5240



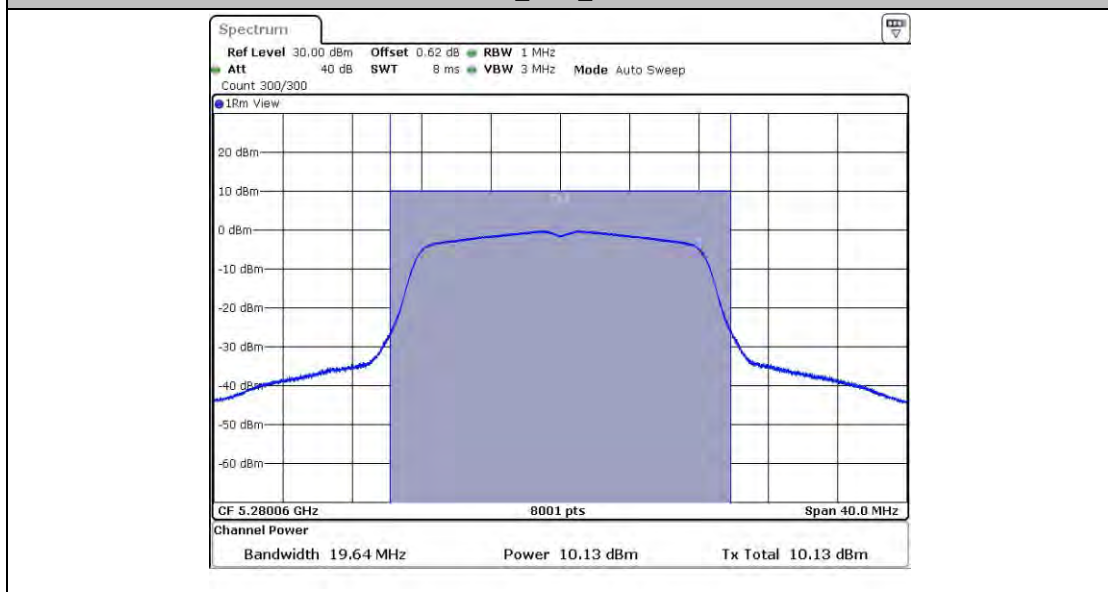
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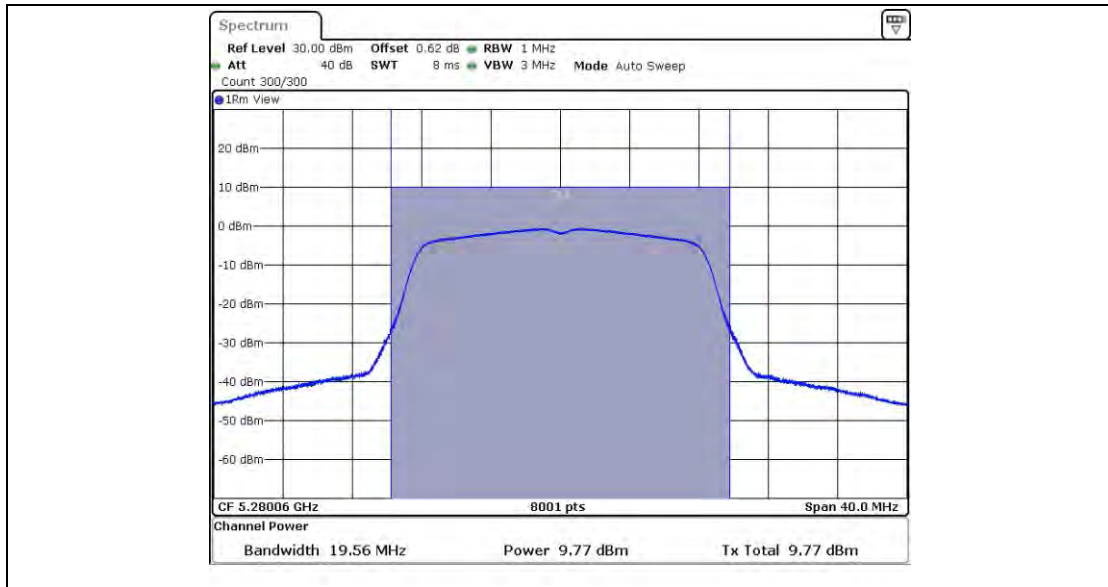
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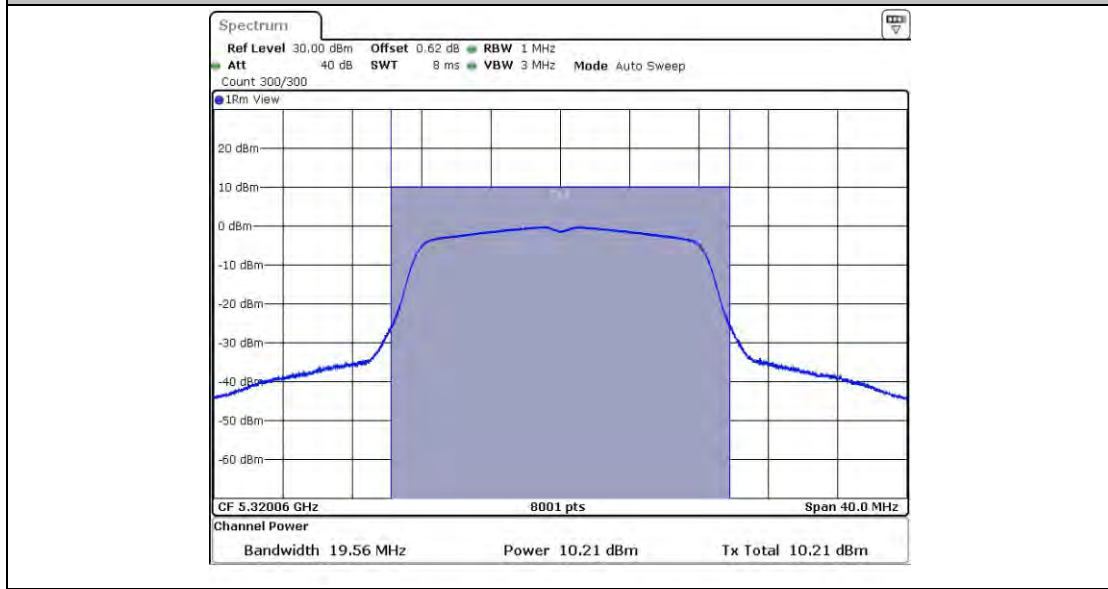
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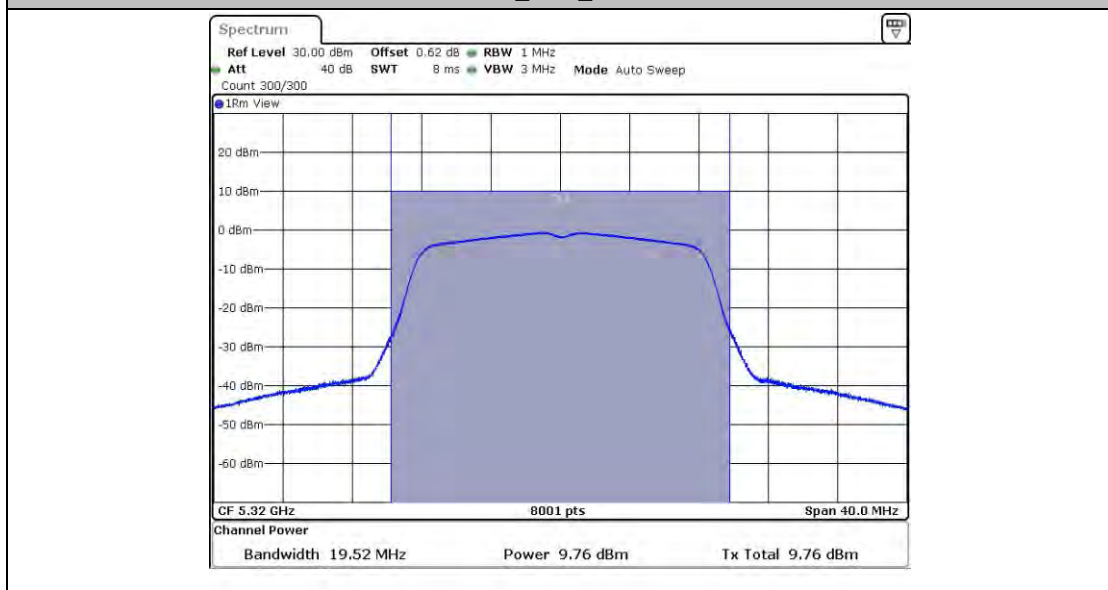
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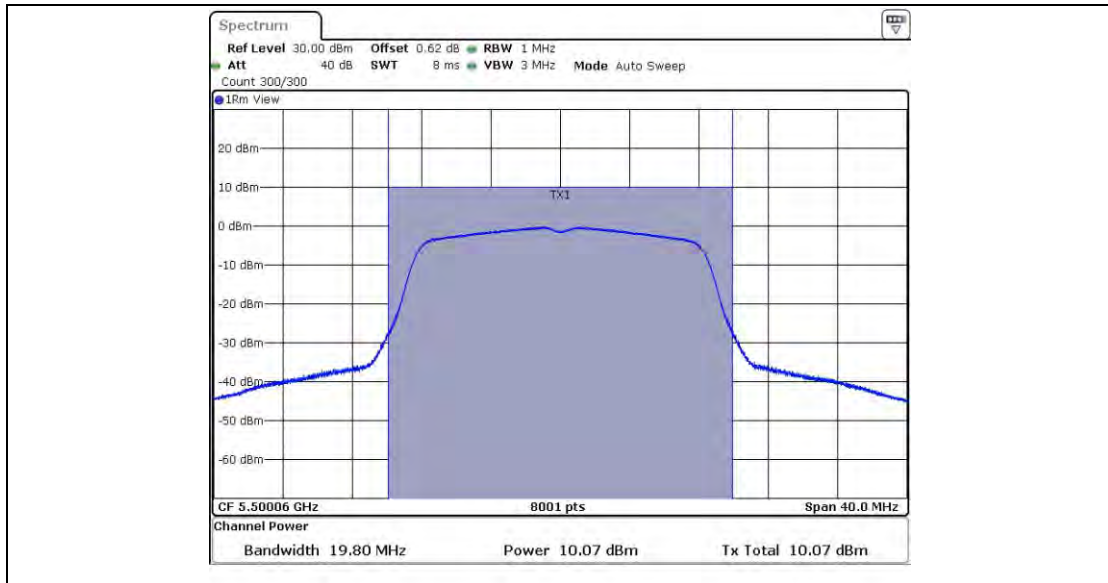
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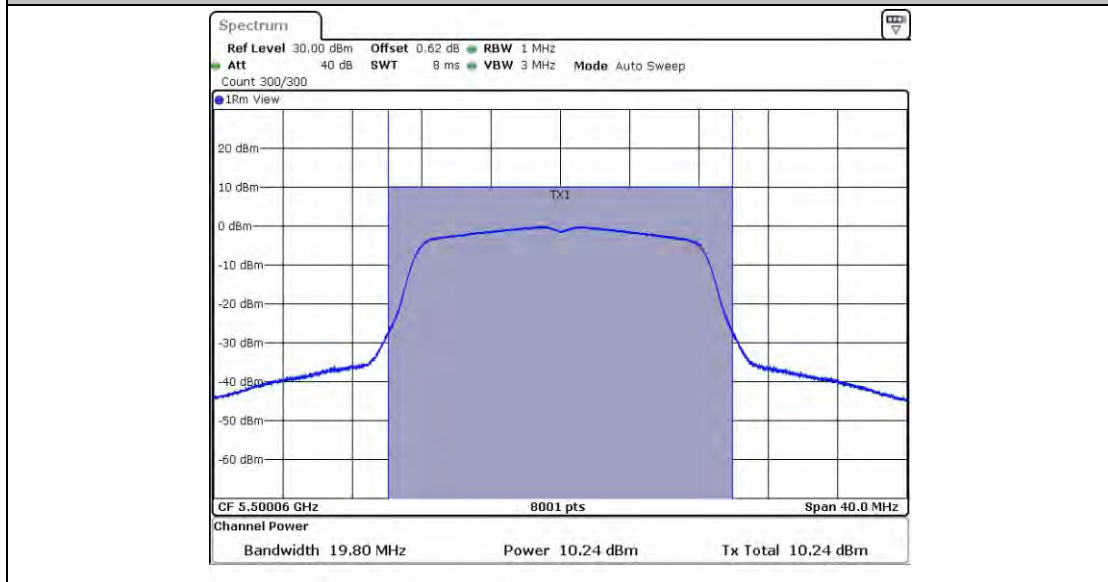
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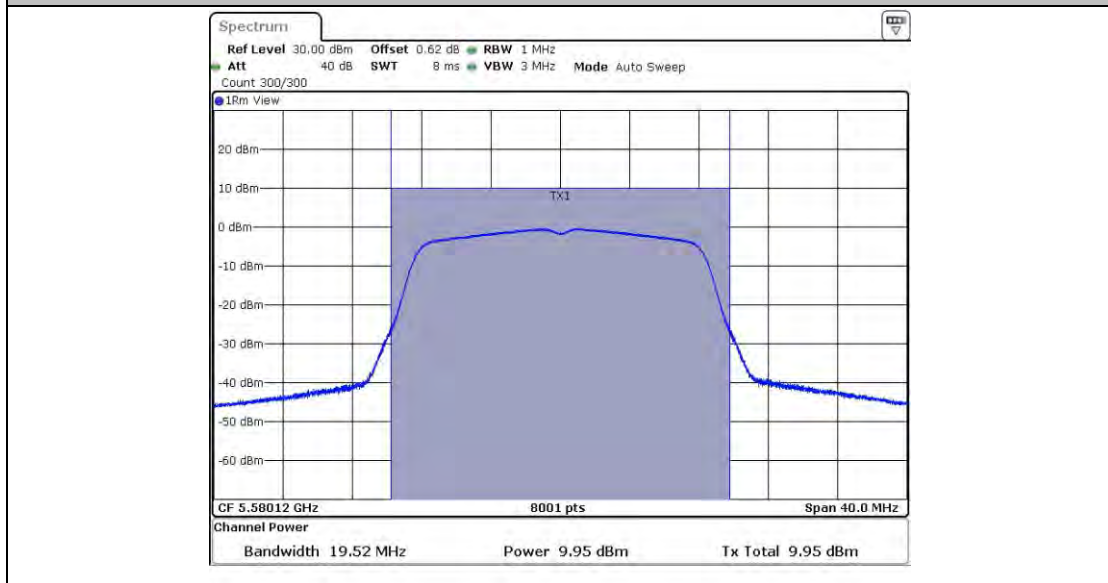
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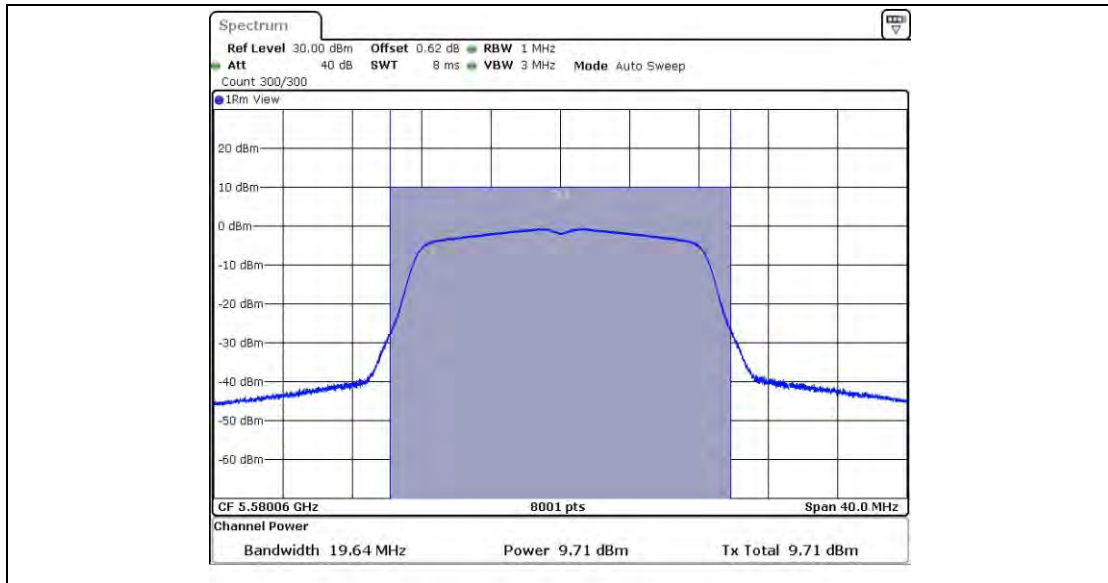
11A_Ant1_5500



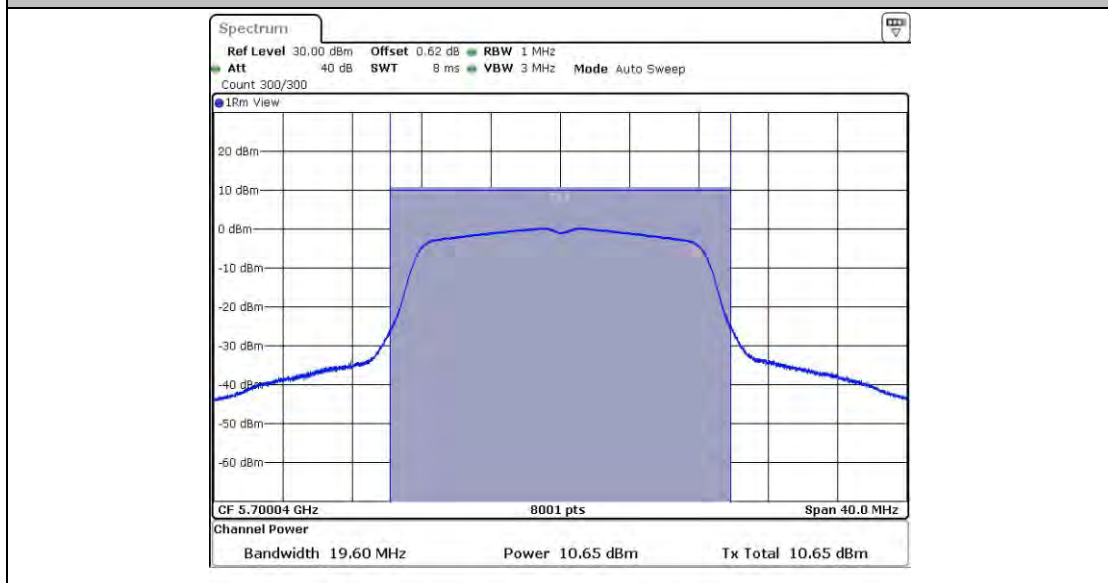
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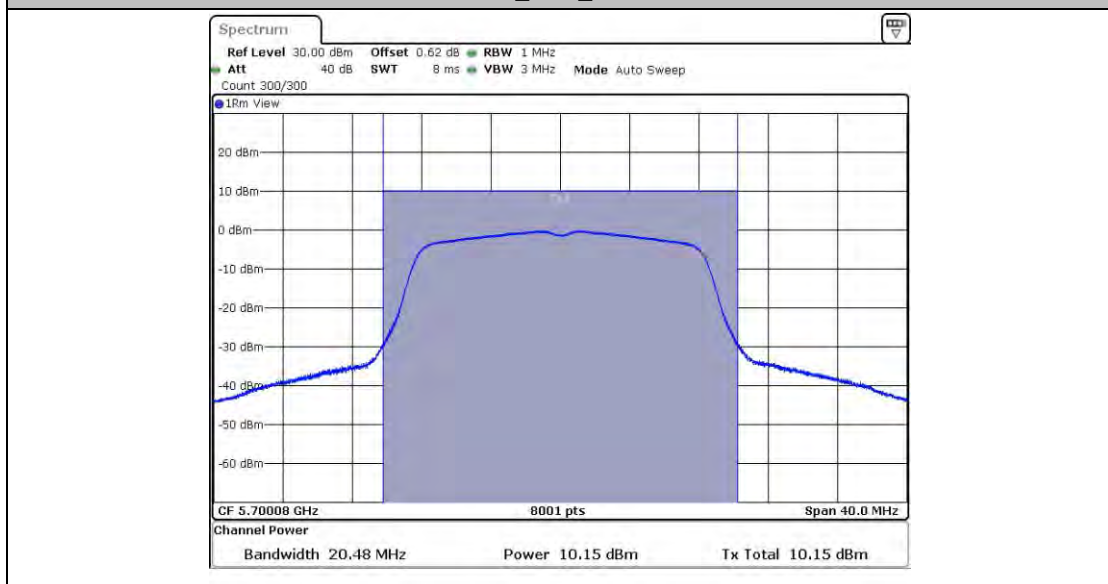
11A_Ant1_5580



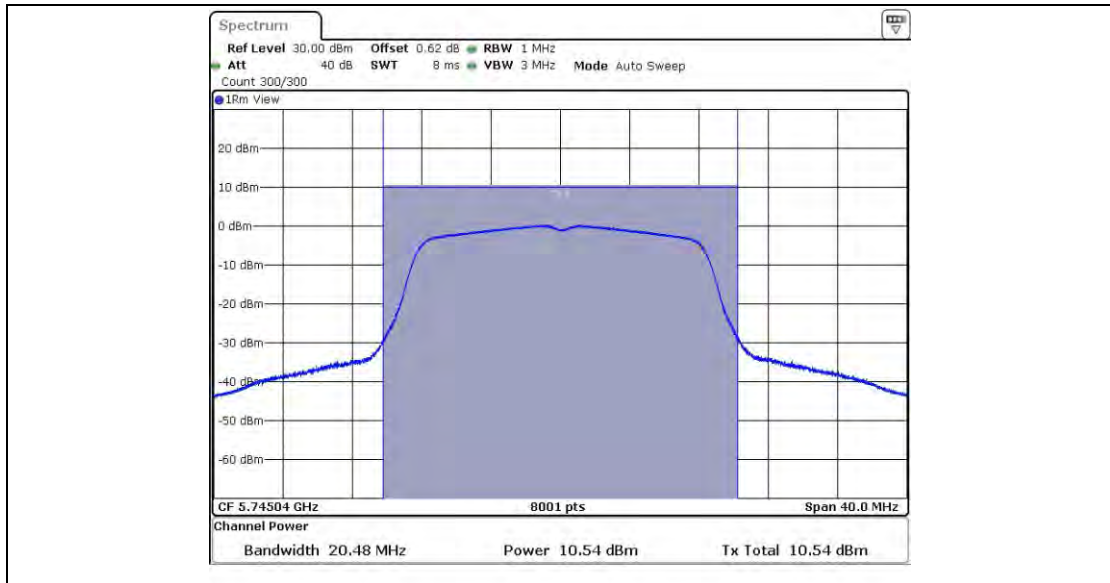
11A_Ant2_5580



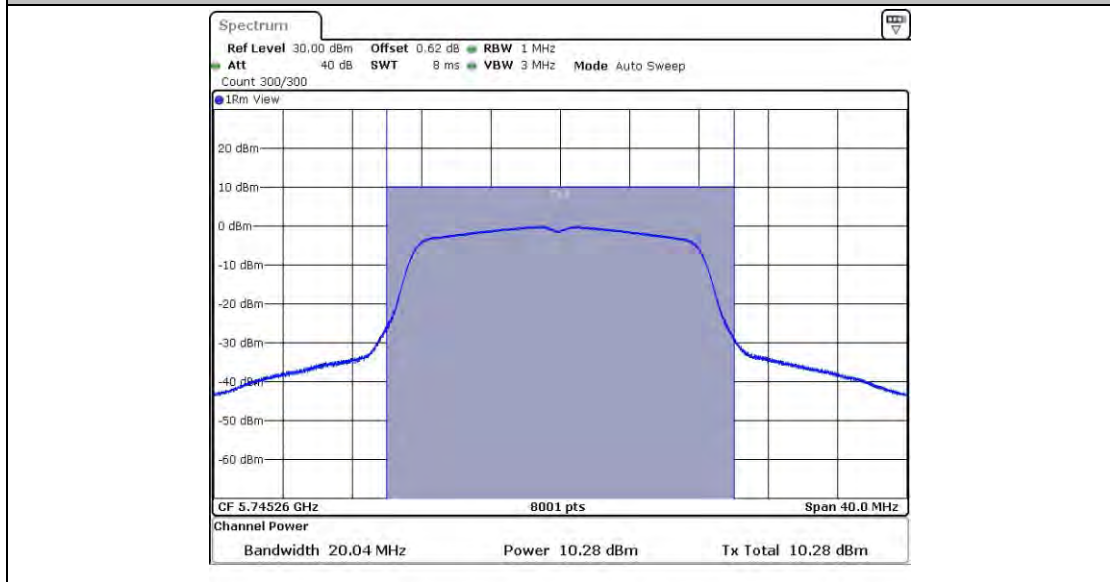
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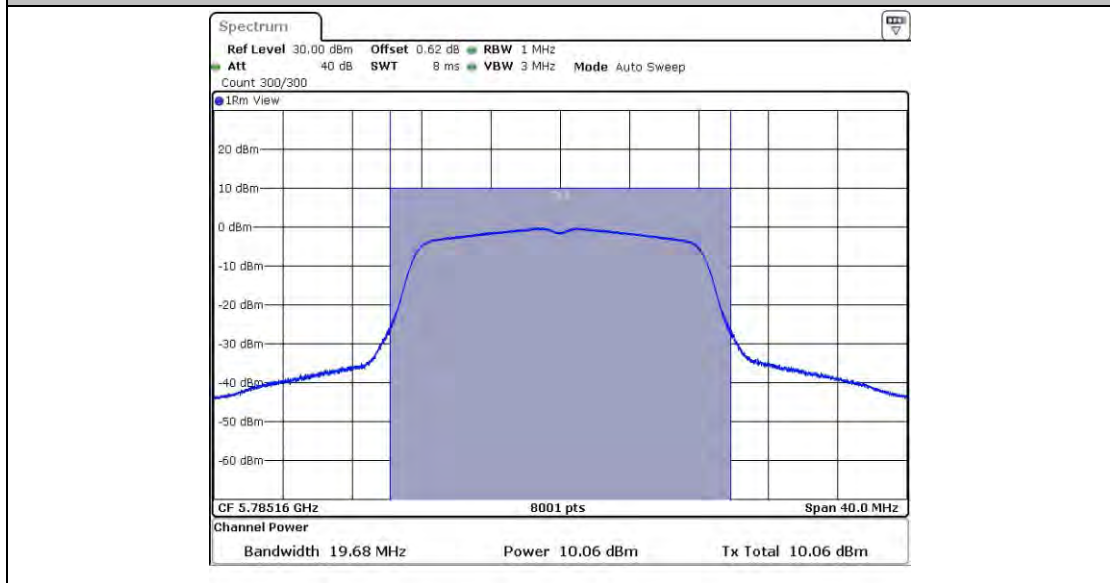
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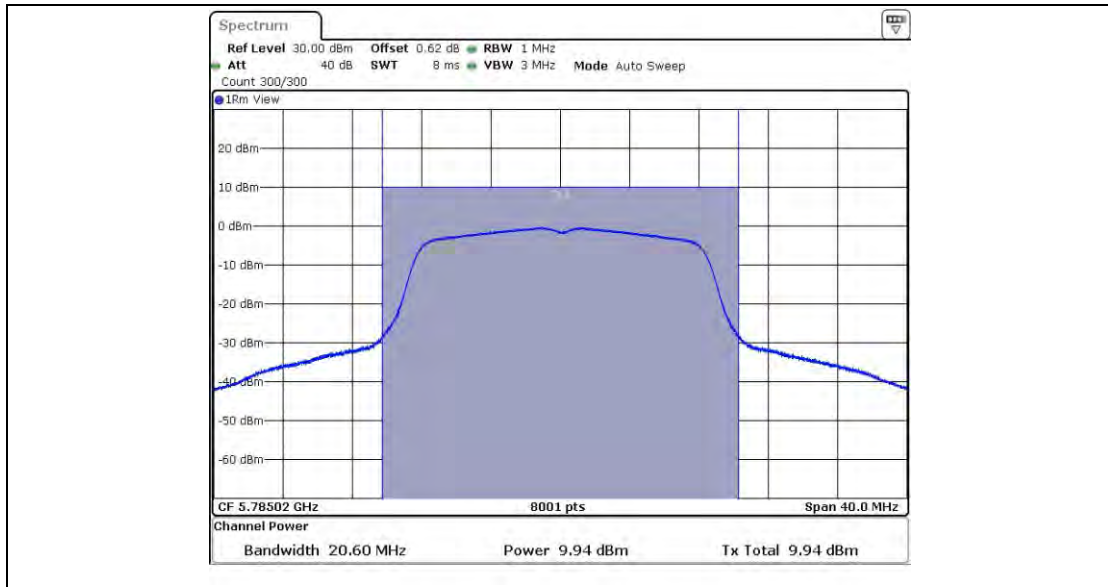
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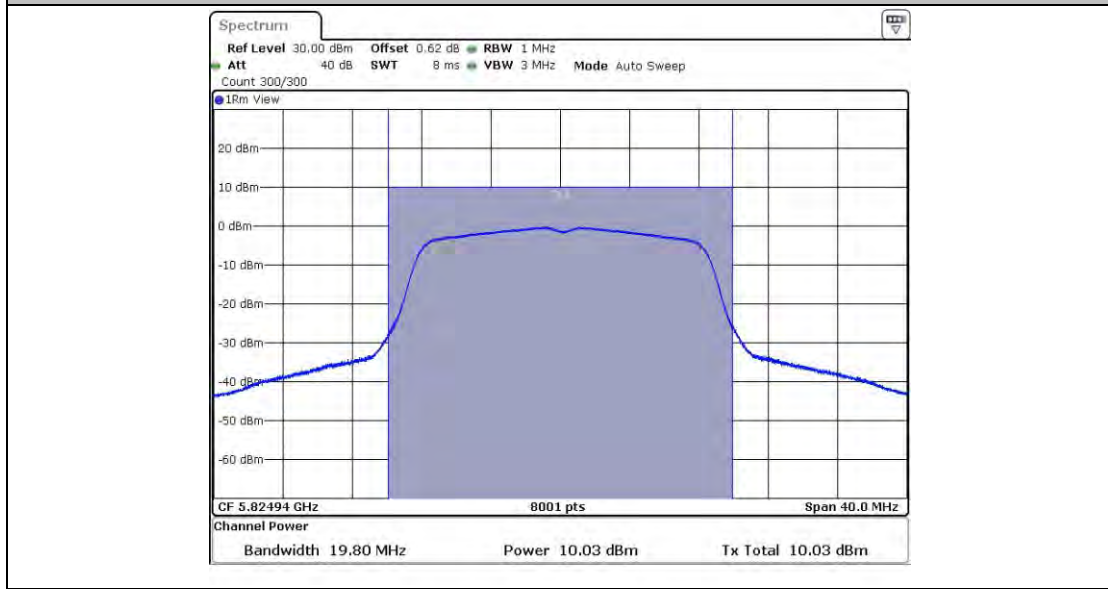
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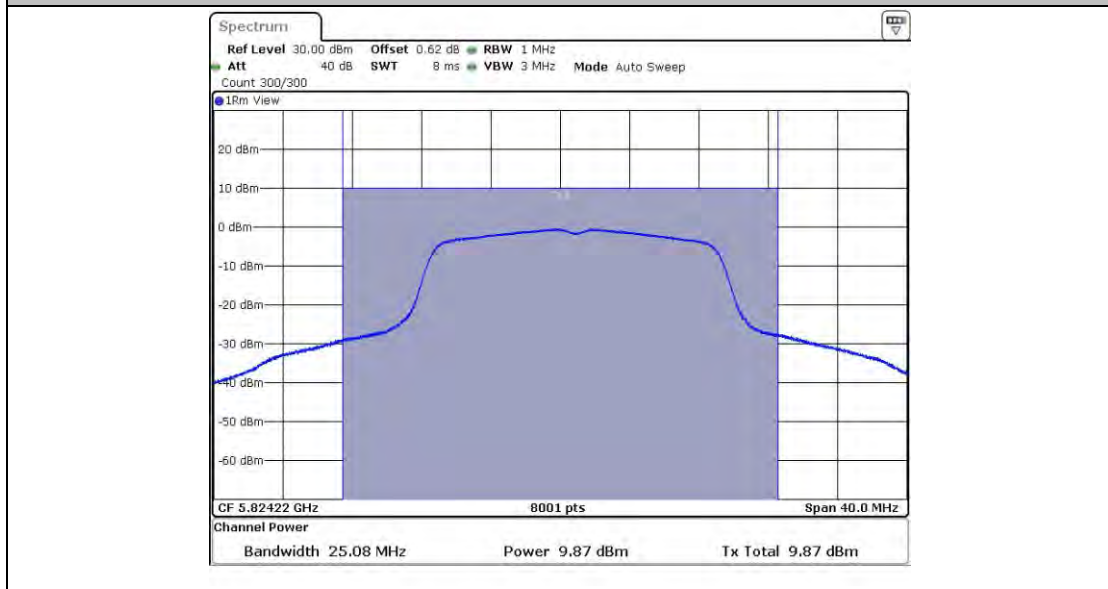
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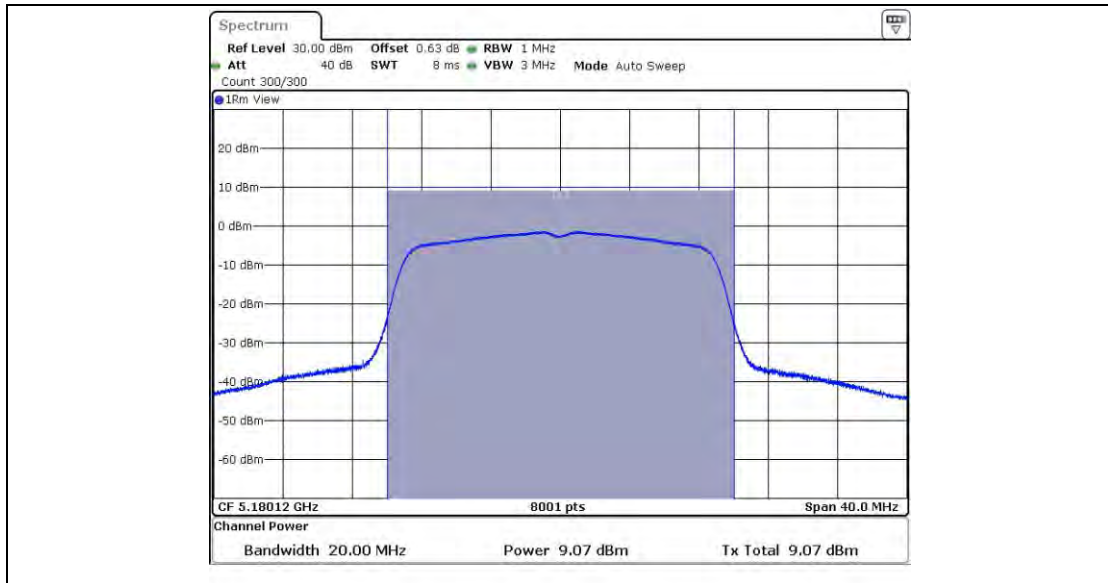
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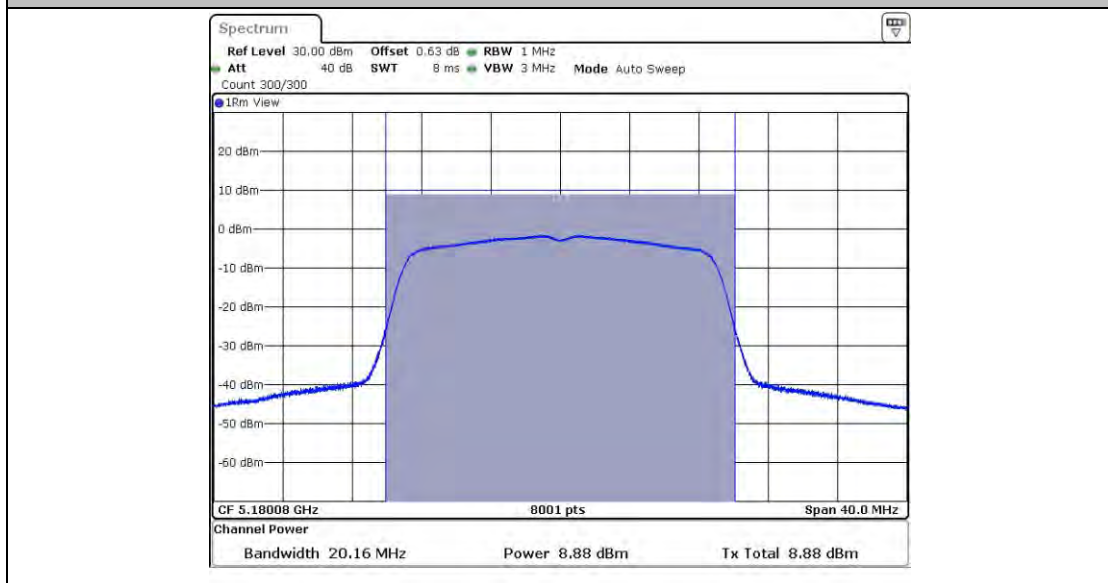
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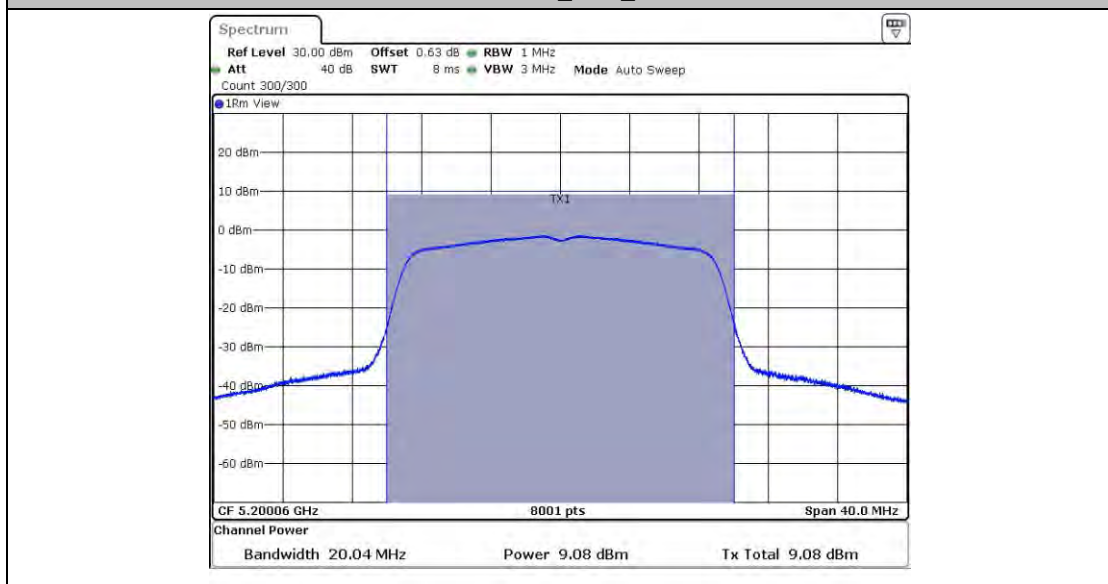
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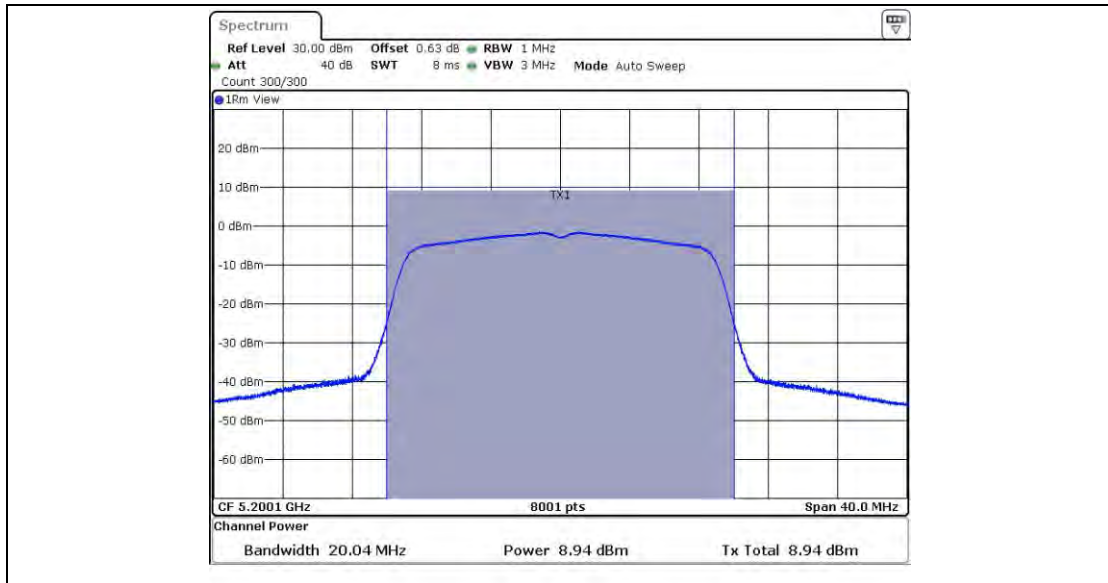
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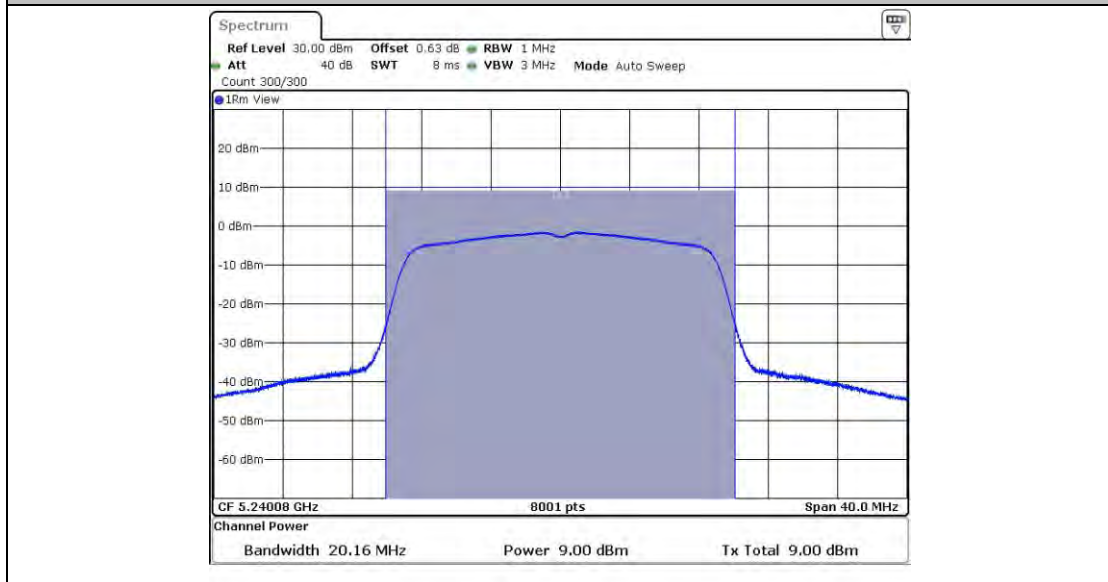
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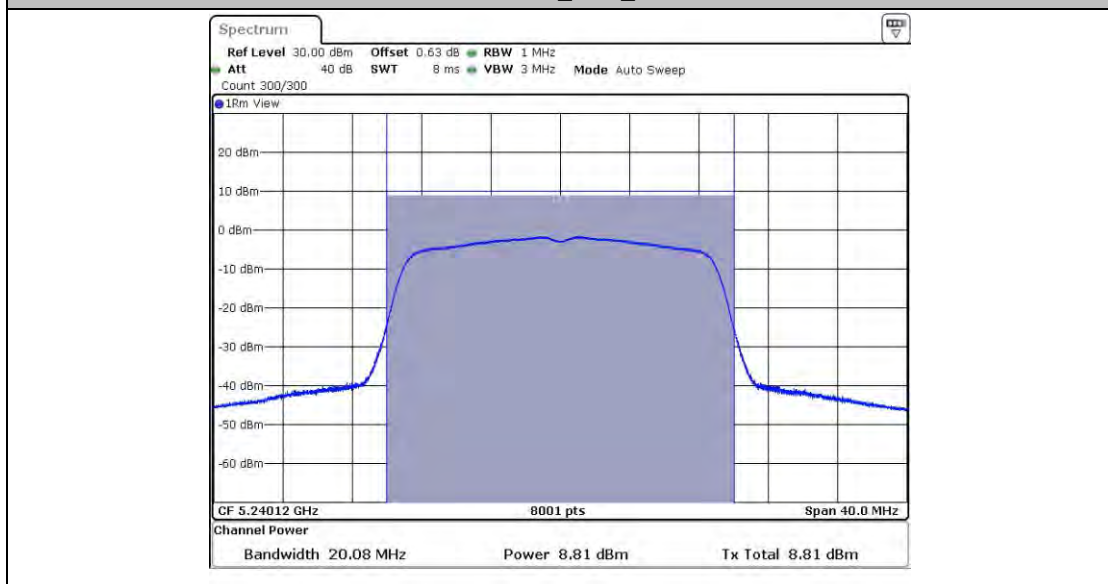
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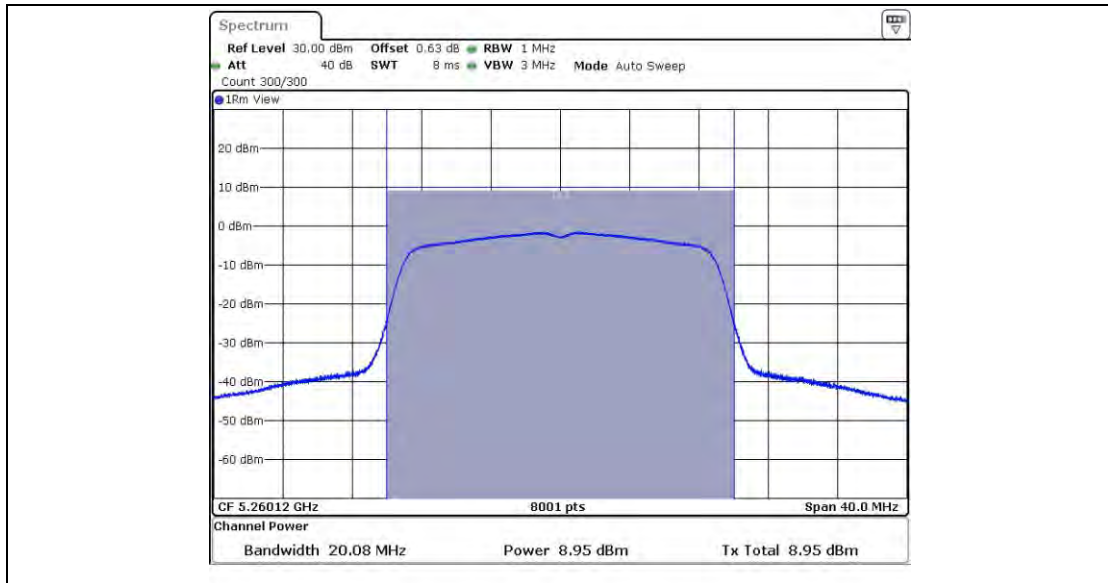
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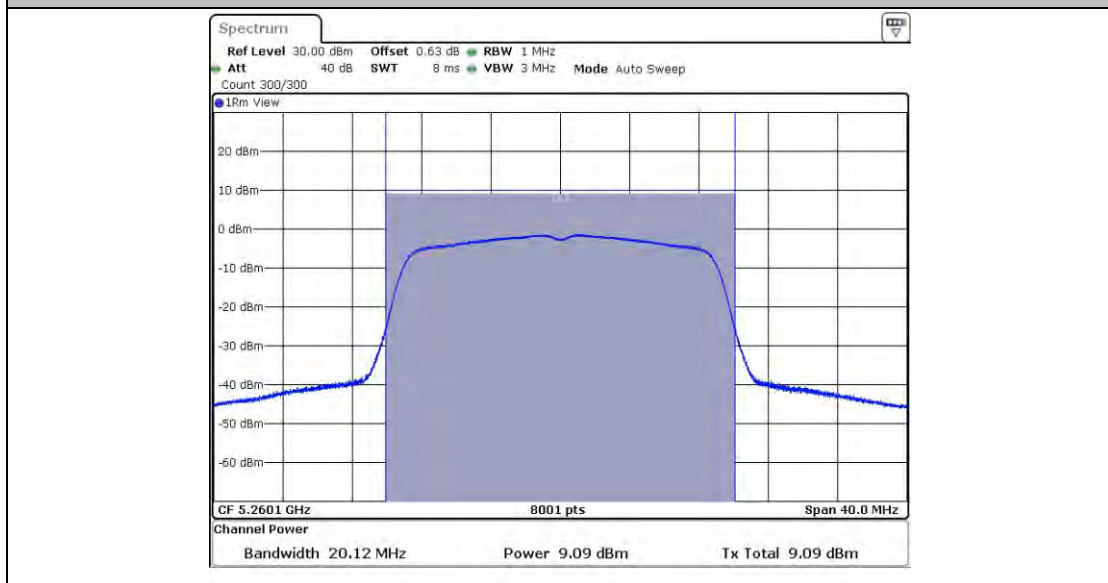
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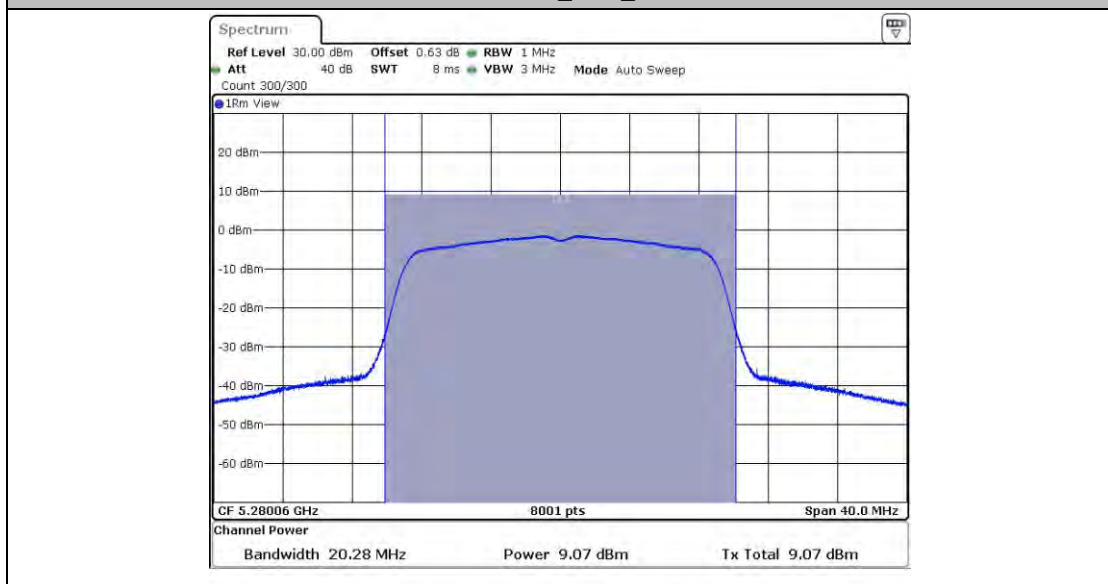
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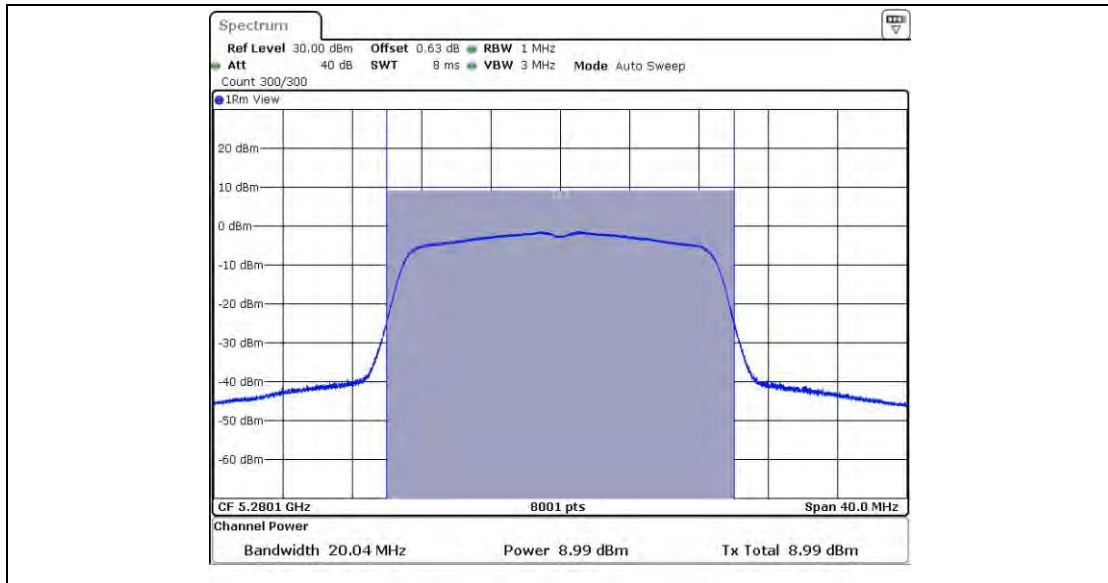
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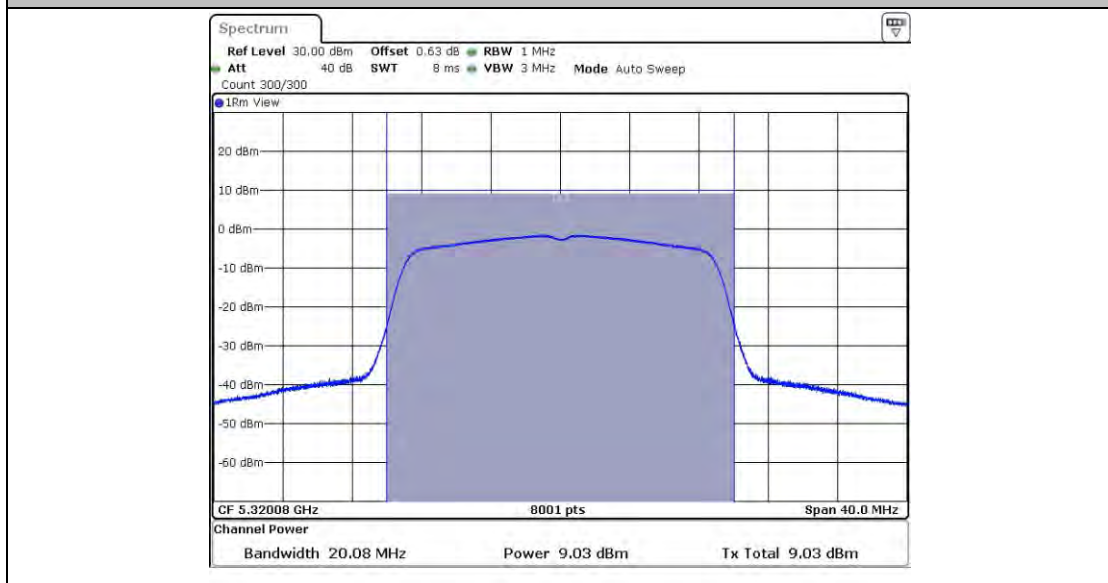
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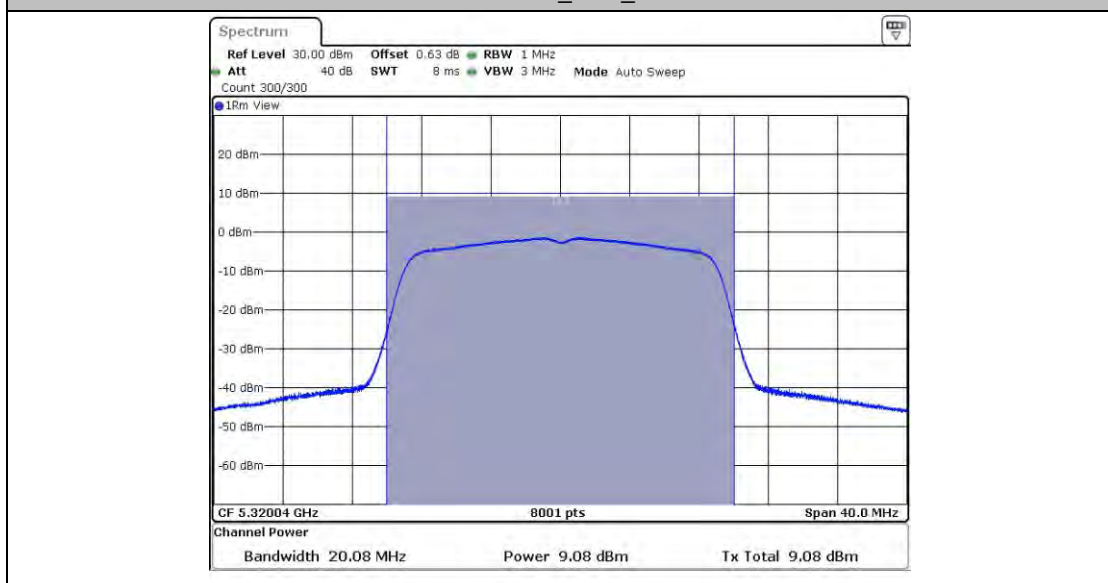
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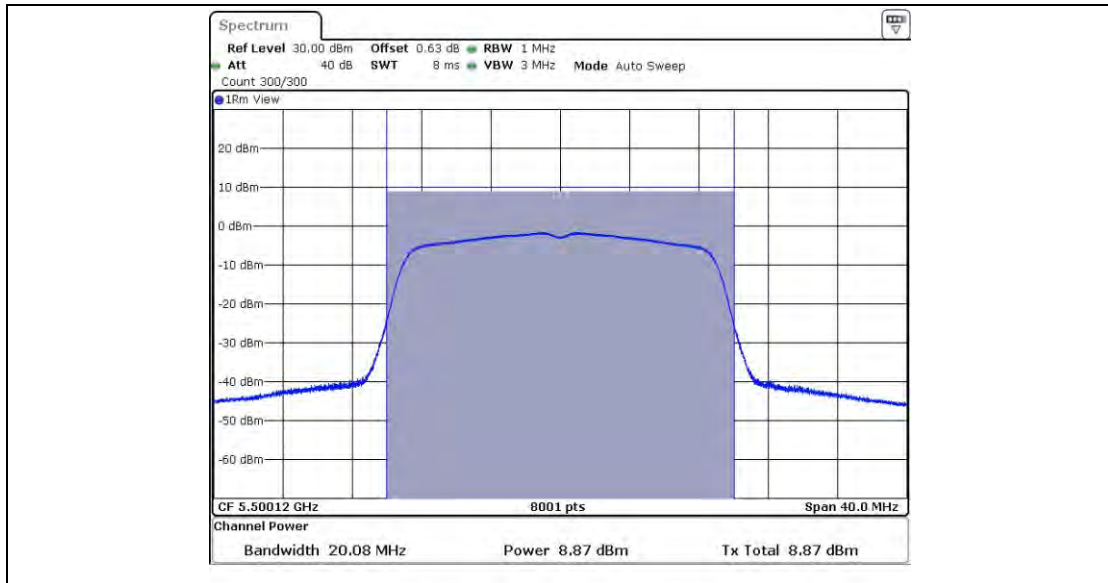
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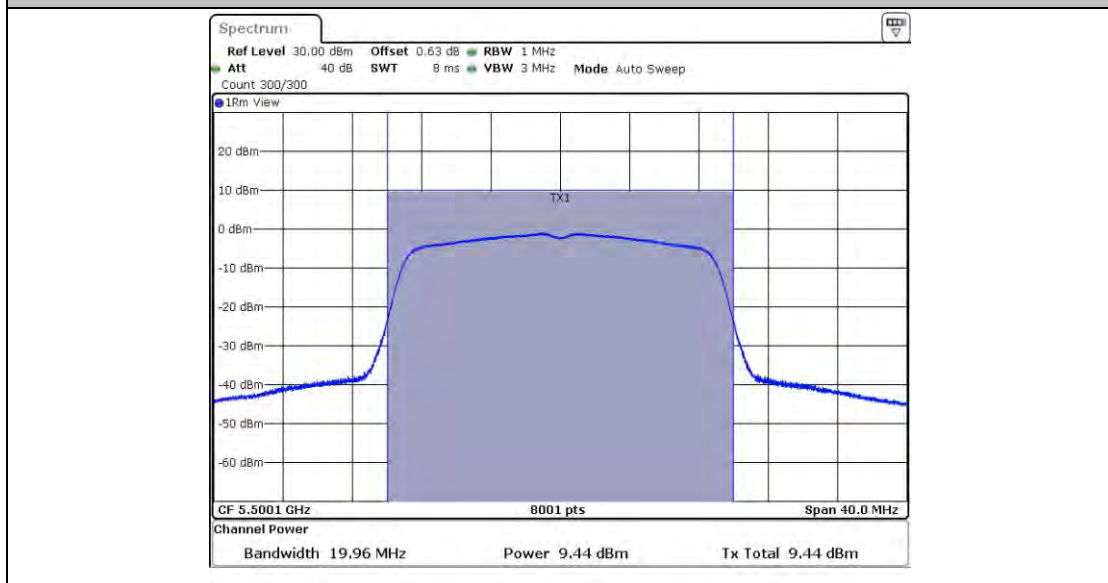
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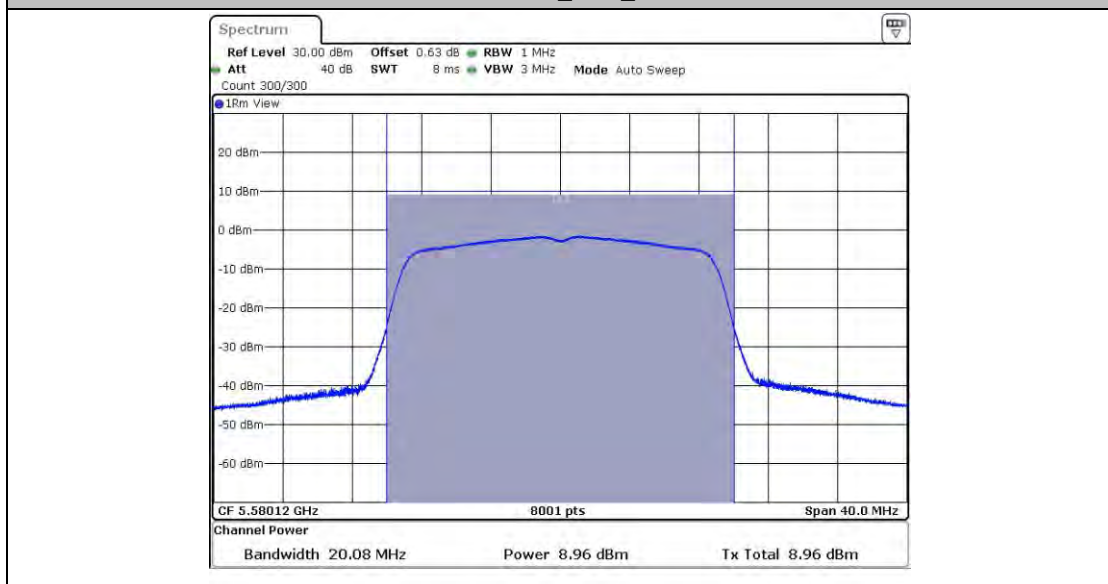
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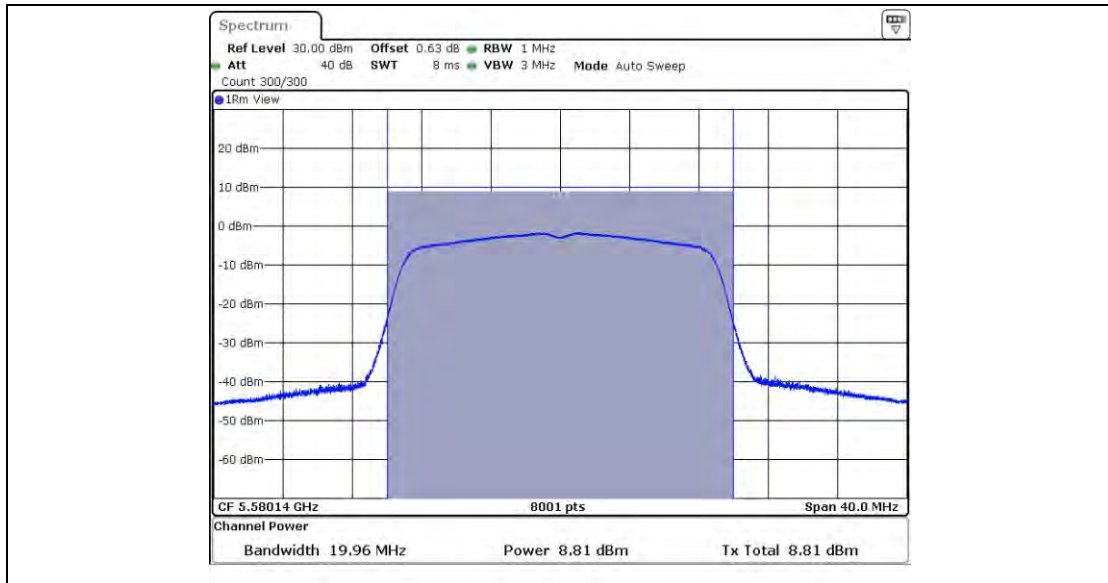
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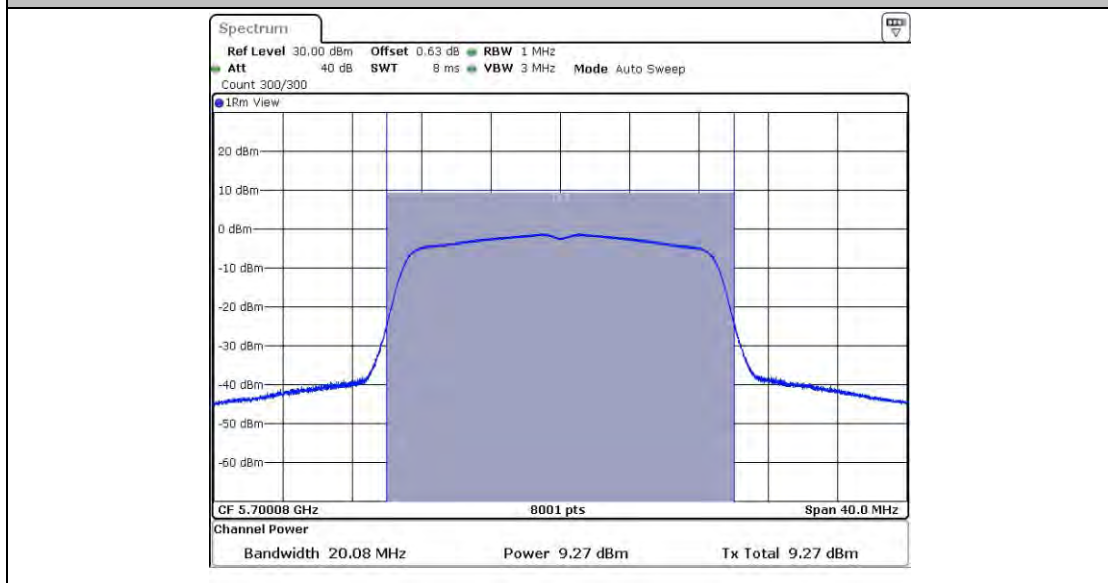
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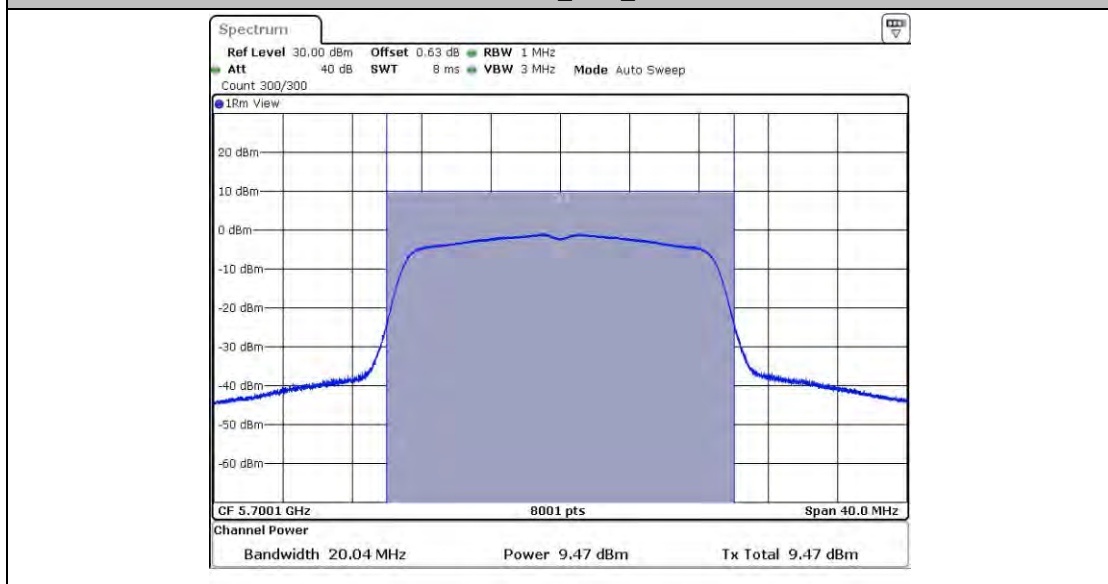
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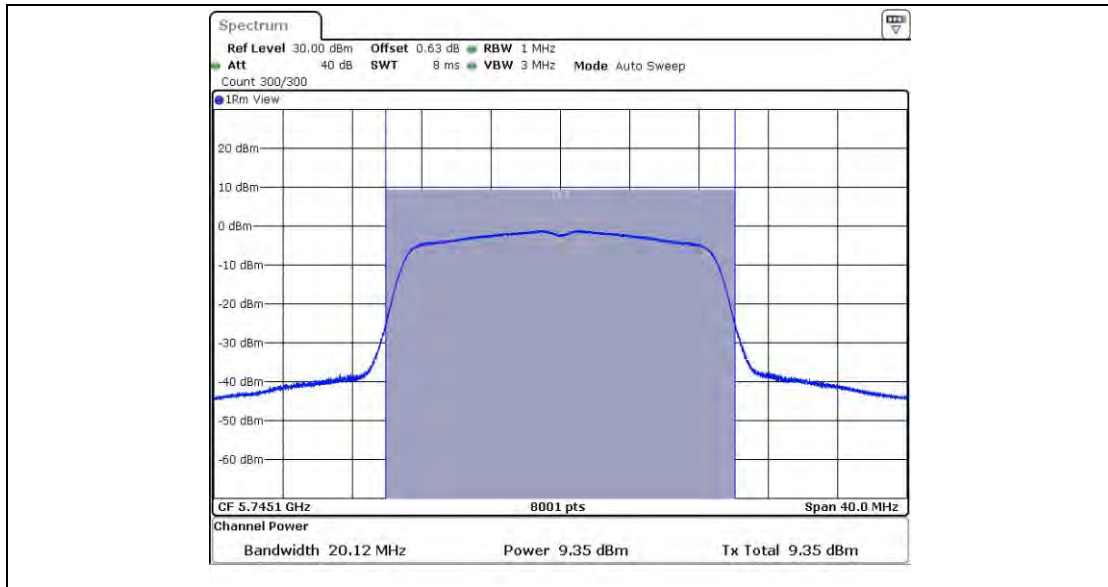
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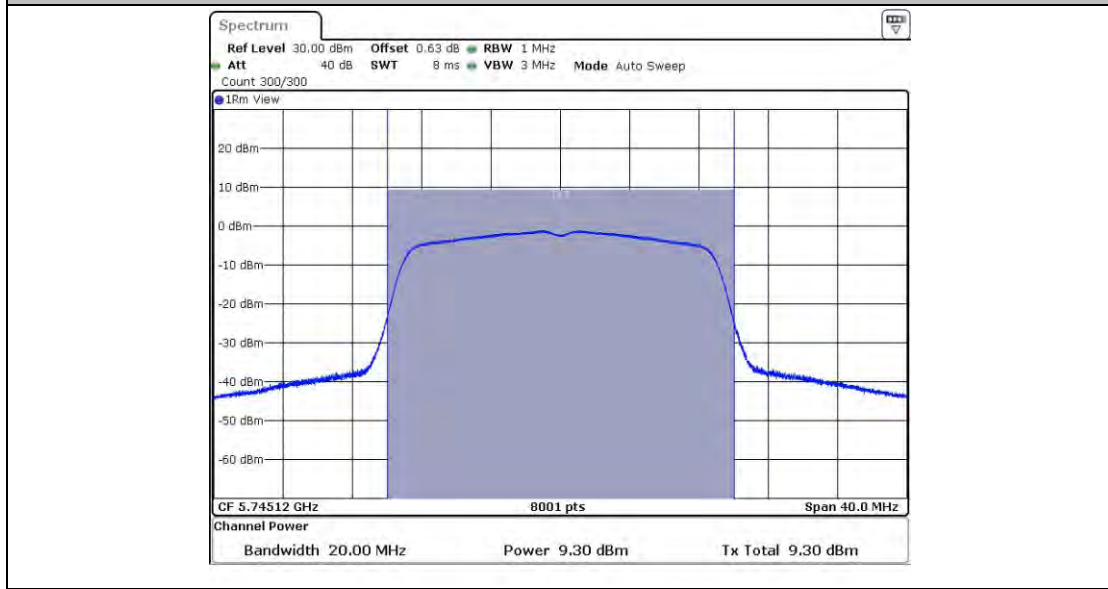
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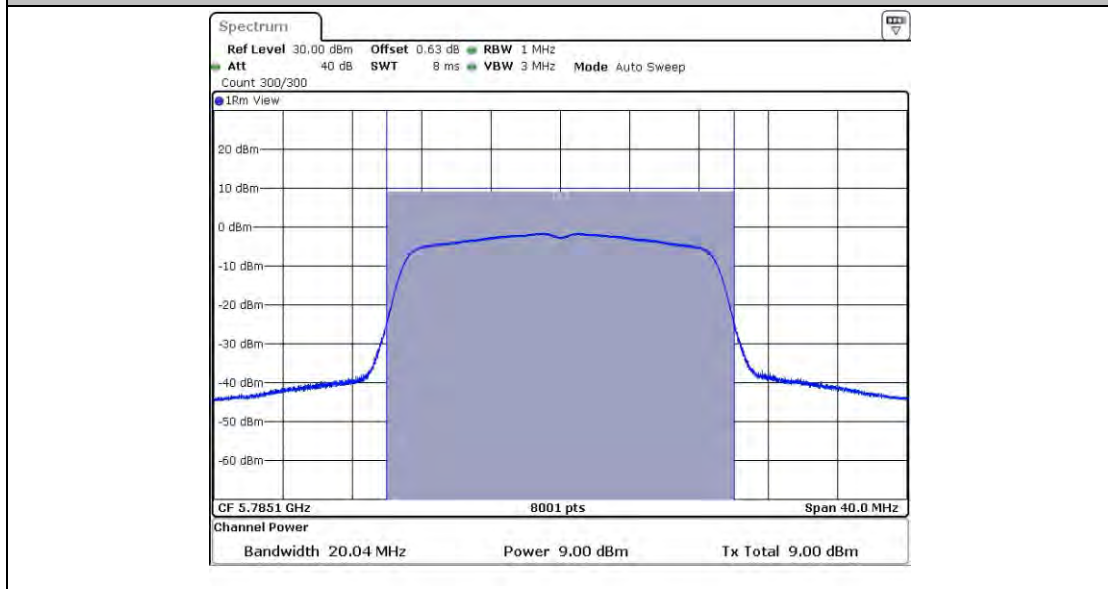
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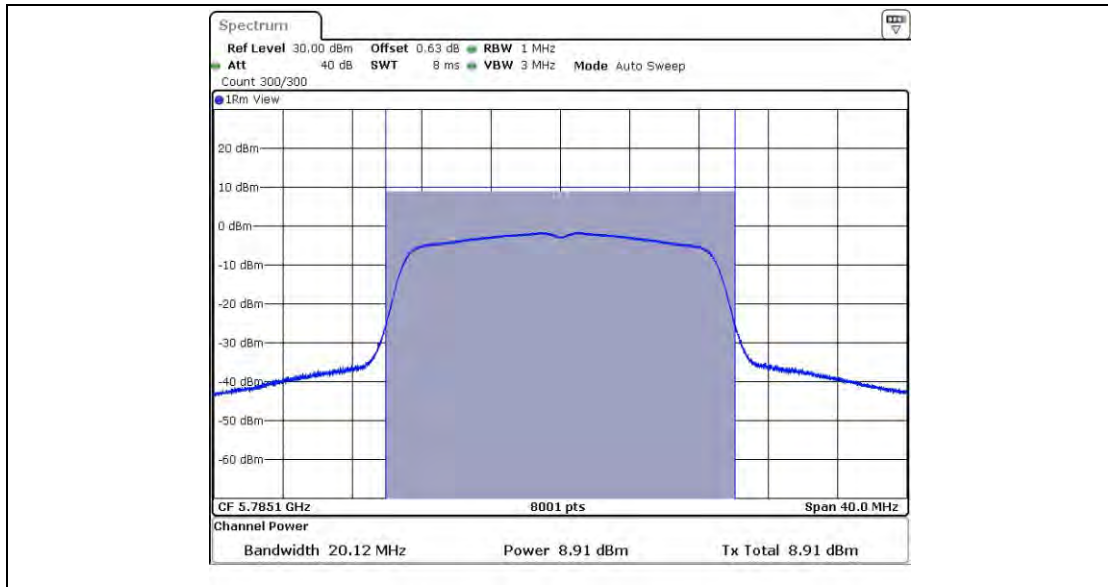
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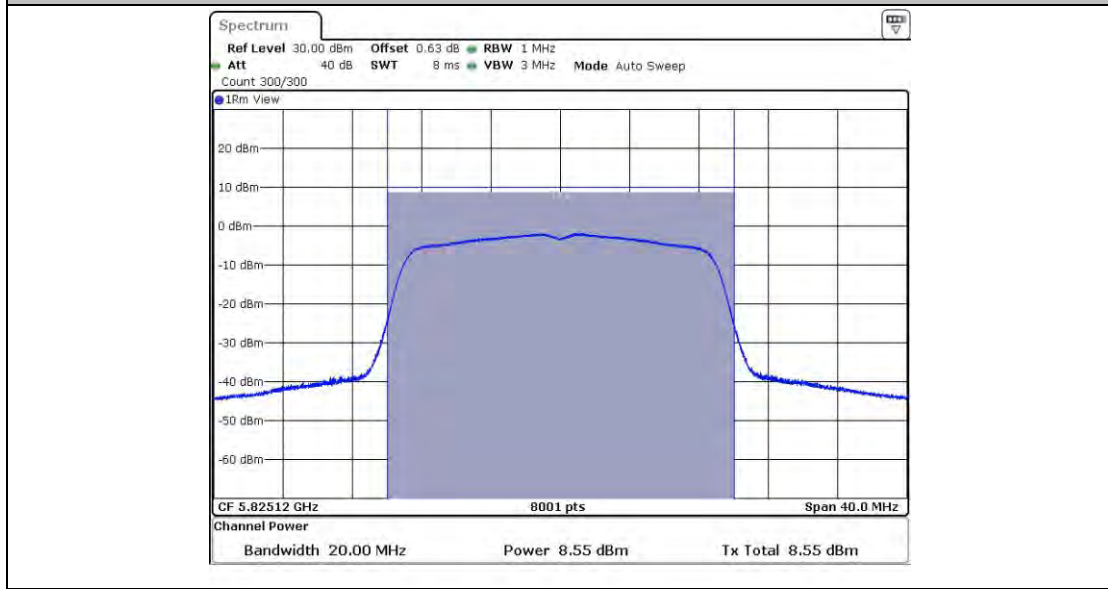
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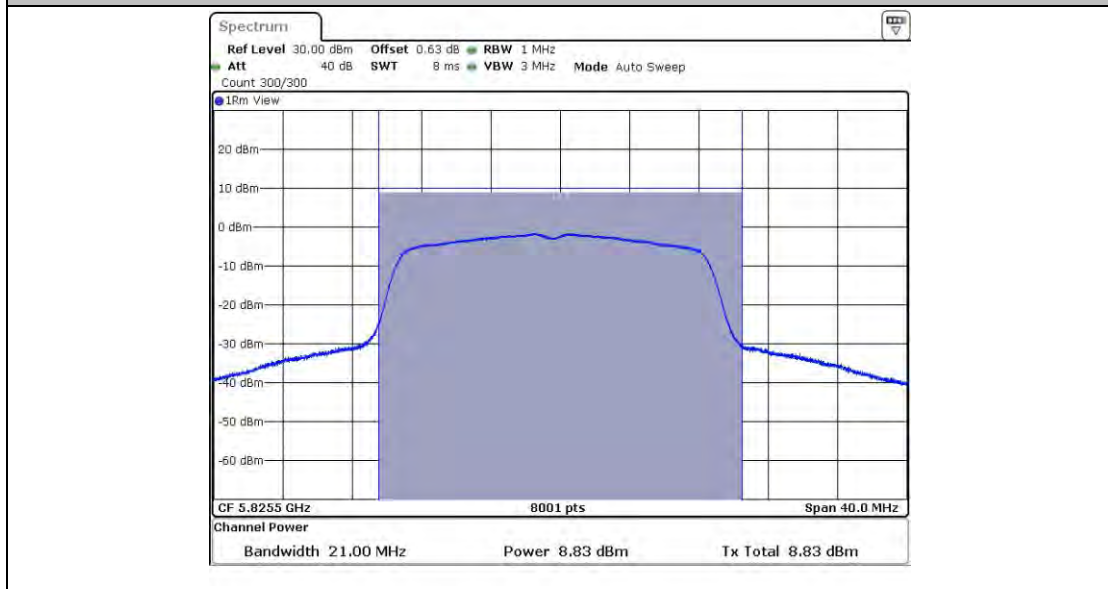
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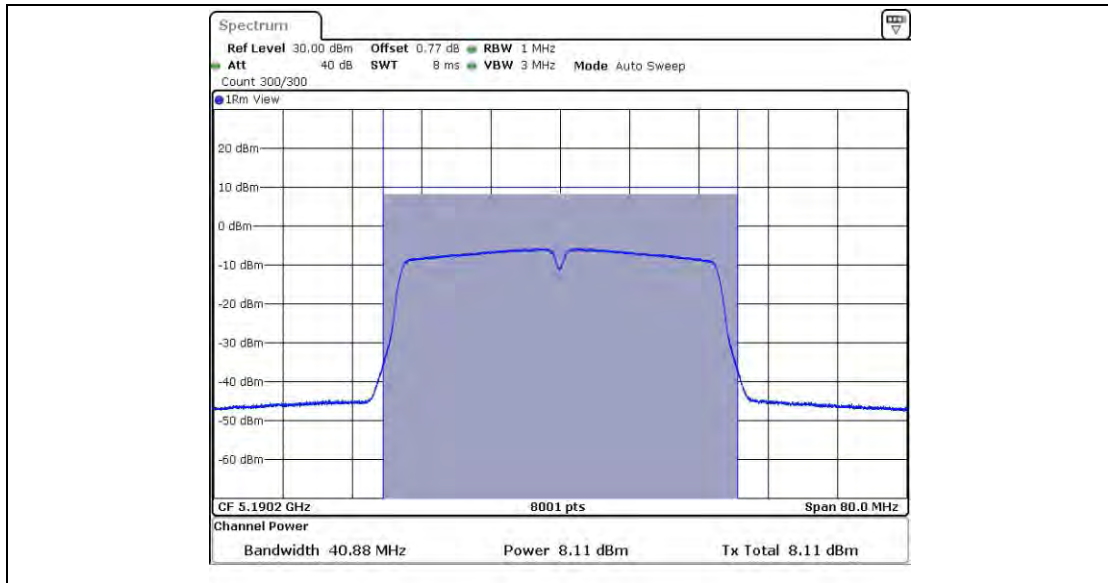
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11N20SISO_Ant1_5825



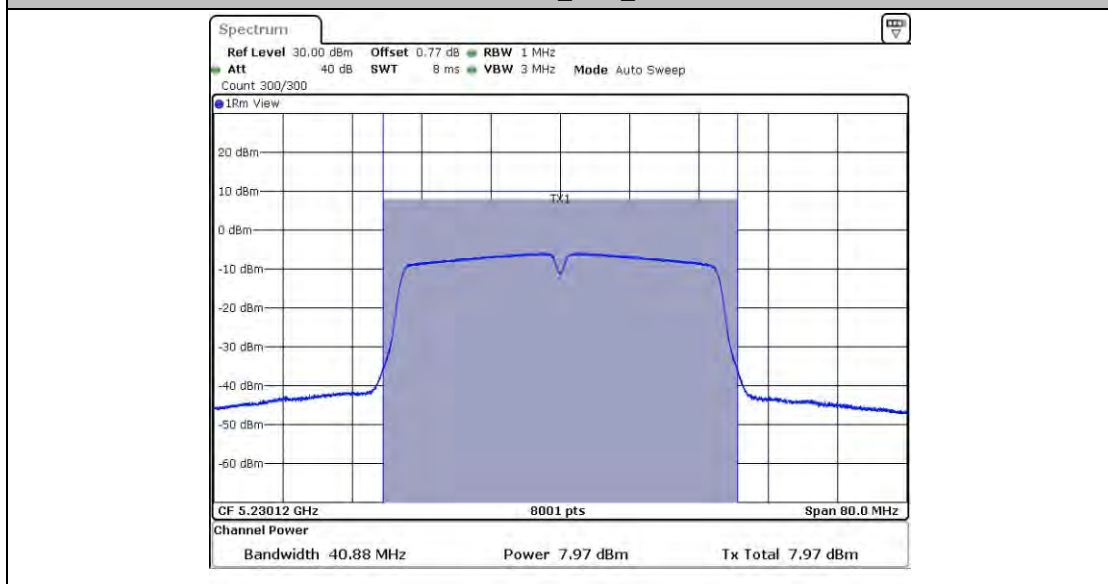
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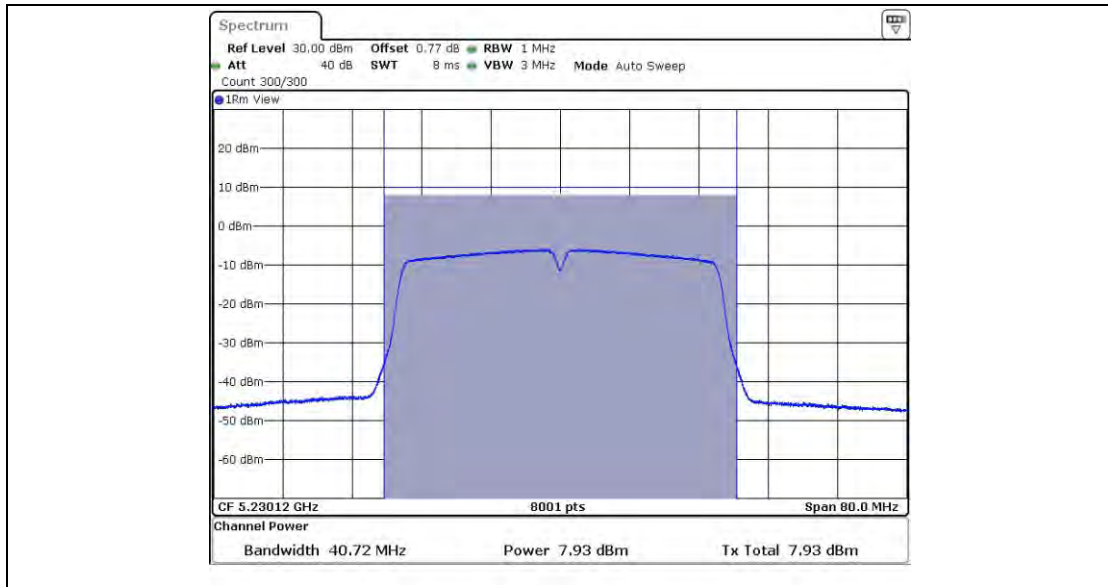
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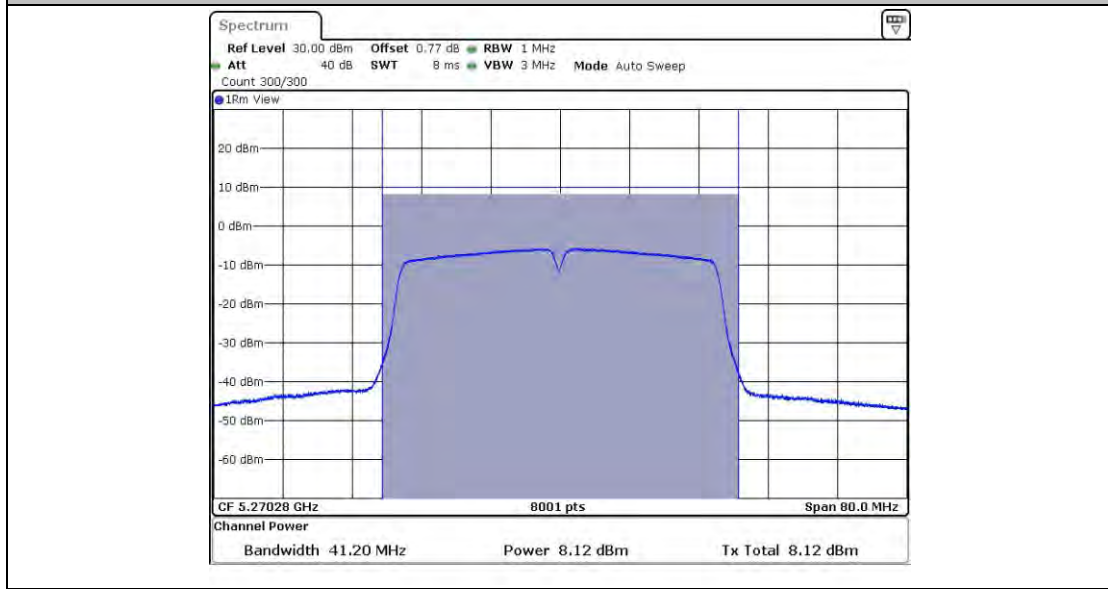
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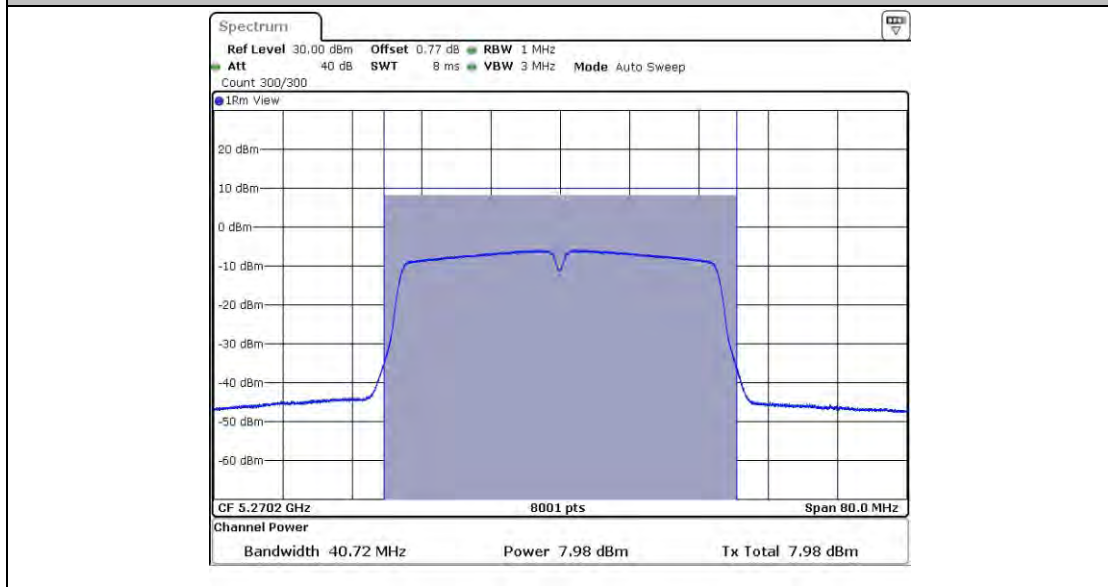
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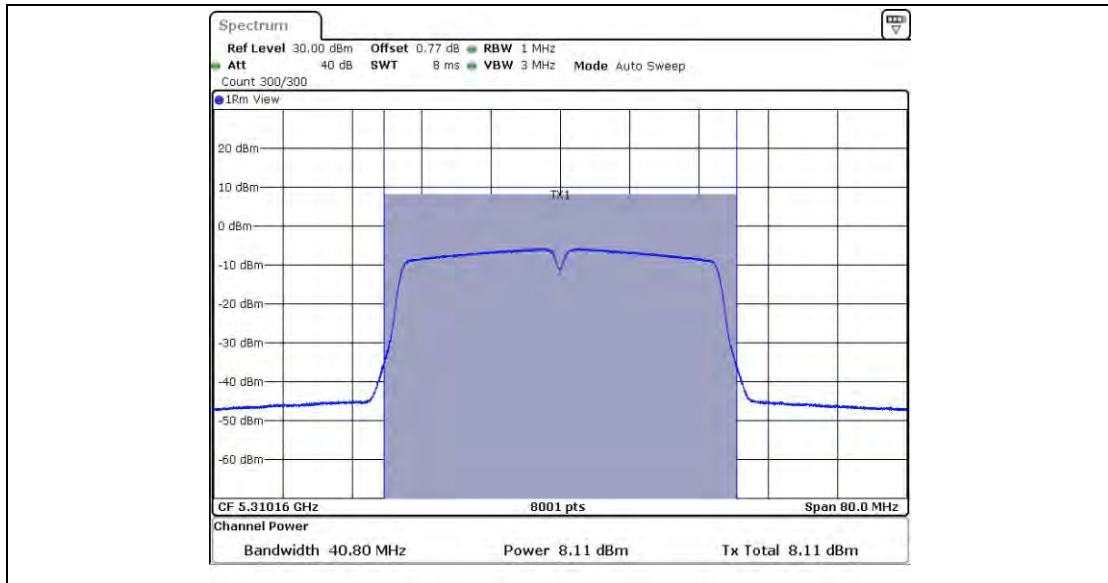
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11N40SISO_Ant1_5270



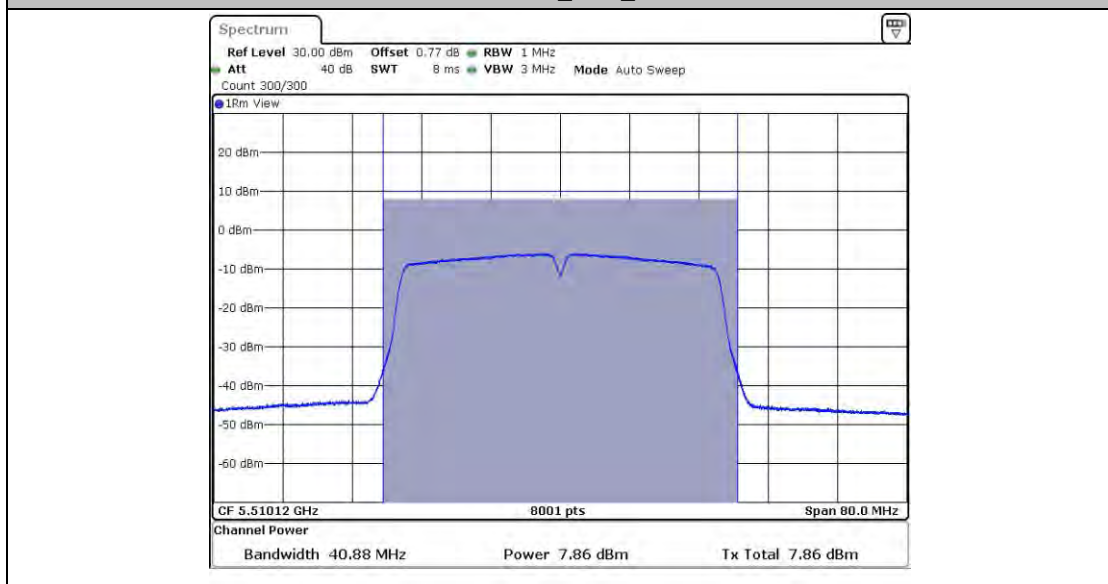
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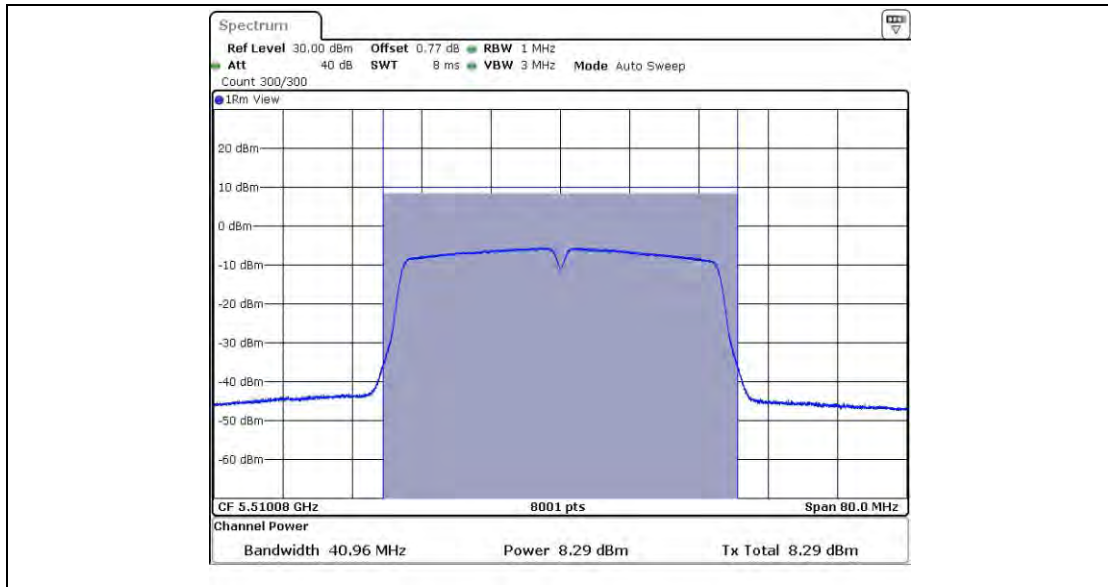
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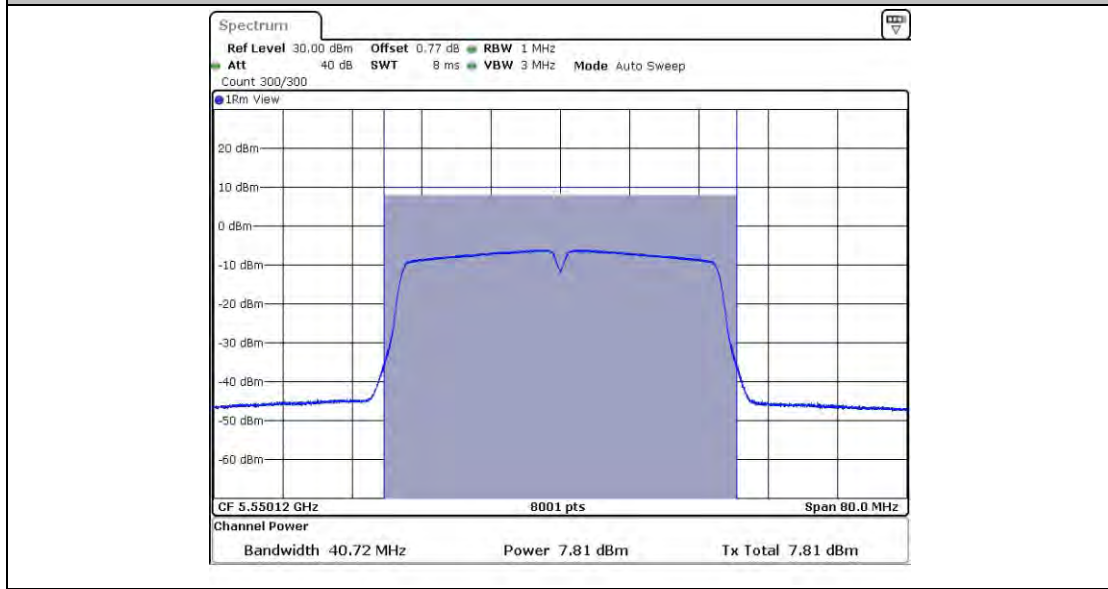
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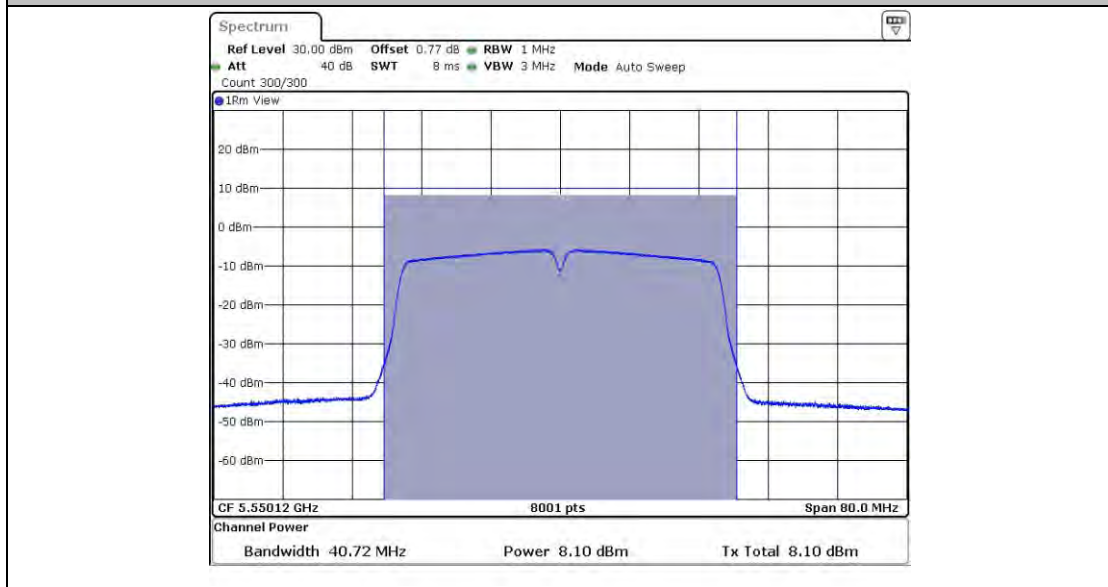
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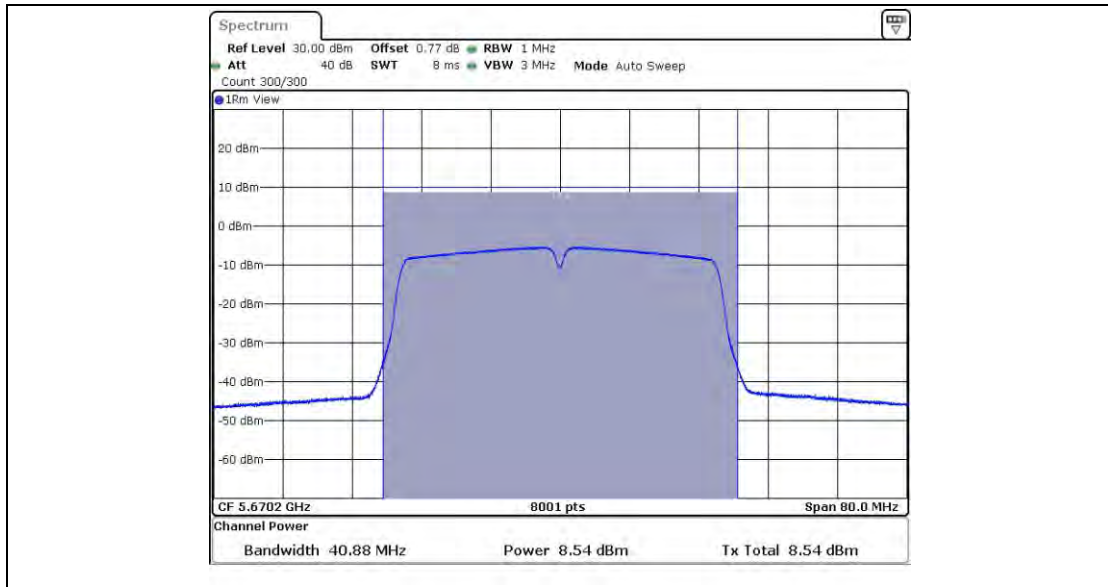
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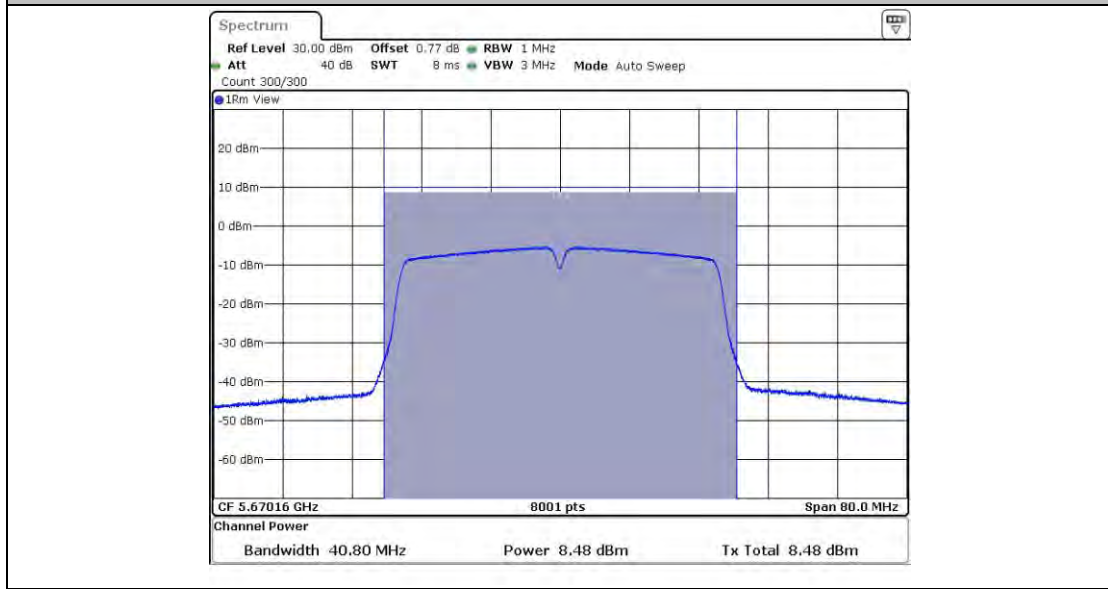
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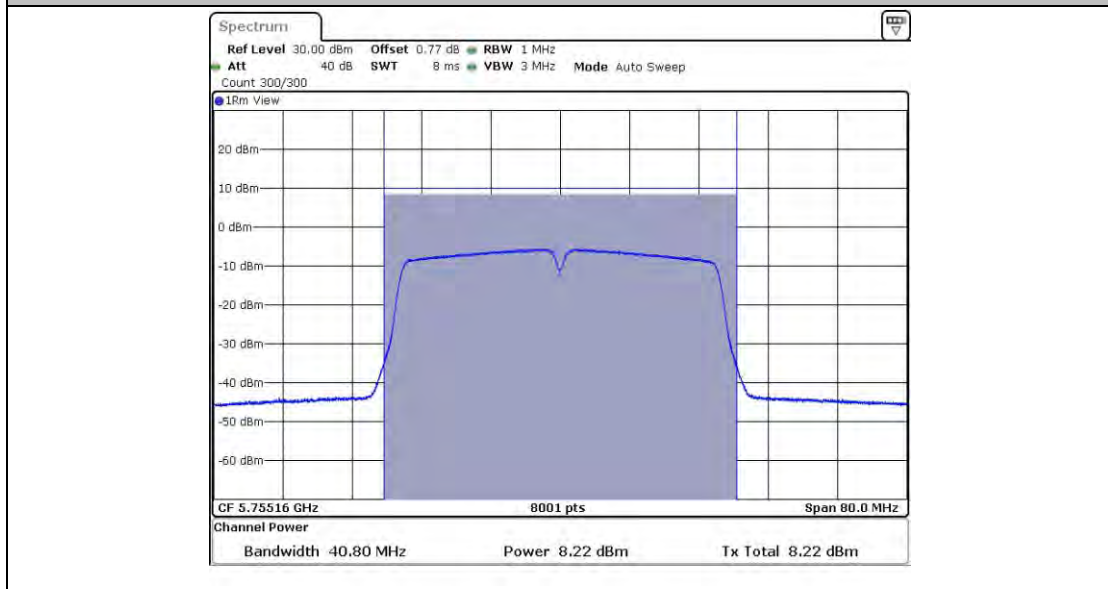
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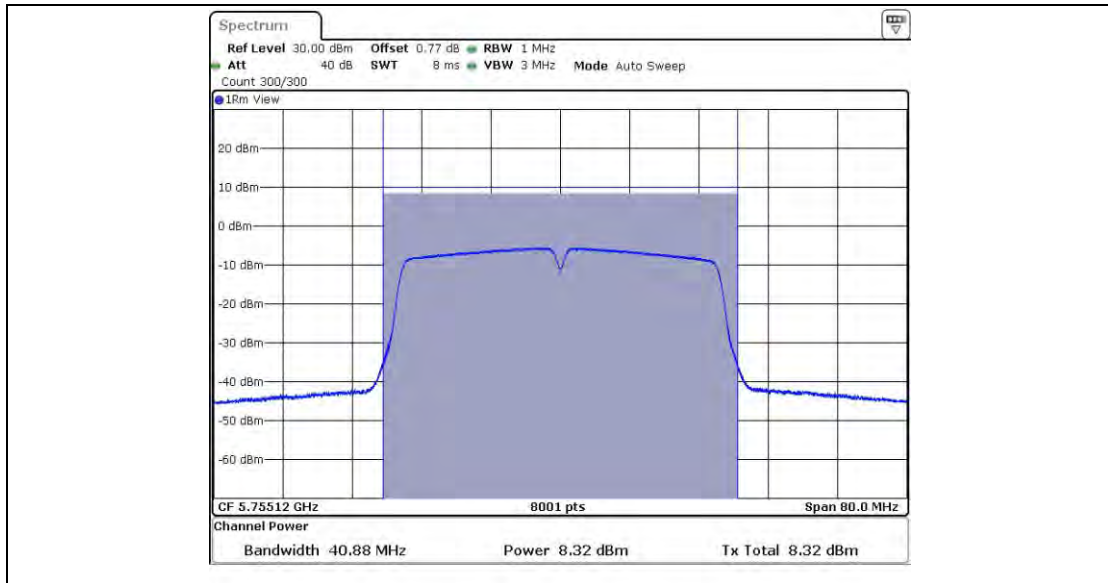
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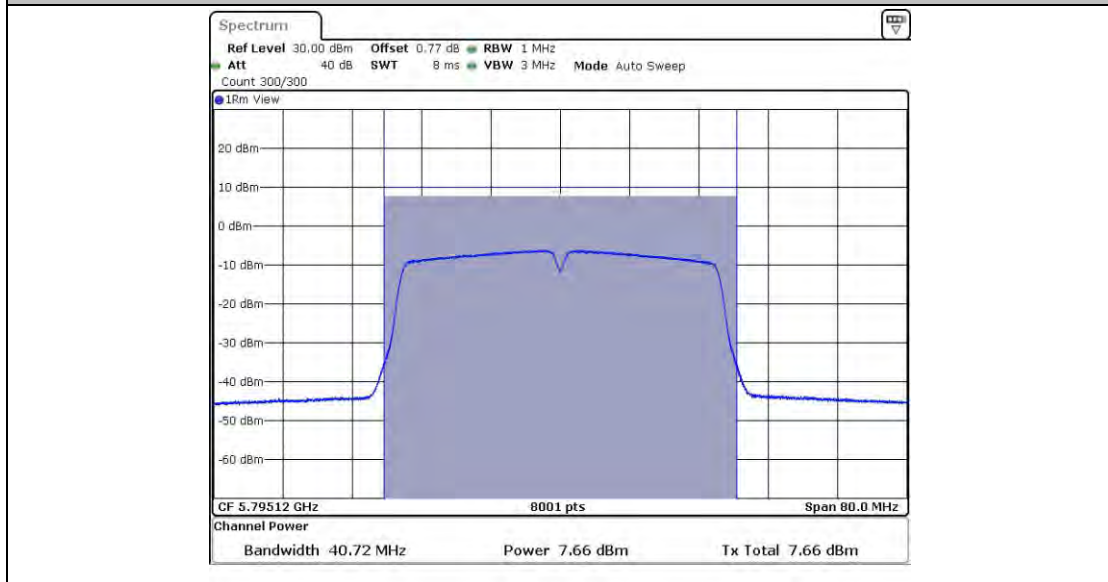
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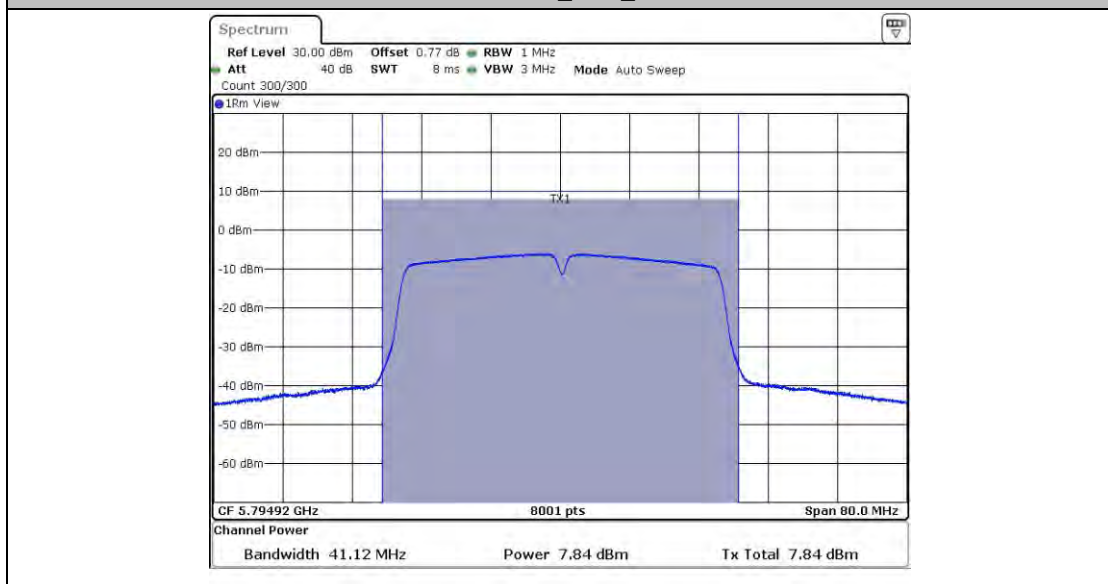
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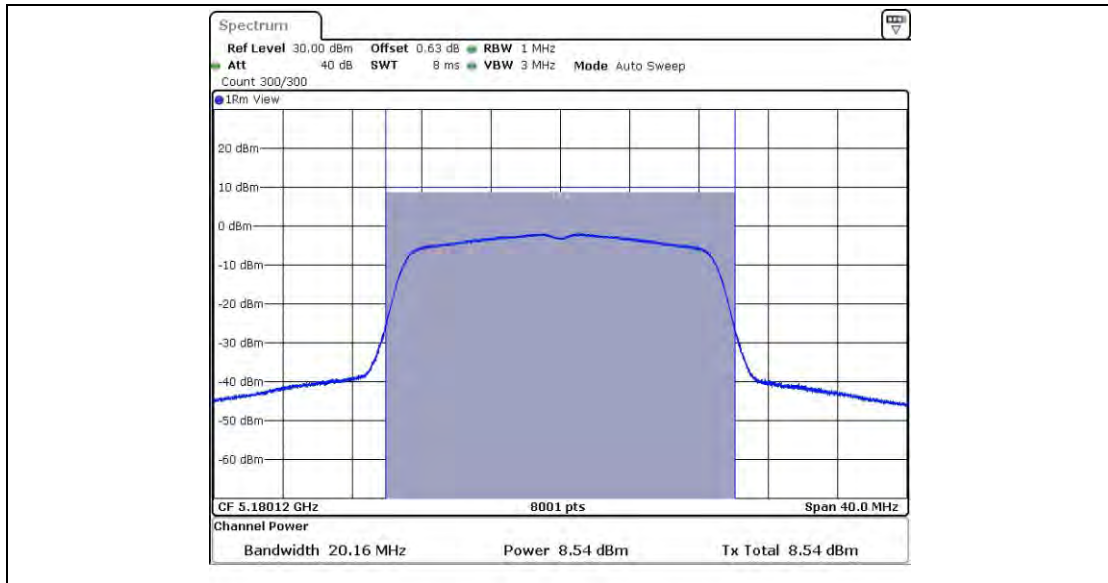
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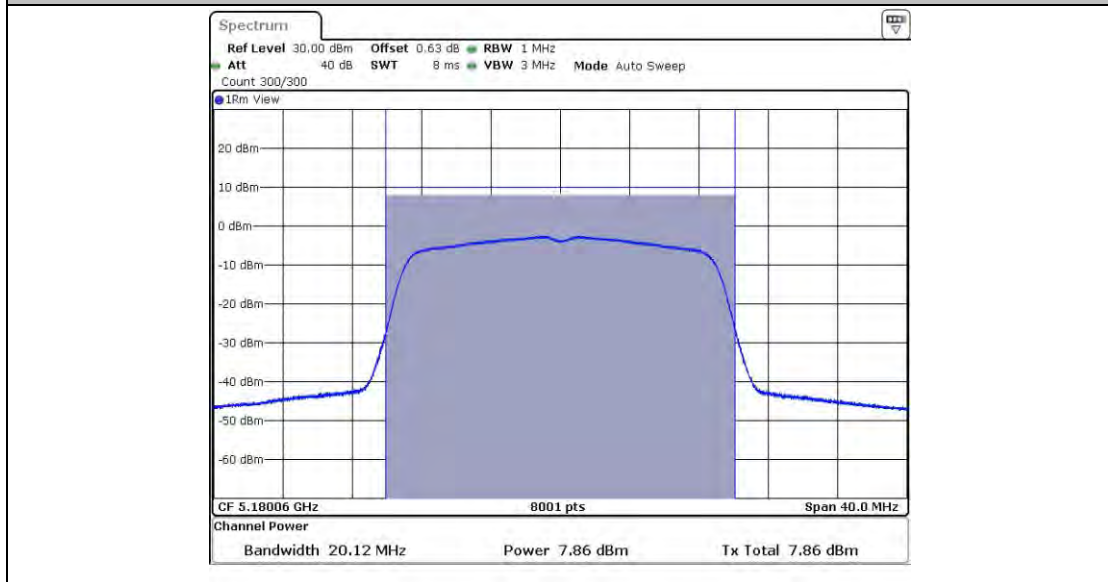
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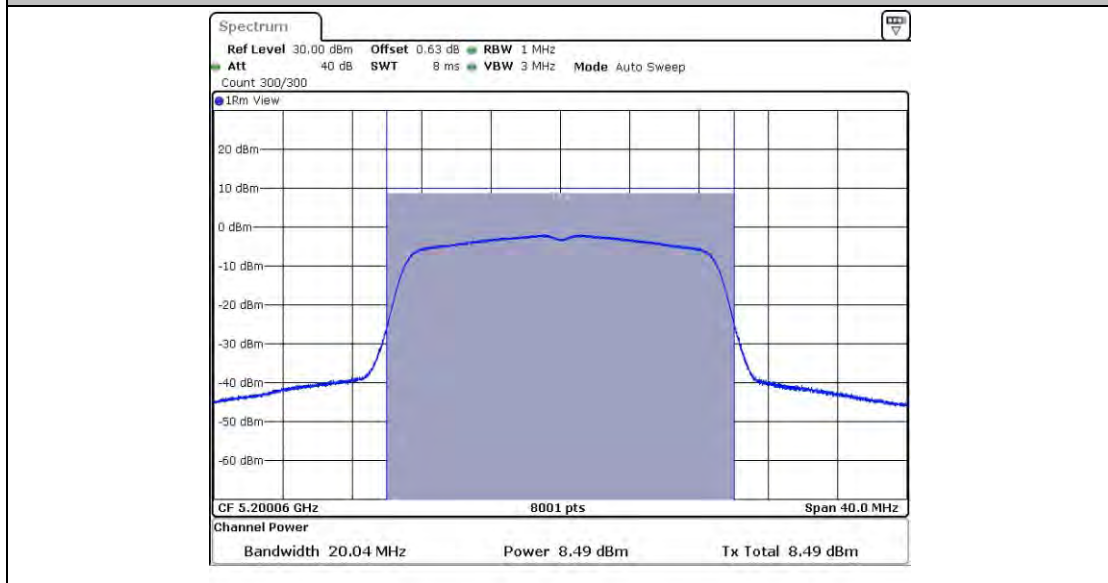
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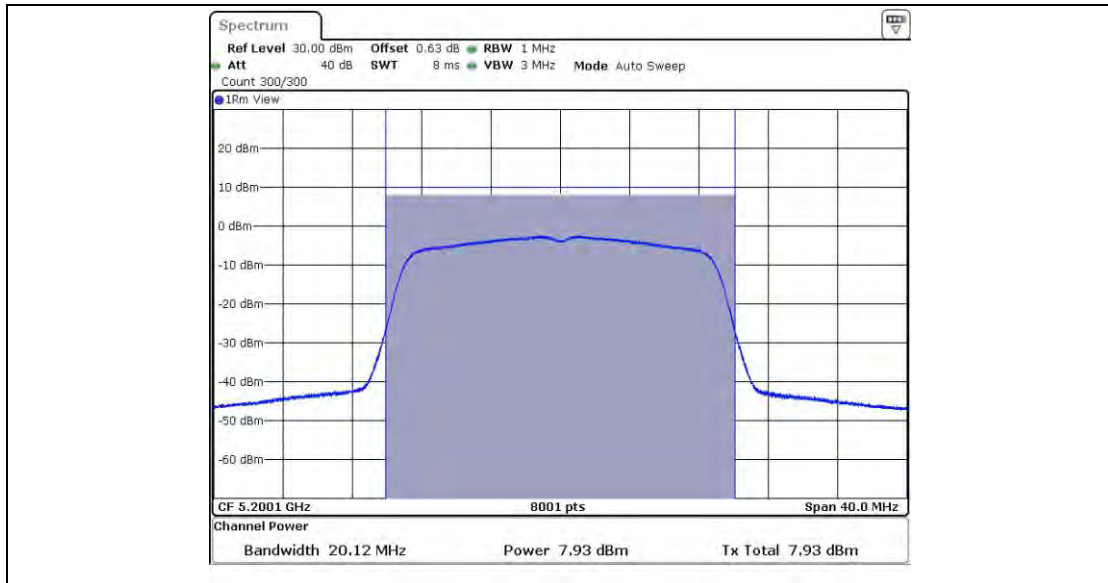
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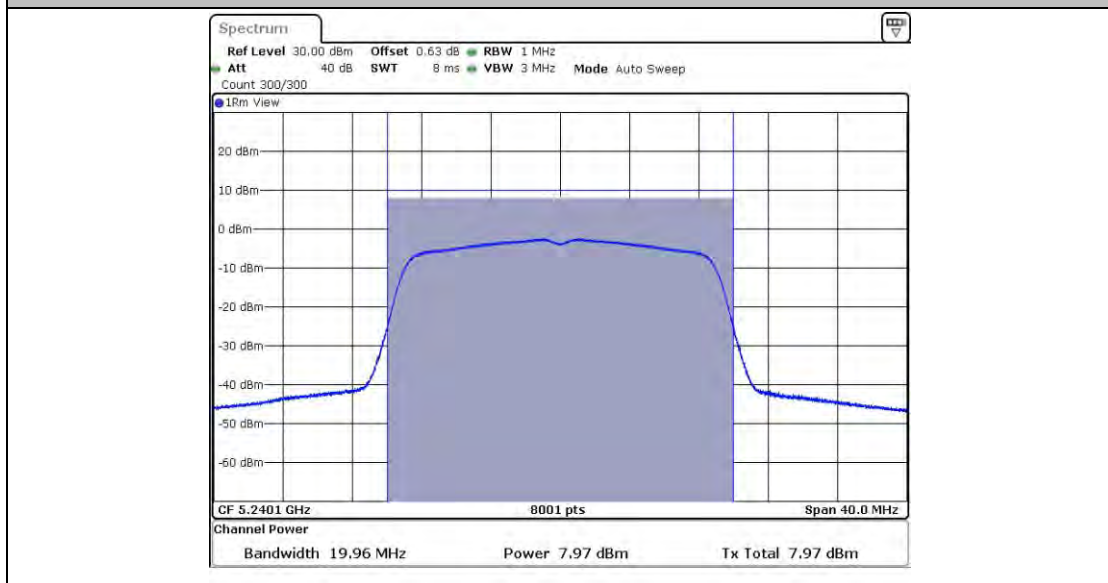
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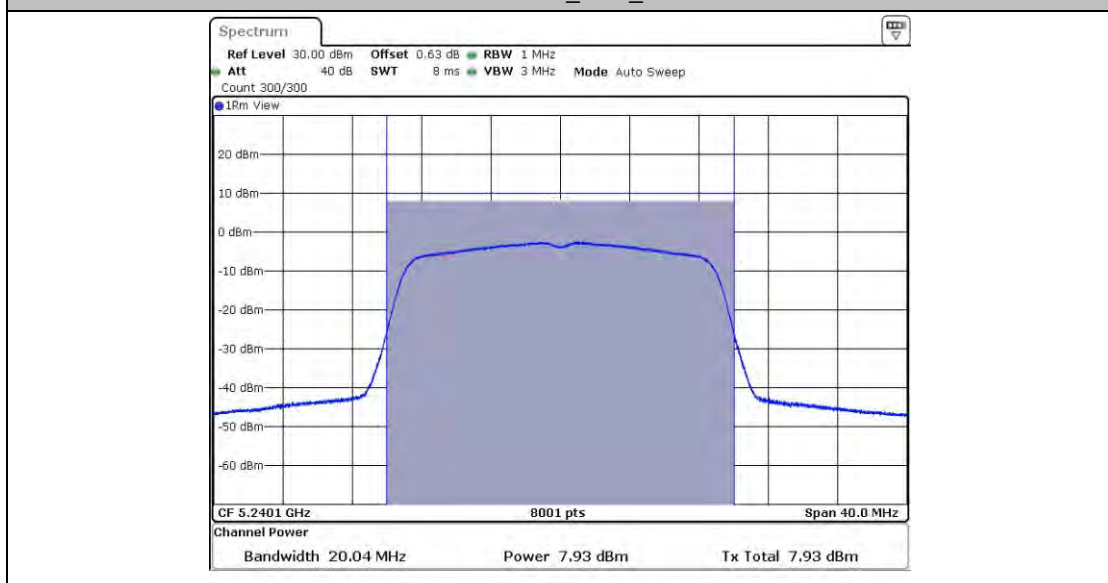
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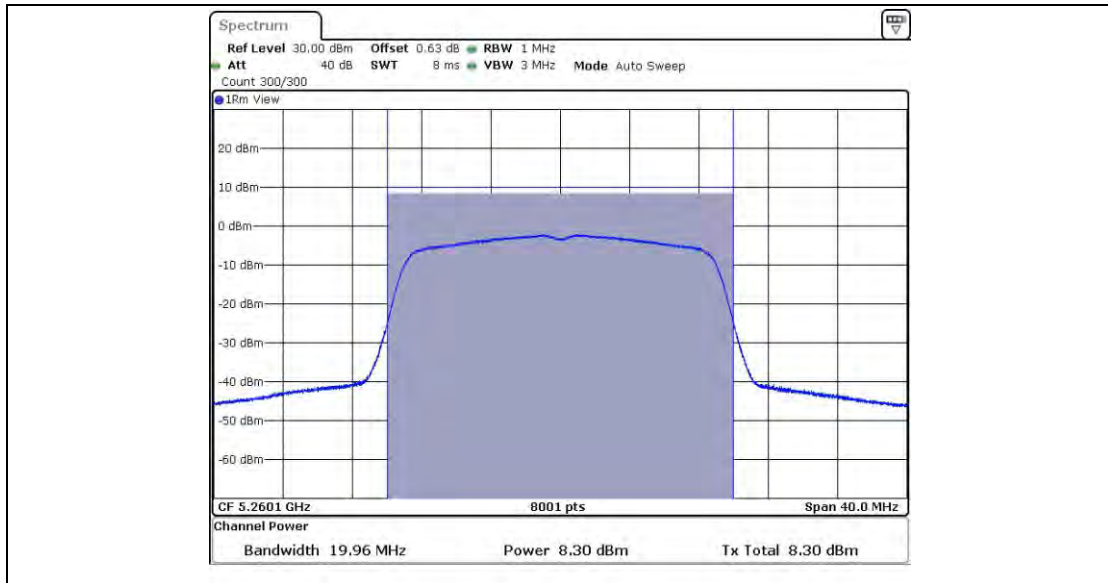
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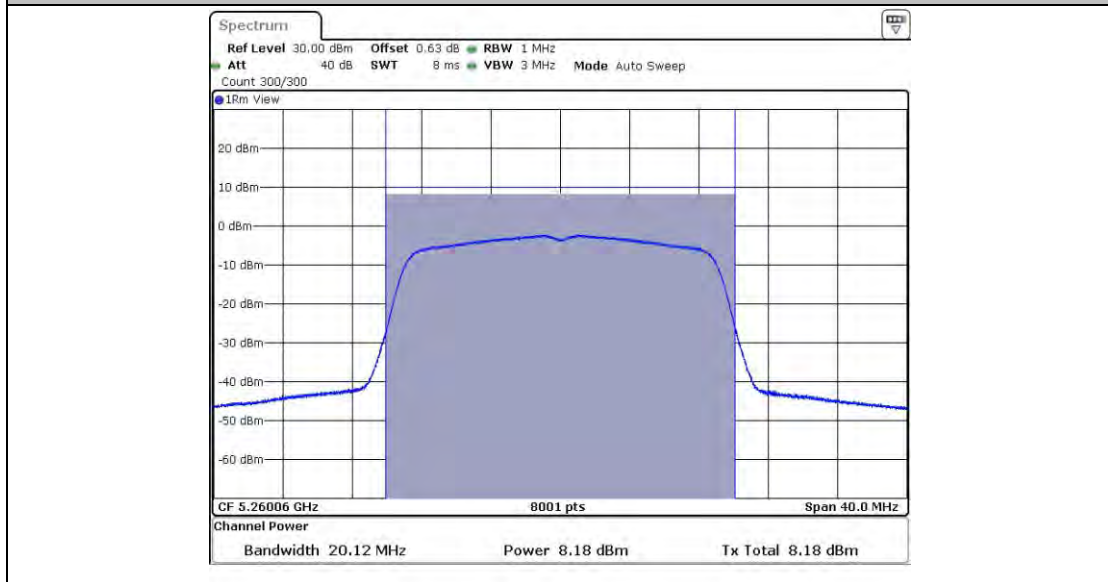
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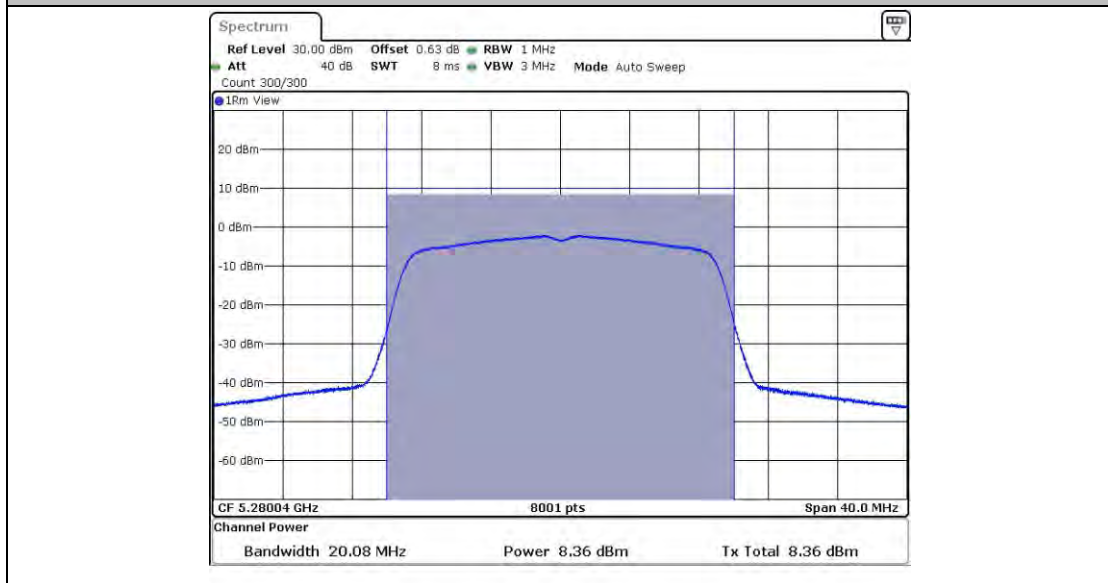
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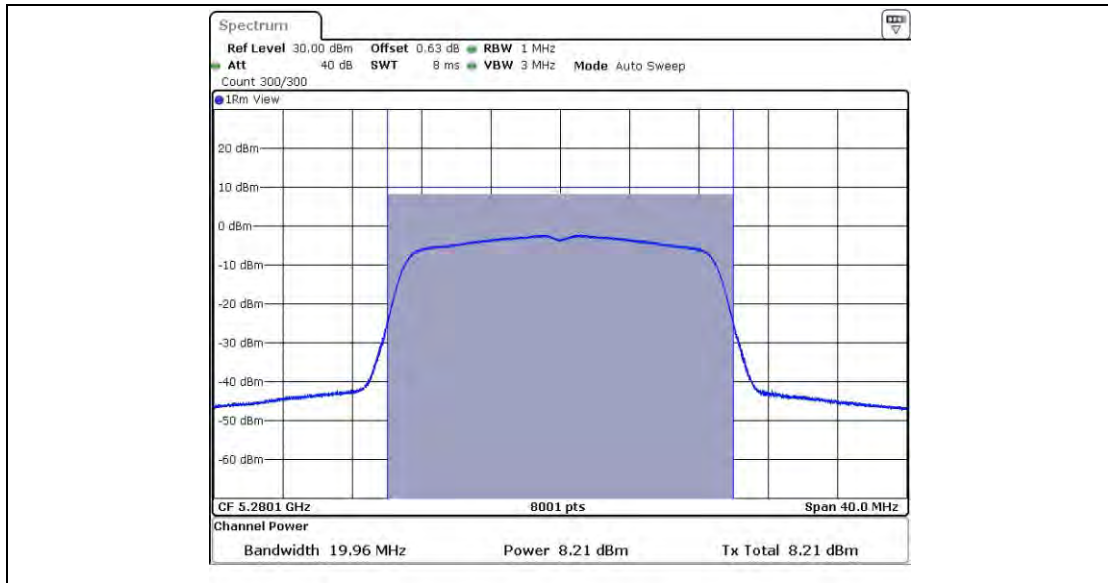
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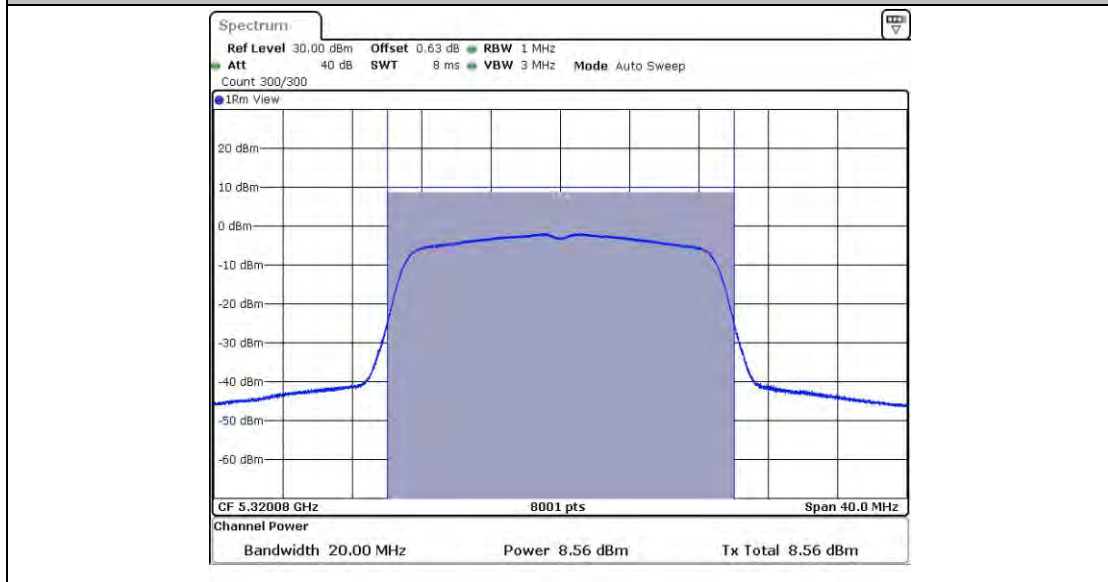
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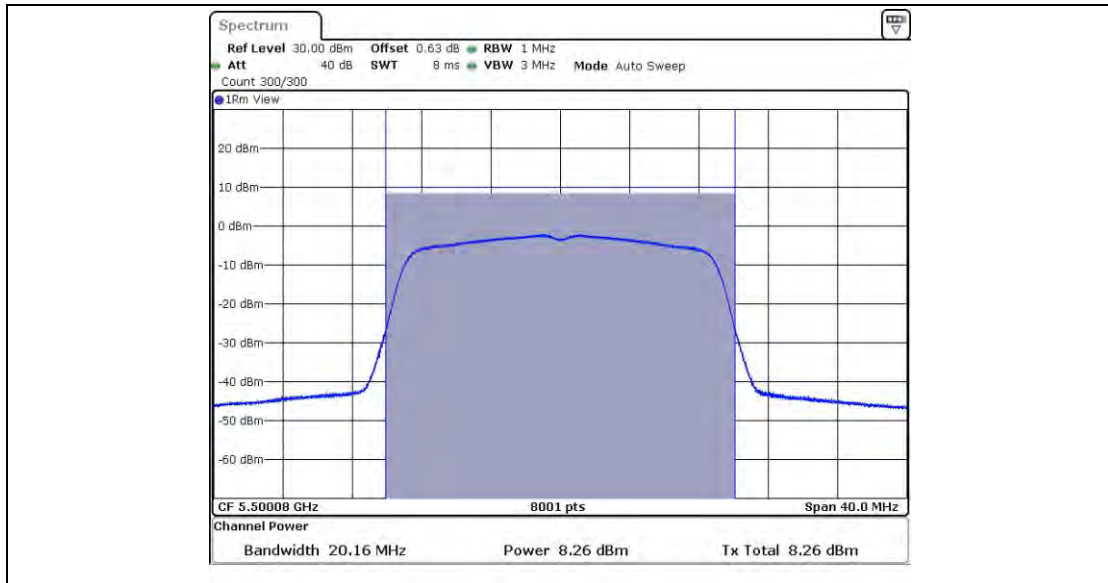
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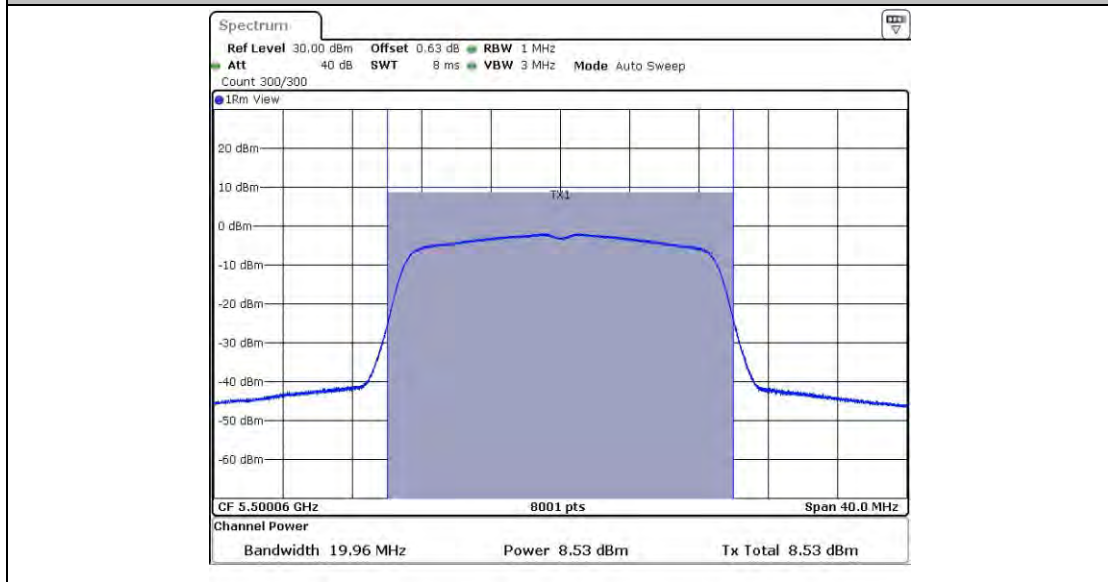
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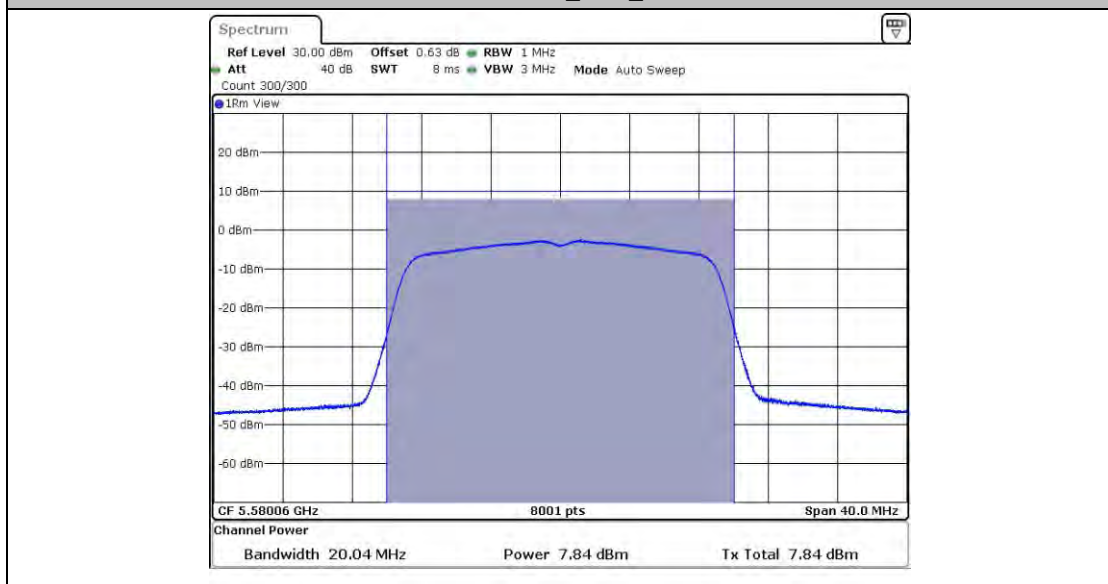
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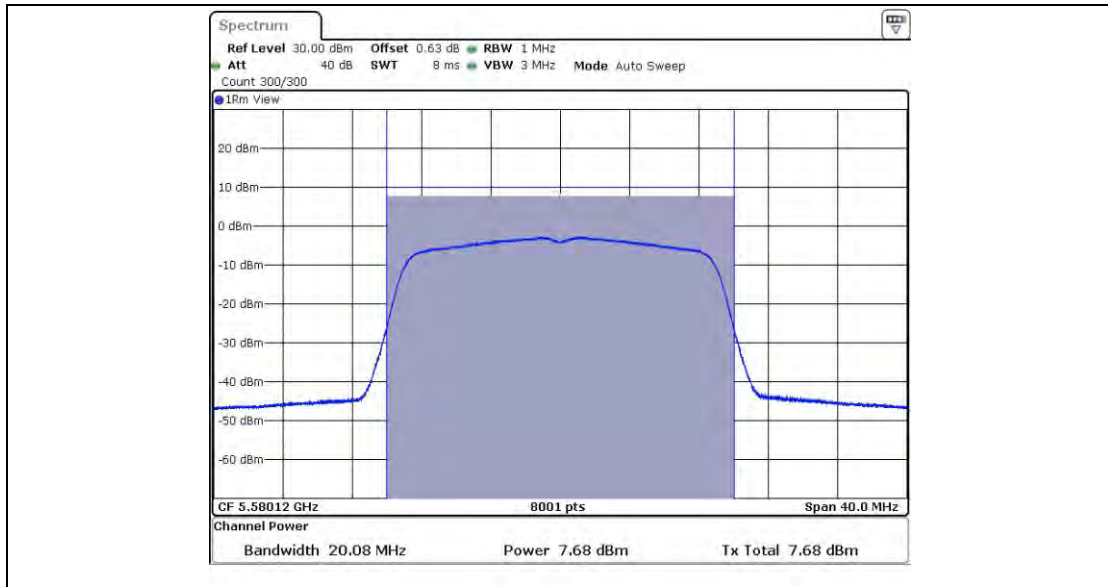
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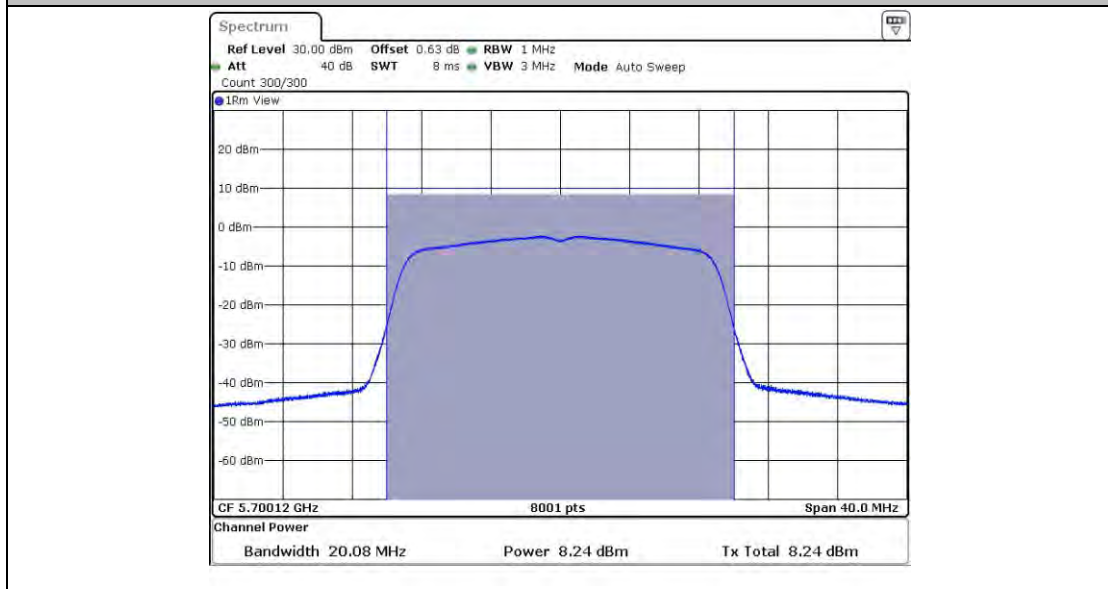
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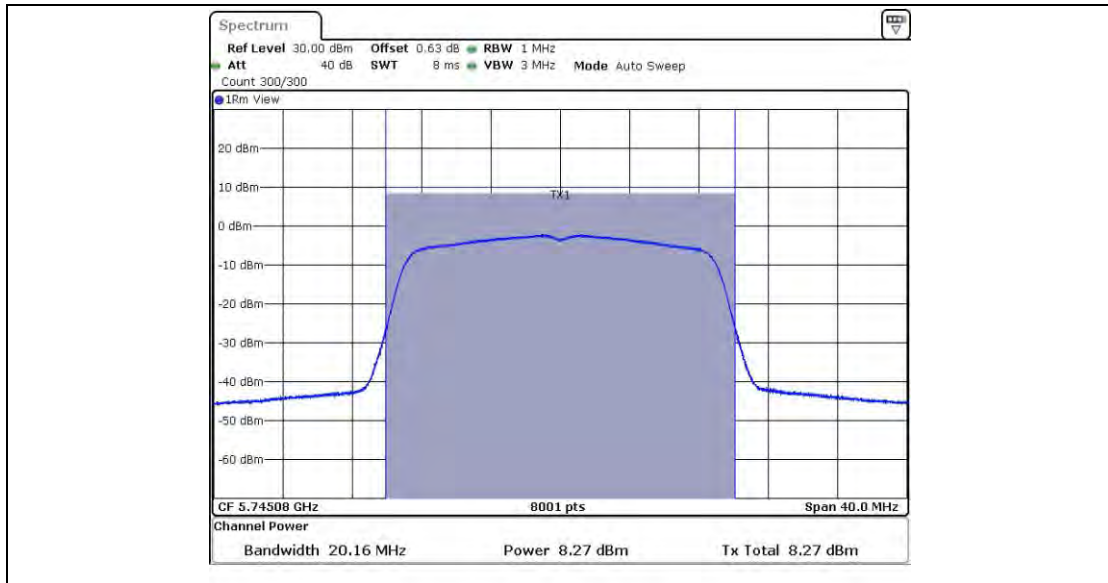
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11AC20SISO_Ant1_5700



11AC20SISO_Ant2_5700



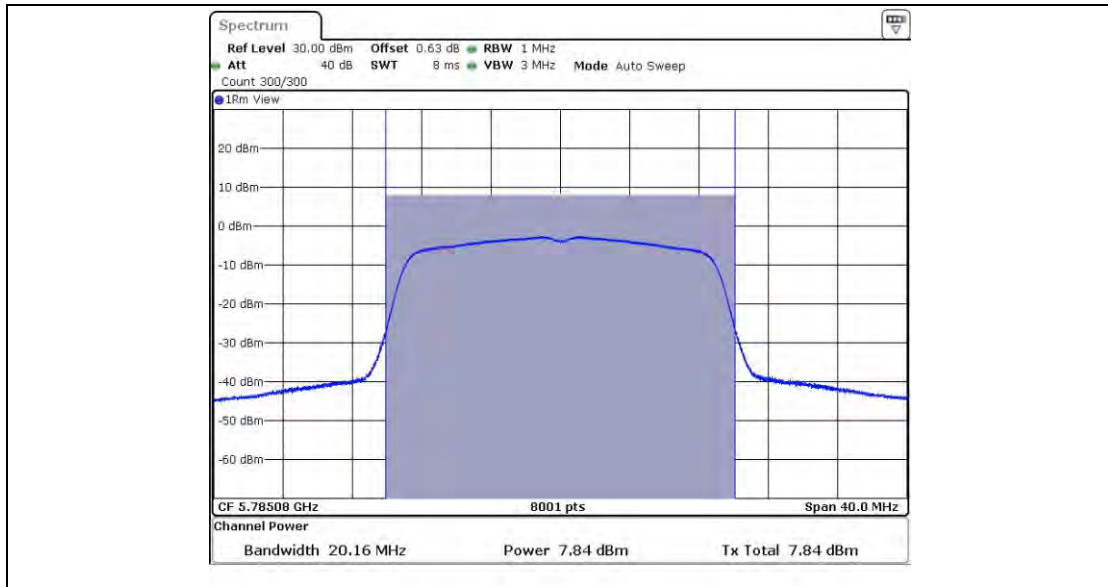
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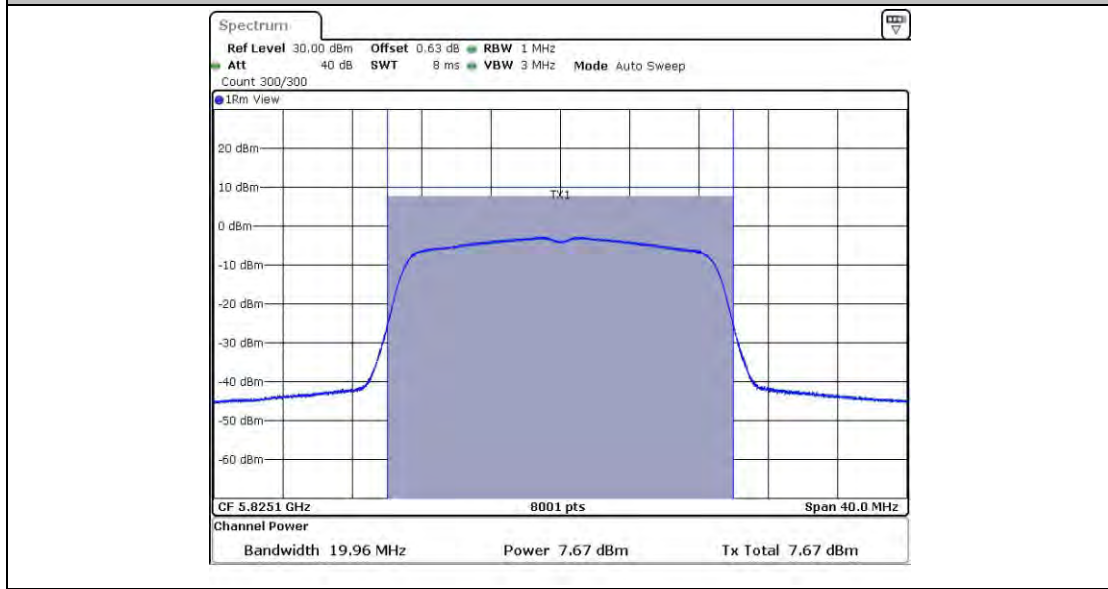
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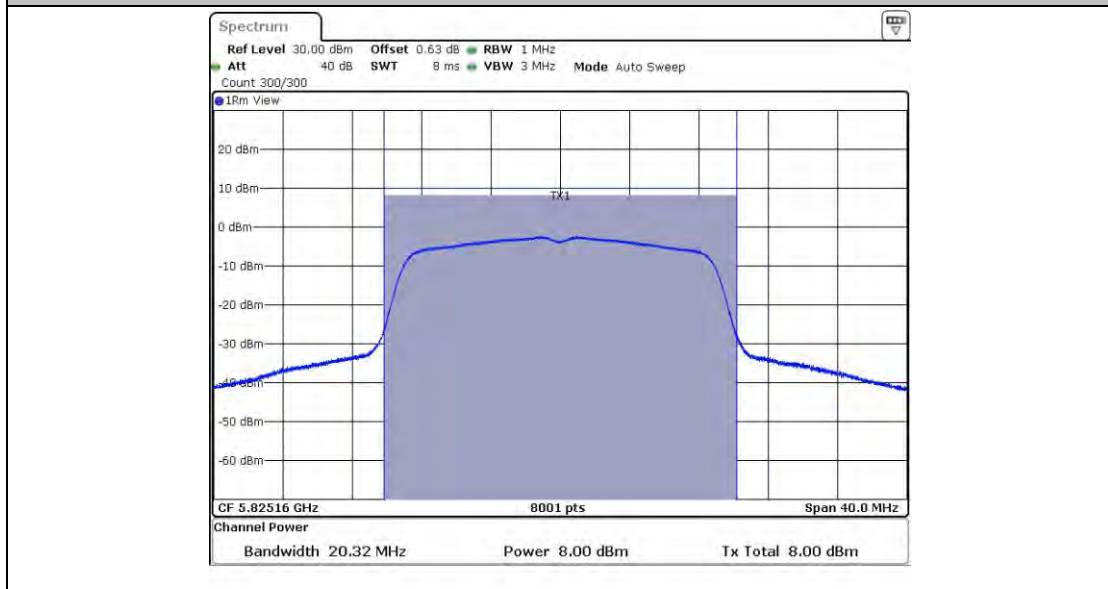
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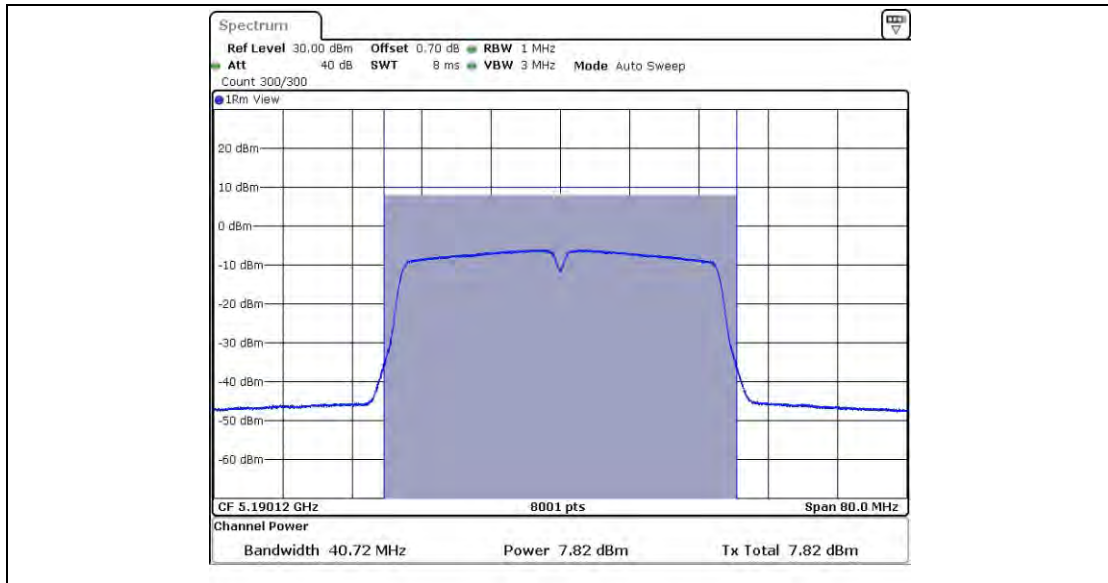
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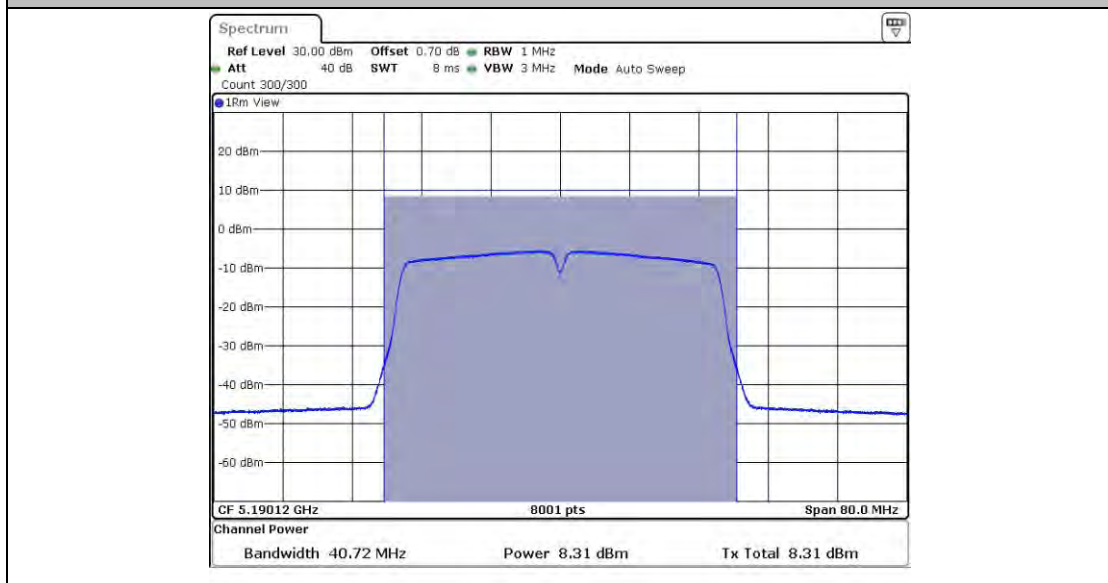
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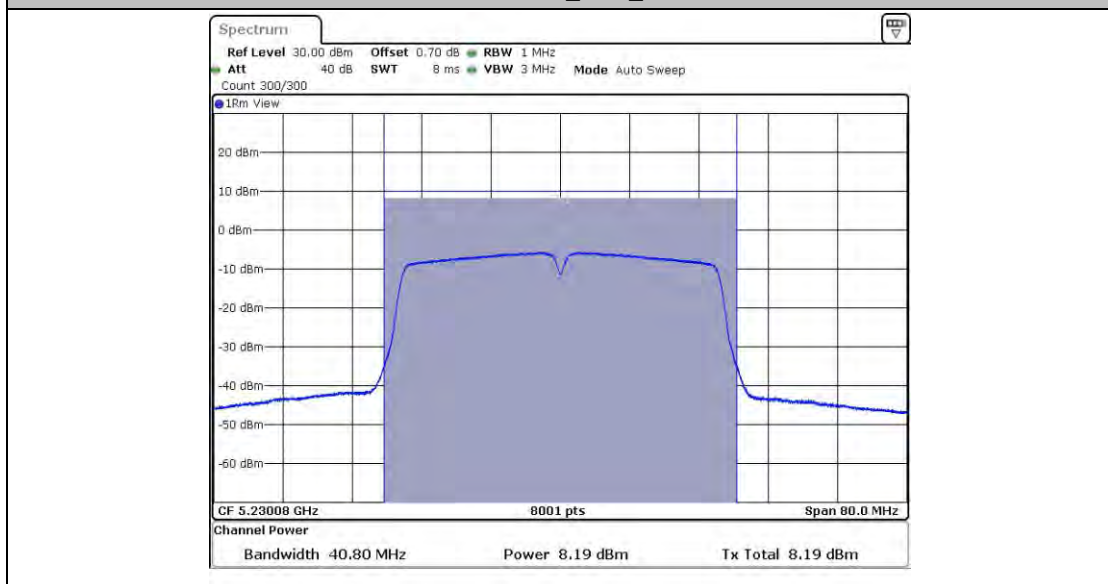
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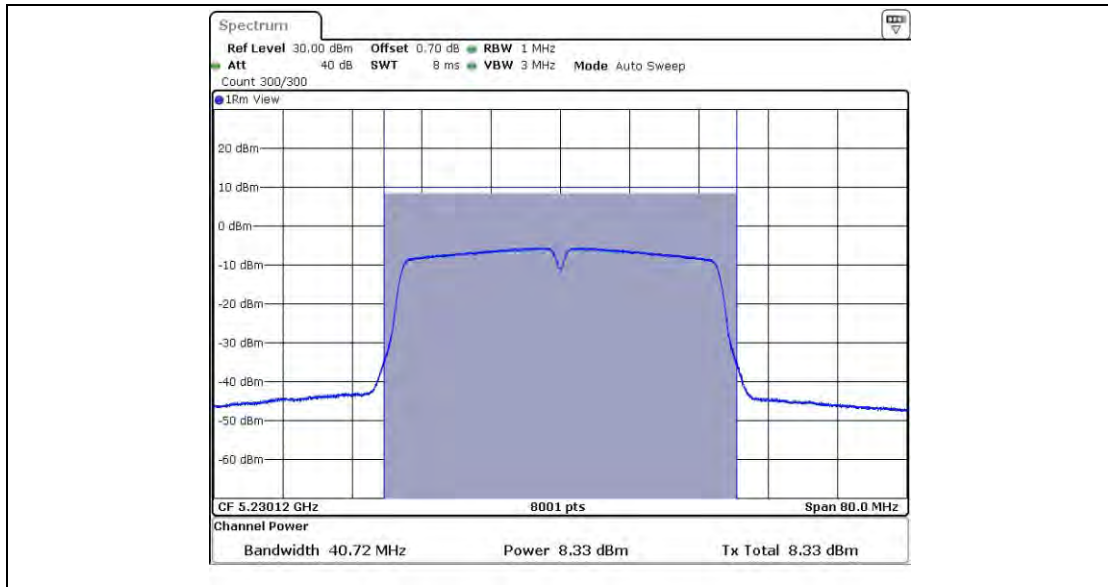
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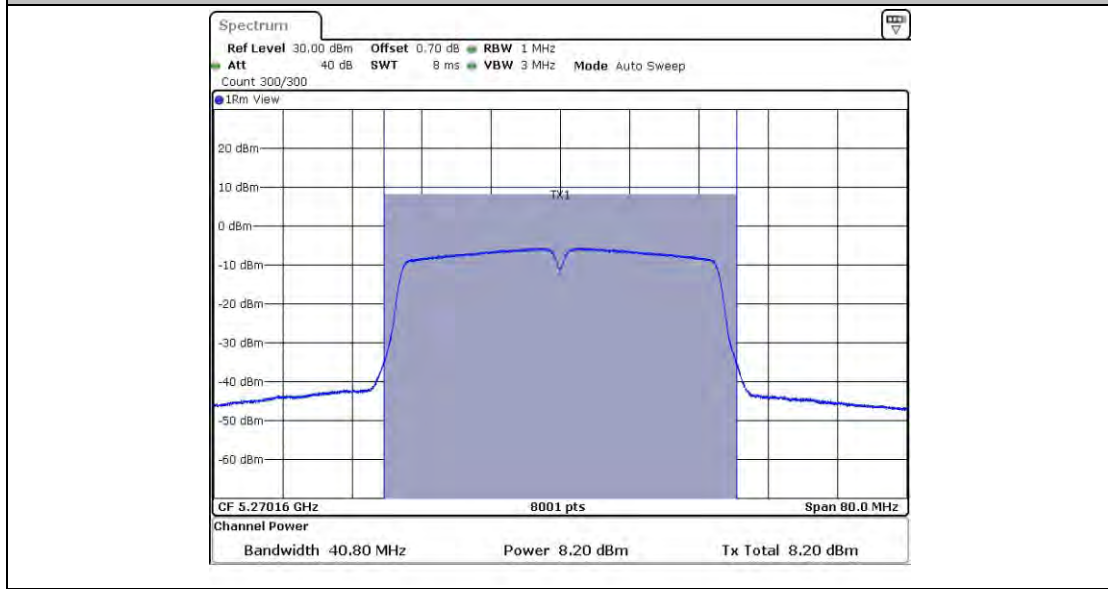
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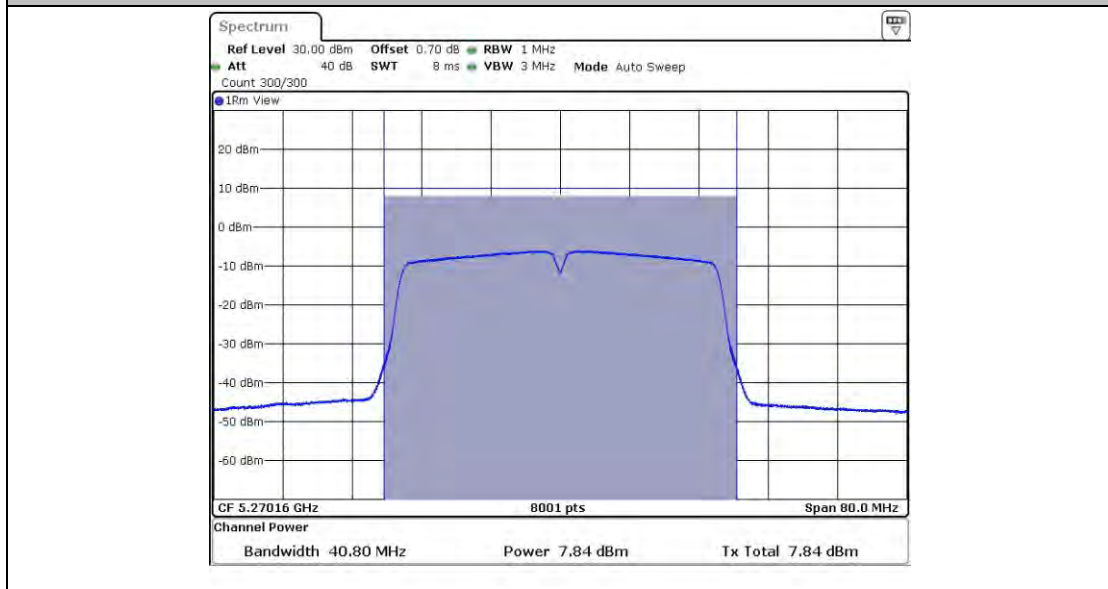
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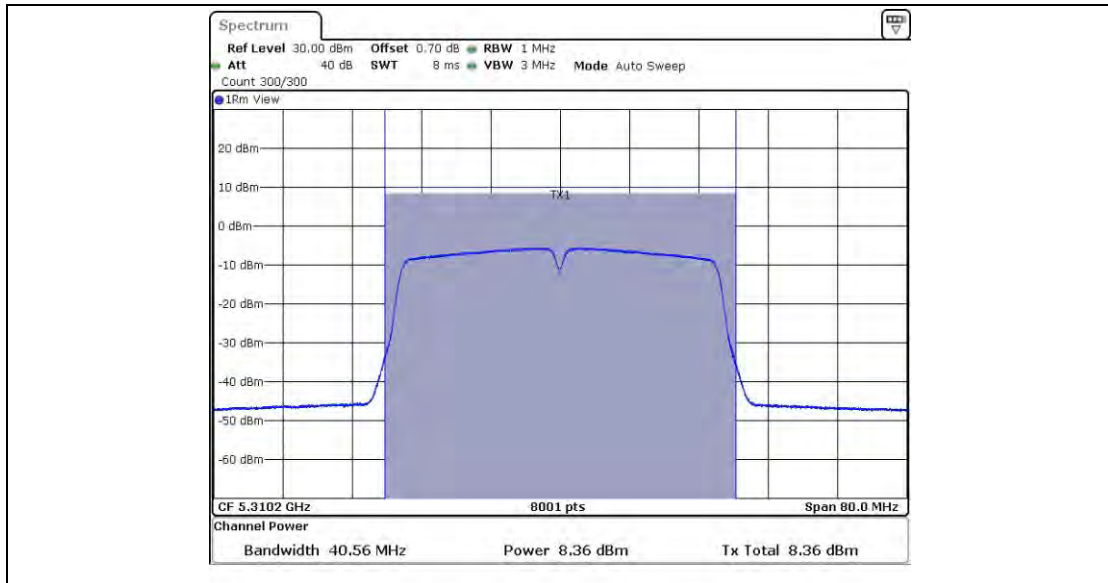
11AC40SISO_Ant2_5230



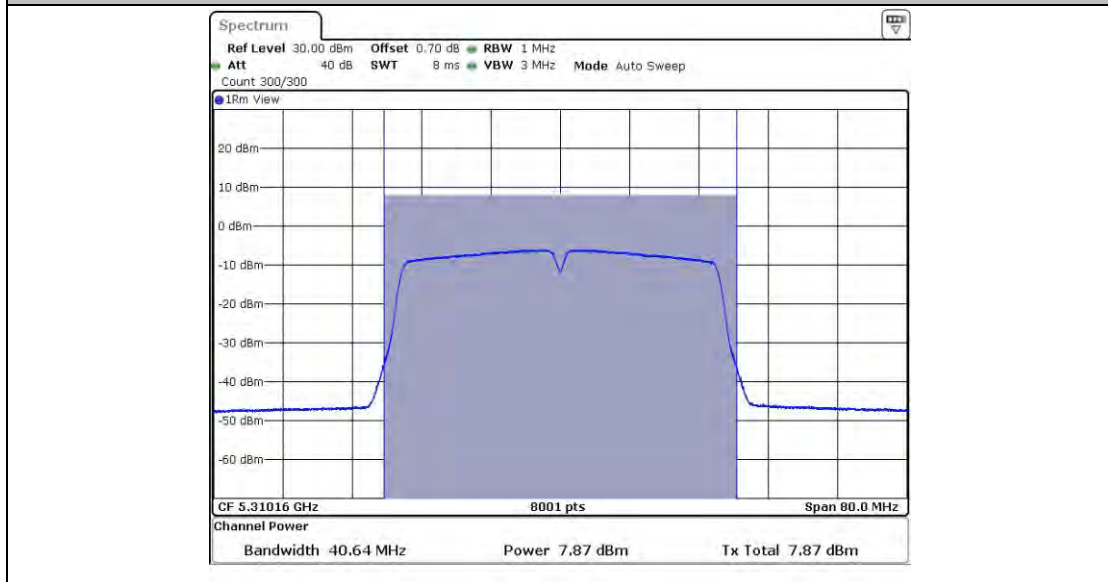
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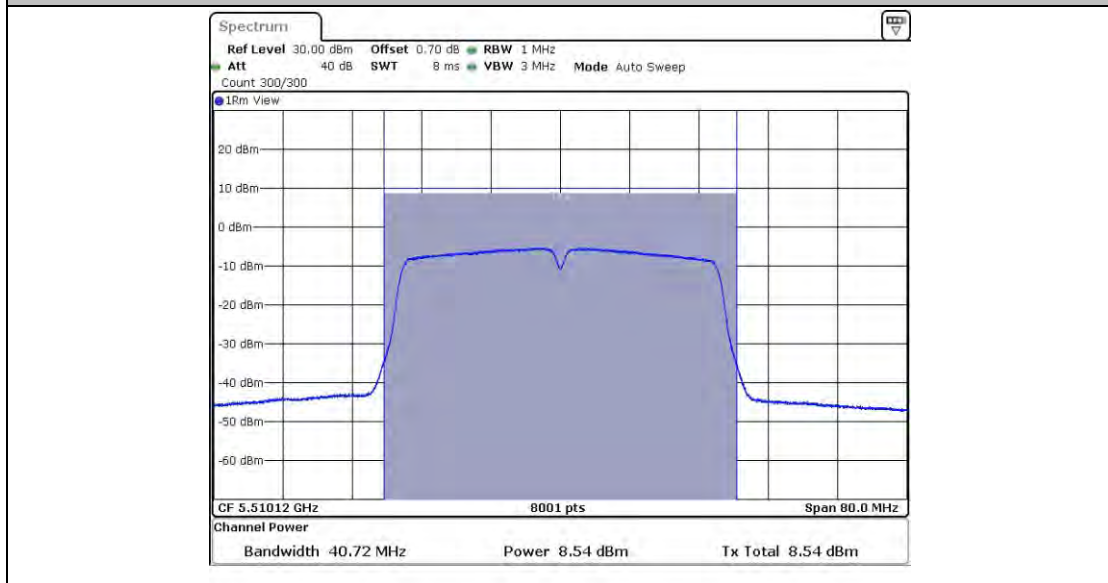
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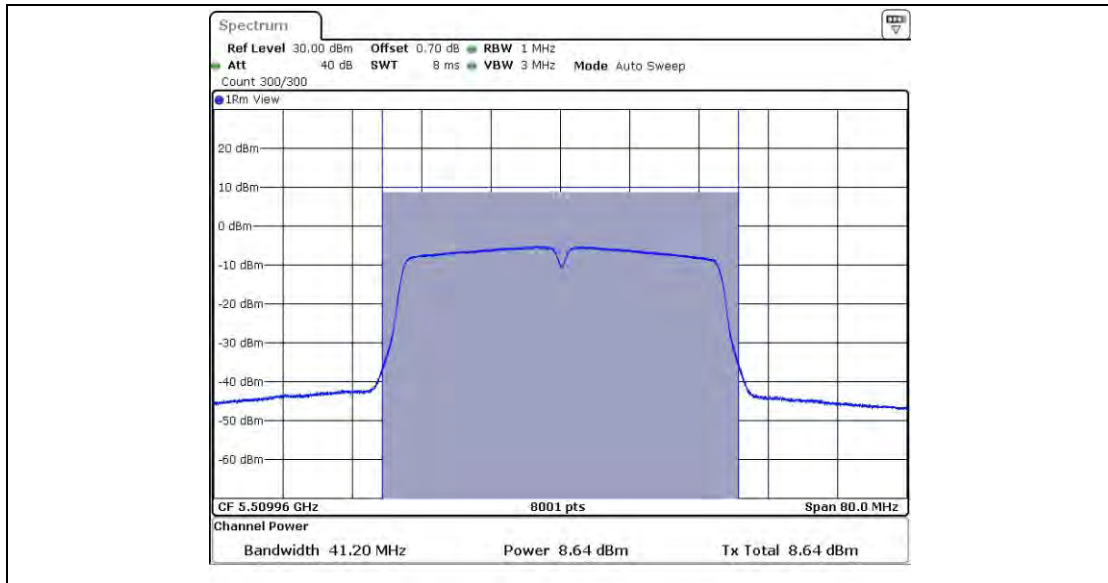
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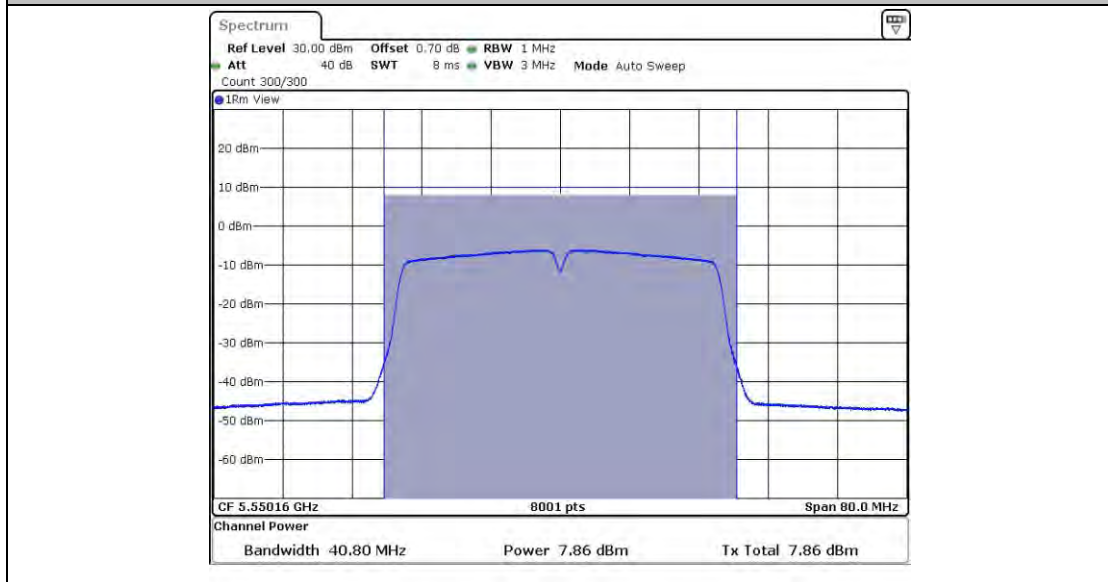
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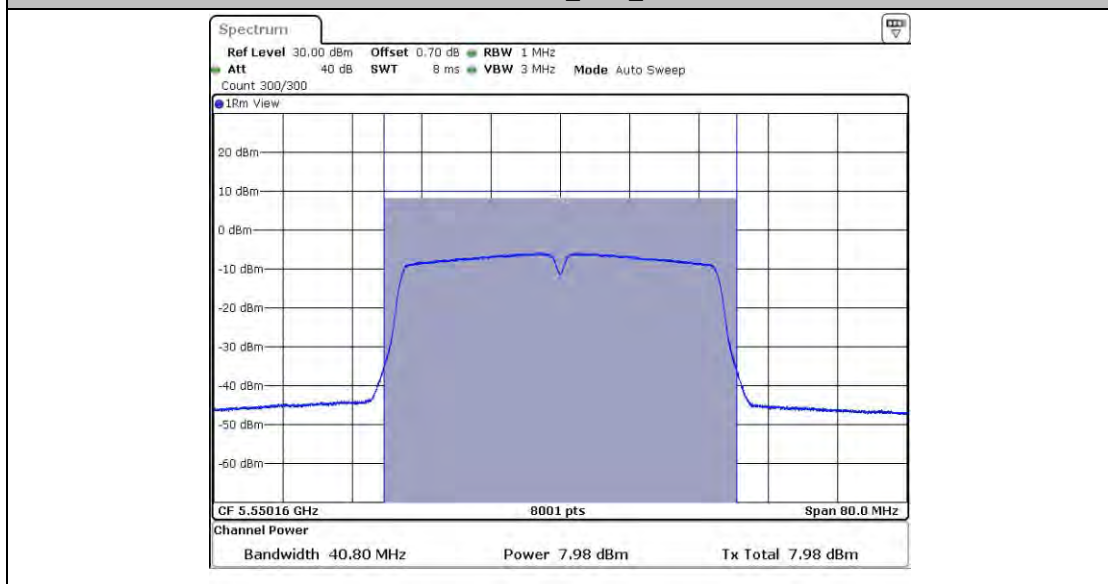
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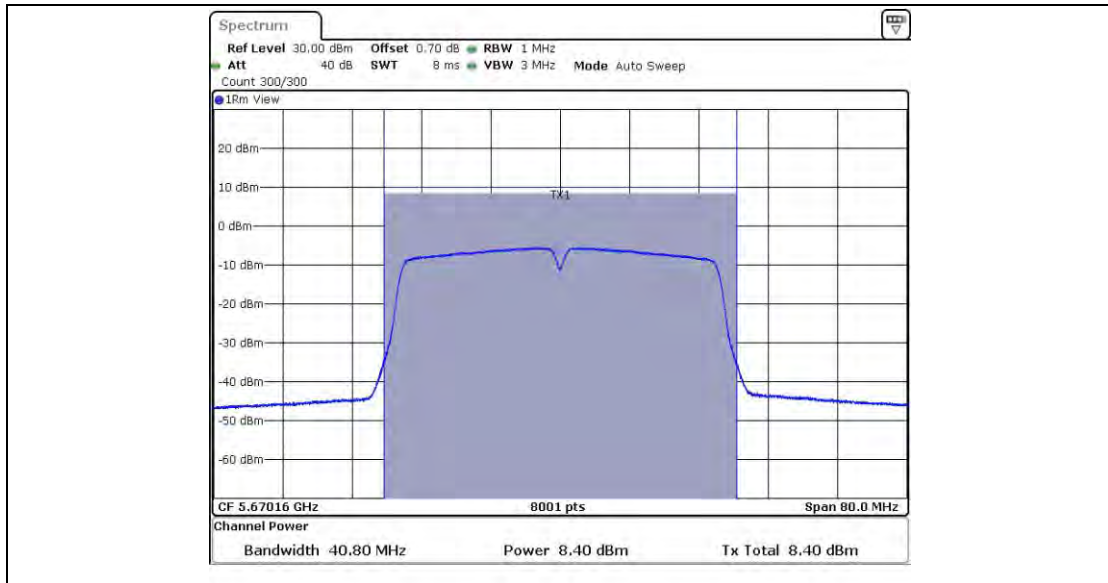
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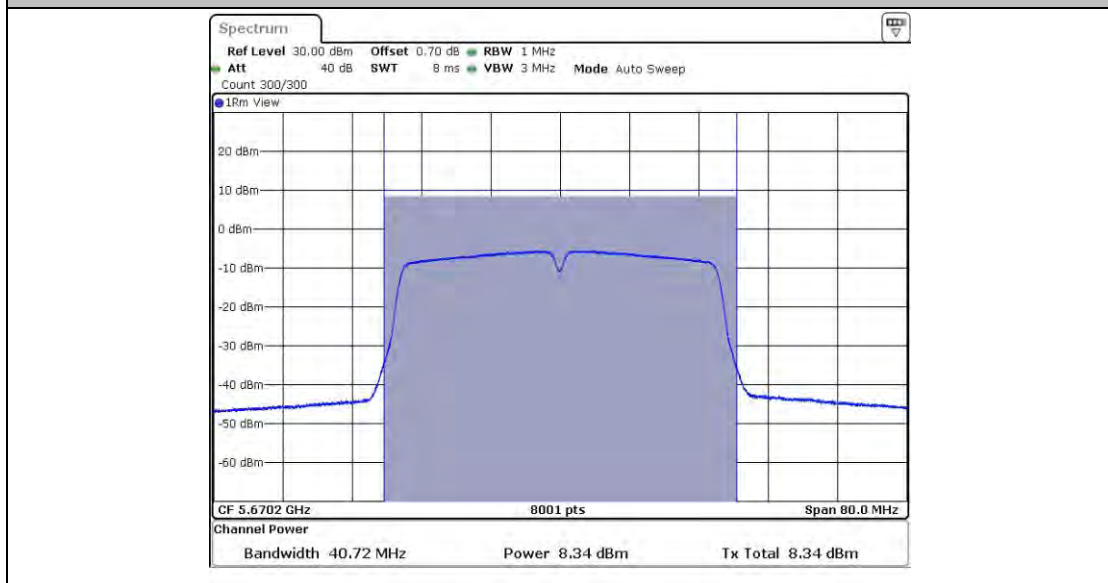
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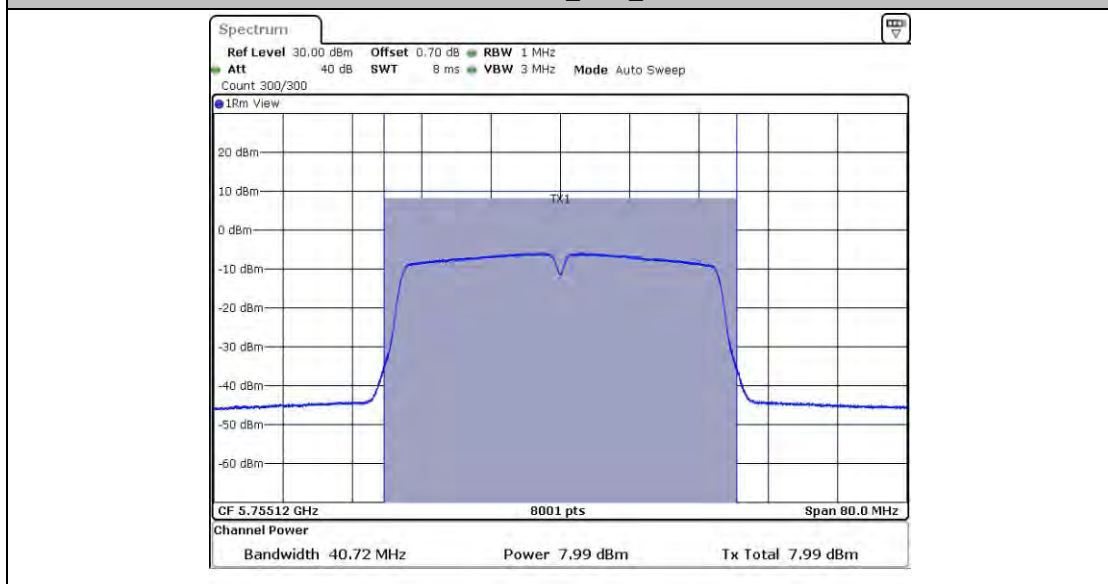
11AC40SISO_Ant2_5550



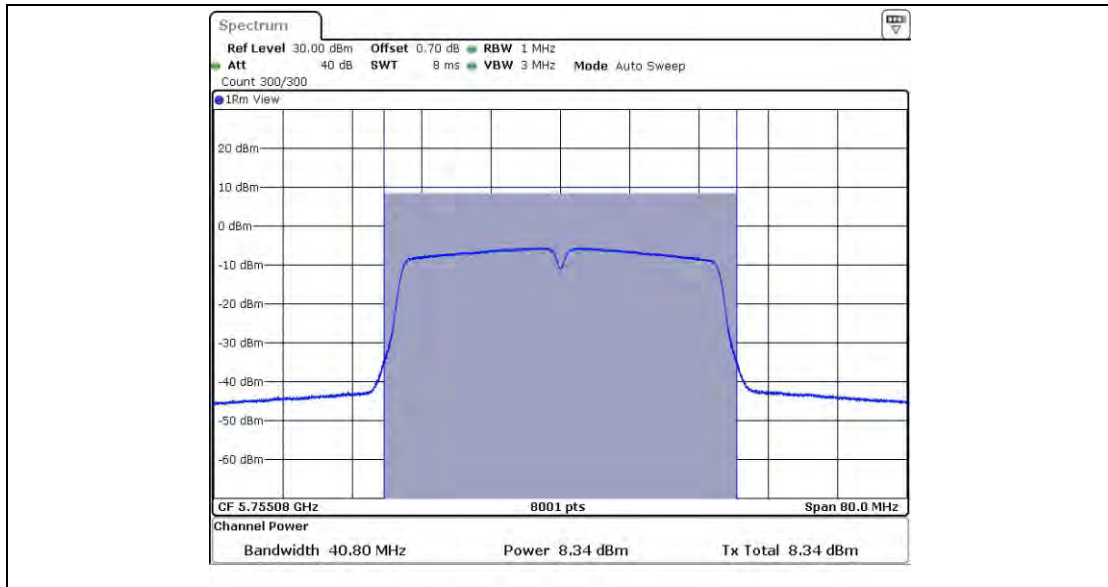
11AC40SISO_Ant1_5670



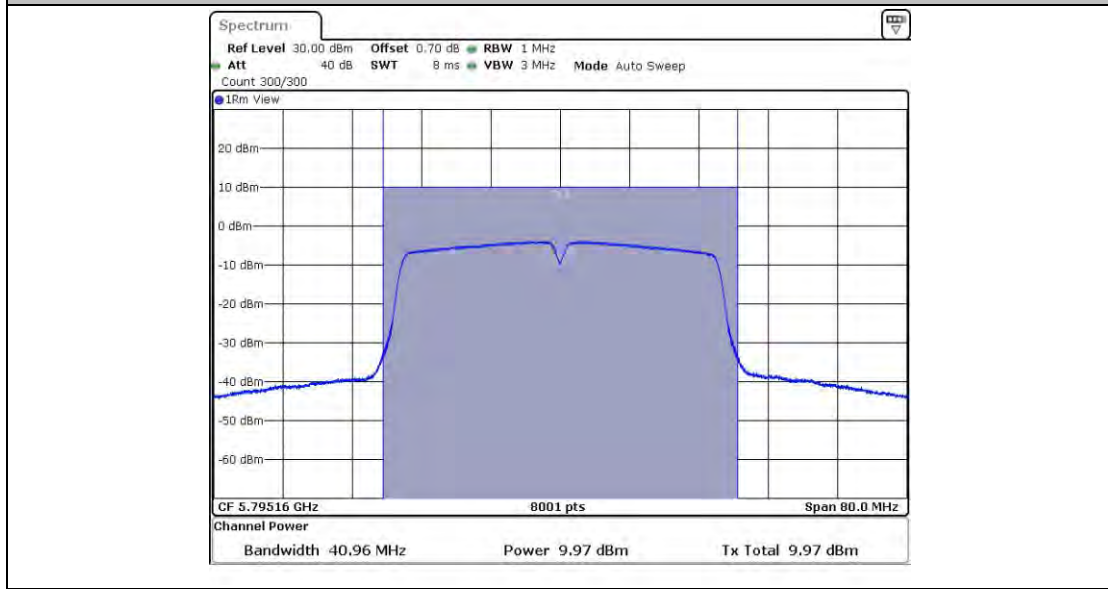
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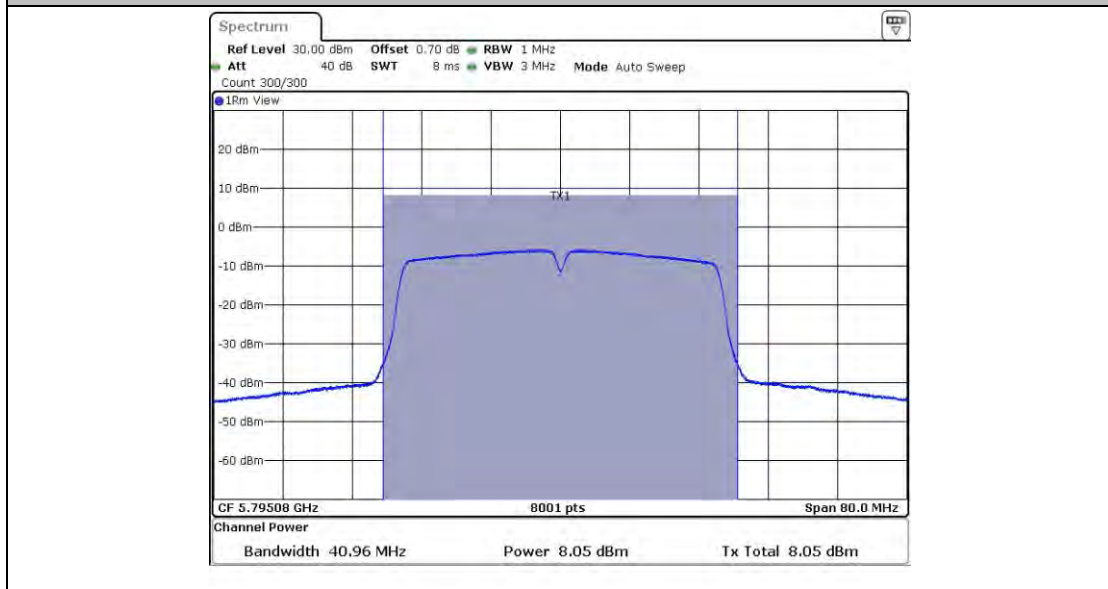
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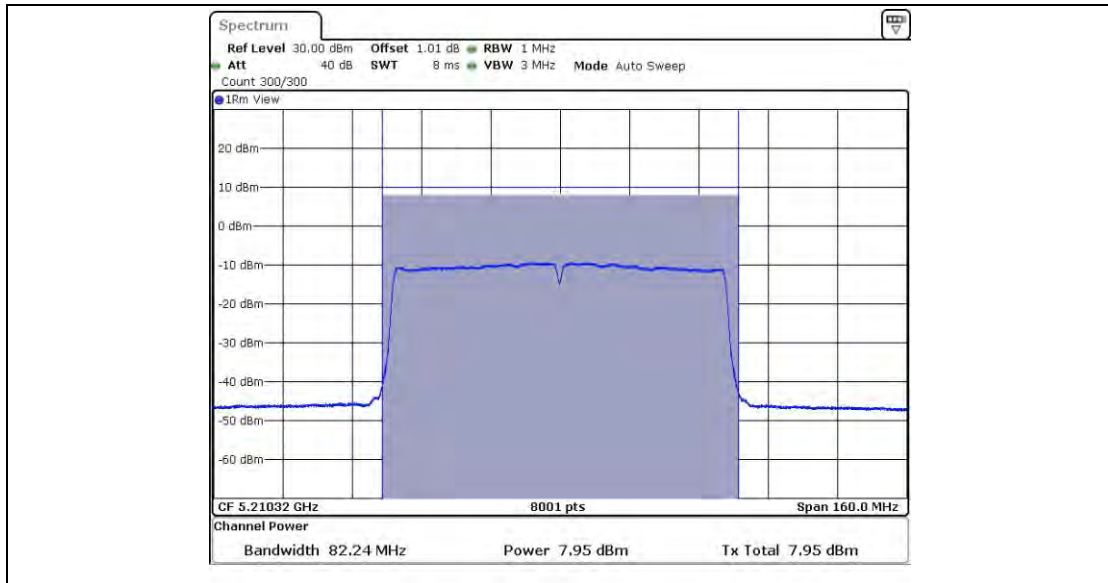
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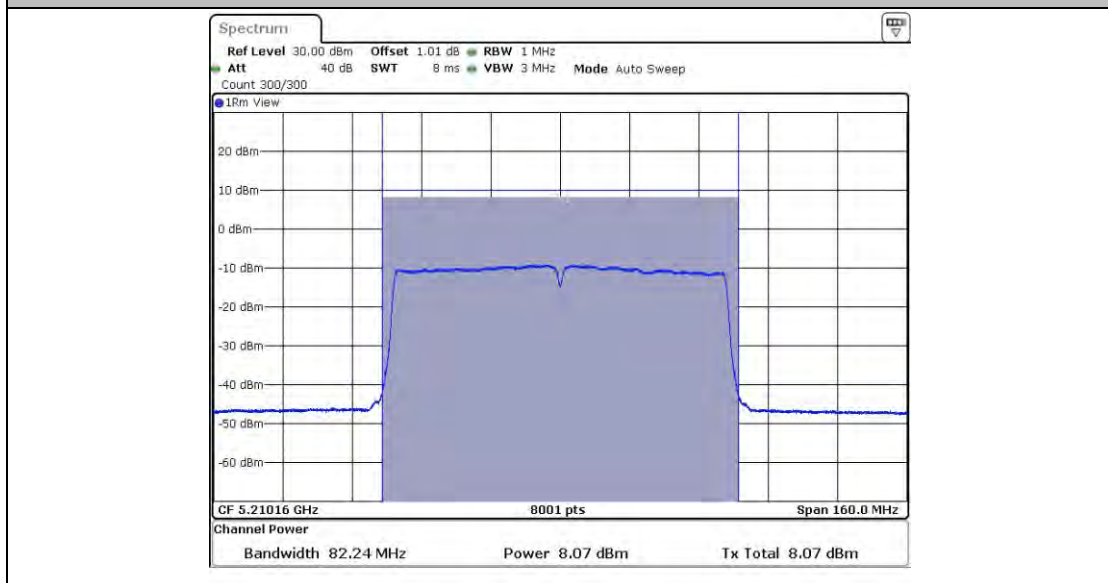
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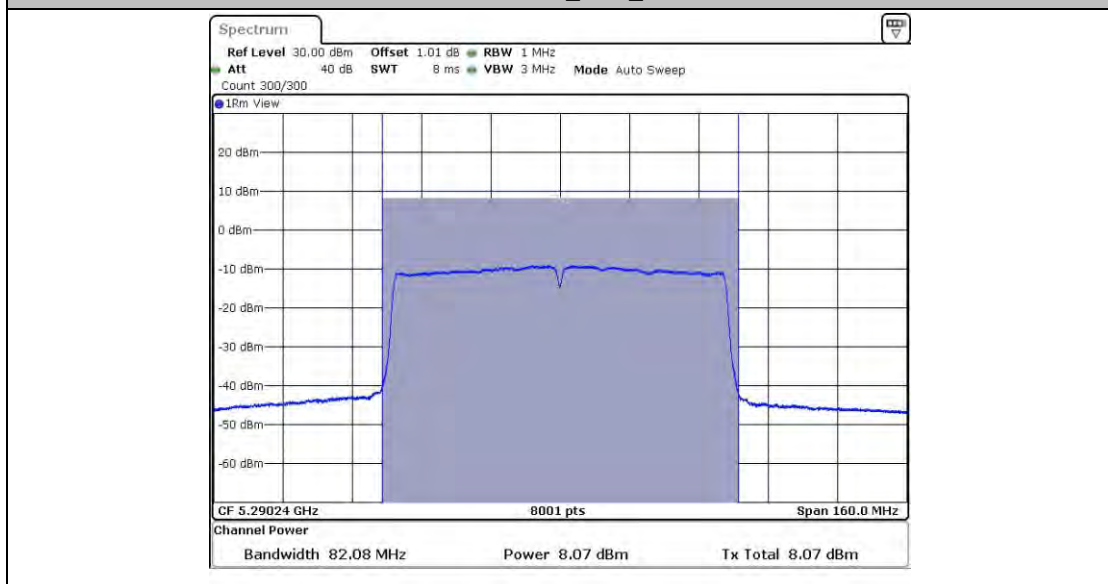
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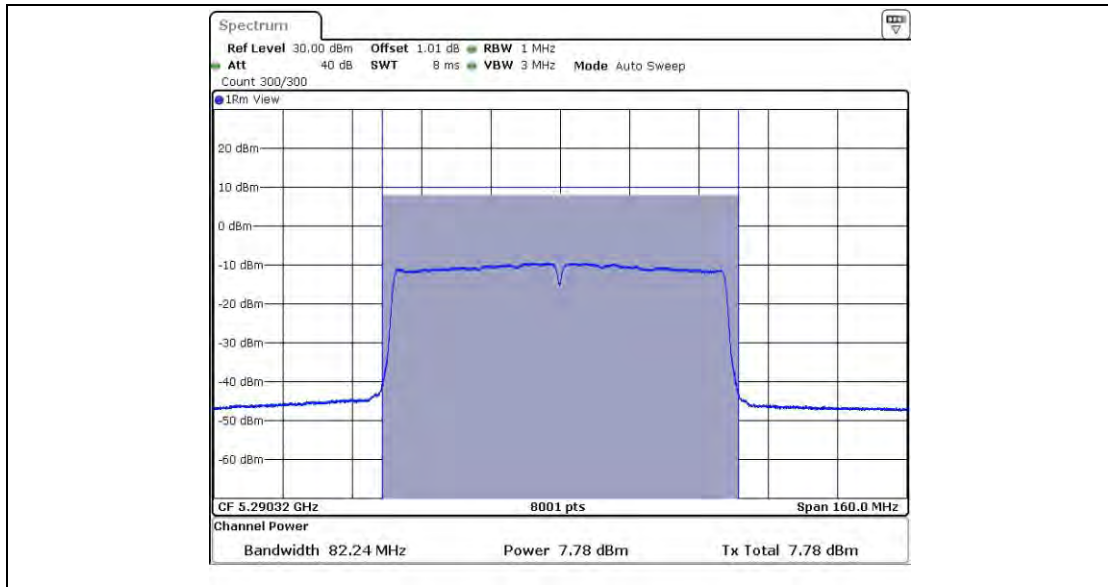
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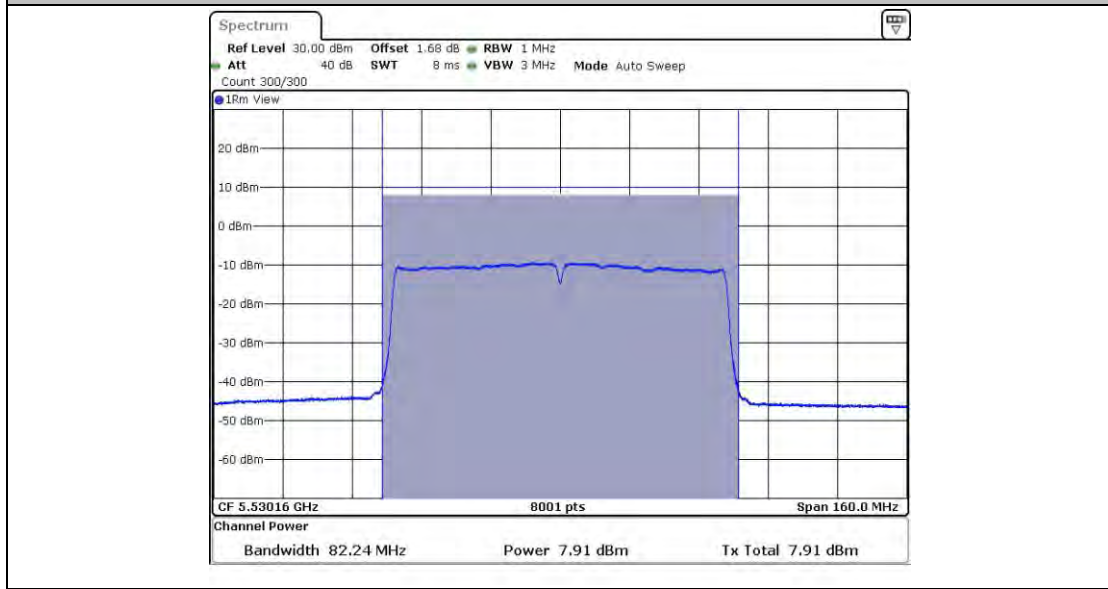
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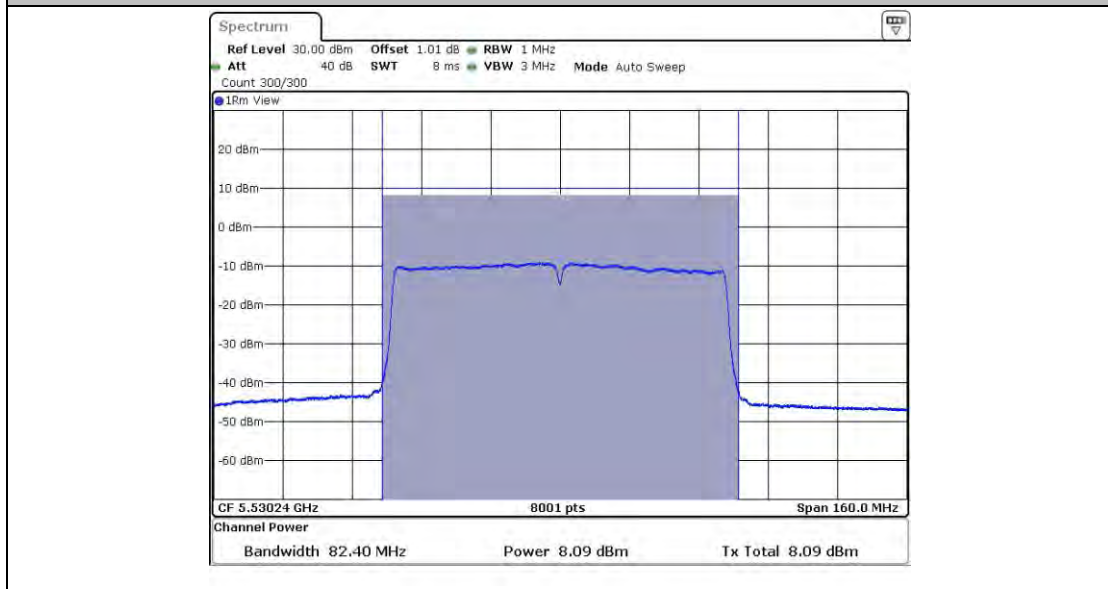
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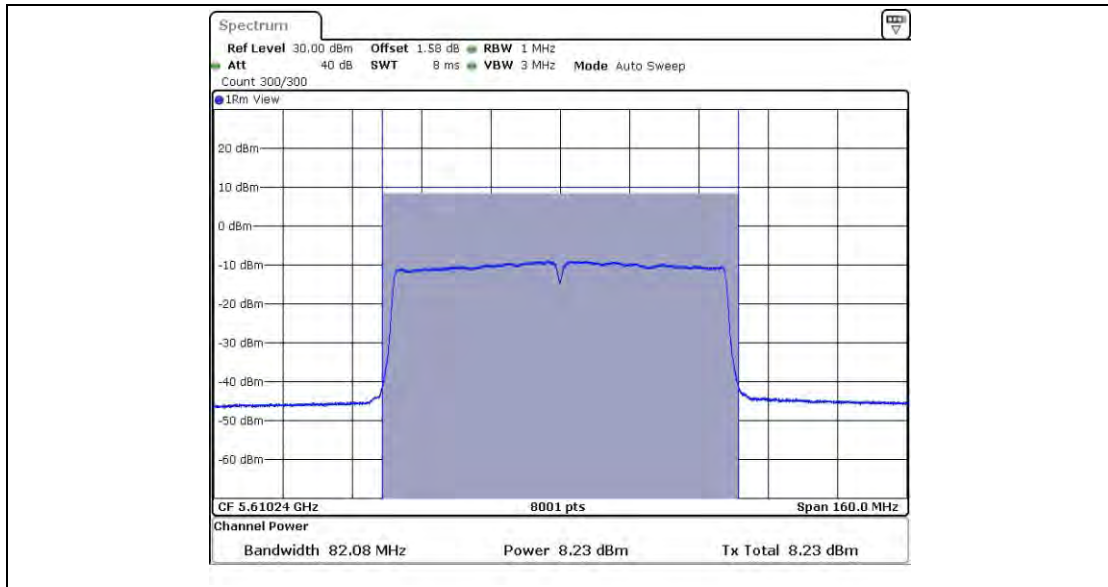
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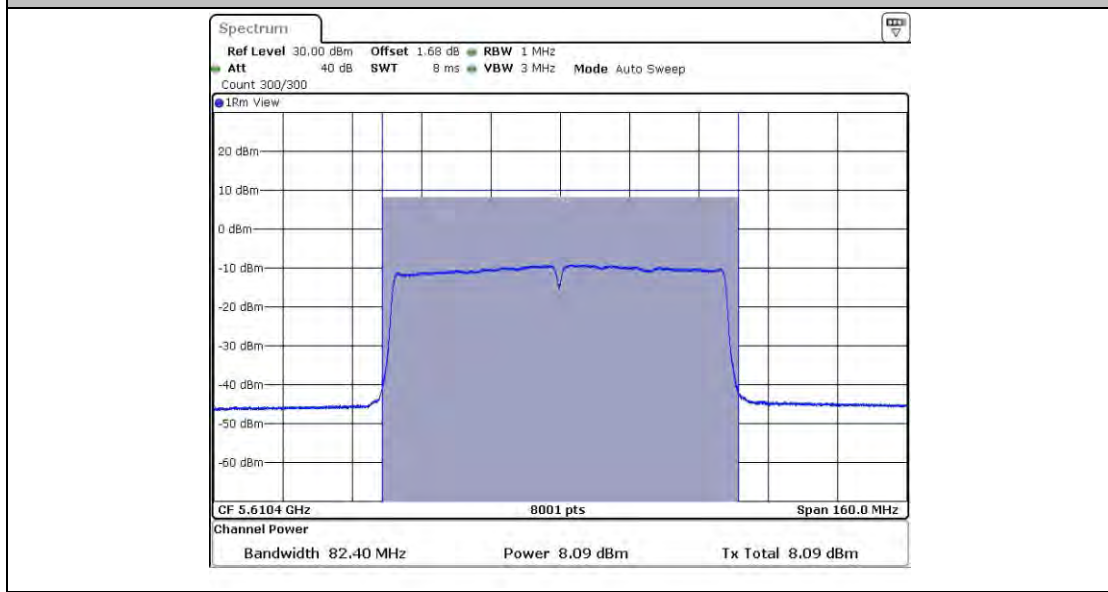
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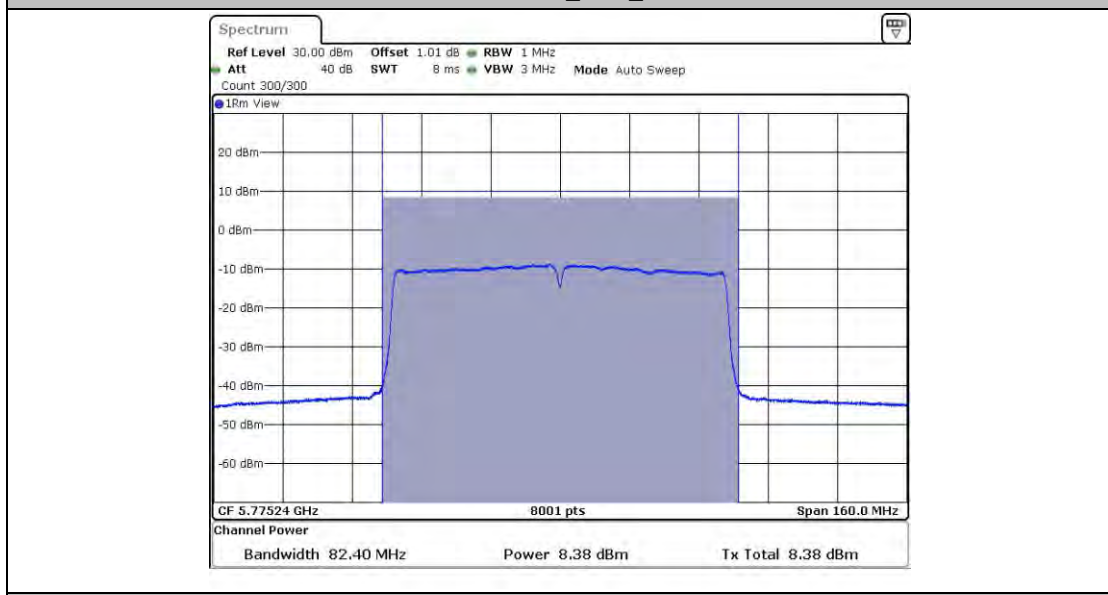
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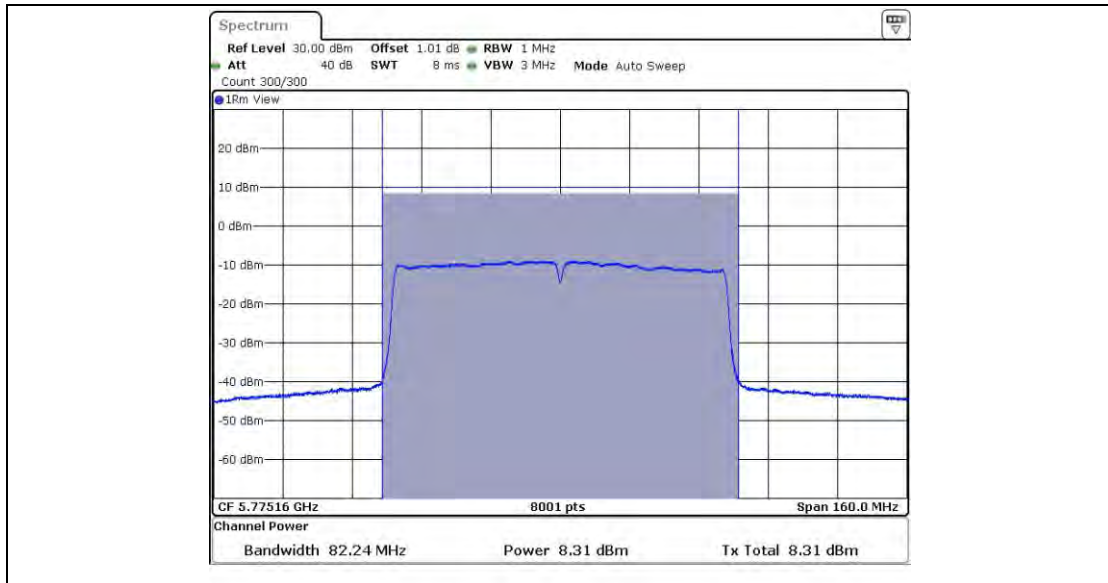
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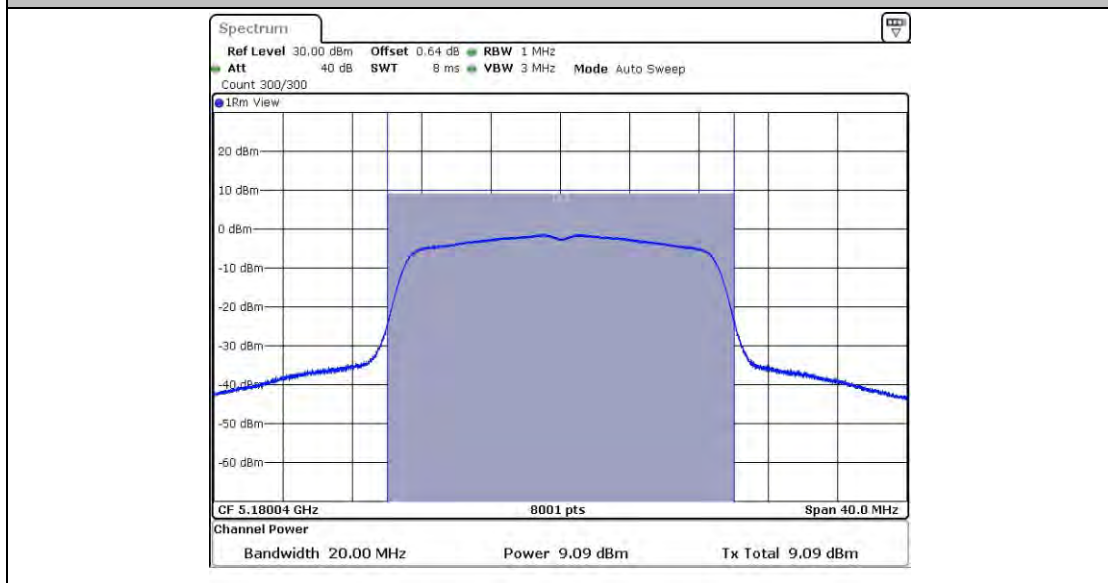
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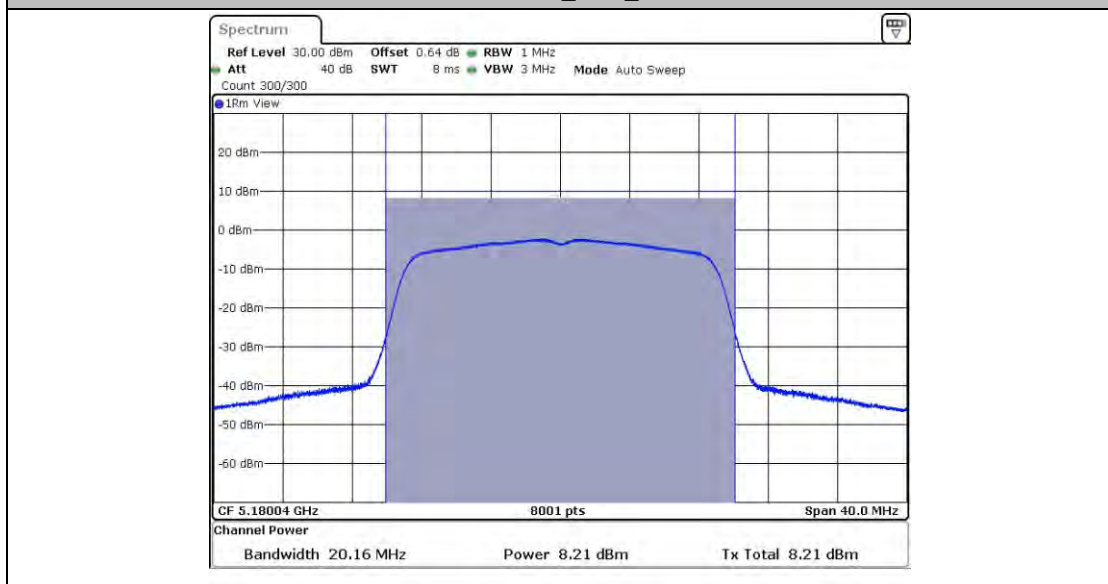
11AC80SISO_Ant1_5775



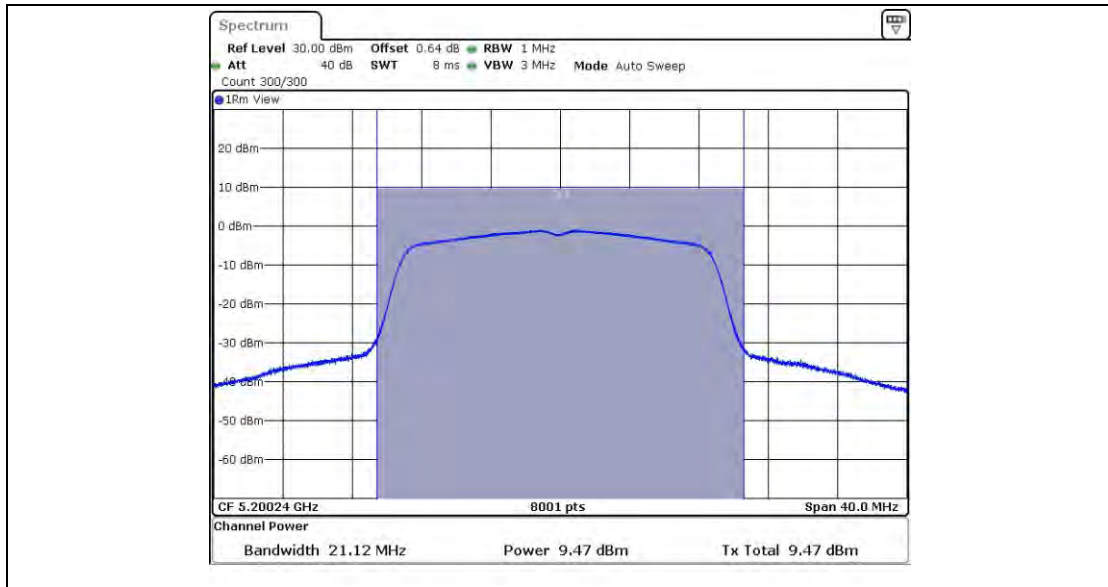
11AC80SISO_Ant2_5775



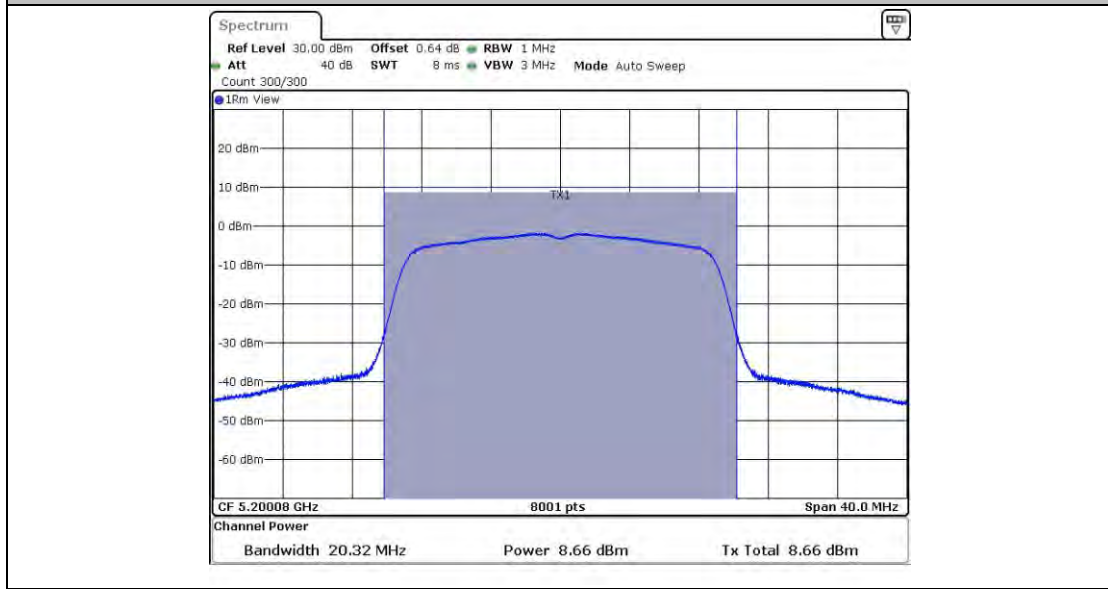
11N20MIMO_Ant1_5180



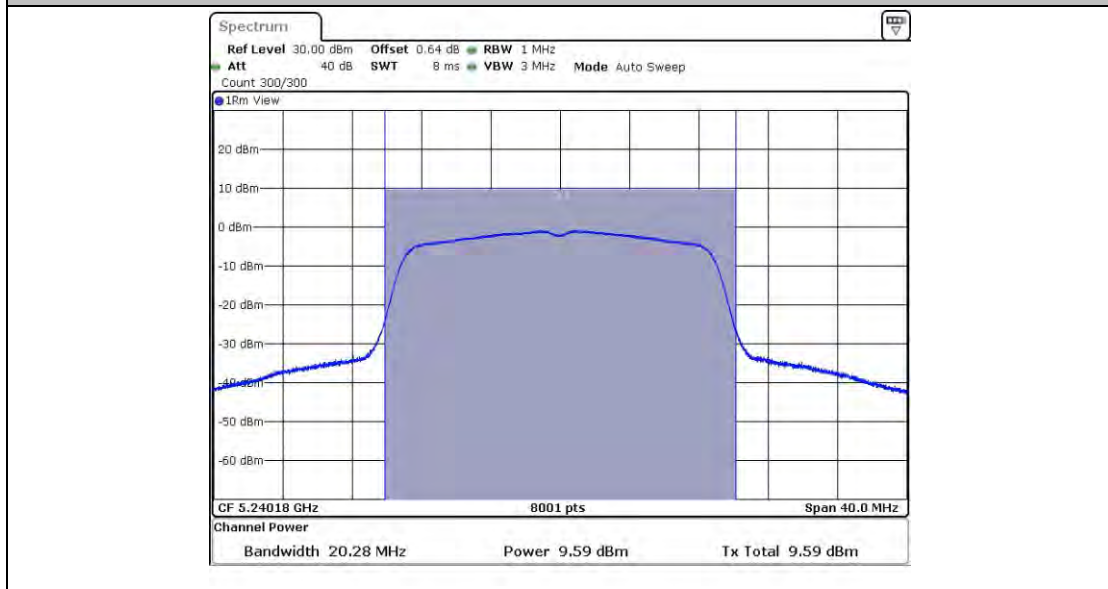
11N20MIMO_Ant2_5180



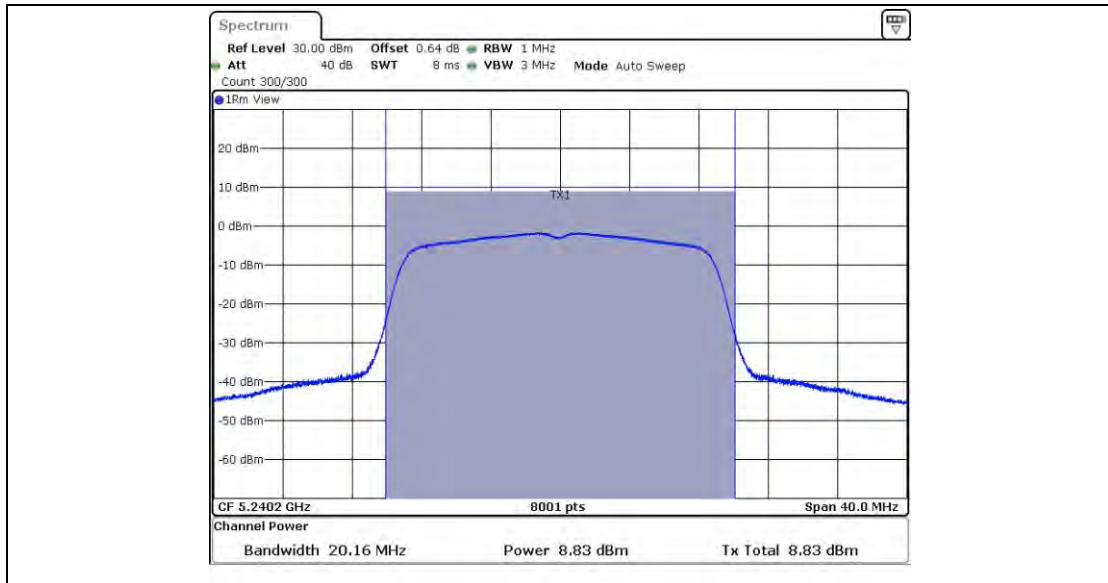
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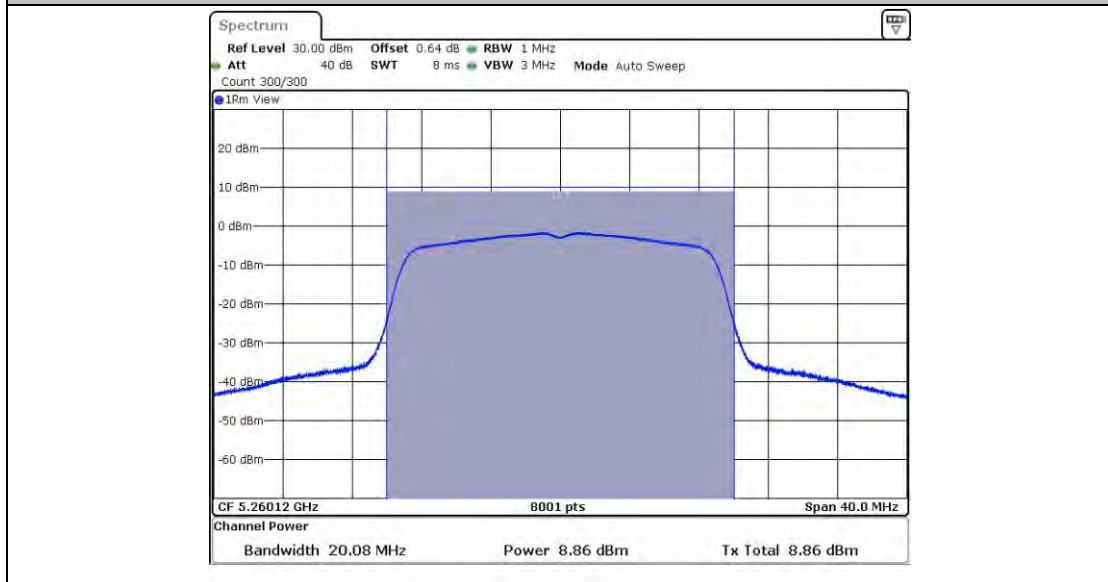
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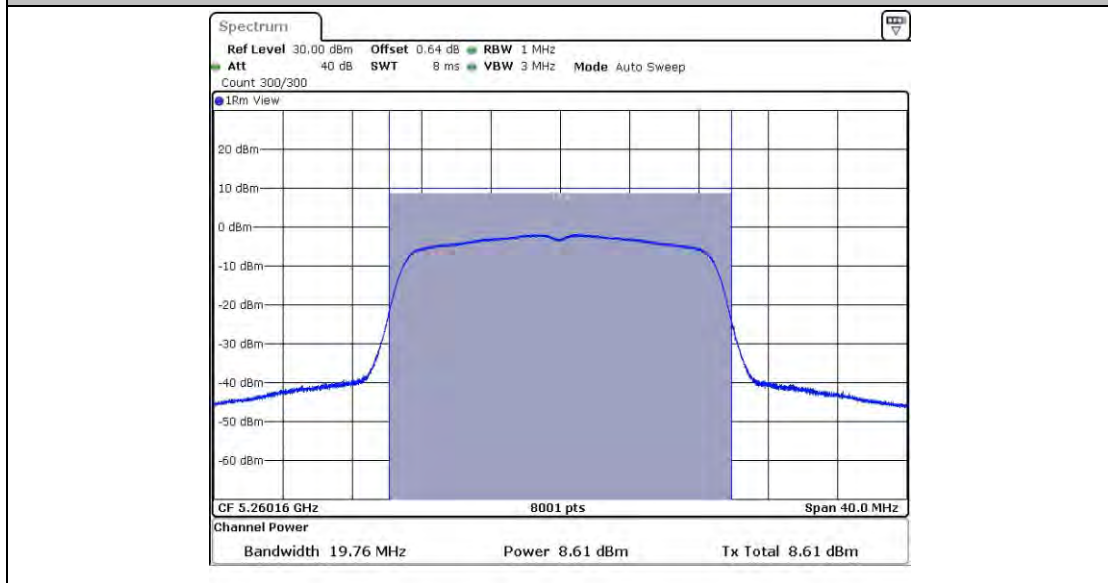
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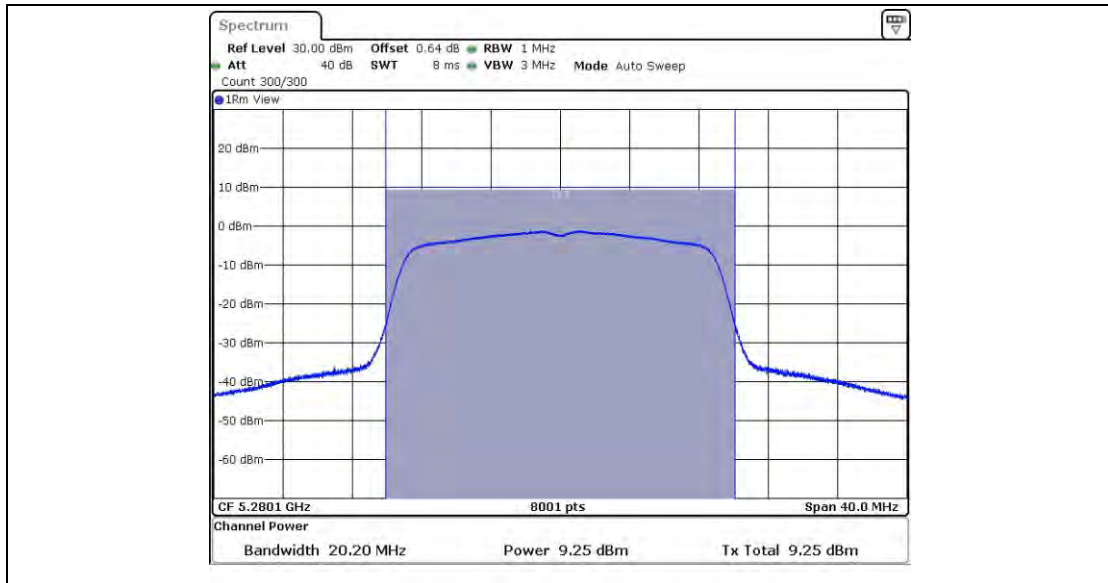
11N20MIMO_Ant2_5240



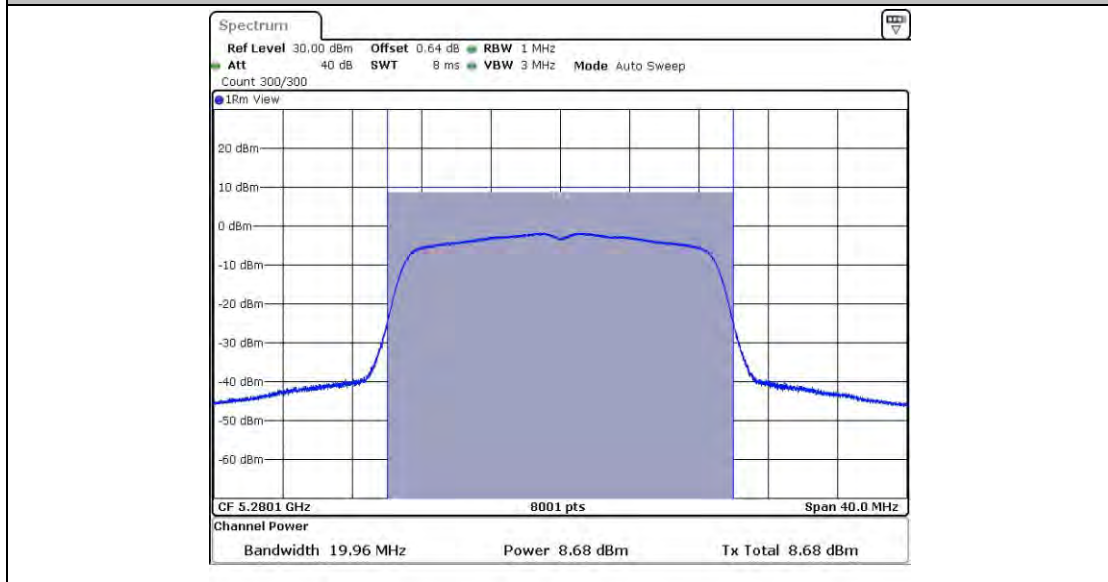
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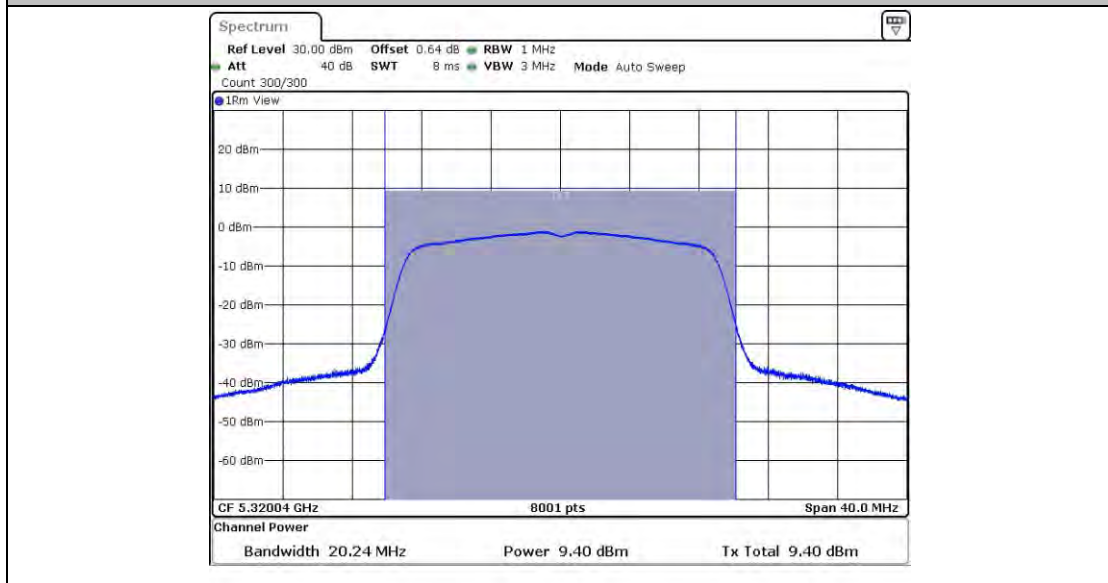
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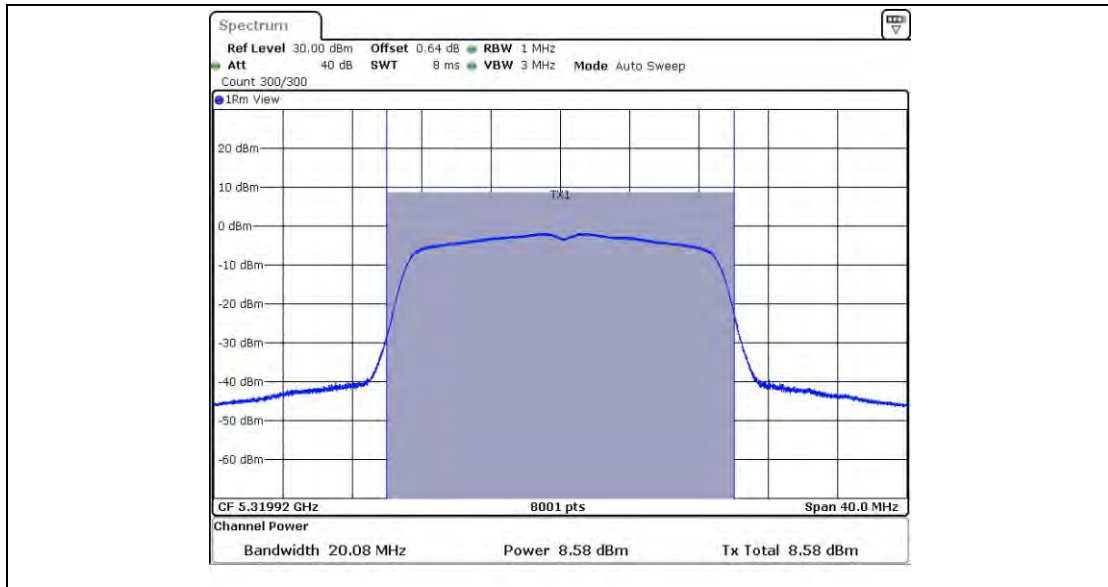
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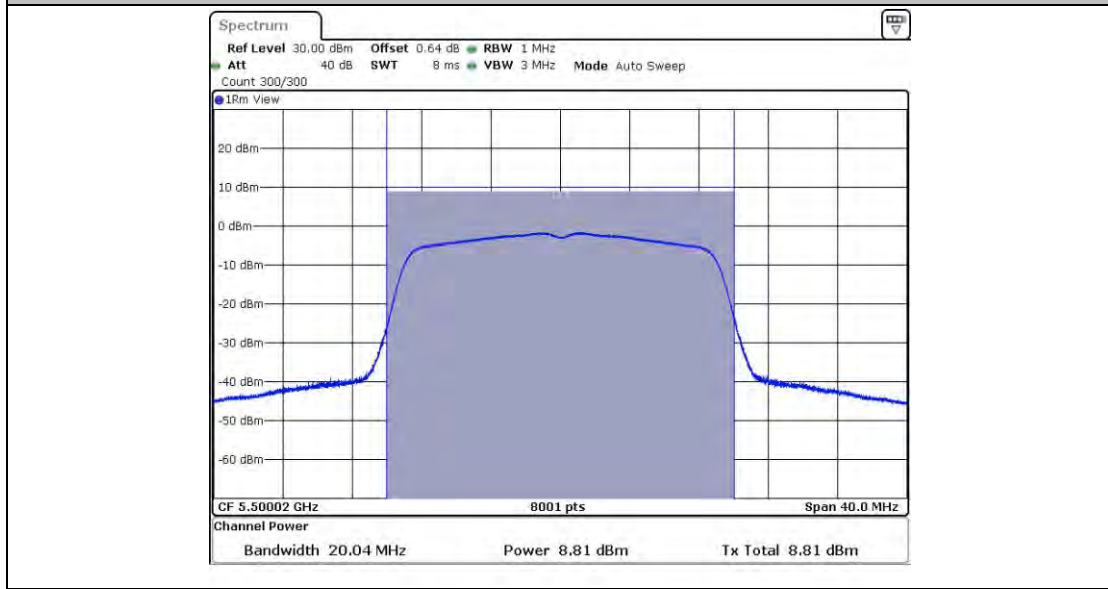
11N20MIMO_Ant2_5280



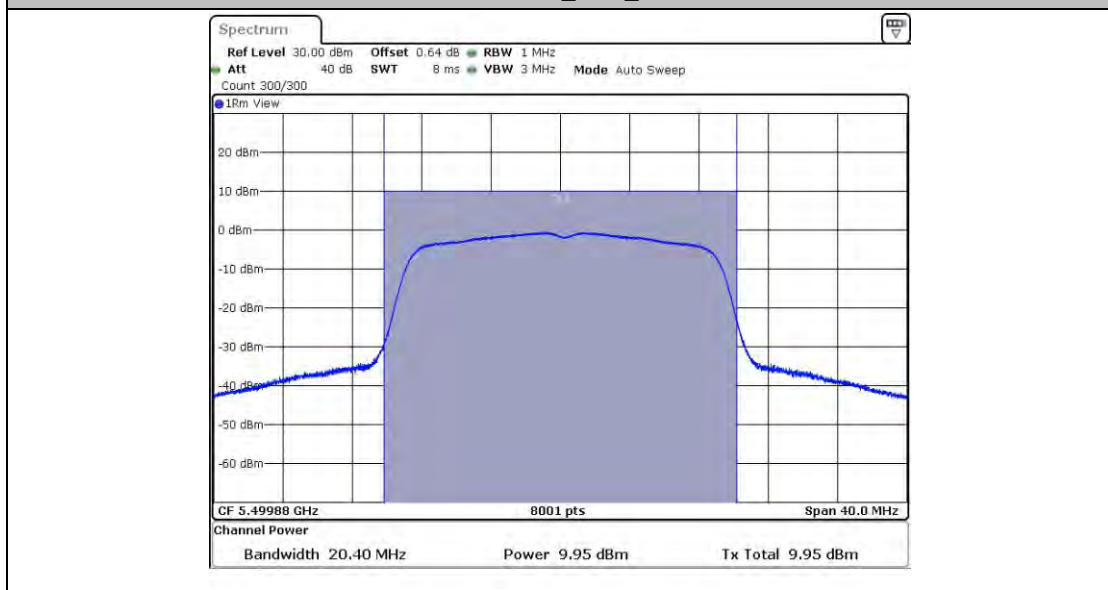
11N20MIMO_Ant1_5320



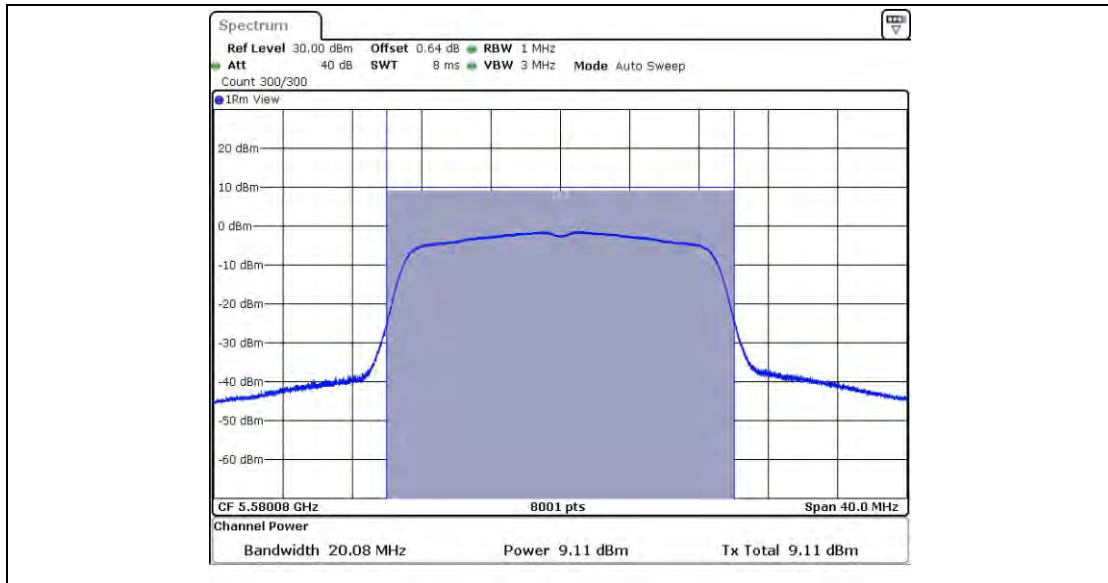
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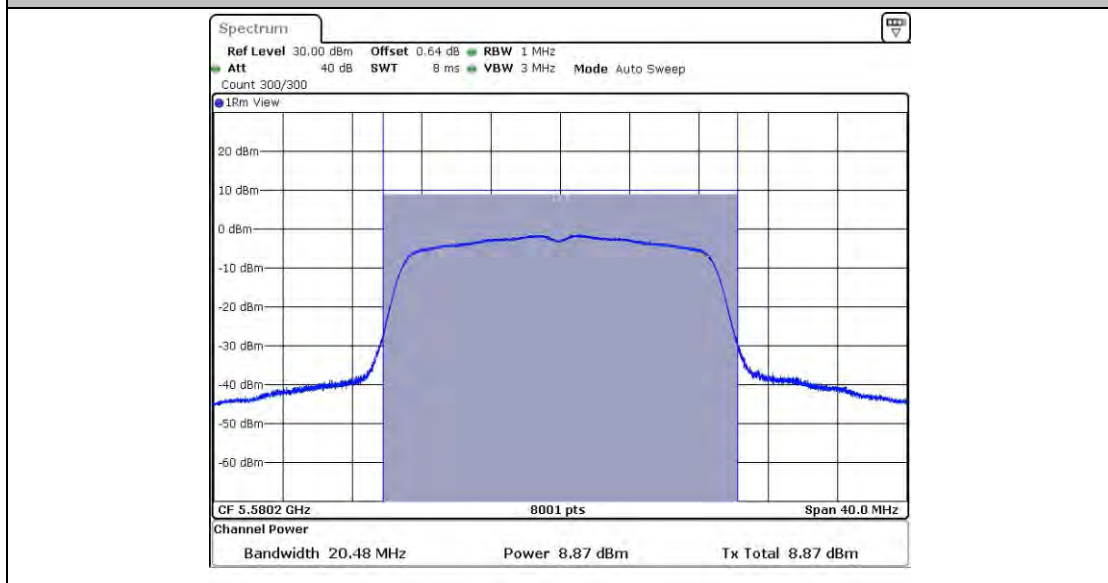
11N20MIMO_Ant1_5500



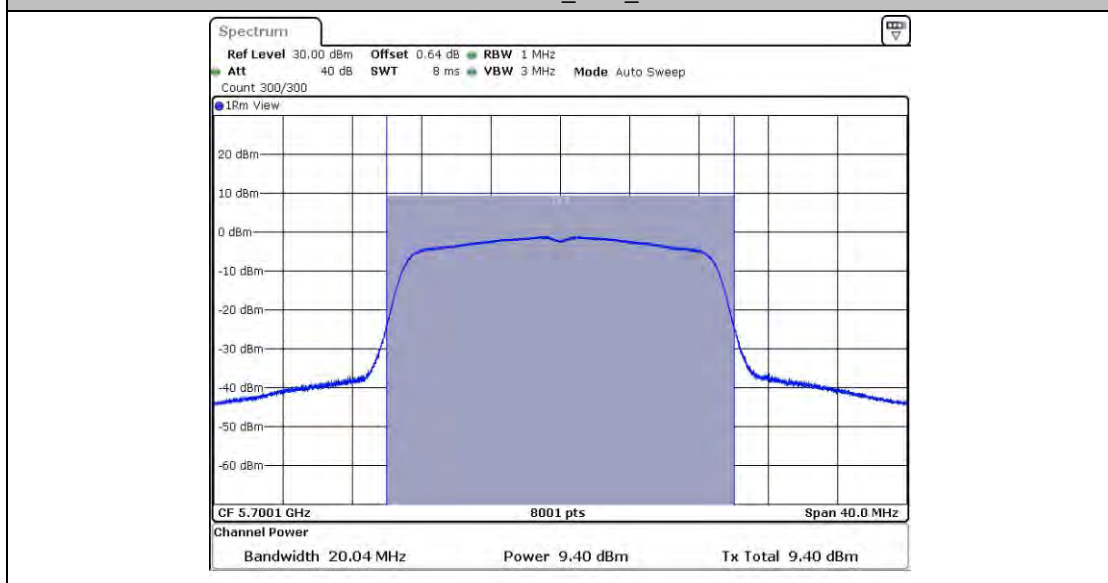
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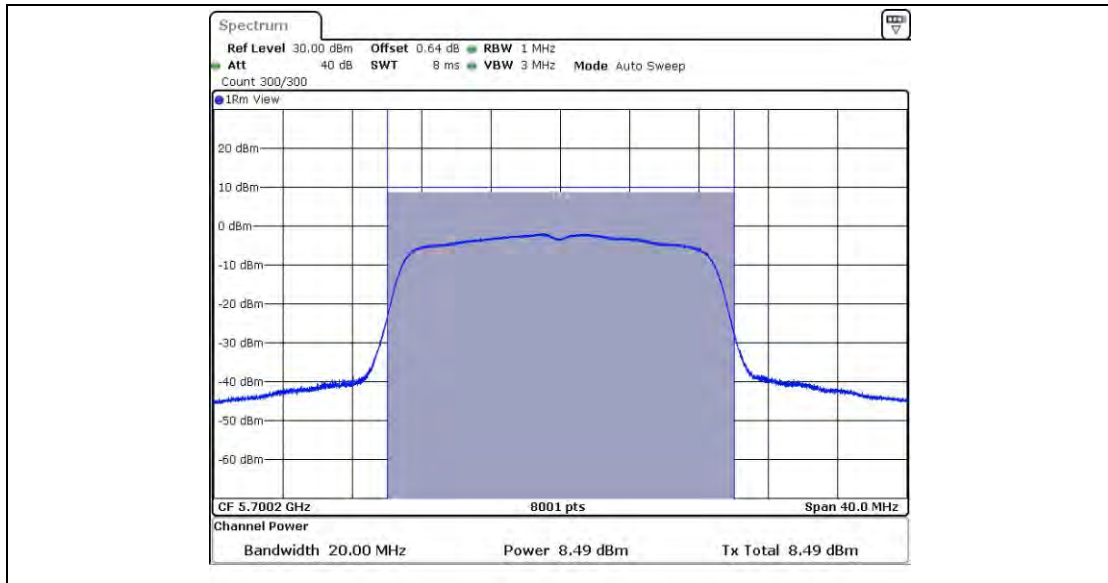
11N20MIMO_Ant1_5580



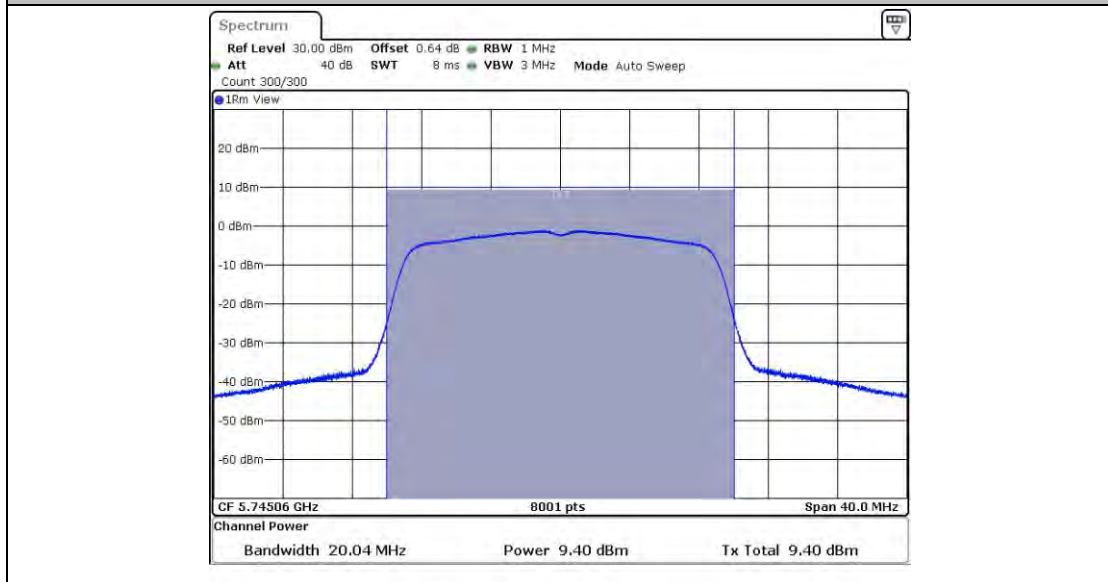
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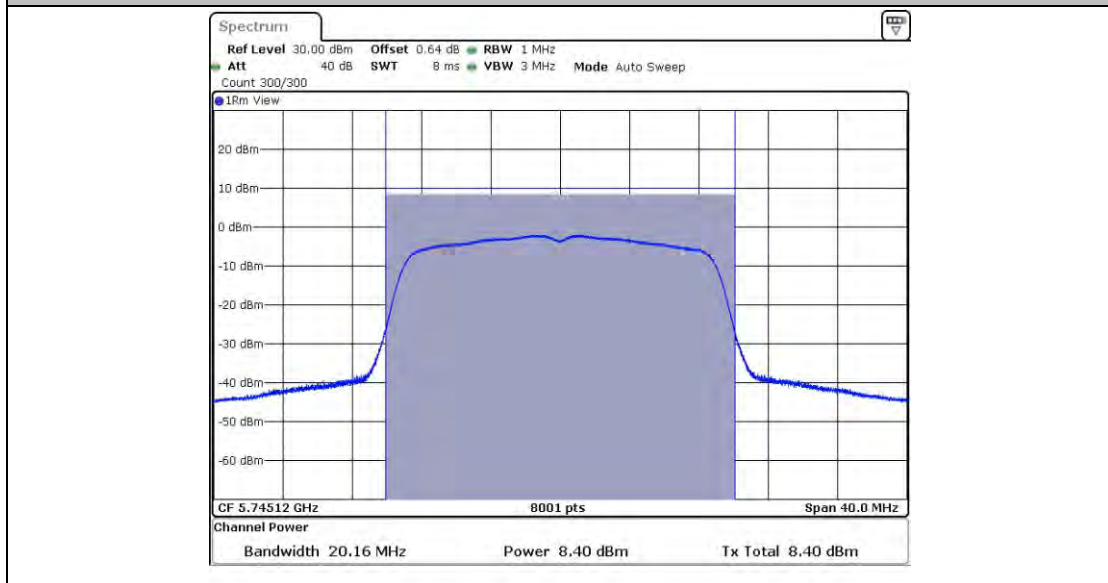
11N20MIMO_Ant1_5700



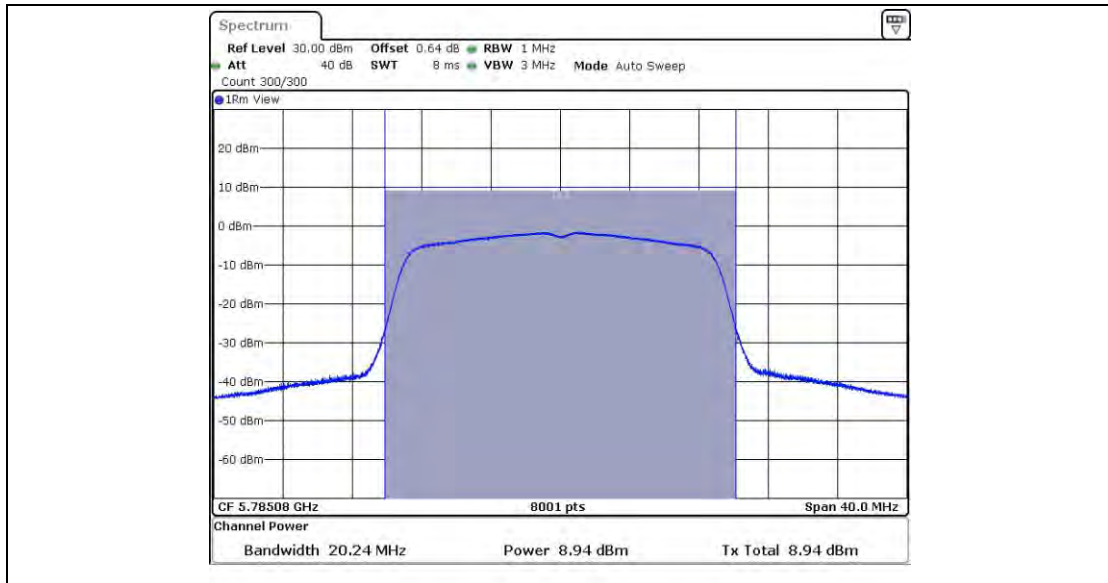
11N20MIMO_Ant2_5700



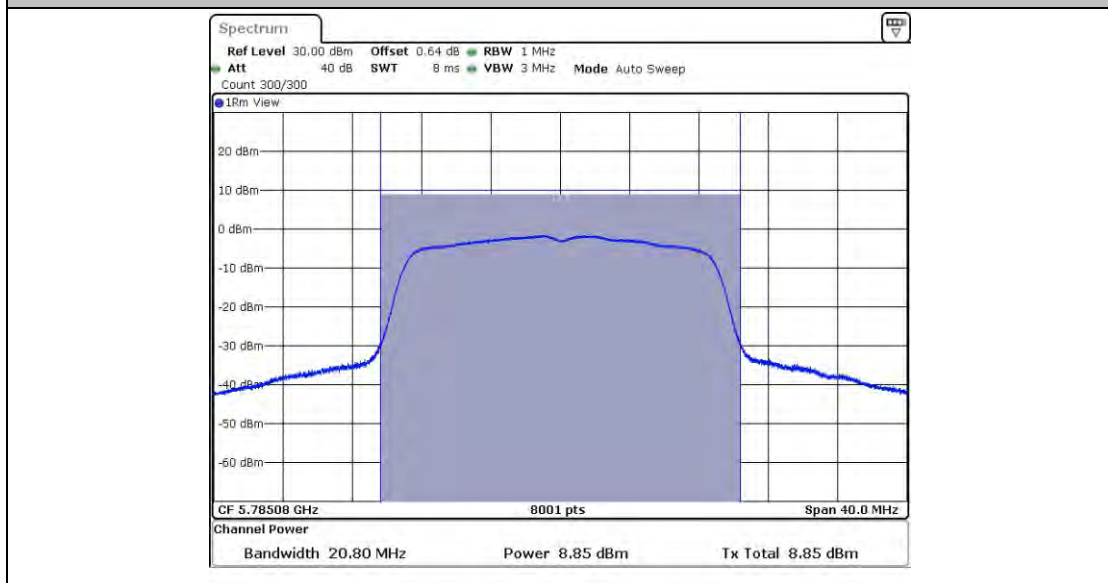
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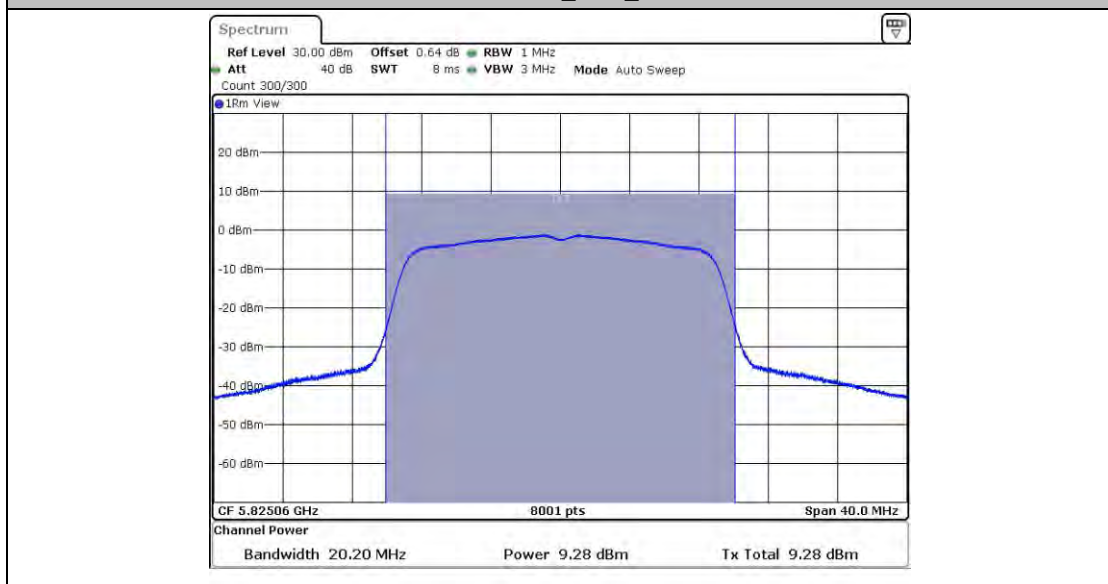
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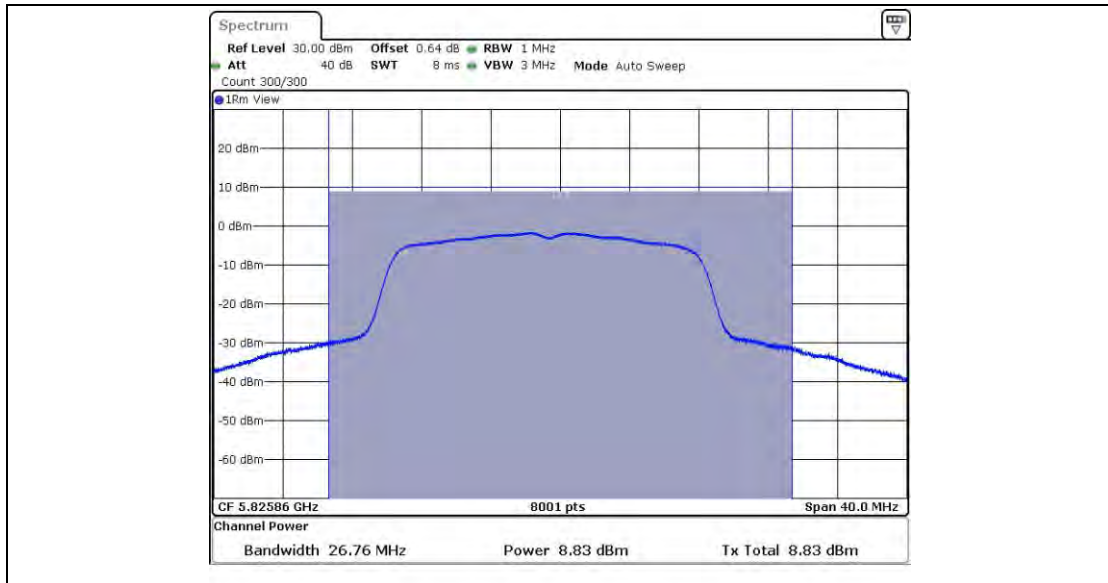
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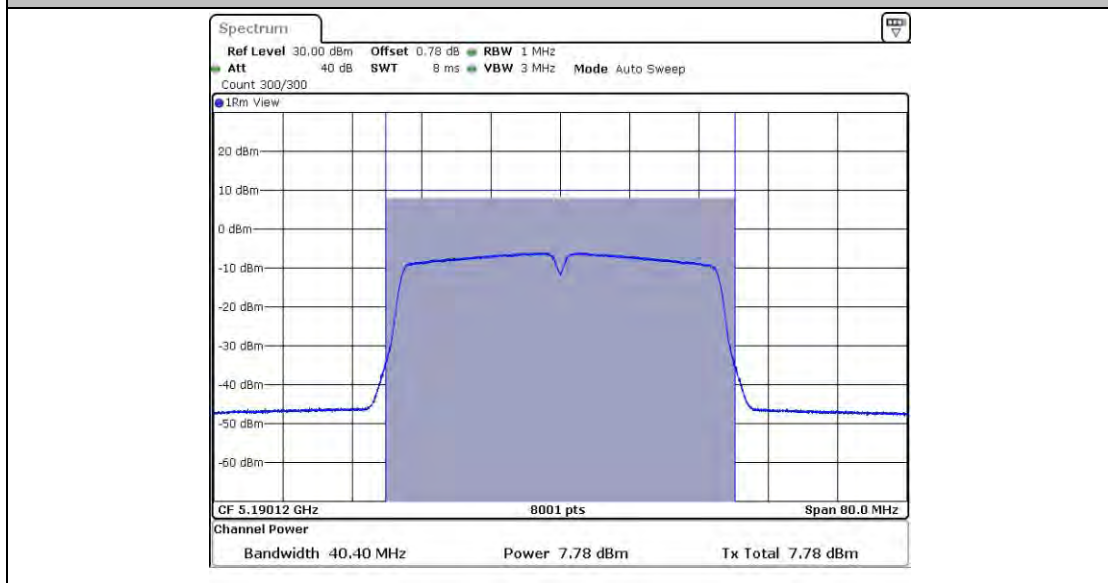
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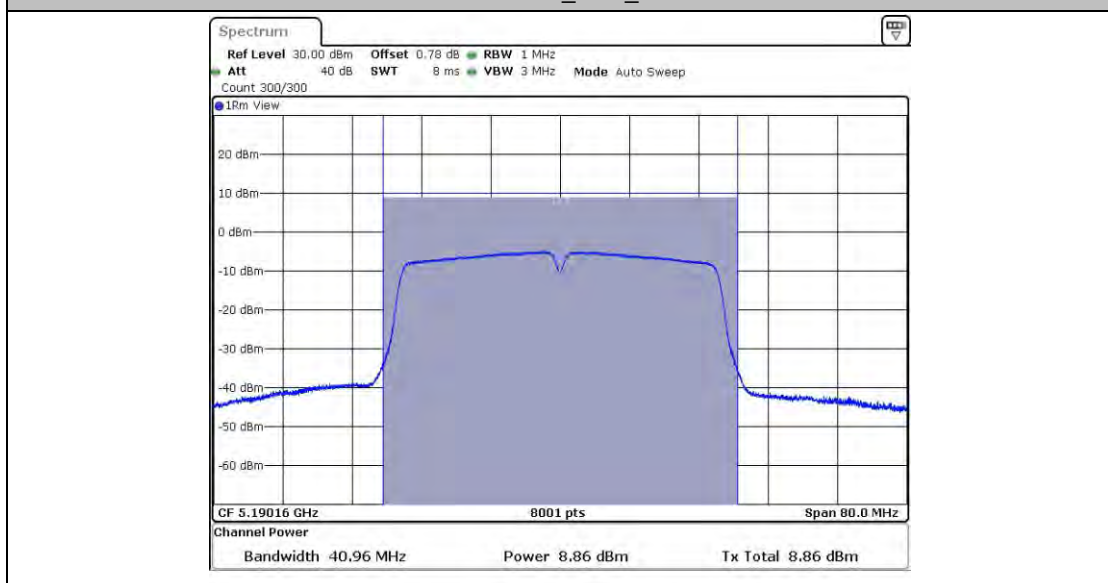
11N20MIMO_Ant1_5825



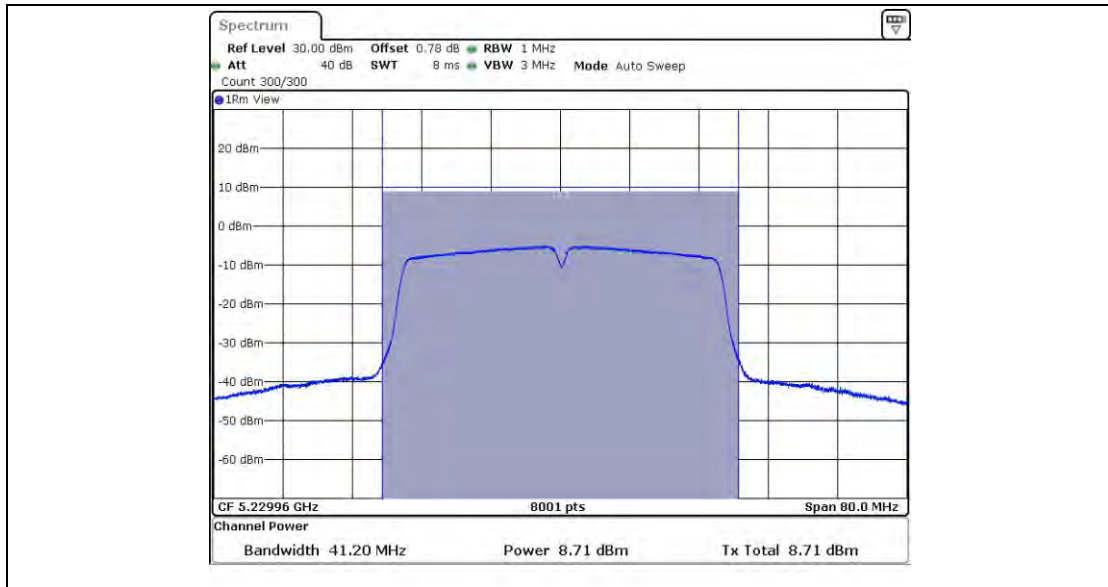
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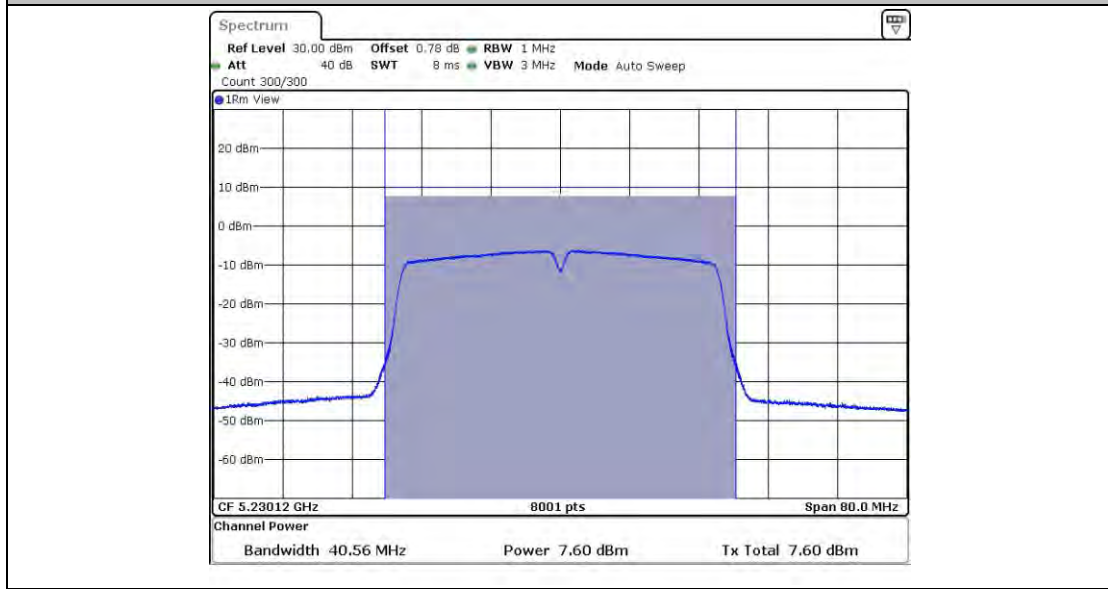
11N40MIMO_Ant1_5190



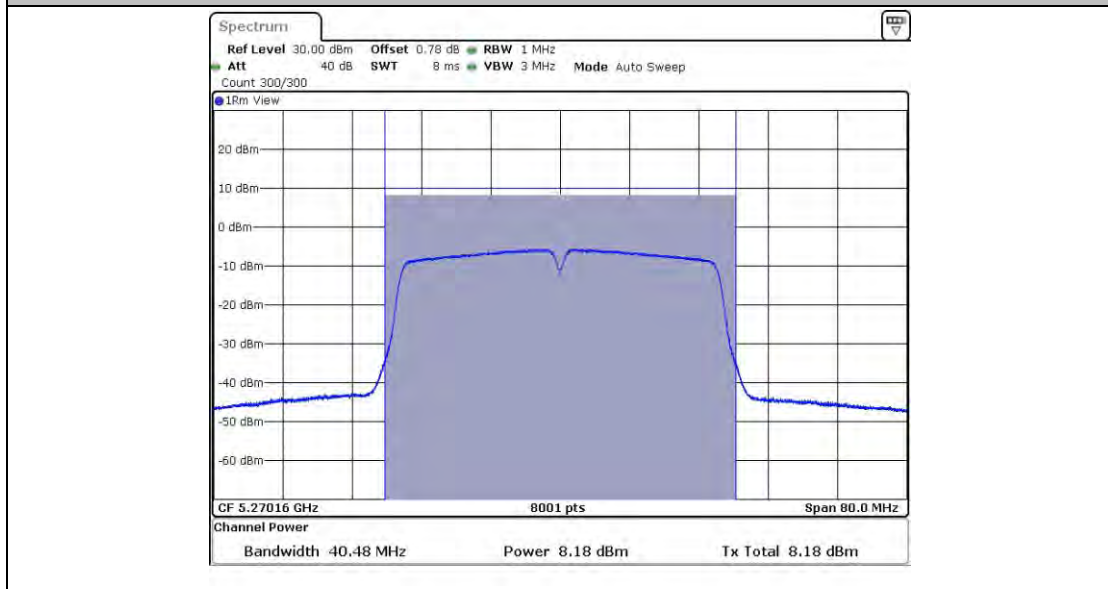
11N40MIMO_Ant2_5190



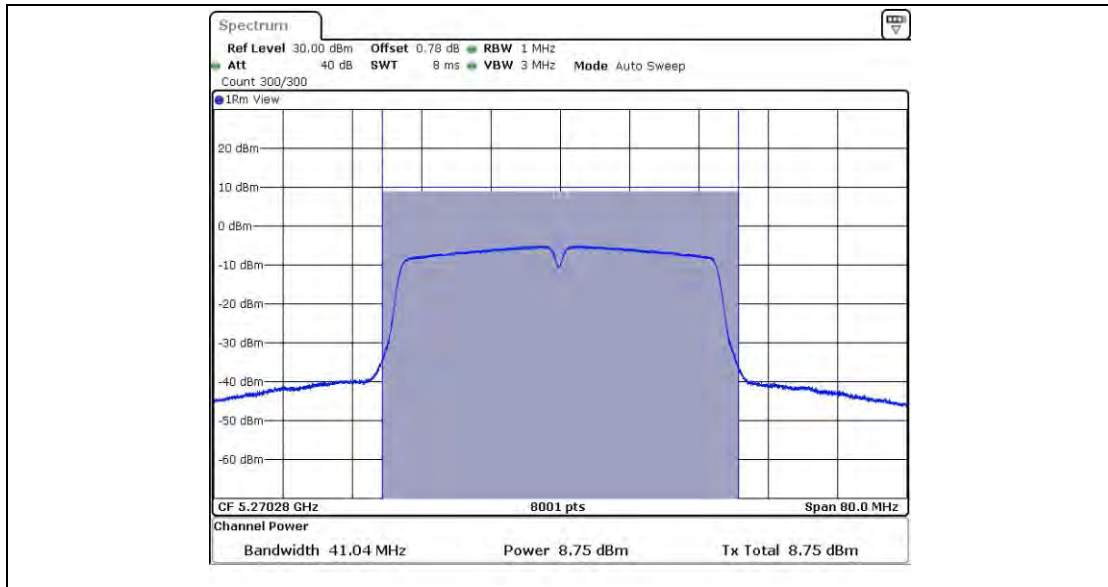
11N40MIMO_Ant1_5230



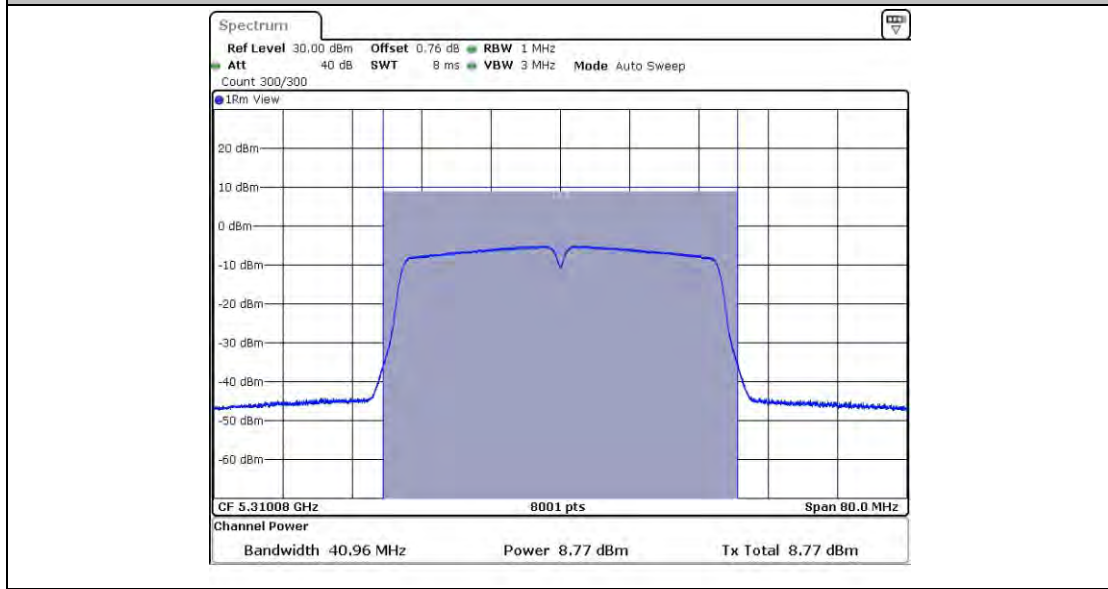
11N40MIMO_Ant2_5230



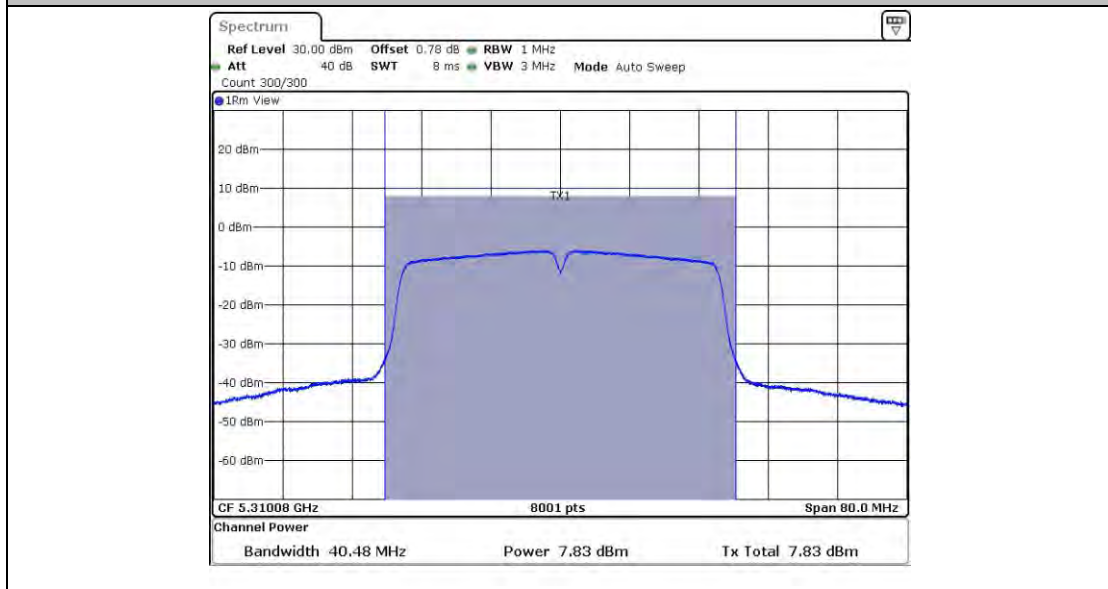
11N40MIMO_Ant1_5270



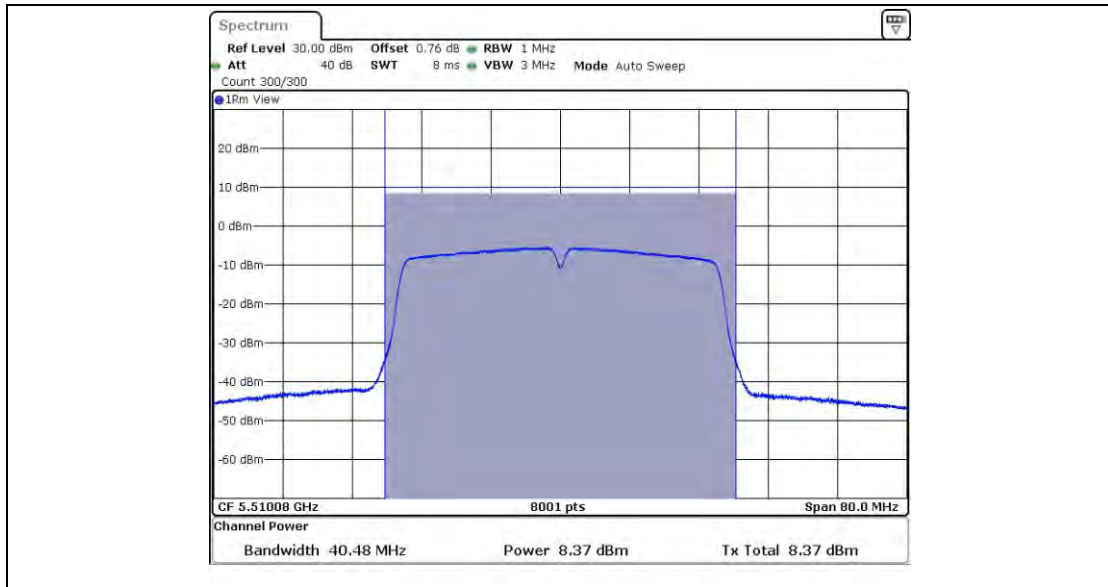
11N40MIMO_Ant2_5270



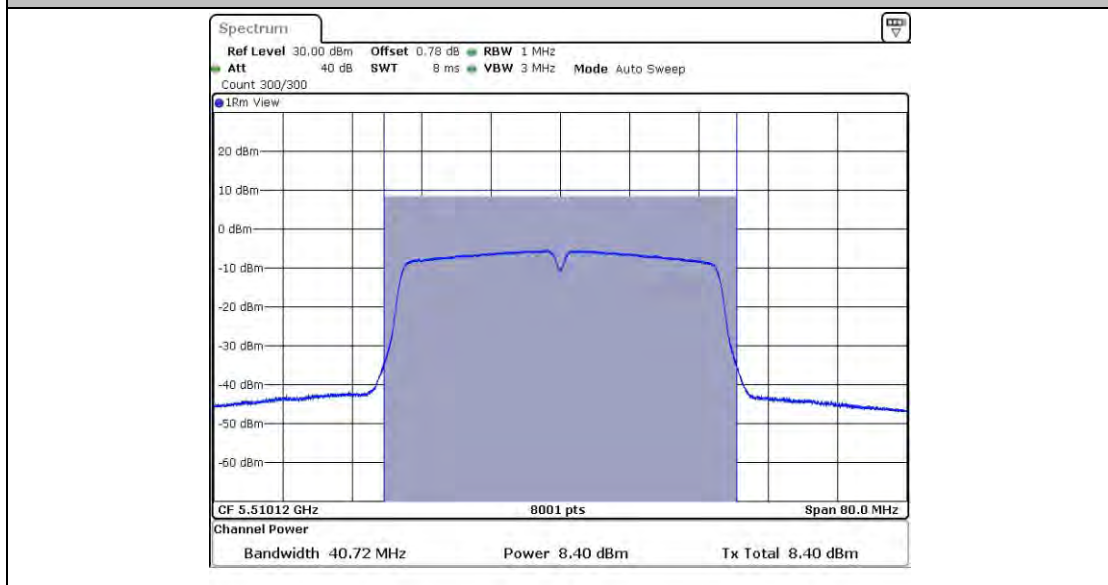
11N40MIMO_Ant1_5310



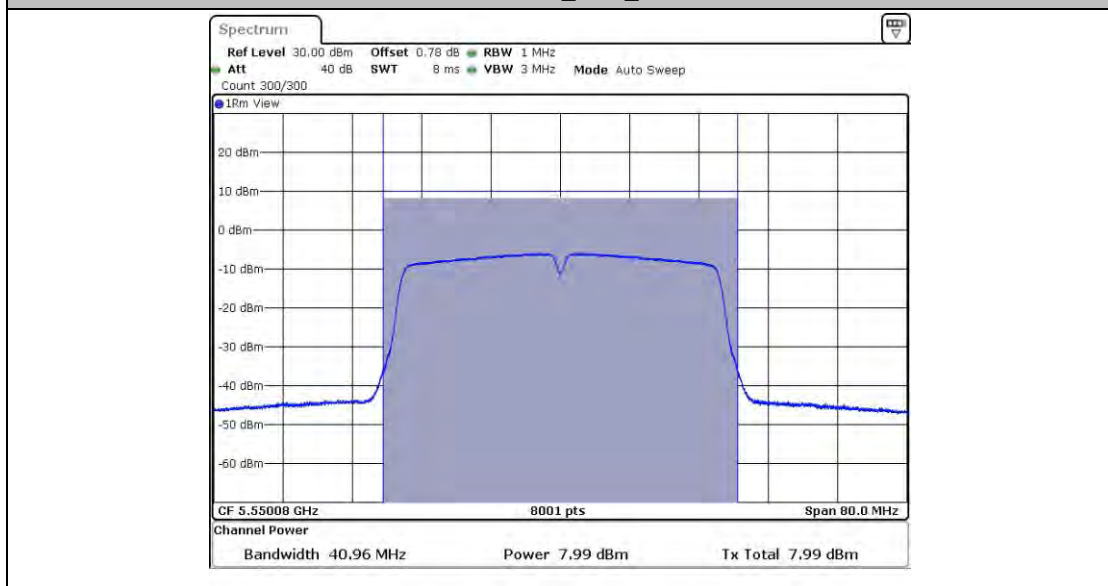
11N40MIMO_Ant2_5310



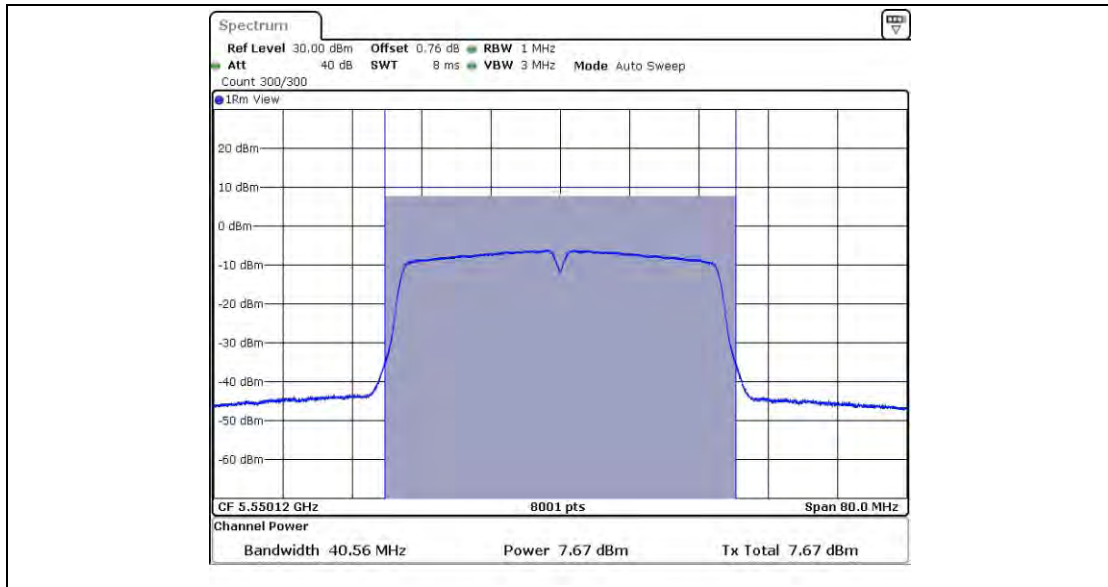
11N40MIMO_Ant1_5510



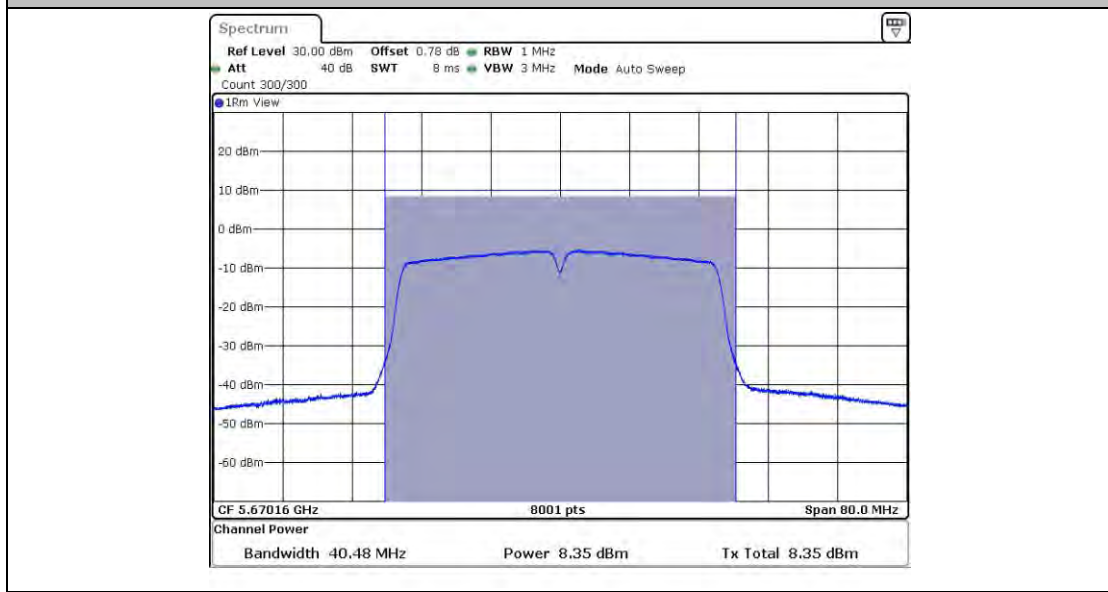
11N40MIMO_Ant2_5510



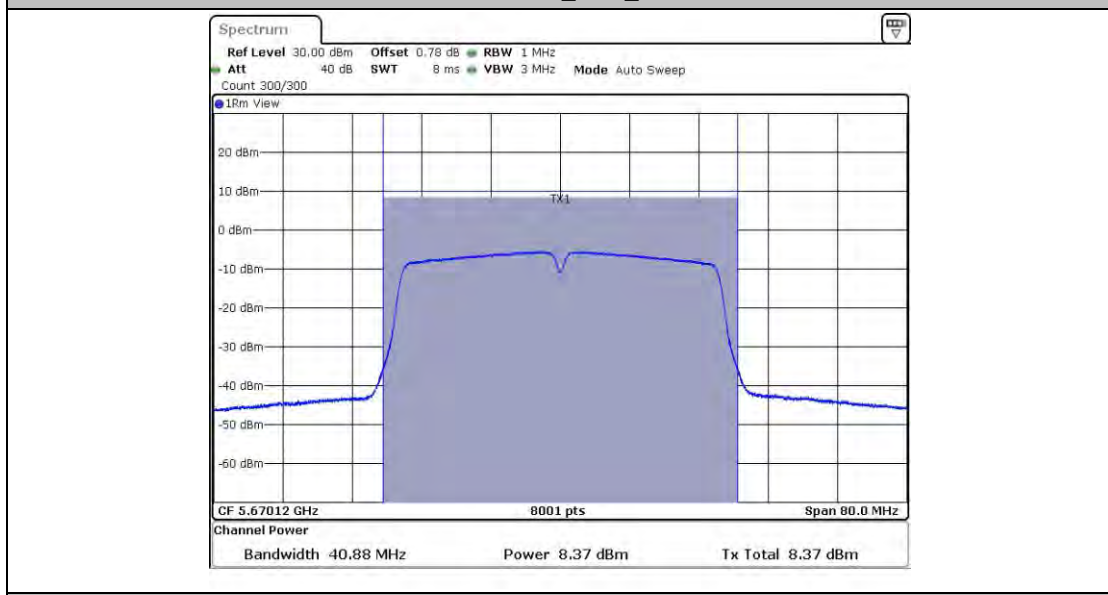
11N40MIMO_Ant1_5550



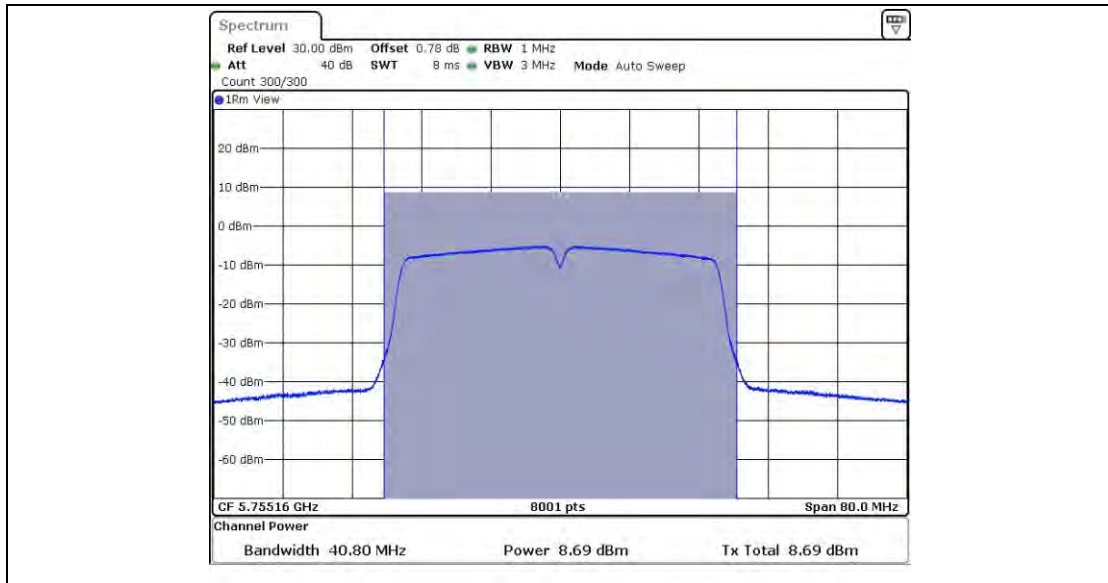
11N40MIMO_Ant2_5550



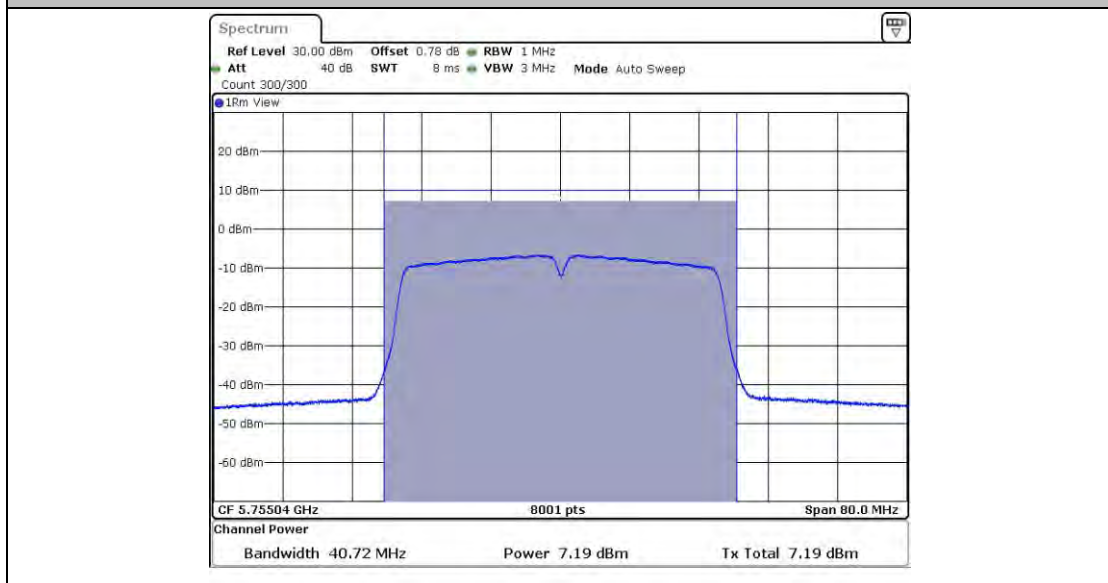
11N40MIMO_Ant1_5670



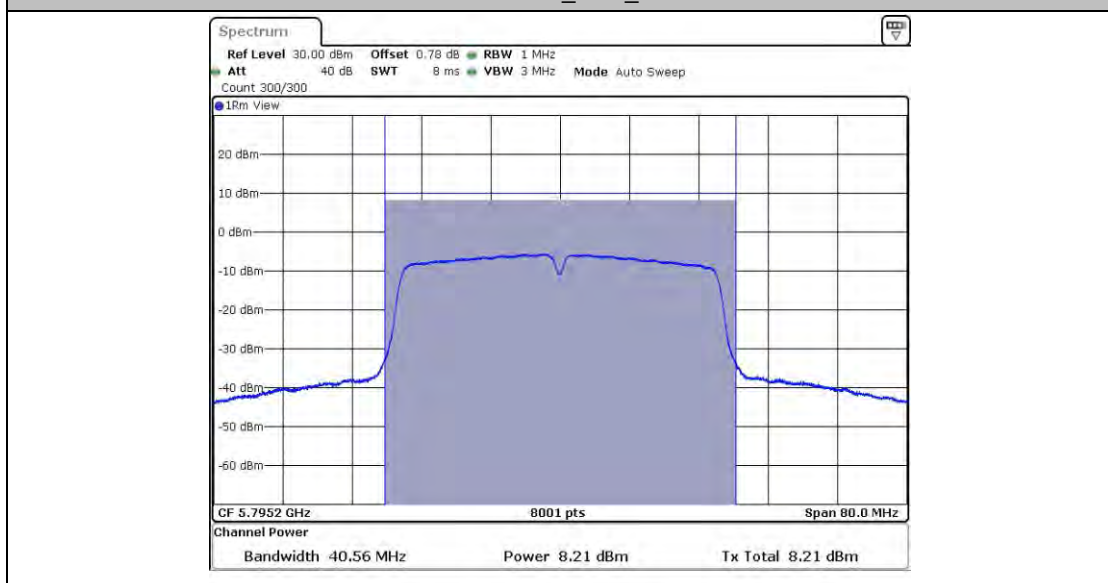
11N40MIMO_Ant2_5670



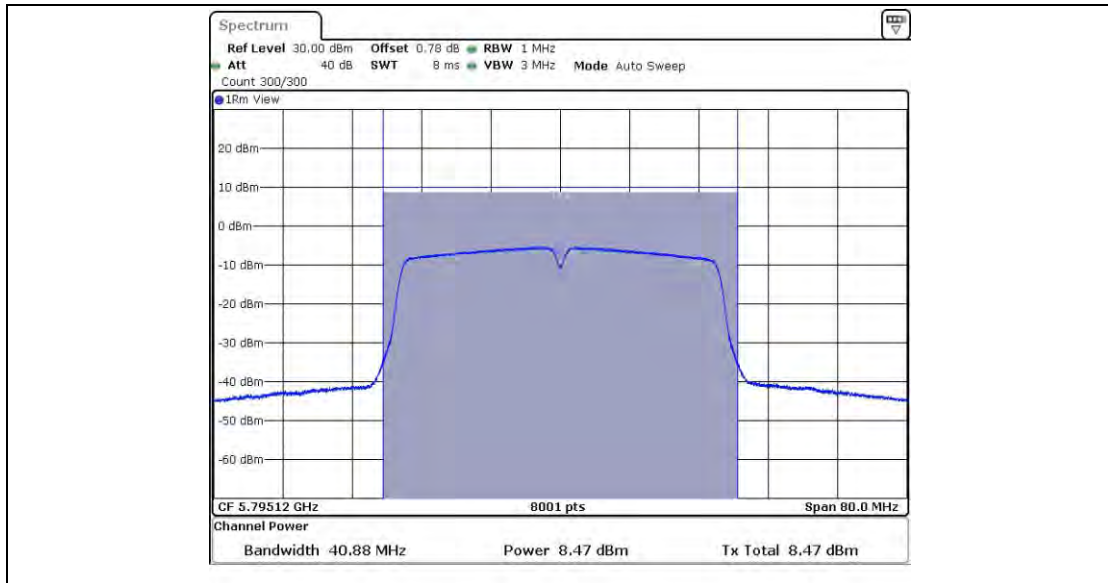
11N40MIMO_Ant1_5755



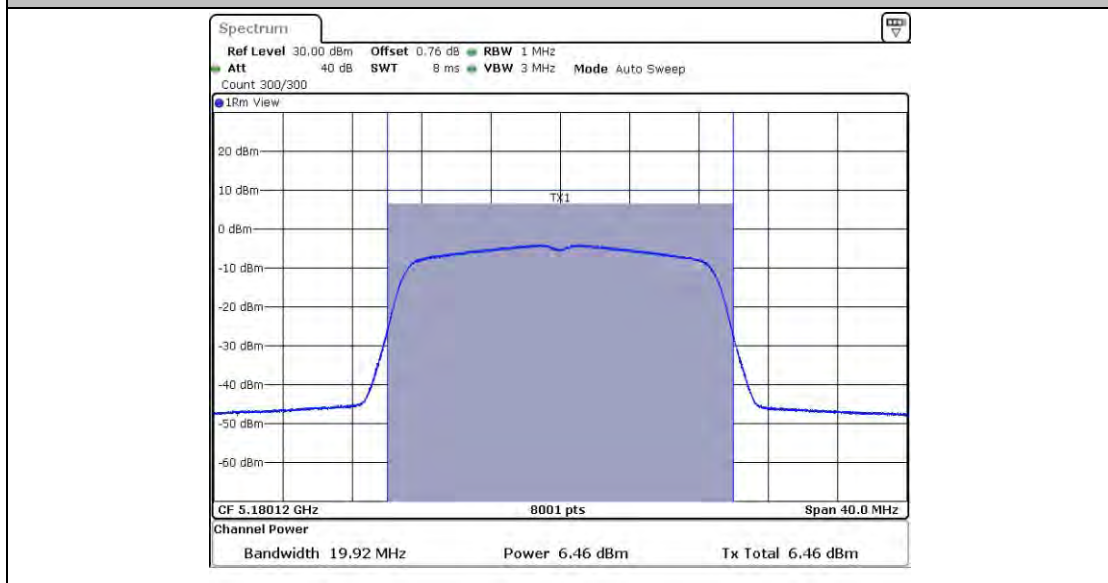
11N40MIMO_Ant2_5755



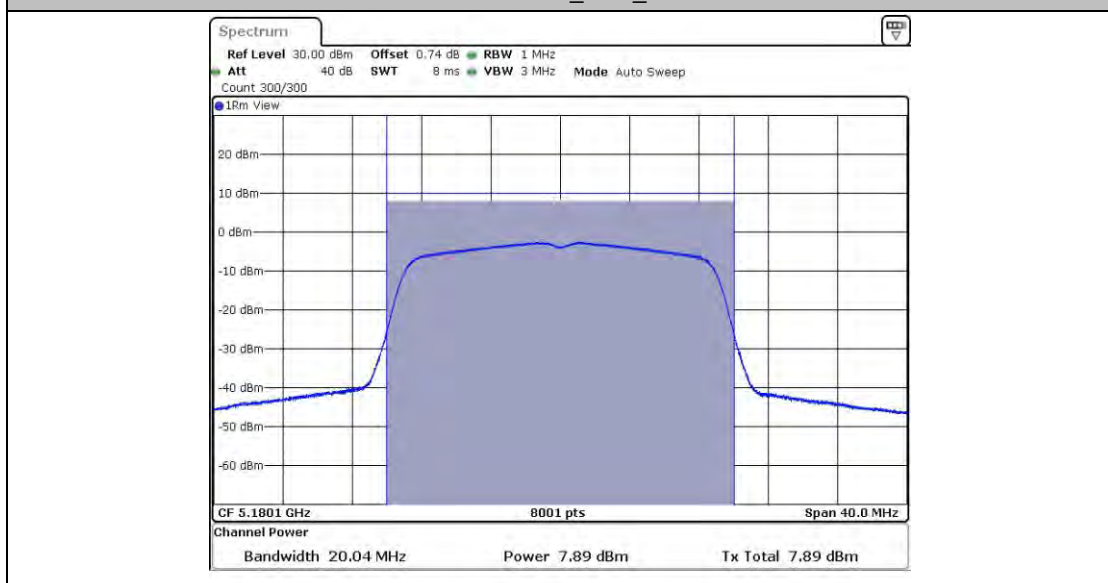
11N40MIMO_Ant1_5795



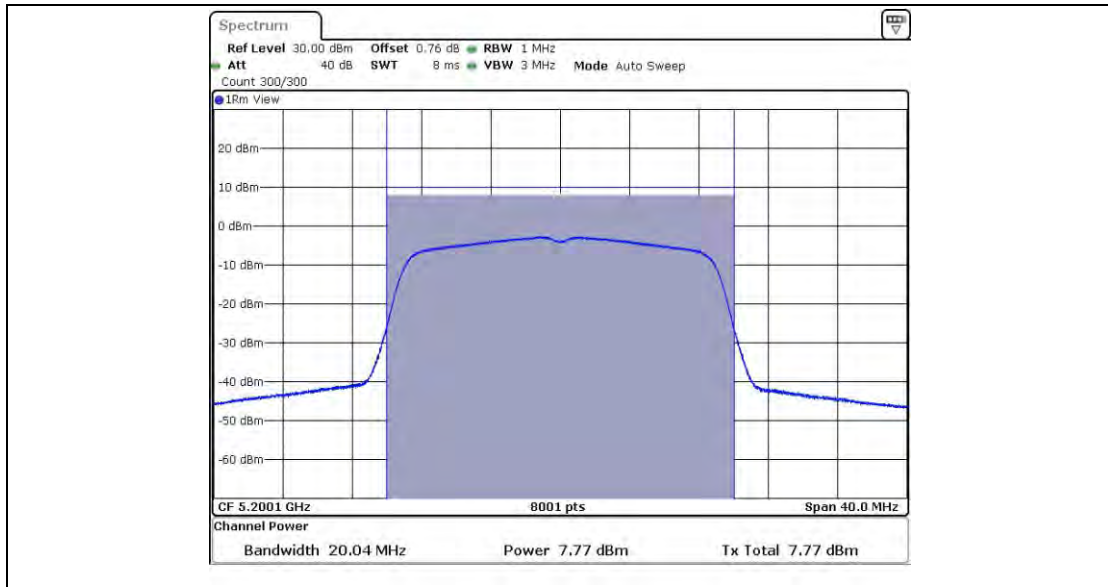
11N40MIMO_Ant2_5795



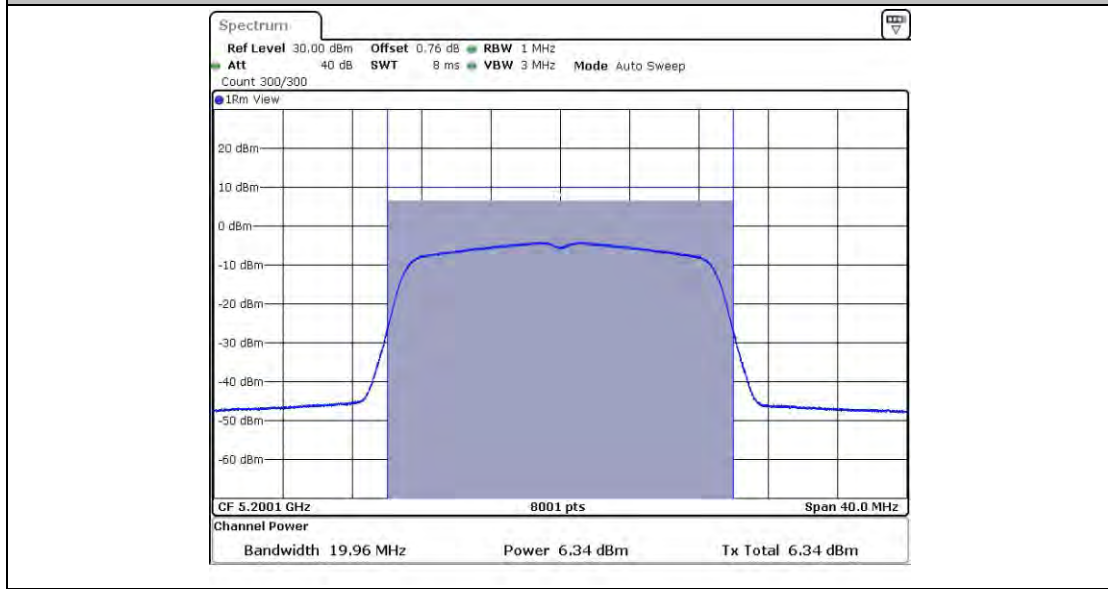
11AC20MIMO_Ant1_5180



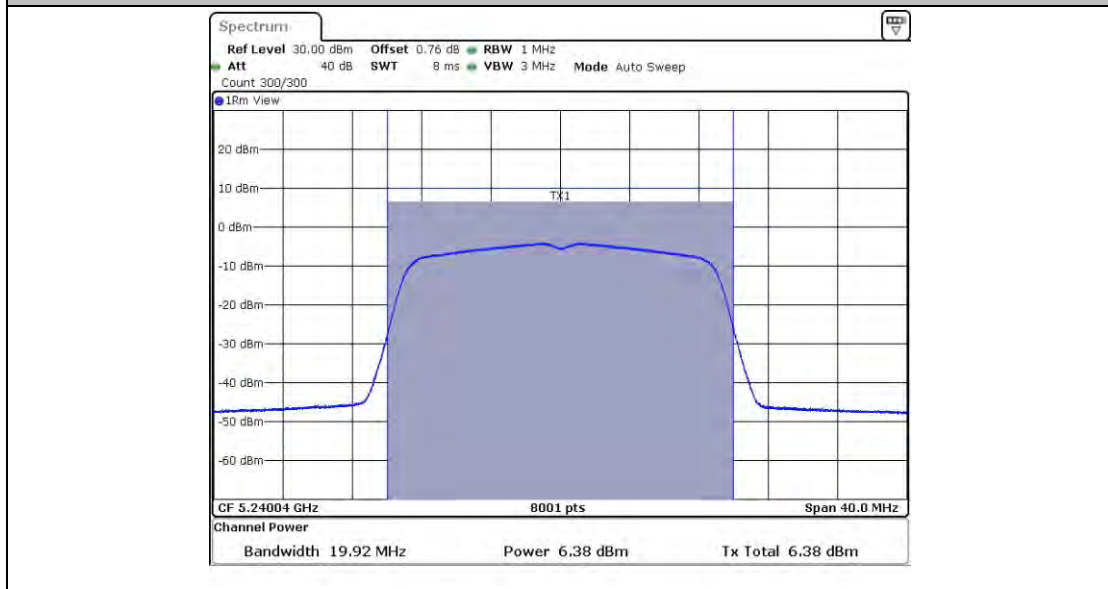
11AC20MIMO_Ant2_5180



11AC20MIMO_Ant1_5200



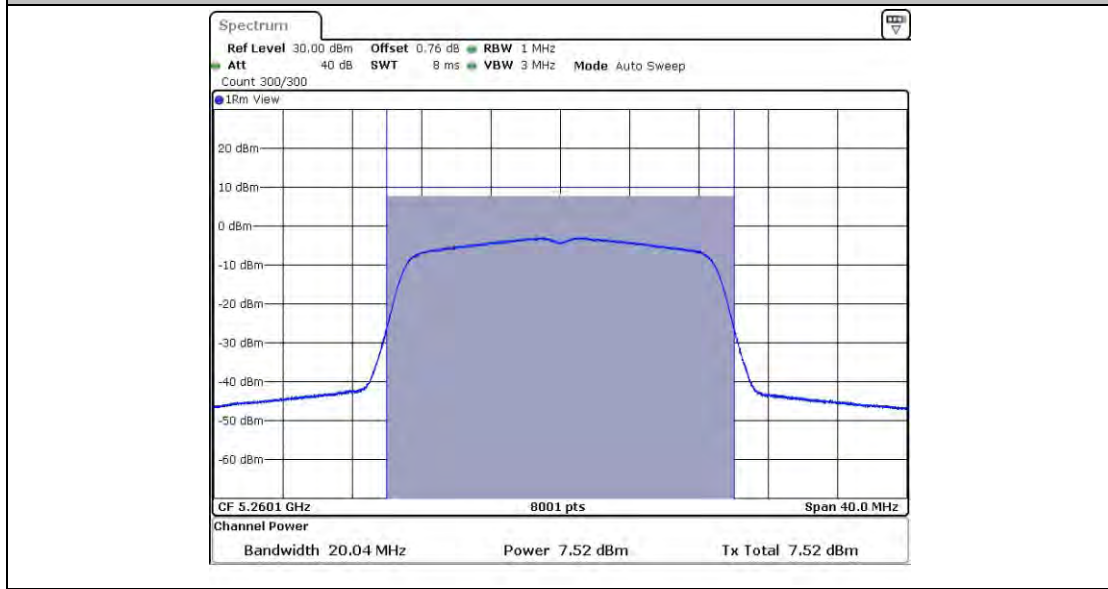
11AC20MIMO_Ant2_5200



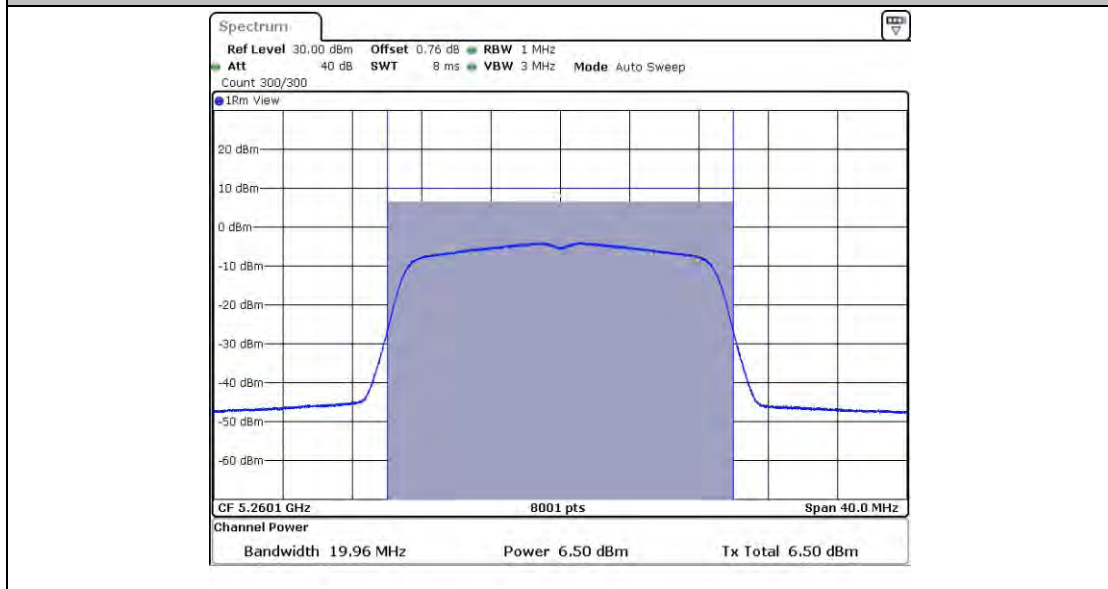
11AC20MIMO_Ant1_5240



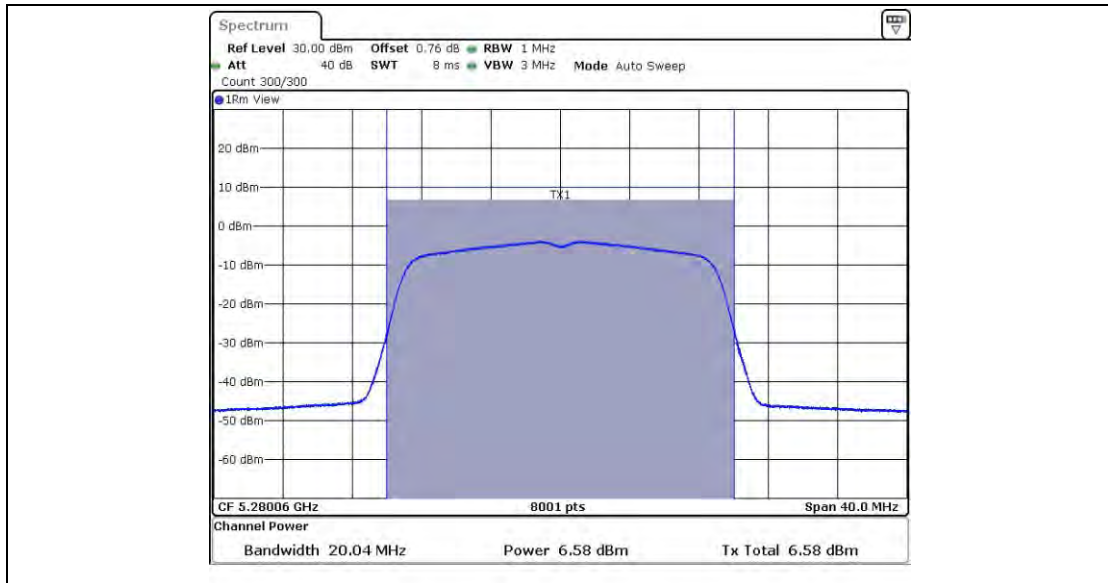
11AC20MIMO_Ant2_5240



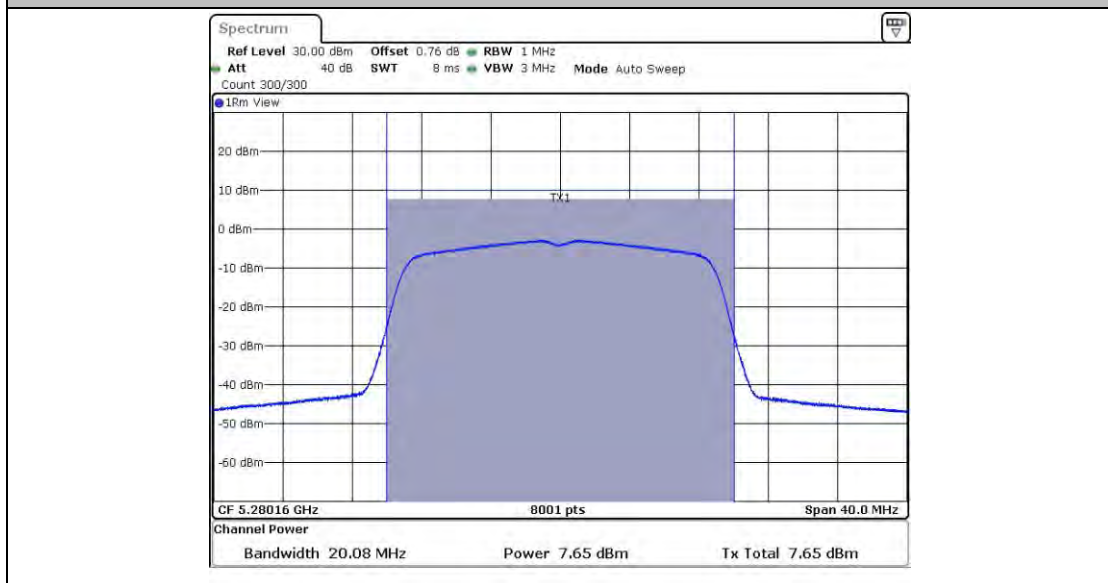
11AC20MIMO_Ant1_5260



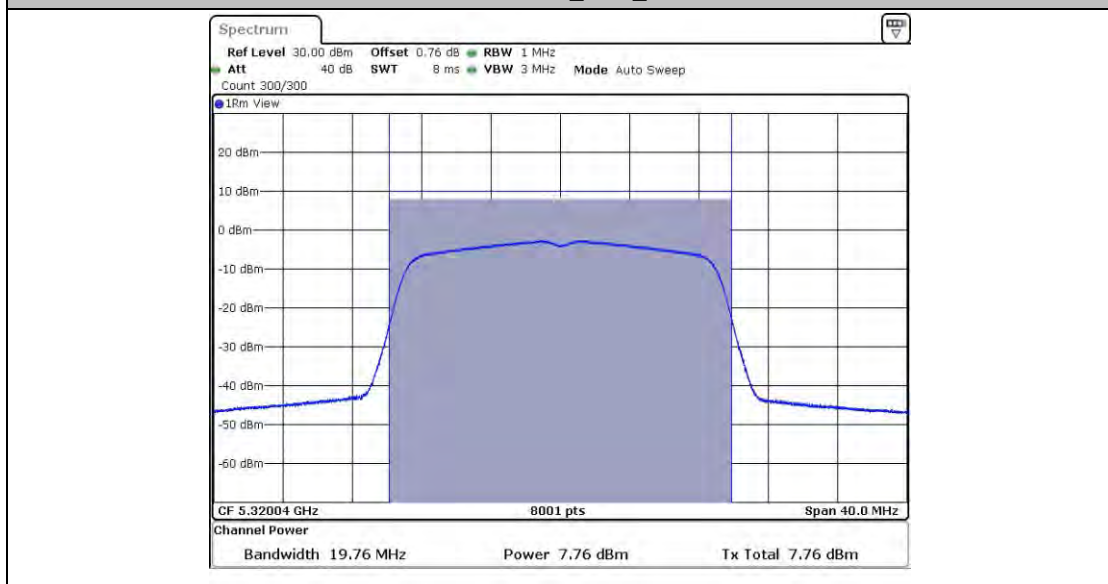
11AC20MIMO_Ant2_5260



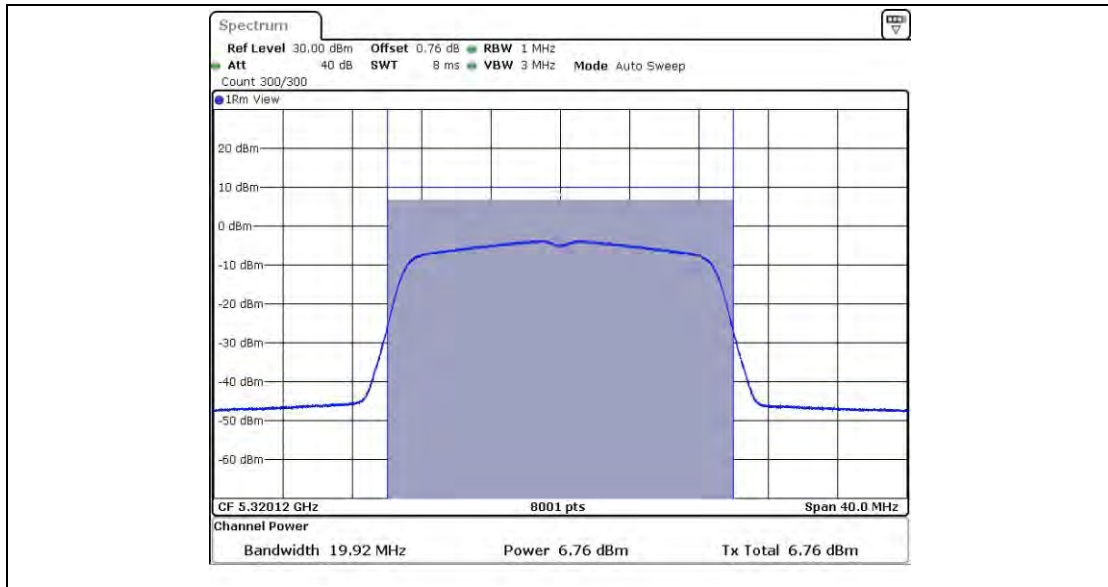
11AC20MIMO_Ant1_5280



11AC20MIMO_Ant2_5280



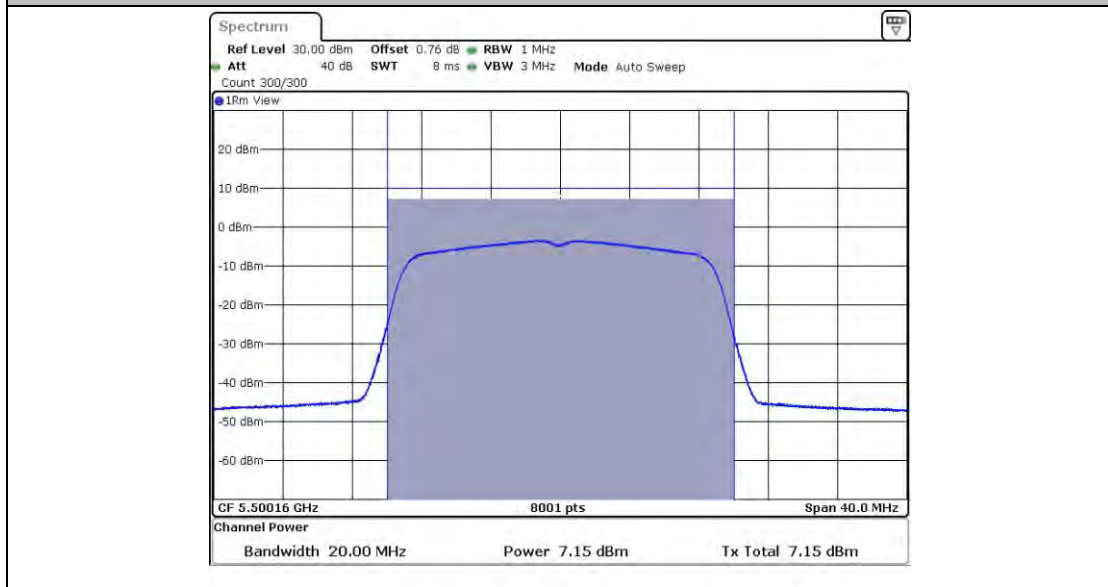
11AC20MIMO_Ant1_5320



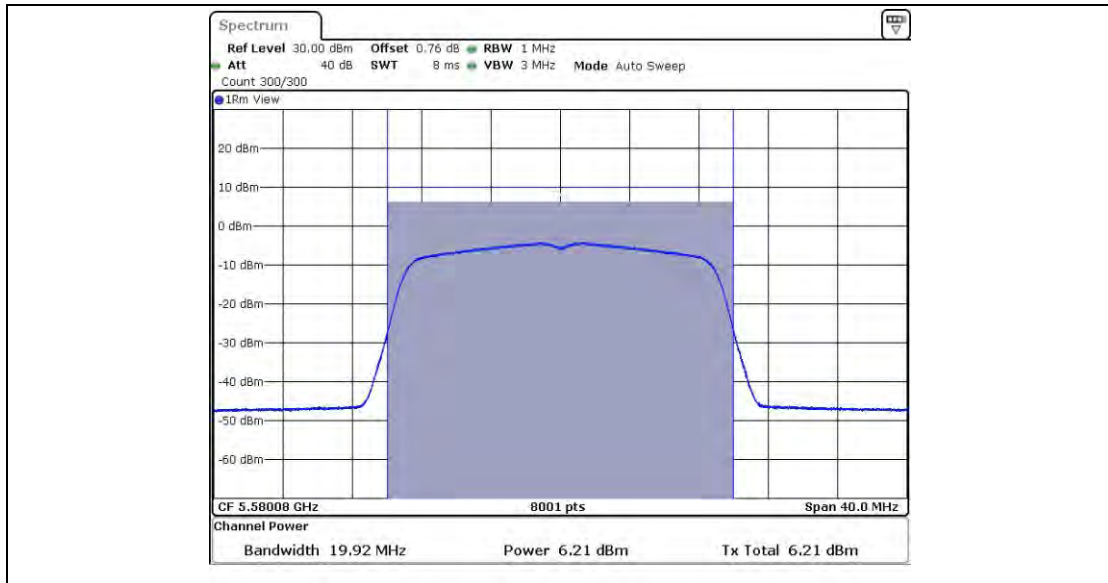
11AC20MIMO_Ant2_5320



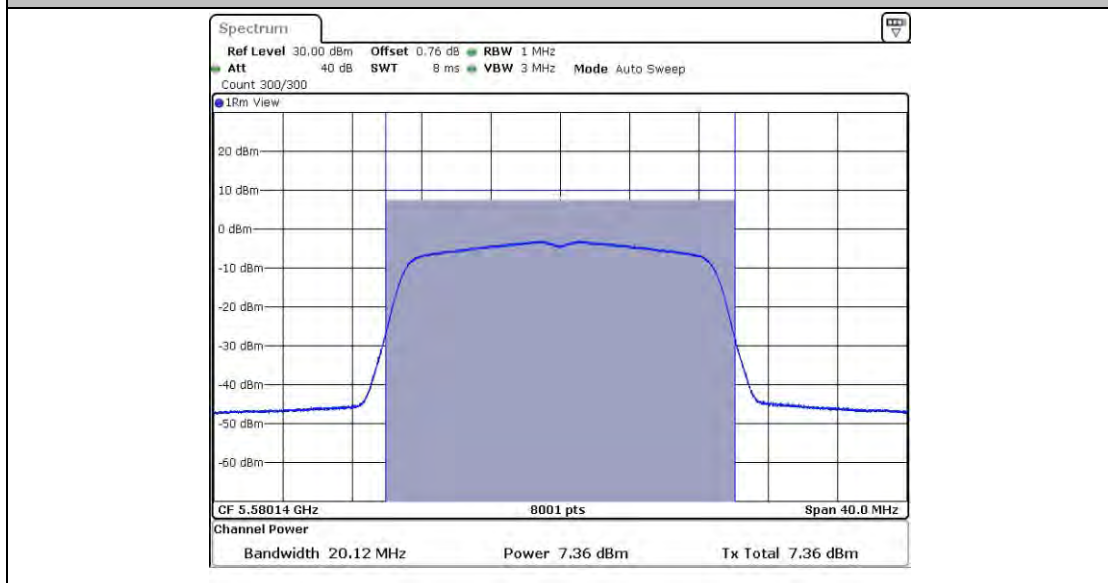
11AC20MIMO_Ant1_5500



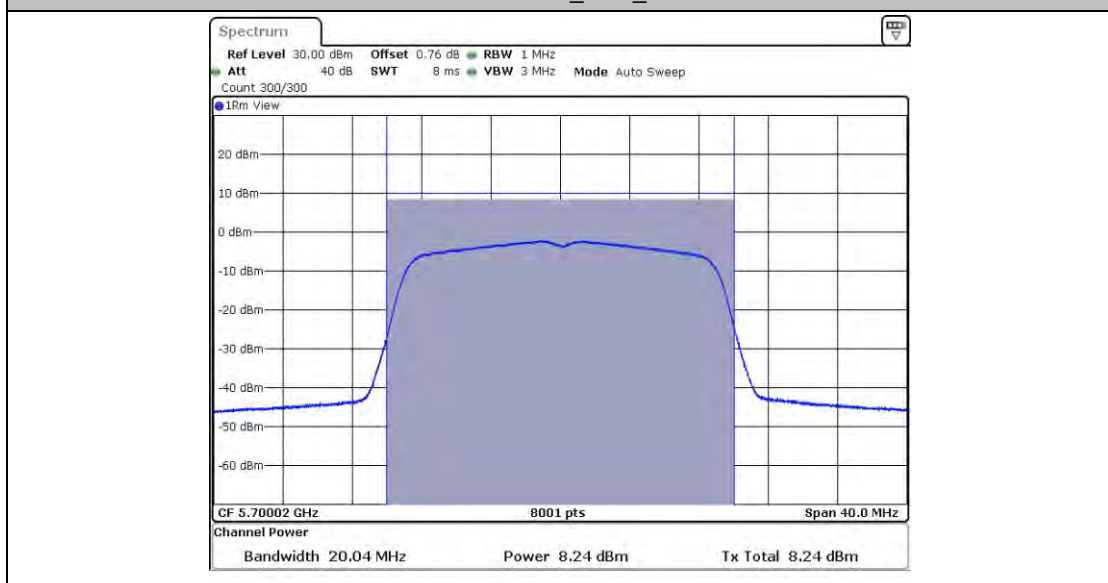
11AC20MIMO_Ant2_5500



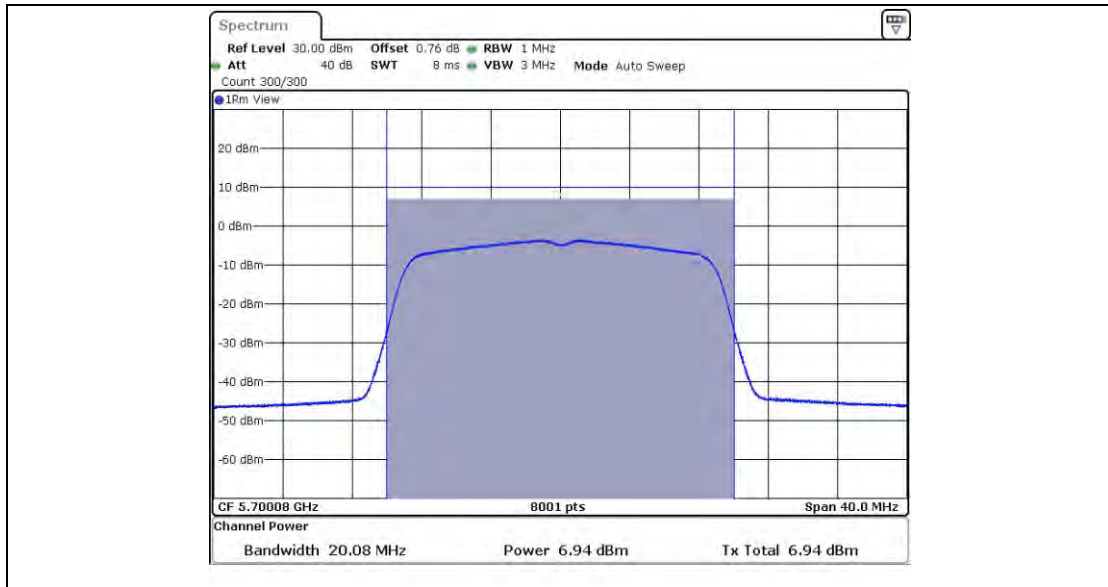
11AC20MIMO_Ant1_5580



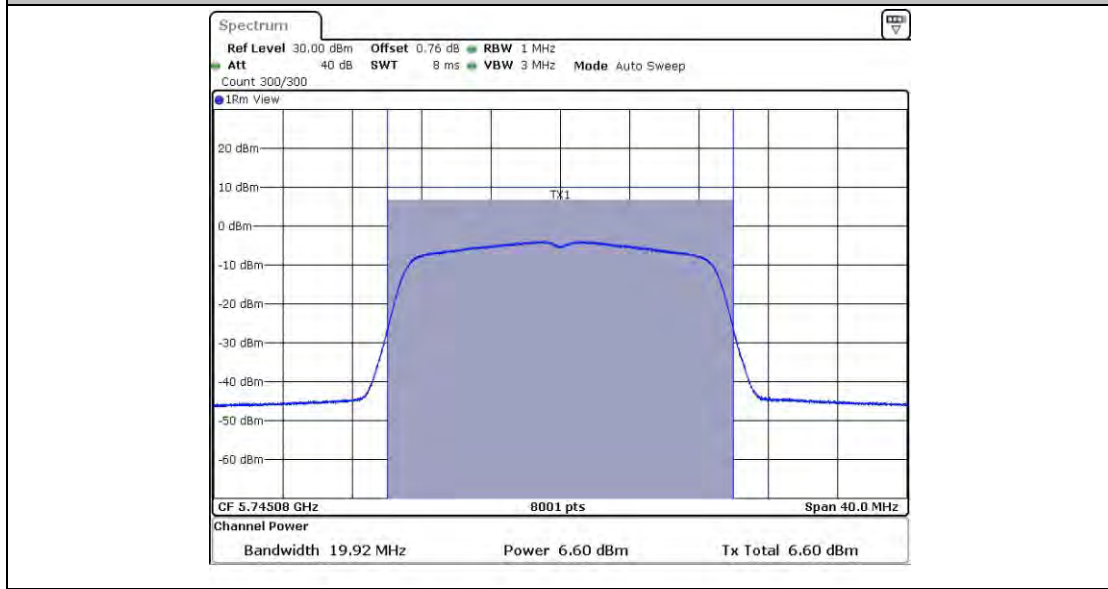
11AC20MIMO_Ant2_5580



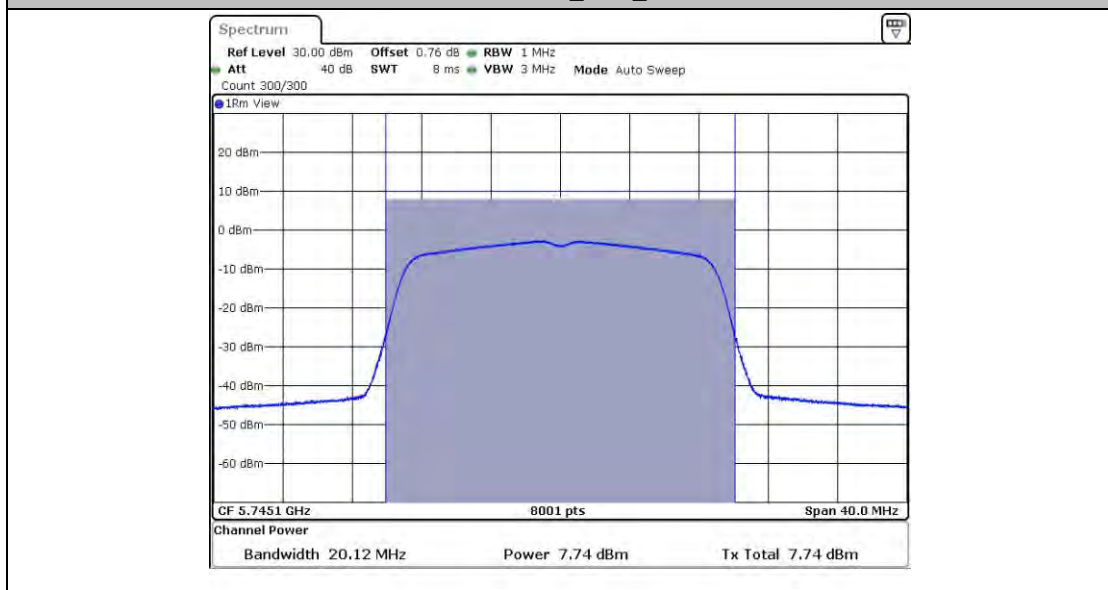
11AC20MIMO_Ant1_5700



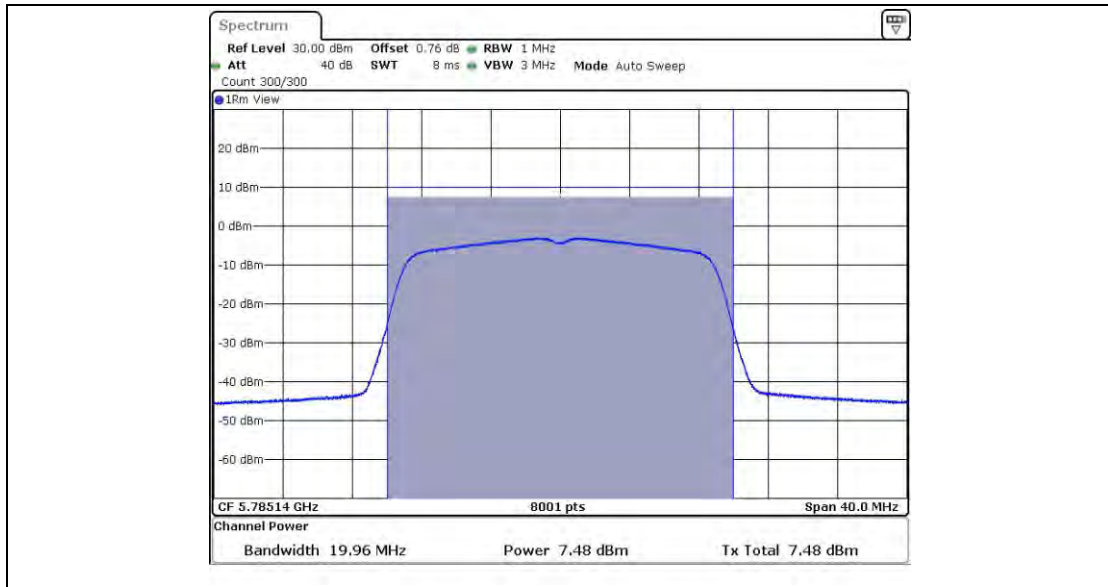
11AC20MIMO_Ant2_5700



11AC20MIMO_Ant1_5745



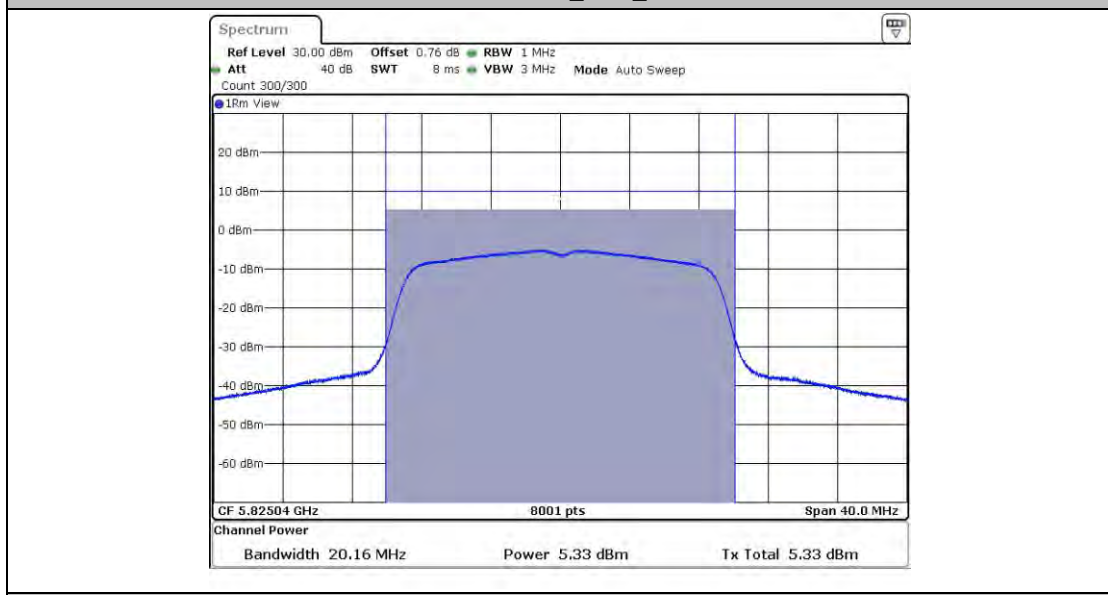
11AC20MIMO_Ant2_5745



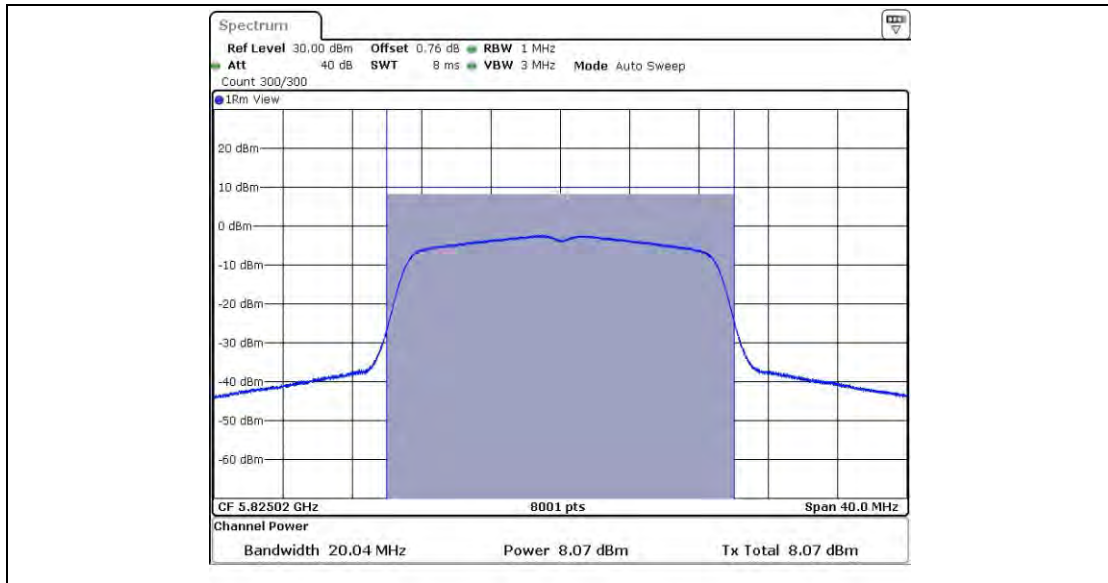
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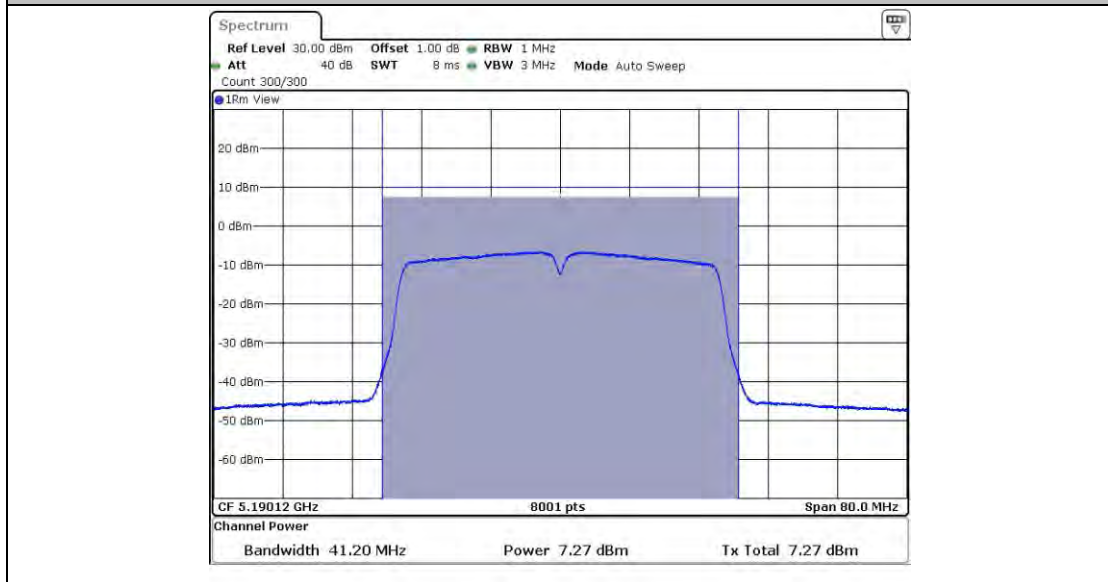
11AC20MIMO_Ant2_5785



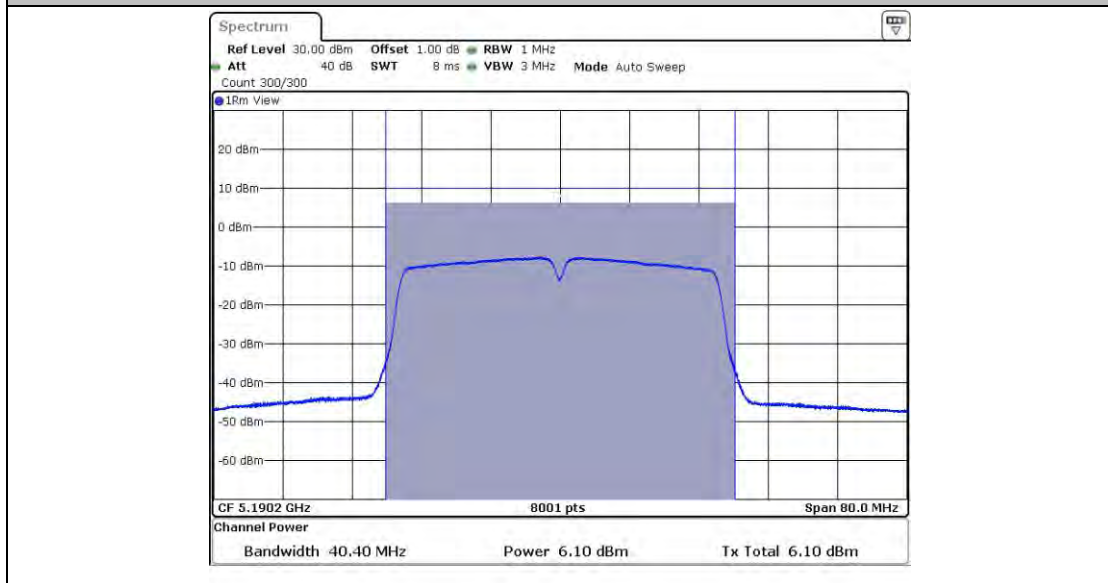
11AC20MIMO_Ant1_5825



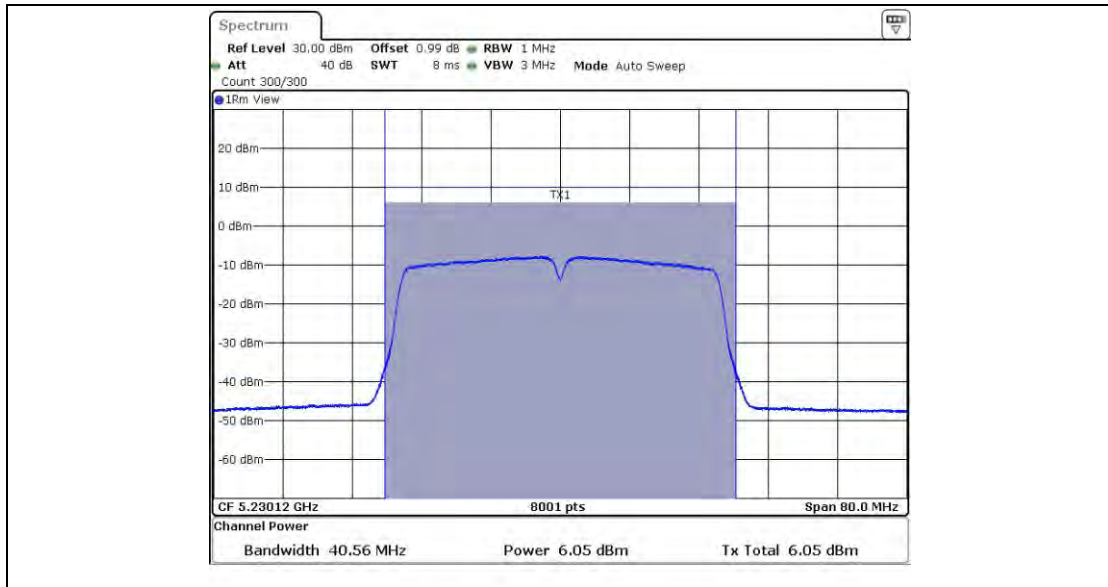
11AC20MIMO_Ant2_5825



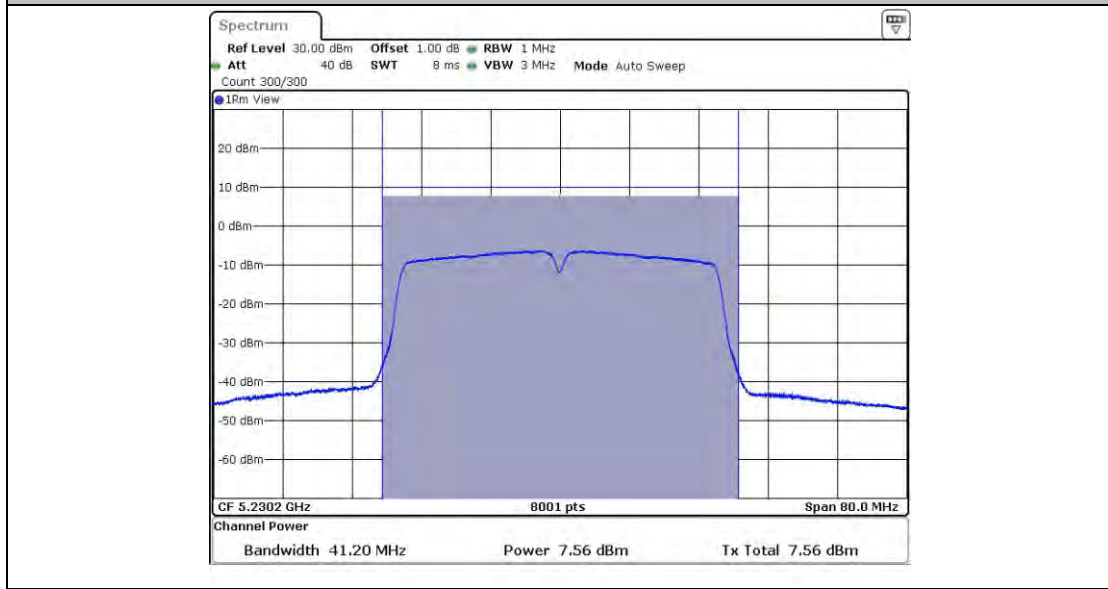
11AC40MIMO_Ant1_5190



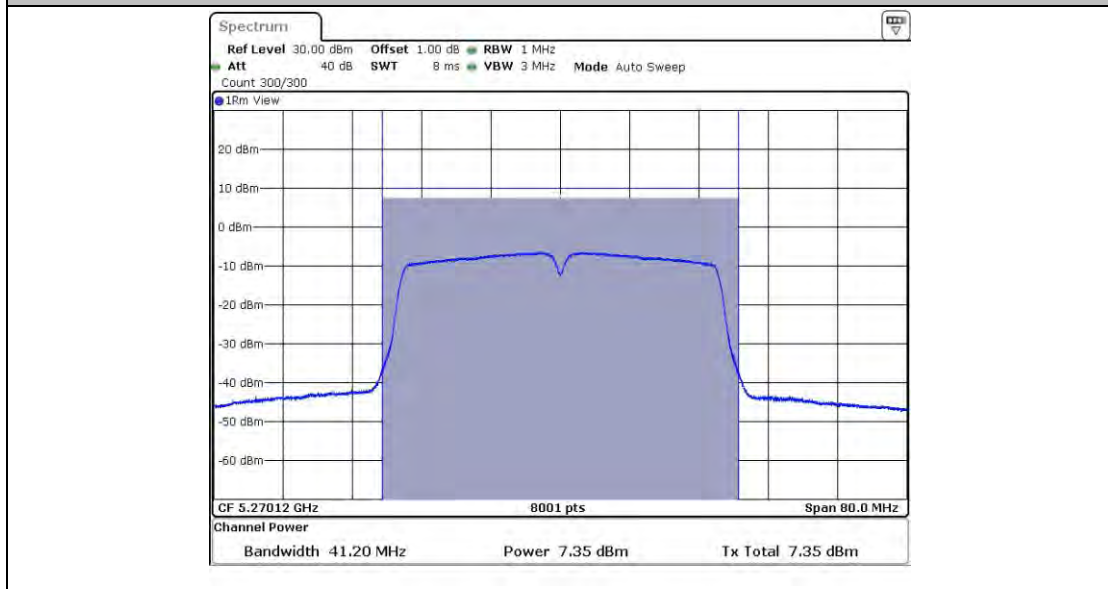
11AC40MIMO_Ant2_5190



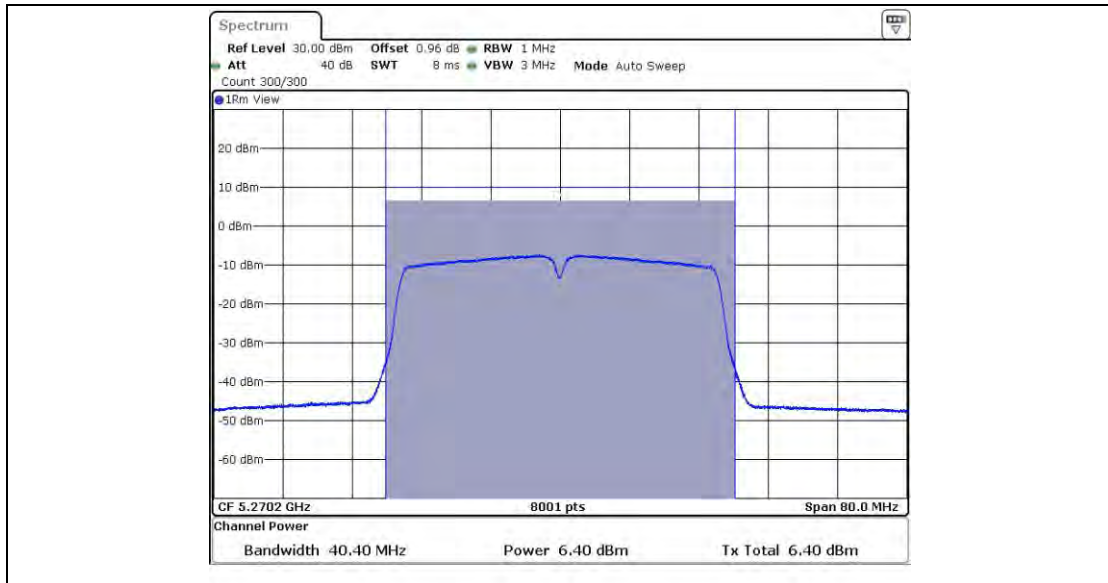
11AC40MIMO_Ant1_5230



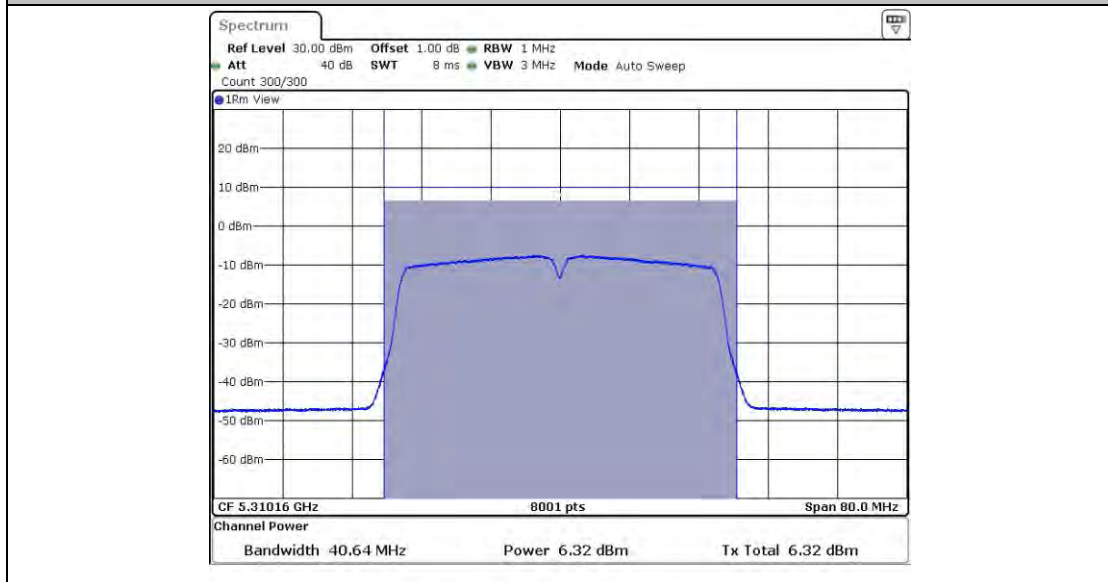
11AC40MIMO_Ant2_5230



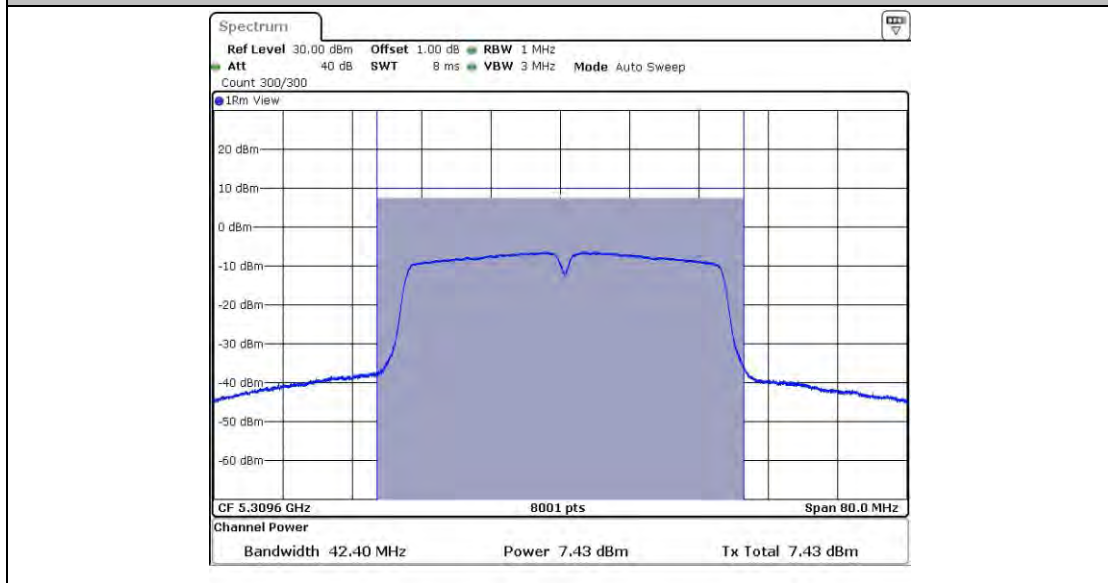
11AC40MIMO_Ant1_5270



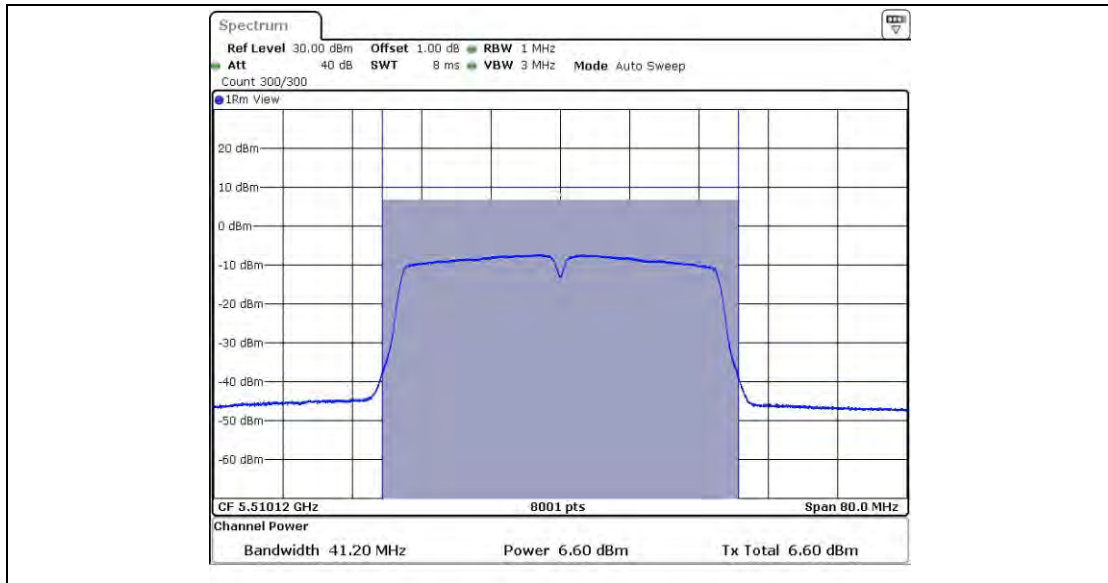
11AC40MIMO_Ant2_5270



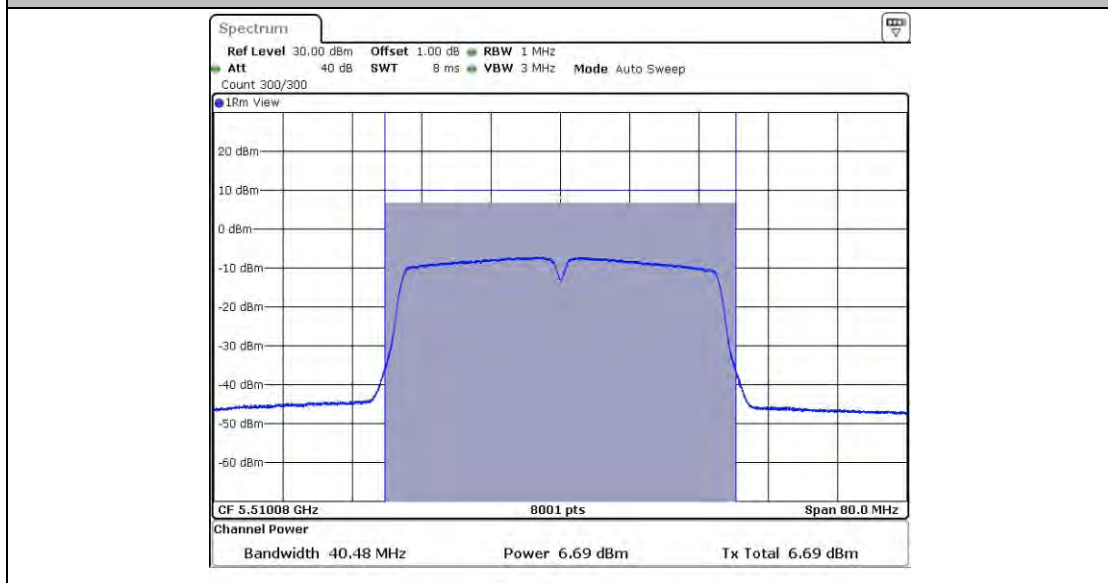
11AC40MIMO_Ant1_5310



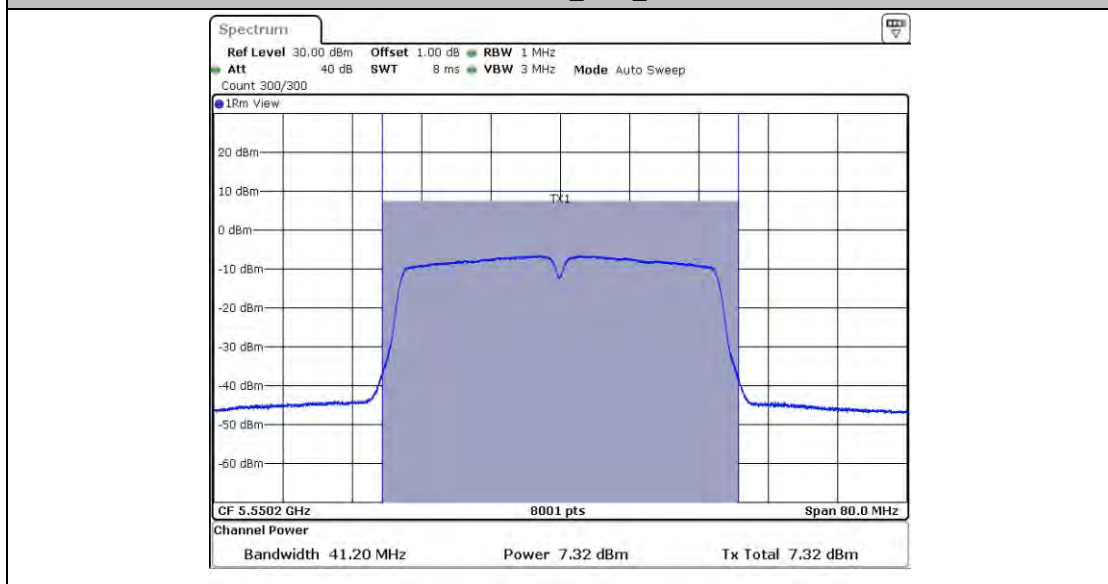
11AC40MIMO_Ant2_5310



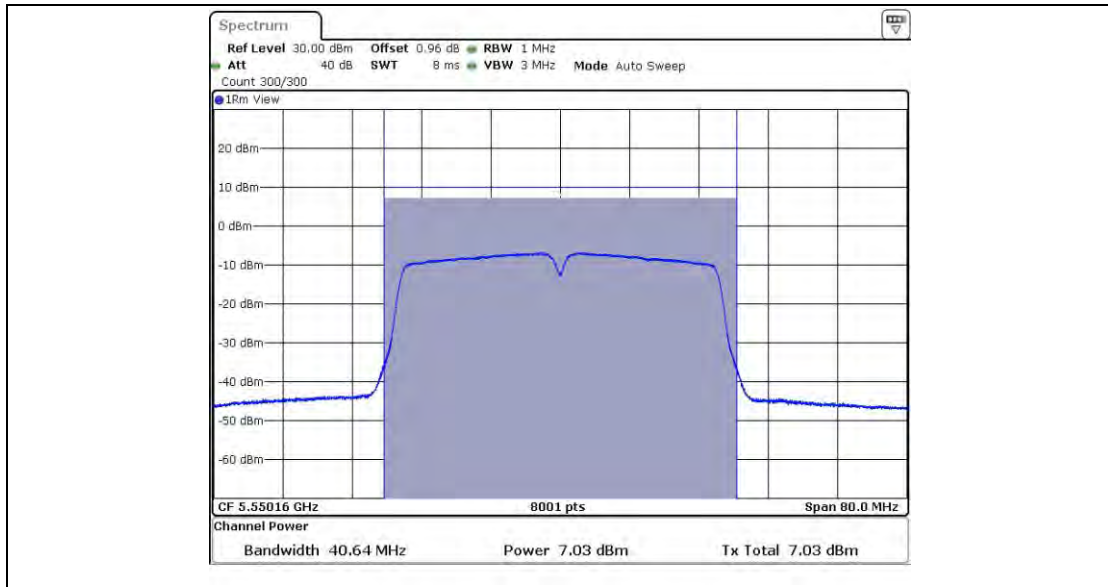
11AC40MIMO_Ant1_5510



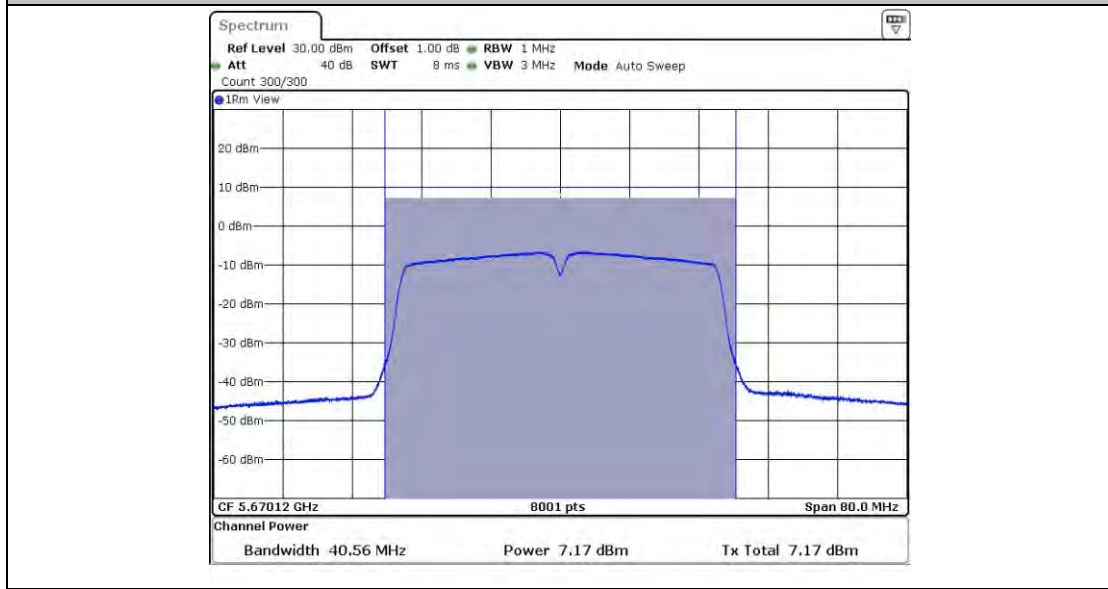
11AC40MIMO_Ant2_5510



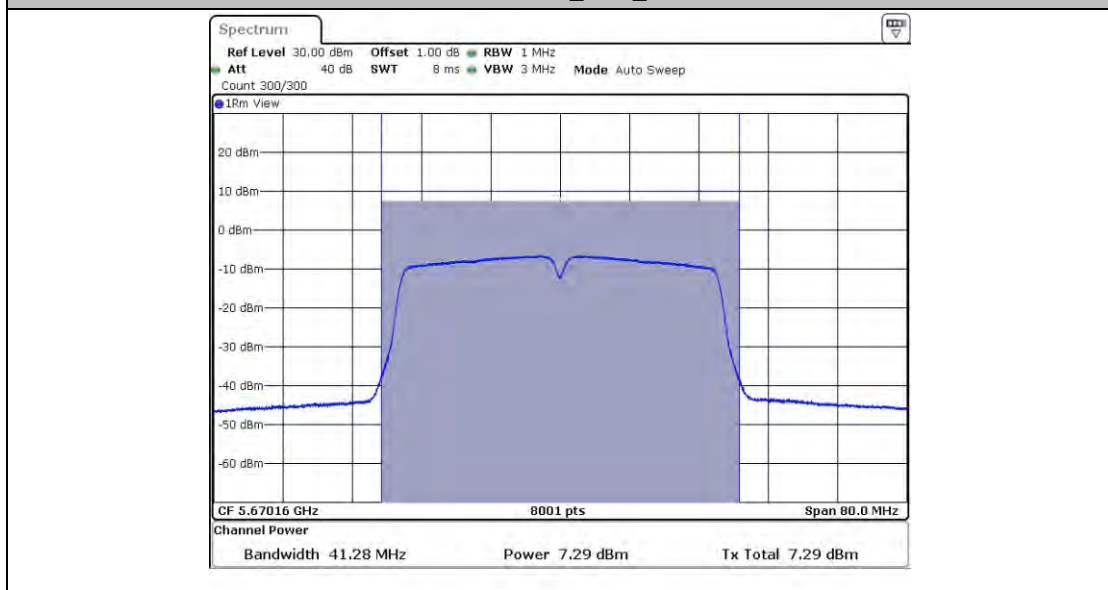
11AC40MIMO_Ant1_5550



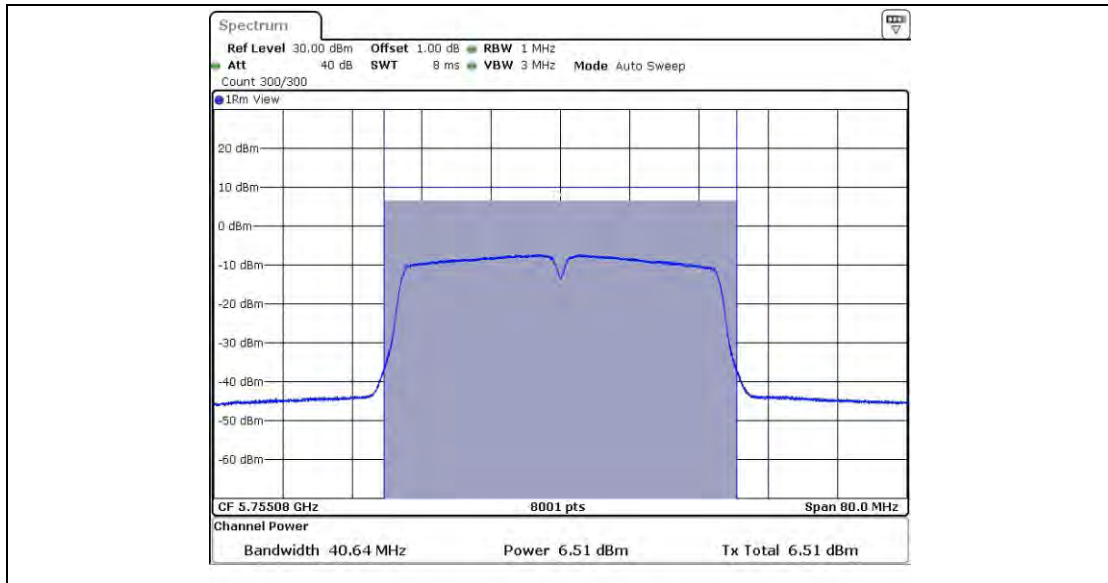
11AC40MIMO_Ant2_5550



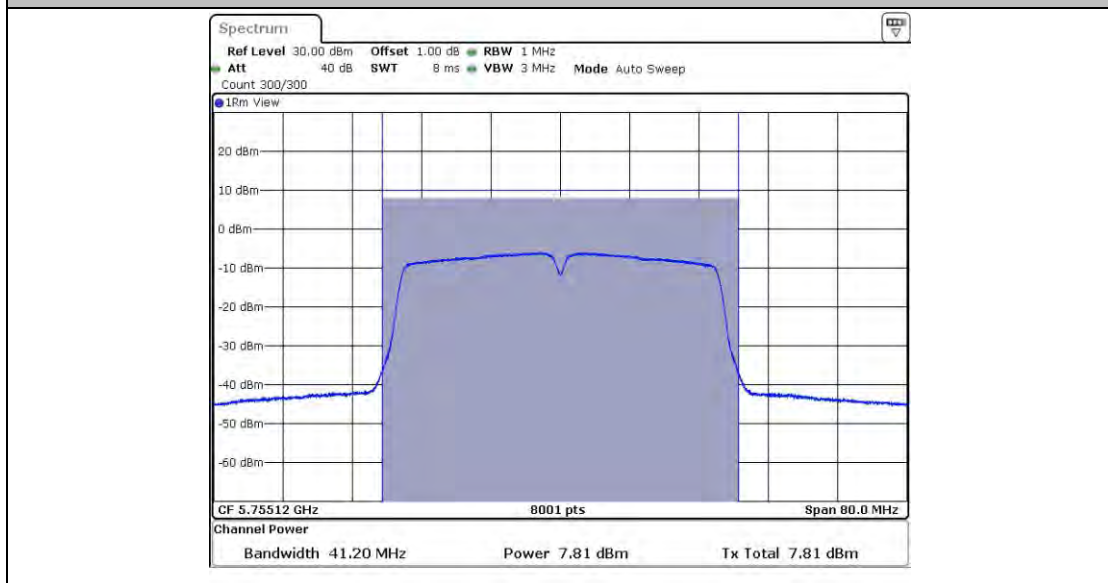
11AC40MIMO_Ant1_5670



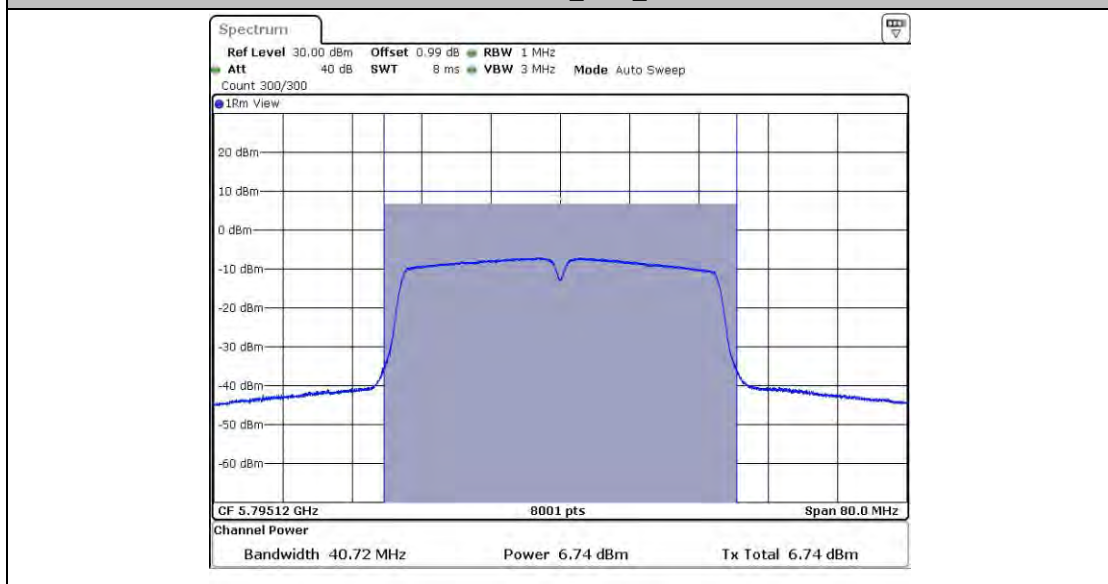
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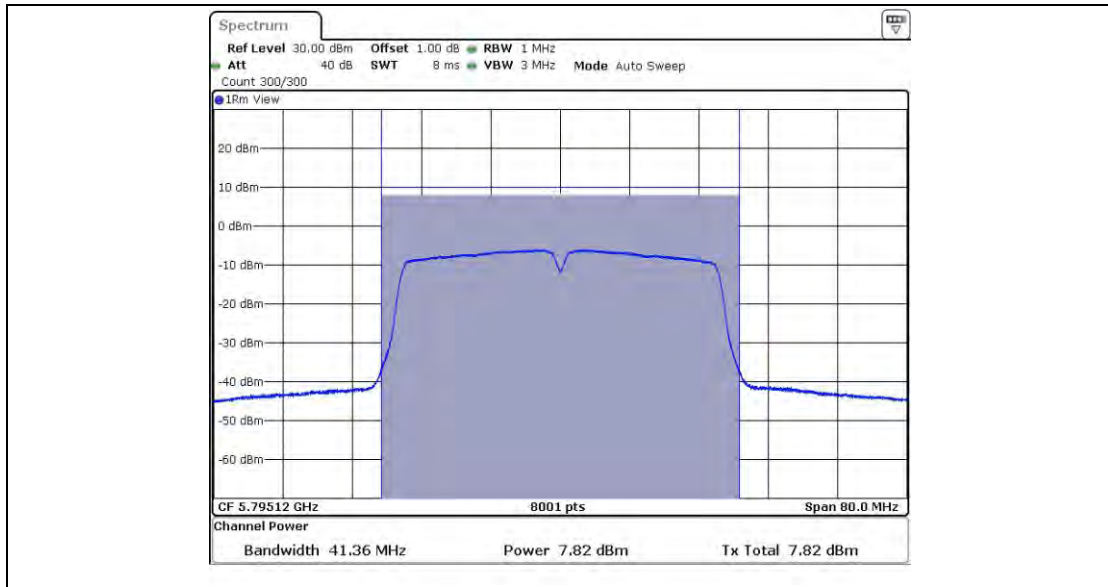
11AC40MIMO_Ant1_5755



11AC40MIMO_Ant2_5755



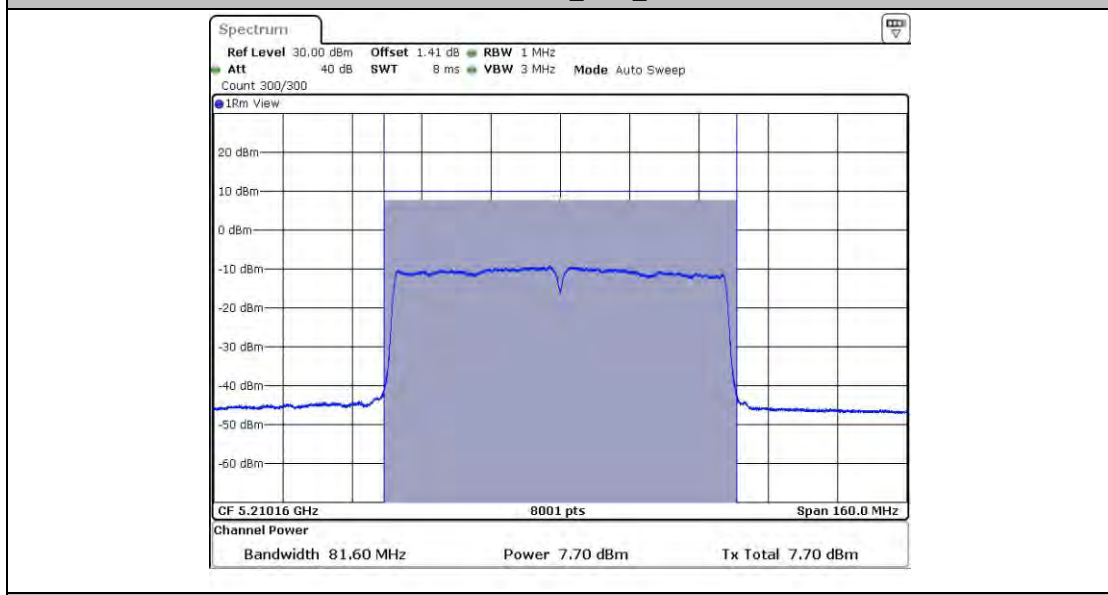
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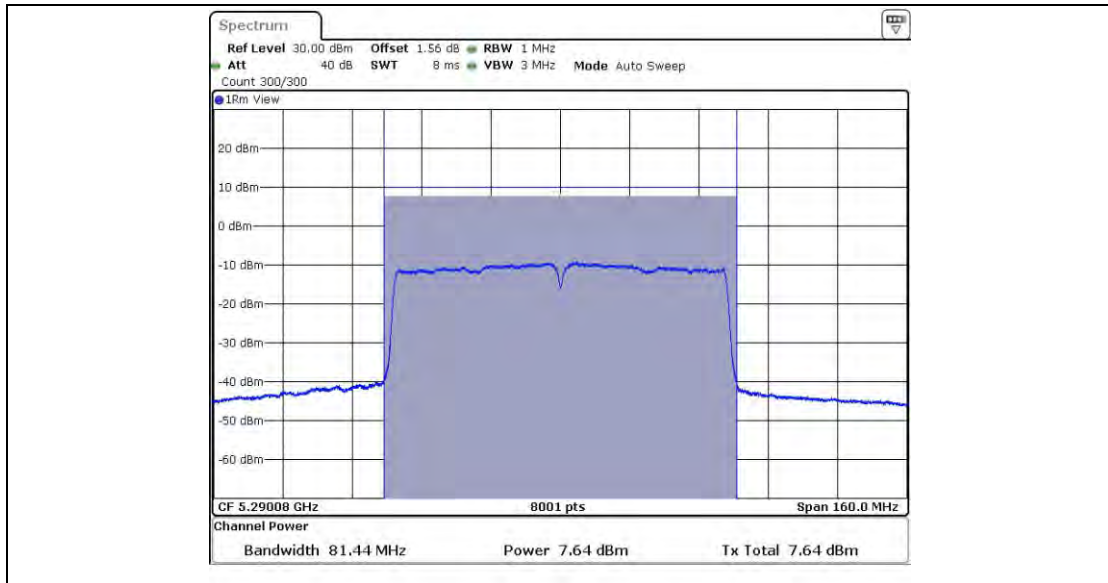
11AC40MIMO_Ant2_5795



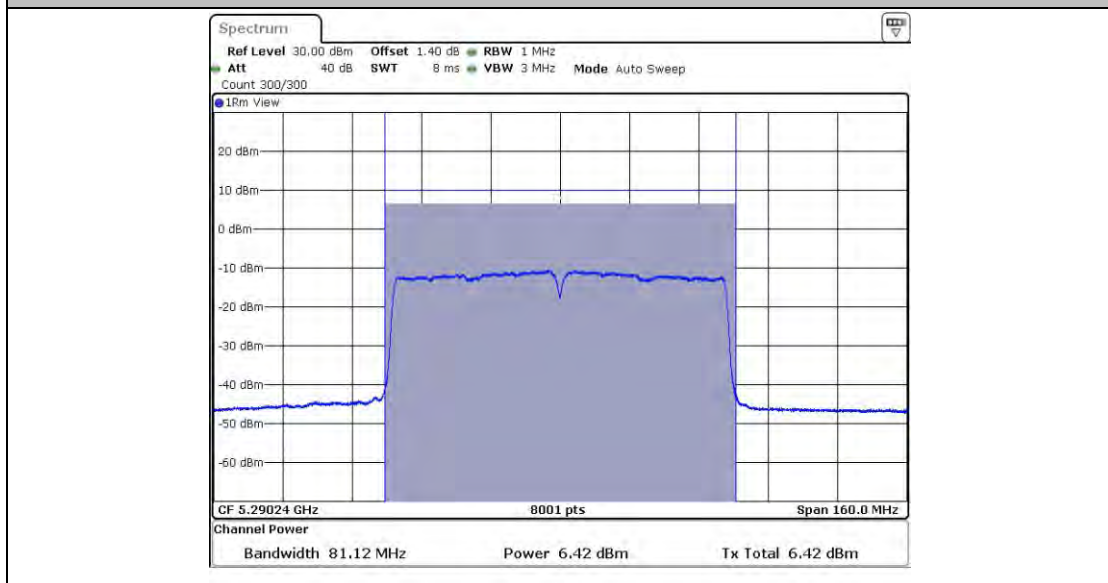
11AC80MIMO_Ant1_5210



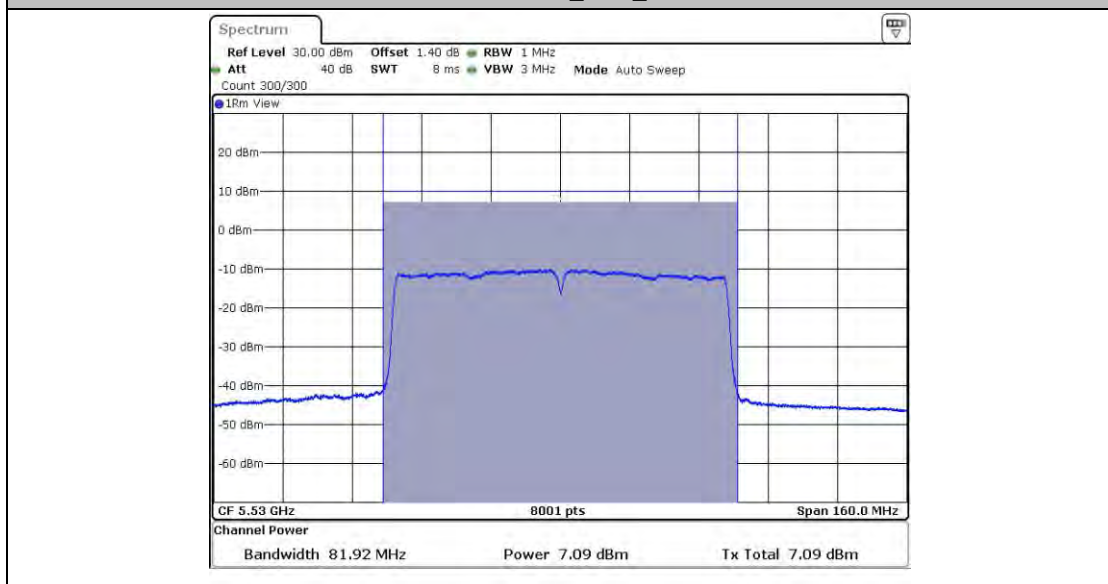
11AC80MIMO_Ant2_5210



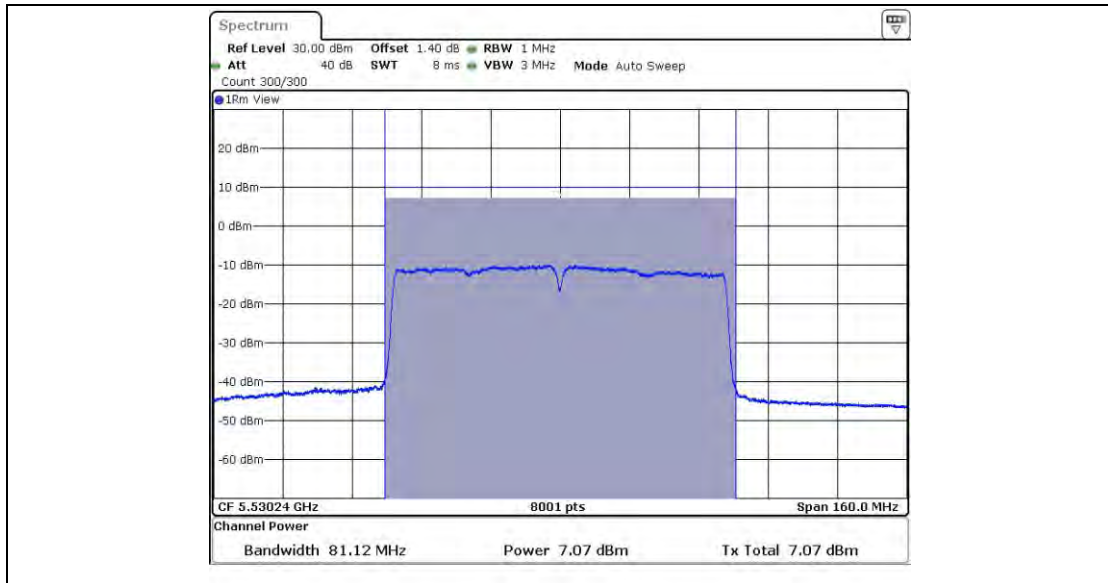
11AC80MIMO_Ant1_5290



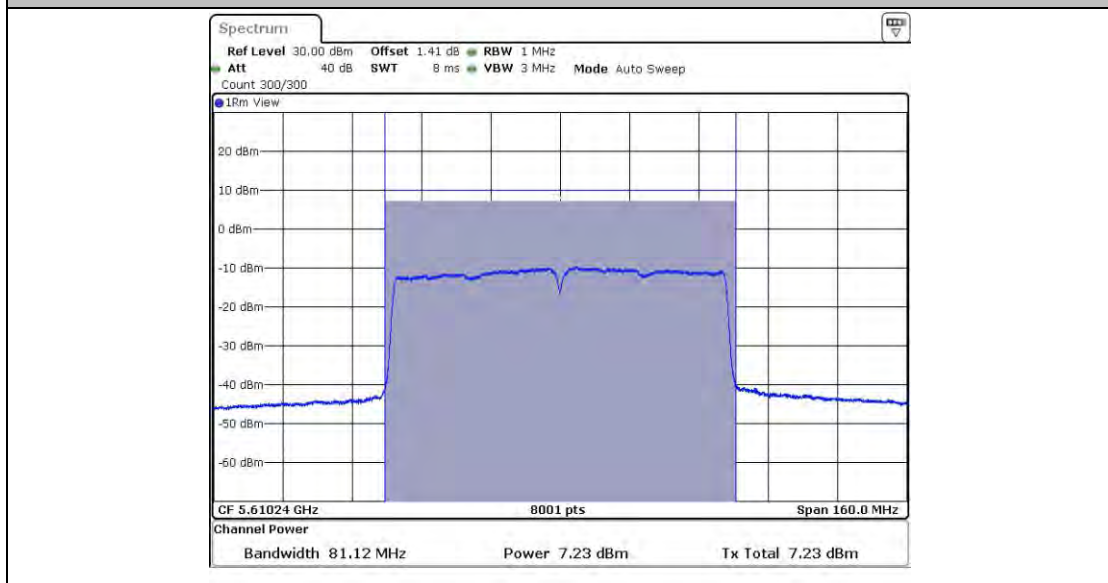
11AC80MIMO_Ant2_5290



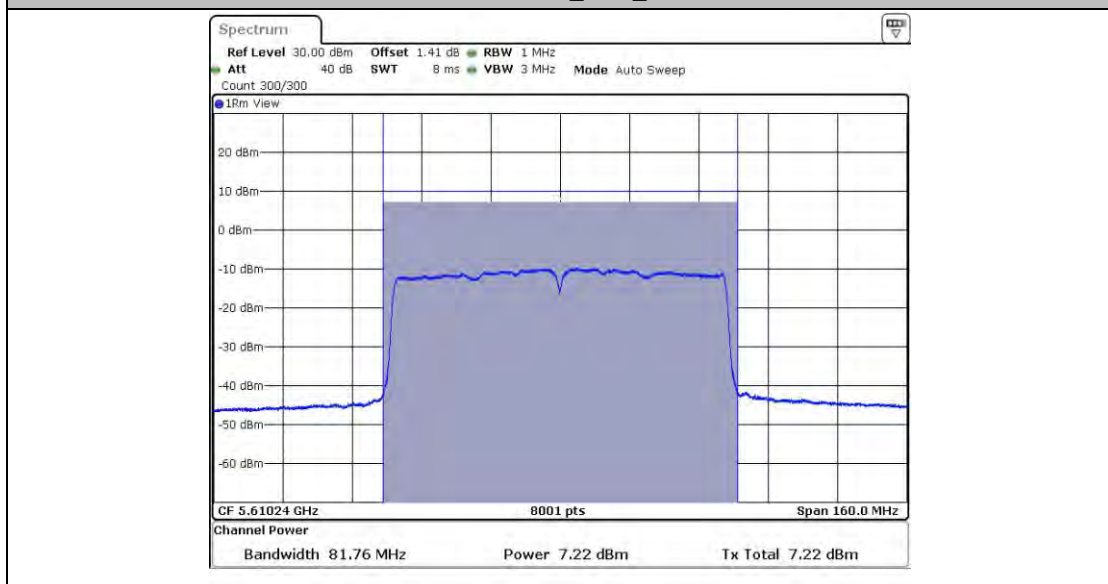
11AC80MIMO_Ant1_5530



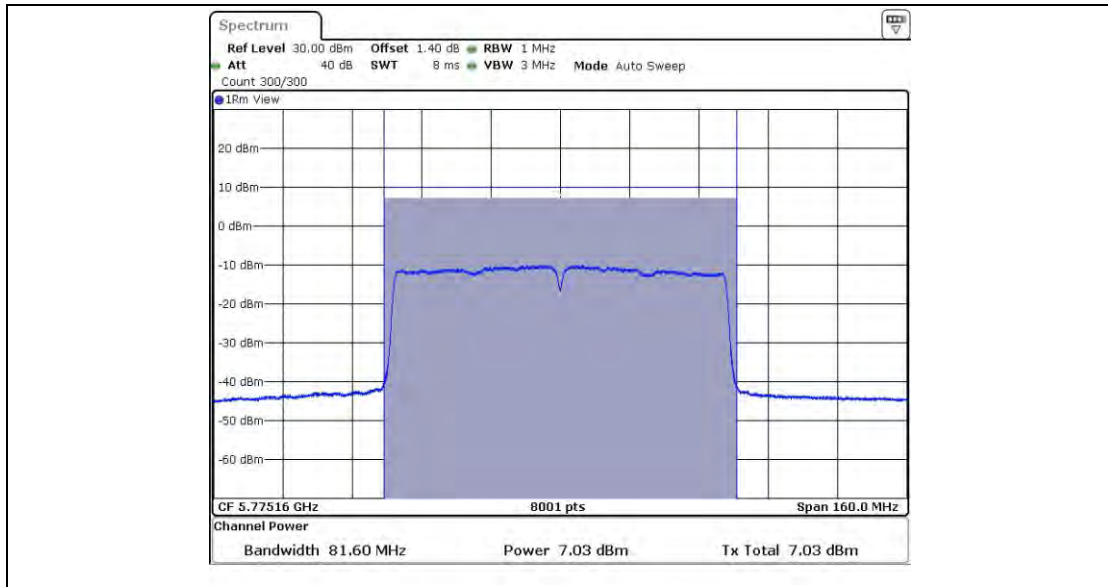
11AC80MIMO_Ant2_5530



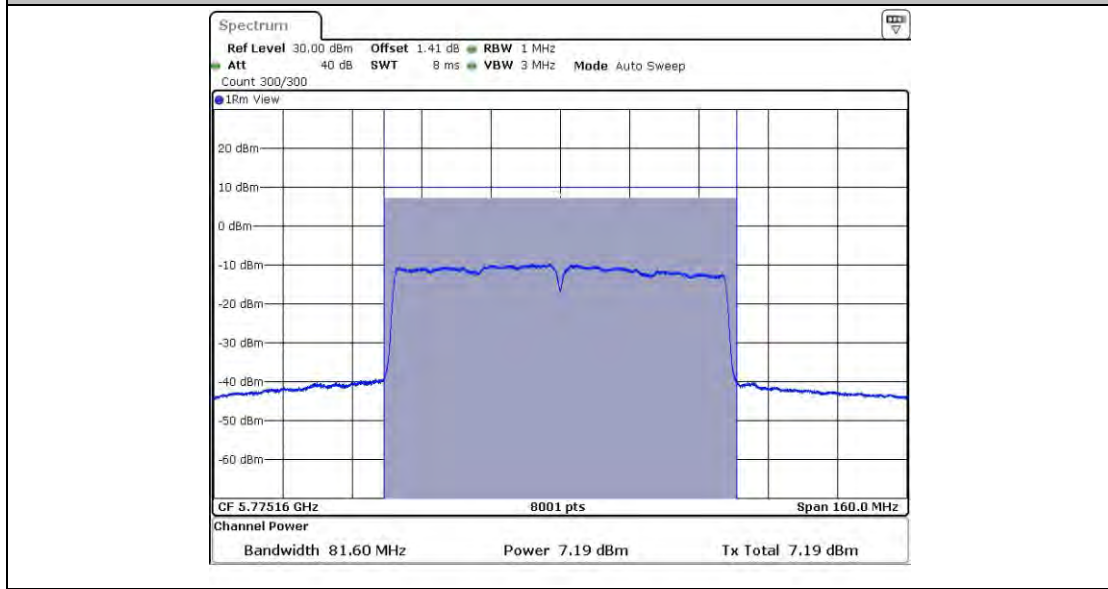
11AC80MIMO_Ant1_5610



11AC80MIMO_Ant2_5610



11AC80MIMO_Ant1_5775



11AC80MIMO_Ant2_5775

Appendix B2: Max EIRP

Test Result

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
11A	Ant1	5180	10.18	2.34	12.52	36	PASS
	Ant2	5180	10.50	2.34	12.84	36	PASS
	Ant1	5200	10.19	2.34	12.53	36	PASS
	Ant2	5200	10.53	2.34	12.87	36	PASS
	Ant1	5240	9.98	2.34	12.32	36	PASS
	Ant2	5240	10.60	2.34	12.94	36	PASS
	Ant1	5260	10.12	2.34	12.46	30	PASS
	Ant2	5260	9.78	2.34	12.12	30	PASS
	Ant1	5280	10.13	2.34	12.47	30	PASS
	Ant2	5280	9.77	2.34	12.11	30	PASS
	Ant1	5320	10.21	2.34	12.55	30	PASS
	Ant2	5320	9.76	2.34	12.10	30	PASS
	Ant1	5500	10.07	2.34	12.41	30	PASS
	Ant2	5500	10.24	2.34	12.58	30	PASS
	Ant1	5580	9.95	2.34	12.29	30	PASS
	Ant2	5580	9.71	2.34	12.05	30	PASS
	Ant1	5700	10.65	2.34	12.99	30	PASS
	Ant2	5700	10.15	2.34	12.49	30	PASS
	Ant1	5745	10.54	2.34	12.88	36	PASS
	Ant2	5745	10.28	2.34	12.62	36	PASS
	Ant1	5785	10.06	2.34	12.40	36	PASS
	Ant2	5785	9.94	2.34	12.28	36	PASS
	Ant1	5825	10.03	2.34	12.37	36	PASS
	Ant2	5825	9.87	2.34	12.21	36	PASS
11N20SISO	Ant1	5180	9.07	2.34	11.41	36	PASS
	Ant2	5180	8.88	2.34	11.22	36	PASS
	Ant1	5200	9.08	2.34	11.42	36	PASS
	Ant2	5200	8.94	2.34	11.28	36	PASS
	Ant1	5240	9.00	2.34	11.34	36	PASS
	Ant2	5240	8.81	2.34	11.15	36	PASS
	Ant1	5260	8.95	2.34	11.29	30	PASS
	Ant2	5260	9.09	2.34	11.43	30	PASS
	Ant1	5280	9.07	2.34	11.41	30	PASS
	Ant2	5280	8.99	2.34	11.33	30	PASS
	Ant1	5320	9.03	2.34	11.37	30	PASS
	Ant2	5320	9.08	2.34	11.42	30	PASS
	Ant1	5500	8.87	2.34	11.21	30	PASS
	Ant2	5500	9.44	2.34	11.78	30	PASS
	Ant1	5580	8.96	2.34	11.30	30	PASS

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant2	5580	8.81	2.34	11.15	30	PASS
	Ant1	5700	9.27	2.34	11.61	30	PASS
	Ant2	5700	9.47	2.34	11.81	30	PASS
	Ant1	5745	9.35	2.34	11.69	36	PASS
	Ant2	5745	9.30	2.34	11.64	36	PASS
	Ant1	5785	9.00	2.34	11.34	36	PASS
	Ant2	5785	8.91	2.34	11.25	36	PASS
	Ant1	5825	8.55	2.34	10.89	36	PASS
	Ant2	5825	8.83	2.34	11.17	36	PASS
11N40SISO	Ant1	5190	8.11	2.34	10.45	36	PASS
	Ant2	5190	7.86	2.34	10.20	36	PASS
	Ant1	5230	7.97	2.34	10.31	36	PASS
	Ant2	5230	7.93	2.34	10.27	36	PASS
	Ant1	5270	8.12	2.34	10.46	30	PASS
	Ant2	5270	7.98	2.34	10.32	30	PASS
	Ant1	5310	8.11	2.34	10.45	30	PASS
	Ant2	5310	8.03	2.34	10.37	30	PASS
	Ant1	5510	7.86	2.34	10.20	30	PASS
	Ant2	5510	8.29	2.34	10.63	30	PASS
	Ant1	5550	7.81	2.34	10.15	30	PASS
	Ant2	5550	8.10	2.34	10.44	30	PASS
	Ant1	5670	8.54	2.34	10.88	30	PASS
	Ant2	5670	8.48	2.34	10.82	30	PASS
	Ant1	5755	8.22	2.34	10.56	36	PASS
	Ant2	5755	8.32	2.34	10.66	36	PASS
	Ant1	5795	7.66	2.34	10.00	36	PASS
	Ant2	5795	7.84	2.34	10.18	36	PASS
11AC20SISO	Ant1	5180	8.54	2.34	10.88	36	PASS
	Ant2	5180	7.86	2.34	10.20	36	PASS
	Ant1	5200	8.49	2.34	10.83	36	PASS
	Ant2	5200	7.93	2.34	10.27	36	PASS
	Ant1	5240	7.97	2.34	10.31	36	PASS
	Ant2	5240	7.93	2.34	10.27	36	PASS
	Ant1	5260	8.30	2.34	10.64	30	PASS
	Ant2	5260	8.18	2.34	10.52	30	PASS
	Ant1	5280	8.36	2.34	10.70	30	PASS
	Ant2	5280	8.21	2.34	10.55	30	PASS
	Ant1	5320	8.56	2.34	10.90	30	PASS
	Ant2	5320	8.15	2.34	10.49	30	PASS
	Ant1	5500	8.26	2.34	10.60	30	PASS
	Ant2	5500	8.53	2.34	10.87	30	PASS
	Ant1	5580	7.84	2.34	10.18	30	PASS
	Ant2	5580	7.68	2.34	10.02	30	PASS

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant1	5700	7.94	2.34	10.28	30	PASS
	Ant2	5700	8.24	2.34	10.58	30	PASS
	Ant1	5745	8.27	2.34	10.61	36	PASS
	Ant2	5745	8.07	2.34	10.41	36	PASS
	Ant1	5785	7.70	2.34	10.04	36	PASS
	Ant2	5785	7.84	2.34	10.18	36	PASS
	Ant1	5825	7.67	2.34	10.01	36	PASS
	Ant2	5825	8.00	2.34	10.34	36	PASS
11AC40SISO	Ant1	5190	7.82	2.34	10.16	36	PASS
	Ant2	5190	8.31	2.34	10.65	36	PASS
	Ant1	5230	8.19	2.34	10.53	36	PASS
	Ant2	5230	8.33	2.34	10.67	36	PASS
	Ant1	5270	8.20	2.34	10.54	30	PASS
	Ant2	5270	7.84	2.34	10.18	30	PASS
	Ant1	5310	8.36	2.34	10.70	30	PASS
	Ant2	5310	7.87	2.34	10.21	30	PASS
	Ant1	5510	8.54	2.34	10.88	30	PASS
	Ant2	5510	8.64	2.34	10.98	30	PASS
	Ant1	5550	7.86	2.34	10.20	30	PASS
	Ant2	5550	7.98	2.34	10.32	30	PASS
	Ant1	5670	8.40	2.34	10.74	30	PASS
	Ant2	5670	8.34	2.34	10.68	30	PASS
	Ant1	5755	7.99	2.34	10.33	36	PASS
	Ant2	5755	8.34	2.34	10.68	36	PASS
	Ant1	5795	9.97	2.34	12.31	36	PASS
	Ant2	5795	8.05	2.34	10.39	36	PASS
11AC80SISO	Ant1	5210	7.95	2.34	10.29	36	PASS
	Ant2	5210	8.07	2.34	10.41	36	PASS
	Ant1	5290	8.07	2.34	10.41	30	PASS
	Ant2	5290	7.78	2.34	10.12	30	PASS
	Ant1	5530	7.91	2.34	10.25	30	PASS
	Ant2	5530	8.09	2.34	10.43	30	PASS
	Ant1	5610	8.23	2.34	10.57	30	PASS
	Ant2	5610	8.09	2.34	10.43	30	PASS
	Ant1	5775	8.38	2.34	10.72	36	PASS
	Ant2	5775	8.31	2.34	10.65	36	PASS
11N20MIMO	Ant1	5180	9.09	--	--	--	--
	Ant2	5180	8.21	--	--	--	--
	total	5180	11.70	5.34	17.04	36	PASS
	Ant1	5200	9.47	--	--	--	--
	Ant2	5200	8.66	--	--	--	--
	total	5200	12.10	5.34	17.44	36	PASS
	Ant1	5240	9.59	--	--	--	--

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant2	5240	8.83	--	--	--	--
	total	5240	12.20	5.34	17.54	36	PASS
	Ant1	5260	8.86	--	--	--	--
	Ant2	5260	8.61	--	--	--	--
	total	5260	11.70	5.34	17.04	30	PASS
	Ant1	5280	9.25	--	--	--	--
	Ant2	5280	8.68	--	--	--	--
	total	5280	12.00	5.34	17.34	30	PASS
	Ant1	5320	9.40	--	--	--	--
	Ant2	5320	8.58	--	--	--	--
	total	5320	12.00	5.34	17.34	30	PASS
	Ant1	5500	8.81	--	--	--	--
	Ant2	5500	9.95	--	--	--	--
	total	5500	12.40	5.34	17.74	30	PASS
	Ant1	5580	9.11	--	--	--	--
	Ant2	5580	8.87	--	--	--	--
	total	5580	12.00	5.34	17.34	30	PASS
	Ant1	5700	9.40	--	--	--	--
	Ant2	5700	8.49	--	--	--	--
	total	5700	12.00	5.34	17.34	30	PASS
	Ant1	5745	9.40	--	--	--	--
	Ant2	5745	8.40	--	--	--	--
	total	5745	11.90	5.34	17.24	36	PASS
	Ant1	5785	8.94	--	--	--	--
	Ant2	5785	8.85	--	--	--	--
	total	5785	11.90	5.34	17.24	36	PASS
	Ant1	5825	9.28	--	--	--	--
	Ant2	5825	8.83	--	--	--	--
total	5825	12.10	5.34	17.44	36	PASS	
11N40MIMO	Ant1	5190	7.78	--	--	--	--
	Ant2	5190	8.86	--	--	--	--
	total	5190	11.40	5.34	16.74	36	PASS
	Ant1	5230	8.71	--	--	--	--
	Ant2	5230	7.60	--	--	--	--
	total	5230	11.20	5.34	16.54	36	PASS
	Ant1	5270	8.18	--	--	--	--
	Ant2	5270	8.75	--	--	--	--
	total	5270	11.50	5.34	16.84	30	PASS
	Ant1	5310	8.77	--	--	--	--
	Ant2	5310	7.83	--	--	--	--
	total	5310	11.30	5.34	16.64	30	PASS
	Ant1	5510	8.37	--	--	--	--
	Ant2	5510	8.40	--	--	--	--

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	total	5510	11.40	5.34	16.74	30	PASS
	Ant1	5550	7.99	--	--	--	--
	Ant2	5550	7.67	--	--	--	--
	total	5550	10.80	5.34	16.14	30	PASS
	Ant1	5670	8.35	--	--	--	--
	Ant2	5670	8.37	--	--	--	--
	total	5670	11.40	5.34	16.74	30	PASS
	Ant1	5755	8.69	--	--	--	--
	Ant2	5755	7.19	--	--	--	--
	total	5755	11.00	5.34	16.34	36	PASS
	Ant1	5795	8.21	--	--	--	--
	Ant2	5795	8.47	--	--	--	--
11AC20MIMO	total	5795	11.40	5.34	16.74	36	PASS
	Ant1	5180	6.46	--	--	--	--
	Ant2	5180	7.89	--	--	--	--
	total	5180	10.20	5.34	15.54	36	PASS
	Ant1	5200	7.77	--	--	--	--
	Ant2	5200	6.34	--	--	--	--
	total	5200	10.10	5.34	15.44	36	PASS
	Ant1	5240	6.38	--	--	--	--
	Ant2	5240	7.46	--	--	--	--
	total	5240	10.00	5.34	15.34	36	PASS
	Ant1	5260	7.52	--	--	--	--
	Ant2	5260	6.50	--	--	--	--
	total	5260	10.10	5.34	15.44	30	PASS
	Ant1	5280	6.58	--	--	--	--
	Ant2	5280	7.65	--	--	--	--
	total	5280	10.20	5.34	15.54	30	PASS
	Ant1	5320	7.76	--	--	--	--
	Ant2	5320	6.76	--	--	--	--
	total	5320	10.30	5.34	15.64	30	PASS
	Ant1	5500	7.71	--	--	--	--
	Ant2	5500	7.15	--	--	--	--
	total	5500	10.40	5.34	15.74	30	PASS
	Ant1	5580	6.21	--	--	--	--
	Ant2	5580	7.36	--	--	--	--
	total	5580	9.80	5.34	15.14	30	PASS
	Ant1	5700	8.24	--	--	--	--
	Ant2	5700	6.94	--	--	--	--
	total	5700	10.60	5.34	15.94	30	PASS
	Ant1	5745	6.60	--	--	--	--
	Ant2	5745	7.74	--	--	--	--
total	5745	10.20	5.34	15.54	36	PASS	

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant1	5785	7.48	--	--	--	--
	Ant2	5785	6.22	--	--	--	--
	total	5785	9.90	5.34	15.24	36	PASS
	Ant1	5825	5.33	--	--	--	--
	Ant2	5825	8.07	--	--	--	--
	total	5825	9.90	5.34	15.24	36	PASS
11AC40MIMO	Ant1	5190	7.27	--	--	--	--
	Ant2	5190	6.10	--	--	--	--
	total	5190	9.70	5.34	15.04	36	PASS
	Ant1	5230	6.05	--	--	--	--
	Ant2	5230	7.56	--	--	--	--
	total	5230	9.90	5.34	15.24	36	PASS
	Ant1	5270	7.35	--	--	--	--
	Ant2	5270	6.40	--	--	--	--
	total	5270	9.90	5.34	15.24	30	PASS
	Ant1	5310	6.32	--	--	--	--
	Ant2	5310	7.43	--	--	--	--
	total	5310	9.90	5.34	15.24	30	PASS
	Ant1	5510	6.60	--	--	--	--
	Ant2	5510	6.69	--	--	--	--
	total	5510	9.70	5.34	15.04	30	PASS
	Ant1	5550	7.32	--	--	--	--
	Ant2	5550	7.03	--	--	--	--
	total	5550	10.20	5.34	15.54	30	PASS
	Ant1	5670	7.17	--	--	--	--
	Ant2	5670	7.29	--	--	--	--
	total	5670	10.20	5.34	15.54	30	PASS
	Ant1	5755	6.51	--	--	--	--
	Ant2	5755	7.81	--	--	--	--
	total	5755	10.20	5.34	15.54	36	PASS
Ant1	5795	6.74	--	--	--	--	
Ant2	5795	7.82	--	--	--	--	
total	5795	10.30	5.34	15.64	36	PASS	
11AC80MIMO	Ant1	5210	6.40	--	--	--	--
	Ant2	5210	7.70	--	--	--	--
	total	5210	10.10	5.34	15.44	36	PASS
	Ant1	5290	7.64	--	--	--	--
	Ant2	5290	6.42	--	--	--	--
	total	5290	10.10	5.34	15.44	30	PASS
	Ant1	5530	7.09	--	--	--	--
	Ant2	5530	7.07	--	--	--	--
	total	5530	10.10	5.34	15.44	30	PASS
Ant1	5610	7.23	--	--	--	--	

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant2	5610	7.22	--	--	--	--
	total	5610	10.20	5.34	15.54	30	PASS
	Ant1	5775	7.03	--	--	--	--
	Ant2	5775	7.19	--	--	--	--
	total	5775	10.10	5.34	15.44	36	PASS