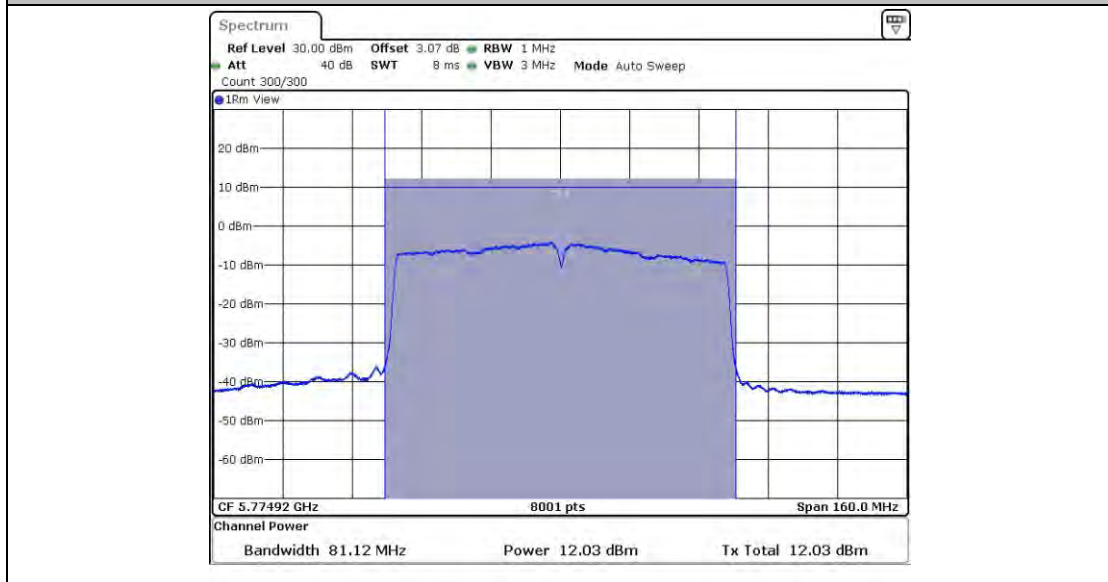


11AC80MIMO\_Ant1\_5775



11AC80MIMO\_Ant2\_5775

## Appendix B2: Max EIRP

### Test Result

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
11A	Ant1	5180	6.63	2.3	8.93	36	PASS
	Ant2	5180	6.68	2.3	8.98	36	PASS
	Ant1	5200	6.45	2.3	8.75	36	PASS
	Ant2	5200	6.58	2.3	8.88	36	PASS
	Ant1	5240	6.42	2.3	8.72	36	PASS
	Ant2	5240	6.59	2.3	8.89	36	PASS
	Ant1	5260	12.72	2.3	15.02	30	PASS
	Ant2	5260	13.36	2.3	15.66	30	PASS
	Ant1	5280	12.76	2.3	15.06	30	PASS
	Ant2	5280	13.47	2.3	15.77	30	PASS
	Ant1	5320	12.81	2.3	15.11	30	PASS
	Ant2	5320	13.42	2.3	15.72	30	PASS
	Ant1	5500	13.51	2.3	15.81	30	PASS
	Ant2	5500	13.31	2.3	15.61	30	PASS
	Ant1	5580	12.52	2.3	14.82	30	PASS
	Ant2	5580	13.16	2.3	15.46	30	PASS
	Ant1	5700	12.94	2.3	15.24	30	PASS
	Ant2	5700	14.12	2.3	16.42	30	PASS
	Ant1	5745	12.67	2.3	14.97	36	PASS
	Ant2	5745	14.07	2.3	16.37	36	PASS
	Ant1	5785	12.66	2.3	14.96	36	PASS
	Ant2	5785	13.71	2.3	16.01	36	PASS
	Ant1	5825	12.68	2.3	14.98	36	PASS
	Ant2	5825	13.38	2.3	15.68	36	PASS
11N20SISO	Ant1	5180	5.60	2.3	7.9	36	PASS
	Ant2	5180	5.83	2.3	8.13	36	PASS
	Ant1	5200	5.52	2.3	7.82	36	PASS
	Ant2	5200	5.68	2.3	7.98	36	PASS
	Ant1	5240	5.54	2.3	7.84	36	PASS
	Ant2	5240	5.73	2.3	8.03	36	PASS
	Ant1	5260	11.72	2.3	14.02	30	PASS
	Ant2	5260	12.00	2.3	14.3	30	PASS
	Ant1	5280	11.84	2.3	14.14	30	PASS
	Ant2	5280	12.05	2.3	14.35	30	PASS
	Ant1	5320	11.90	2.3	14.2	30	PASS
	Ant2	5320	12.05	2.3	14.35	30	PASS
	Ant1	5500	12.53	2.3	14.83	30	PASS
	Ant2	5500	12.90	2.3	15.2	30	PASS
	Ant1	5580	12.10	2.3	14.4	30	PASS

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant2	5580	12.79	2.3	15.09	30	PASS
	Ant1	5700	11.79	2.3	14.09	30	PASS
	Ant2	5700	12.87	2.3	15.17	30	PASS
	Ant1	5745	11.72	2.3	14.02	36	PASS
	Ant2	5745	12.77	2.3	15.07	36	PASS
	Ant1	5785	11.57	2.3	13.87	36	PASS
	Ant2	5785	12.56	2.3	14.86	36	PASS
	Ant1	5825	11.51	2.3	13.81	36	PASS
	Ant2	5825	12.14	2.3	14.44	36	PASS
11N40SISO	Ant1	5190	8.62	2.3	10.92	36	PASS
	Ant2	5190	8.65	2.3	10.95	36	PASS
	Ant1	5230	8.68	2.3	10.98	36	PASS
	Ant2	5230	8.74	2.3	11.04	36	PASS
	Ant1	5270	12.03	2.3	14.33	30	PASS
	Ant2	5270	12.26	2.3	14.56	30	PASS
	Ant1	5310	12.09	2.3	14.39	30	PASS
	Ant2	5310	12.07	2.3	14.37	30	PASS
	Ant1	5510	12.70	2.3	15	30	PASS
	Ant2	5510	13.10	2.3	15.4	30	PASS
	Ant1	5550	12.50	2.3	14.8	30	PASS
	Ant2	5550	12.99	2.3	15.29	30	PASS
	Ant1	5670	12.09	2.3	14.39	30	PASS
	Ant2	5670	13.09	2.3	15.39	30	PASS
	Ant1	5755	11.94	2.3	14.24	36	PASS
	Ant2	5755	12.84	2.3	15.14	36	PASS
	Ant1	5795	12.99	2.3	16.29	36	PASS
	Ant2	5795	12.53	2.3	14.83	36	PASS
11AC20SISO	Ant1	5180	5.73	2.3	8.03	36	PASS
	Ant2	5180	5.85	2.3	8.15	36	PASS
	Ant1	5200	5.55	2.3	7.85	36	PASS
	Ant2	5200	5.71	2.3	8.01	36	PASS
	Ant1	5240	5.58	2.3	7.88	36	PASS
	Ant2	5240	5.66	2.3	7.96	36	PASS
	Ant1	5260	11.85	2.3	14.15	30	PASS
	Ant2	5260	12.03	2.3	14.33	30	PASS
	Ant1	5280	11.92	2.3	14.22	30	PASS
	Ant2	5280	11.89	2.3	14.19	30	PASS
	Ant1	5320	11.96	2.3	14.26	30	PASS
	Ant2	5320	11.92	2.3	14.22	30	PASS
	Ant1	5500	11.90	2.3	14.2	30	PASS
	Ant2	5500	12.80	2.3	15.1	30	PASS
	Ant1	5580	11.79	2.3	14.09	30	PASS
	Ant2	5580	12.68	2.3	14.98	30	PASS

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant1	5700	12.16	2.3	14.46	30	PASS
	Ant2	5700	12.79	2.3	15.09	30	PASS
	Ant1	5745	12.14	2.3	14.44	36	PASS
	Ant2	5745	12.63	2.3	14.93	36	PASS
	Ant1	5785	12.16	2.3	14.46	36	PASS
	Ant2	5785	12.40	2.3	14.7	36	PASS
	Ant1	5825	12.06	2.3	14.36	36	PASS
	Ant2	5825	12.10	2.3	14.4	36	PASS
11AC40SISO	Ant1	5190	8.73	2.3	11.03	36	PASS
	Ant2	5190	8.68	2.3	10.98	36	PASS
	Ant1	5230	8.54	2.3	10.84	36	PASS
	Ant2	5230	8.70	2.3	11	36	PASS
	Ant1	5270	12.09	2.3	14.39	30	PASS
	Ant2	5270	12.01	2.3	14.31	30	PASS
	Ant1	5310	11.99	2.3	14.29	30	PASS
	Ant2	5310	11.92	2.3	14.22	30	PASS
	Ant1	5510	12.08	2.3	14.38	30	PASS
	Ant2	5510	12.76	2.3	15.06	30	PASS
	Ant1	5550	11.98	2.3	14.28	30	PASS
	Ant2	5550	12.86	2.3	15.16	30	PASS
	Ant1	5670	12.12	2.3	14.42	30	PASS
	Ant2	5670	12.88	2.3	15.18	30	PASS
	Ant1	5755	12.37	2.3	14.67	36	PASS
	Ant2	5755	12.66	2.3	14.96	36	PASS
	Ant1	5795	12.23	2.3	14.53	36	PASS
	Ant2	5795	12.46	2.3	14.76	36	PASS
11AC80SISO	Ant1	5210	10.61	2.3	12.91	36	PASS
	Ant2	5210	10.63	2.3	12.93	36	PASS
	Ant1	5290	12.12	2.3	14.42	30	PASS
	Ant2	5290	12.02	2.3	14.32	30	PASS
	Ant1	5530	12.00	2.3	14.3	30	PASS
	Ant2	5530	12.89	2.3	15.19	30	PASS
	Ant1	5775	11.98	2.3	14.28	36	PASS
	Ant2	5775	12.89	2.3	15.19	36	PASS
11N20MIMO	Ant1	5180	6.12	--	--	--	--
	Ant2	5180	5.56	--	--	--	--
	total	5180	8.9	5.3	14.2	36	PASS
	Ant1	5200	5.38	--	--	--	--
	Ant2	5200	5.43	--	--	--	--
	total	5200	8.4	5.3	13.7	36	PASS
	Ant1	5240	5.32	--	--	--	--
	Ant2	5240	5.33	--	--	--	--
total	5240	8.3	5.3	13.6	36	PASS	

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant1	5260	11.81	--	--	--	--
	Ant2	5260	12.34	--	--	--	--
	total	5260	15.1	5.3	20.4	30	PASS
	Ant1	5280	12.37	--	--	--	--
	Ant2	5280	11.44	--	--	--	--
	total	5280	14.9	5.3	20.2	30	PASS
	Ant1	5320	11.36	--	--	--	--
	Ant2	5320	10.99	--	--	--	--
	total	5320	14.2	5.3	19.5	30	PASS
	Ant1	5500	10.31	--	--	--	--
	Ant2	5500	11.39	--	--	--	--
	total	5500	13.9	5.3	19.2	30	PASS
	Ant1	5580	12.37	--	--	--	--
	Ant2	5580	11.97	--	--	--	--
	total	5580	15.2	5.3	20.5	30	PASS
	Ant1	5700	12.25	--	--	--	--
	Ant2	5700	11.55	--	--	--	--
	total	5700	14.9	5.3	20.2	30	PASS
	Ant1	5745	13.15	--	--	--	--
	Ant2	5745	10.67	--	--	--	--
	total	5745	15.1	5.3	20.4	36	PASS
	Ant1	5785	10.43	--	--	--	--
	Ant2	5785	12.06	--	--	--	--
	total	5785	14.3	5.3	19.6	36	PASS
Ant1	5825	12.06	--	--	--	--	
Ant2	5825	11.18	--	--	--	--	
total	5825	14.7	5.3	20.0	36	PASS	
11N40MIMO	Ant1	5190	7.42	--	--	--	--
	Ant2	5190	7.36	--	--	--	--
	total	5190	10.4	5.3	15.7	36	PASS
	Ant1	5230	8.41	--	--	--	--
	Ant2	5230	8.42	--	--	--	--
	total	5230	11.4	5.3	16.7	36	PASS
	Ant1	5270	11.60	--	--	--	--
	Ant2	5270	12.08	--	--	--	--
	total	5270	14.9	5.3	20.2	30	PASS
	Ant1	5310	12.13	--	--	--	--
	Ant2	5310	11.50	--	--	--	--
	total	5310	14.8	5.3	20.1	30	PASS
	Ant1	5510	11.01	--	--	--	--
	Ant2	5510	12.72	--	--	--	--
	total	5510	15.0	5.3	20.3	30	PASS
	Ant1	5550	11.44	--	--	--	--

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant2	5550	12.12	--	--	--	--
	total	5550	14.8	5.3	20.1	30	PASS
	Ant1	5670	11.49	--	--	--	--
	Ant2	5670	13.27	--	--	--	--
	total	5670	15.5	5.3	20.8	30	PASS
	Ant1	5755	13.06	--	--	--	--
	Ant2	5755	12.09	--	--	--	--
	total	5755	15.6	5.3	20.9	36	PASS
	Ant1	5795	12.06	--	--	--	--
	Ant2	5795	12.08	--	--	--	--
	total	5795	15.1	5.3	20.4	36	PASS
11AC20MIMO	Ant1	5180	5.58	--	--	--	--
	Ant2	5180	5.57	--	--	--	--
	total	5180	8.6	5.3	13.9	36	PASS
	Ant1	5200	5.48	--	--	--	--
	Ant2	5200	5.56	--	--	--	--
	total	5200	8.5	5.3	13.8	36	PASS
	Ant1	5240	5.54	--	--	--	--
	Ant2	5240	5.45	--	--	--	--
	total	5240	8.5	5.3	13.8	36	PASS
	Ant1	5260	11.95	--	--	--	--
	Ant2	5260	12.21	--	--	--	--
	total	5260	15.1	5.3	20.4	30	PASS
	Ant1	5280	12.04	--	--	--	--
	Ant2	5280	11.74	--	--	--	--
	total	5280	14.9	5.3	20.2	30	PASS
	Ant1	5320	11.98	--	--	--	--
	Ant2	5320	11.64	--	--	--	--
	total	5320	14.8	5.3	20.1	30	PASS
	Ant1	5500	11.84	--	--	--	--
	Ant2	5500	11.79	--	--	--	--
	total	5500	14.8	5.3	20.1	30	PASS
	Ant1	5580	11.33	--	--	--	--
	Ant2	5580	11.49	--	--	--	--
	total	5580	14.4	5.3	19.7	30	PASS
	Ant1	5700	11.57	--	--	--	--
	Ant2	5700	12.82	--	--	--	--
	total	5700	15.3	5.3	20.6	30	PASS
	Ant1	5745	11.50	--	--	--	--
	Ant2	5745	12.40	--	--	--	--
	total	5745	15.0	5.3	20.3	36	PASS
Ant1	5785	11.82	--	--	--	--	
Ant2	5785	12.55	--	--	--	--	

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	total	5785	15.2	5.3	20.5	36	PASS
	Ant1	5825	10.34	--	--	--	--
	Ant2	5825	12.55	--	--	--	--
	total	5825	14.6	5.3	19.9	36	PASS
11AC40MIMO	Ant1	5190	8.44	--	--	--	--
	Ant2	5190	8.60	--	--	--	--
	total	5190	11.5	5.3	16.8	36	PASS
	Ant1	5230	8.45	--	--	--	--
	Ant2	5230	8.50	--	--	--	--
	total	5230	11.5	5.3	16.8	36	PASS
	Ant1	5270	11.61	--	--	--	--
	Ant2	5270	12.37	--	--	--	--
	total	5270	15.0	5.3	20.3	30	PASS
	Ant1	5310	12.23	--	--	--	--
	Ant2	5310	12.26	--	--	--	--
	total	5310	15.3	5.3	20.6	30	PASS
	Ant1	5510	12.87	--	--	--	--
	Ant2	5510	13.27	--	--	--	--
	total	5510	16.1	5.3	21.4	30	PASS
	Ant1	5550	11.87	--	--	--	--
	Ant2	5550	11.48	--	--	--	--
	total	5550	14.7	5.3	20.0	30	PASS
	Ant1	5670	11.68	--	--	--	--
	Ant2	5670	13.39	--	--	--	--
	total	5670	15.6	5.3	20.9	30	PASS
	Ant1	5755	11.62	--	--	--	--
	Ant2	5755	13.19	--	--	--	--
	total	5755	15.5	5.3	20.8	36	PASS
Ant1	5795	10.46	--	--	--	--	
Ant2	5795	11.04	--	--	--	--	
total	5795	13.8	5.3	19.1	36	PASS	
11AC80MIMO	Ant1	5210	10.56	--	--	--	--
	Ant2	5210	10.55	--	--	--	--
	total	5210	13.6	5.3	18.9	36	PASS
	Ant1	5290	12.13	--	--	--	--
	Ant2	5290	11.59	--	--	--	--
	total	5290	14.9	5.3	20.2	30	PASS
	Ant1	5530	10.03	--	--	--	--
	Ant2	5530	12.24	--	--	--	--
	total	5530	14.3	5.3	19.6	30	PASS
	Ant1	5610	11.12	--	--	--	--
	Ant2	5610	11.71	--	--	--	--
	total	5610	14.4	5.3	19.7	30	PASS

Test Mode	Antenna	Frequency (MHz)	Output Power (dBm)	Gain (dBi)	E.I.R.P. (dBm)	Limit	Verdict
	Ant1	5775	10.17				
	Ant2	5775	12.03				
	total	5775	14.2	5.3	19.5	36	PASS



## Appendix C: Maximum power spectral density

### Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	2.11	<=11	PASS
	Ant2	5180	1.41	<=11	PASS
	Ant1	5200	1.47	<=11	PASS
	Ant2	5200	1.51	<=11	PASS
	Ant1	5240	1.3	<=11	PASS
	Ant2	5240	1.57	<=11	PASS
	Ant1	5260	7.36	<=11	PASS
	Ant2	5260	8.09	<=11	PASS
	Ant1	5280	7.94	<=11	PASS
	Ant2	5280	8	<=11	PASS
	Ant1	5320	7.83	<=11	PASS
	Ant2	5320	8.11	<=11	PASS
	Ant1	5500	8.18	<=11	PASS
	Ant2	5500	8.48	<=11	PASS
	Ant1	5580	7.72	<=11	PASS
	Ant2	5580	7.86	<=11	PASS
	Ant1	5700	7.94	<=11	PASS
	Ant2	5700	9.44	<=11	PASS
	Ant1	5745	6.08	<=30	PASS
	Ant2	5745	7.34	<=30	PASS
	Ant1	5785	6.44	<=30	PASS
	Ant2	5785	7.23	<=30	PASS
	Ant1	5825	6.27	<=30	PASS
	Ant2	5825	6.53	<=30	PASS
11N20SISO	Ant1	5180	0.01	<=11	PASS
	Ant2	5180	0.25	<=11	PASS
	Ant1	5200	0.02	<=11	PASS
	Ant2	5200	0.81	<=11	PASS
	Ant1	5240	-0.04	<=11	PASS
	Ant2	5240	0.46	<=11	PASS
	Ant1	5260	6.52	<=11	PASS
	Ant2	5260	6.62	<=11	PASS
	Ant1	5280	6.74	<=11	PASS
	Ant2	5280	6.9	<=11	PASS
	Ant1	5320	6.88	<=11	PASS
	Ant2	5320	7.3	<=11	PASS
	Ant1	5500	7.26	<=11	PASS
	Ant2	5500	7.99	<=11	PASS
	Ant1	5580	6.82	<=11	PASS

	Ant2	5580	7.49	<=11	PASS
	Ant1	5700	6.24	<=11	PASS
	Ant2	5700	7.82	<=11	PASS
	Ant1	5745	5.36	<=30	PASS
	Ant2	5745	6.56	<=30	PASS
	Ant1	5785	4.98	<=30	PASS
	Ant2	5785	5.83	<=30	PASS
	Ant1	5825	4.64	<=30	PASS
	Ant2	5825	5.19	<=30	PASS
11N40SISO	Ant1	5190	1.13	<=11	PASS
	Ant2	5190	0.49	<=11	PASS
	Ant1	5230	0.03	<=11	PASS
	Ant2	5230	0.63	<=11	PASS
	Ant1	5270	4.36	<=11	PASS
	Ant2	5270	3.72	<=11	PASS
	Ant1	5310	3.99	<=11	PASS
	Ant2	5310	3.79	<=11	PASS
	Ant1	5510	4.74	<=11	PASS
	Ant2	5510	4.86	<=11	PASS
	Ant1	5550	4.41	<=11	PASS
	Ant2	5550	4.67	<=11	PASS
	Ant1	5670	3.86	<=11	PASS
	Ant2	5670	5.11	<=11	PASS
	Ant1	5755	5.31	<=30	PASS
	Ant2	5755	3.46	<=30	PASS
	Ant1	5795	6.99	<=30	PASS
	Ant2	5795	3.1	<=30	PASS
11AC20SISO	Ant1	5180	0.35	<=11	PASS
	Ant2	5180	0.03	<=11	PASS
	Ant1	5200	0.07	<=11	PASS
	Ant2	5200	0.33	<=11	PASS
	Ant1	5240	0.43	<=11	PASS
	Ant2	5240	0.18	<=11	PASS
	Ant1	5260	6.36	<=11	PASS
	Ant2	5260	6.84	<=11	PASS
	Ant1	5280	6.74	<=11	PASS
	Ant2	5280	6.53	<=11	PASS
	Ant1	5320	6.44	<=11	PASS
	Ant2	5320	6.75	<=11	PASS
	Ant1	5500	7.32	<=11	PASS
	Ant2	5500	7.12	<=11	PASS
	Ant1	5580	7.93	<=11	PASS
	Ant2	5580	7.58	<=11	PASS
	Ant1	5700	7.67	<=11	PASS
	Ant2	5700	7.42	<=11	PASS

	Ant1	5745	5.18	<=30	PASS
	Ant2	5745	6.2	<=30	PASS
	Ant1	5785	5.12	<=30	PASS
	Ant2	5785	5.95	<=30	PASS
	Ant1	5825	5.44	<=30	PASS
	Ant2	5825	5.85	<=30	PASS
11AC40SISO	Ant1	5190	0.53	<=11	PASS
	Ant2	5190	0.35	<=11	PASS
	Ant1	5230	0.68	<=11	PASS
	Ant2	5230	0.13	<=11	PASS
	Ant1	5270	3.83	<=11	PASS
	Ant2	5270	3.97	<=11	PASS
	Ant1	5310	3.77	<=11	PASS
	Ant2	5310	3.43	<=11	PASS
	Ant1	5510	3.78	<=11	PASS
	Ant2	5510	4.99	<=11	PASS
	Ant1	5550	4.23	<=11	PASS
	Ant2	5550	4.96	<=11	PASS
	Ant1	5670	4.09	<=11	PASS
	Ant2	5670	4.72	<=11	PASS
	Ant1	5755	3.71	<=30	PASS
	Ant2	5755	3.28	<=30	PASS
	Ant1	5795	2.83	<=30	PASS
	Ant2	5795	2.93	<=30	PASS
11AC80SISO	Ant1	5210	-0.3	<=11	PASS
	Ant2	5210	0.03	<=11	PASS
	Ant1	5290	1.12	<=11	PASS
	Ant2	5290	1	<=11	PASS
	Ant1	5530	1.54	<=11	PASS
	Ant2	5530	2.28	<=11	PASS
	Ant1	5610	1.69	<=11	PASS
	Ant2	5610	1.9	<=11	PASS
	Ant1	5775	-0.29	<=30	PASS
	Ant2	5775	1.23	<=30	PASS
11N20MIMO	Ant1	5180	0.82	<=11	PASS
	Ant2	5180	1.65	<=11	PASS
	total	5180	4.27	<=11	PASS
	Ant1	5200	-0.23	<=11	PASS
	Ant2	5200	1.96	<=11	PASS
	total	5200	4.01	<=11	PASS
	Ant1	5240	-0.16	<=11	PASS
	Ant2	5240	1.6	<=11	PASS
	total	5240	3.82	<=11	PASS
	Ant1	5260	6.53	<=11	PASS
	Ant2	5260	8.79	<=11	PASS

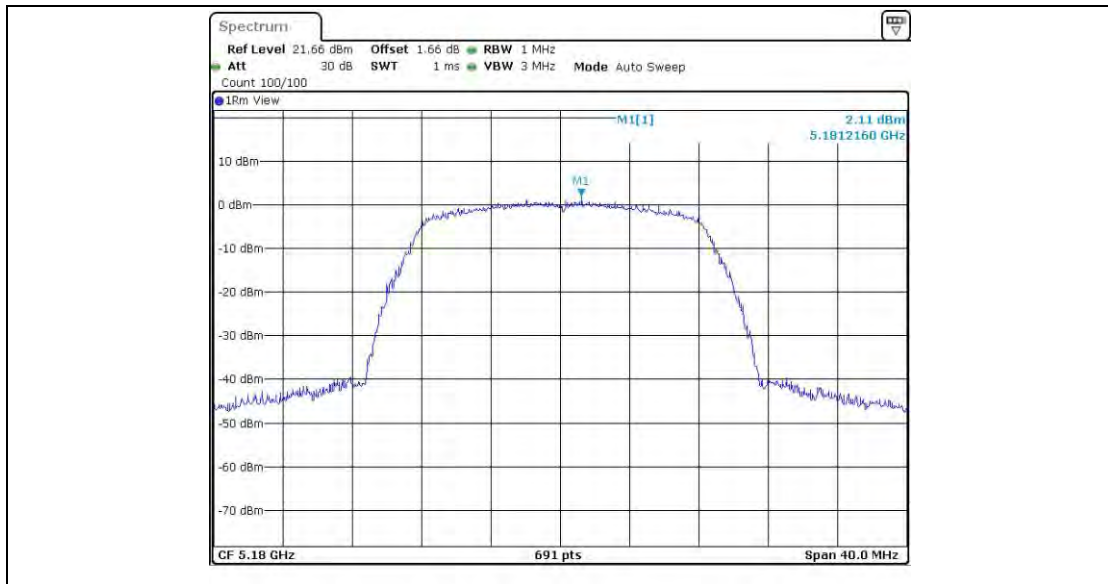
	total	5260	10.82	<=11	PASS
	Ant1	5280	8.69	<=11	PASS
	Ant2	5280	6.53	<=11	PASS
	total	5280	10.75	<=11	PASS
	Ant1	5320	6.07	<=11	PASS
	Ant2	5320	5.57	<=11	PASS
	total	5320	8.84	<=11	PASS
	Ant1	5500	5.22	<=11	PASS
	Ant2	5500	7.86	<=11	PASS
	total	5500	9.75	<=11	PASS
	Ant1	5580	8.23	<=11	PASS
	Ant2	5580	6.2	<=11	PASS
	total	5580	10.34	<=11	PASS
	Ant1	5700	6.73	<=11	PASS
	Ant2	5700	8.73	<=11	PASS
	total	5700	10.85	<=11	PASS
	Ant1	5745	7.65	<=30	PASS
	Ant2	5745	4.07	<=30	PASS
	total	5745	9.23	<=30	PASS
	Ant1	5785	3.81	<=30	PASS
	Ant2	5785	7.95	<=30	PASS
	total	5785	9.37	<=30	PASS
	Ant1	5825	7	<=30	PASS
	Ant2	5825	3.66	<=30	PASS
total	5825	8.65	<=30	PASS	
11N40MIMO	Ant1	5190	-0.48	<=11	PASS
	Ant2	5190	0.6	<=11	PASS
	total	5190	3.10	<=11	PASS
	Ant1	5230	0.21	<=11	PASS
	Ant2	5230	1.97	<=11	PASS
	total	5230	4.19	<=11	PASS
	Ant1	5270	3.44	<=11	PASS
	Ant2	5270	6.05	<=11	PASS
	total	5270	7.95	<=11	PASS
	Ant1	5310	4.77	<=11	PASS
	Ant2	5310	3.21	<=11	PASS
	total	5310	7.07	<=11	PASS
	Ant1	5510	2.96	<=11	PASS
	Ant2	5510	6.25	<=11	PASS
	total	5510	7.92	<=11	PASS
	Ant1	5550	3.24	<=11	PASS
	Ant2	5550	5.92	<=11	PASS
	total	5550	7.79	<=11	PASS
Ant1	5670	3.49	<=11	PASS	
Ant2	5670	6.38	<=11	PASS	

	total	5670	8.18	<=11	PASS
	Ant1	5755	4.64	<=30	PASS
	Ant2	5755	2.6	<=30	PASS
	total	5755	6.75	<=30	PASS
	Ant1	5795	2.7	<=30	PASS
	Ant2	5795	3.17	<=30	PASS
	total	5795	5.95	<=30	PASS
11AC20MIMO	Ant1	5180	-0.02	<=11	PASS
	Ant2	5180	1.6	<=11	PASS
	total	5180	3.88	<=11	PASS
	Ant1	5200	0.22	<=11	PASS
	Ant2	5200	2.47	<=11	PASS
	total	5200	4.50	<=11	PASS
	Ant1	5240	-0.04	<=11	PASS
	Ant2	5240	1.69	<=11	PASS
	total	5240	3.92	<=11	PASS
	Ant1	5260	6.72	<=11	PASS
	Ant2	5260	8.84	<=11	PASS
	total	5260	10.92	<=11	PASS
	Ant1	5280	7.21	<=11	PASS
	Ant2	5280	8.23	<=11	PASS
	total	5280	10.76	<=11	PASS
	Ant1	5320	6.61	<=11	PASS
	Ant2	5320	8.32	<=11	PASS
	total	5320	10.56	<=11	PASS
	Ant1	5500	6.93	<=11	PASS
	Ant2	5500	8.02	<=11	PASS
	total	5500	10.52	<=11	PASS
	Ant1	5580	6.39	<=11	PASS
	Ant2	5580	8.22	<=11	PASS
	total	5580	10.41	<=11	PASS
	Ant1	5700	6.36	<=11	PASS
	Ant2	5700	8.82	<=11	PASS
	total	5700	10.77	<=11	PASS
	Ant1	5745	5.22	<=30	PASS
	Ant2	5745	7.95	<=30	PASS
	total	5745	9.81	<=30	PASS
	Ant1	5785	5.26	<=30	PASS
	Ant2	5785	7.01	<=30	PASS
	total	5785	9.23	<=30	PASS
Ant1	5825	3.86	<=30	PASS	
Ant2	5825	7.69	<=30	PASS	
total	5825	9.19	<=30	PASS	
11AC40MIMO	Ant1	5190	0.53	<=11	PASS
	Ant2	5190	2.11	<=11	PASS

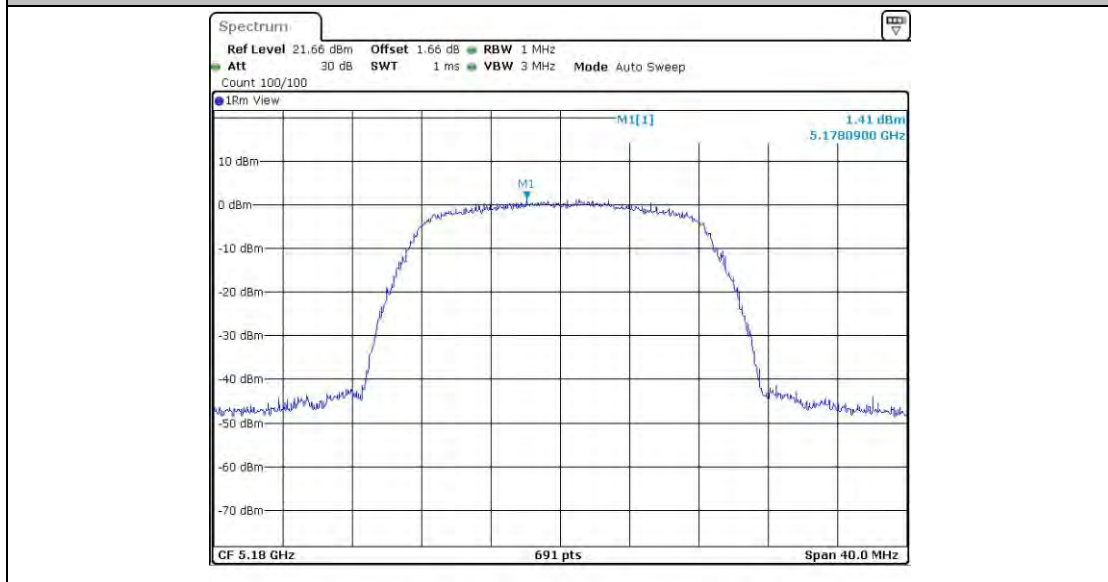
	total	5190	4.40	<=11	PASS
	Ant1	5230	0.20	<=11	PASS
	Ant2	5230	2.40	<=11	PASS
	total	5230	4.45	<=11	PASS
	Ant1	5270	3.48	<=11	PASS
	Ant2	5270	6.53	<=11	PASS
	total	5270	8.28	<=11	PASS
	Ant1	5310	4.46	<=11	PASS
	Ant2	5310	6.67	<=11	PASS
	total	5310	8.71	<=11	PASS
	Ant1	5510	5.36	<=11	PASS
	Ant2	5510	7.26	<=11	PASS
	total	5510	9.42	<=11	PASS
	Ant1	5550	3.95	<=11	PASS
	Ant2	5550	6.56	<=11	PASS
	total	5550	8.46	<=11	PASS
	Ant1	5670	4.13	<=11	PASS
	Ant2	5670	7.11	<=11	PASS
	total	5670	8.88	<=11	PASS
	Ant1	5755	2.33	<=30	PASS
	Ant2	5755	4.63	<=30	PASS
	total	5755	6.64	<=30	PASS
	Ant1	5795	0.84	<=30	PASS
	Ant2	5795	3.31	<=30	PASS
total	5795	5.26	<=30	PASS	
11AC80MIMO	Ant1	5210	-0.63	<=11	PASS
	Ant2	5210	1.55	<=11	PASS
	total	5210	3.61	<=11	PASS
	Ant1	5290	1.15	<=11	PASS
	Ant2	5290	2.08	<=11	PASS
	total	5290	4.65	<=11	PASS
	Ant1	5530	0.2	<=11	PASS
	Ant2	5530	3.51	<=11	PASS
	total	5530	5.17	<=11	PASS
	Ant1	5610	0.17	<=11	PASS
	Ant2	5610	3.25	<=11	PASS
	total	5610	4.99	<=11	PASS
	Ant1	5775	-1.56	<=30	PASS
	Ant2	5775	0.84	<=30	PASS
	total	5775	2.81	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.

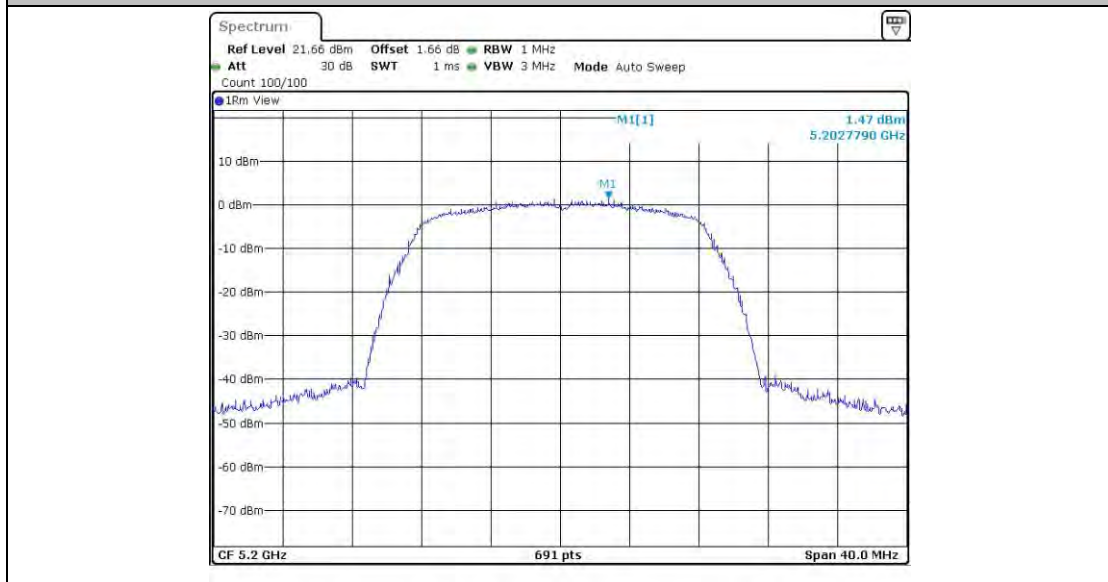
## Test Graphs



11A\_Ant1\_5180



11A\_Ant2\_5180



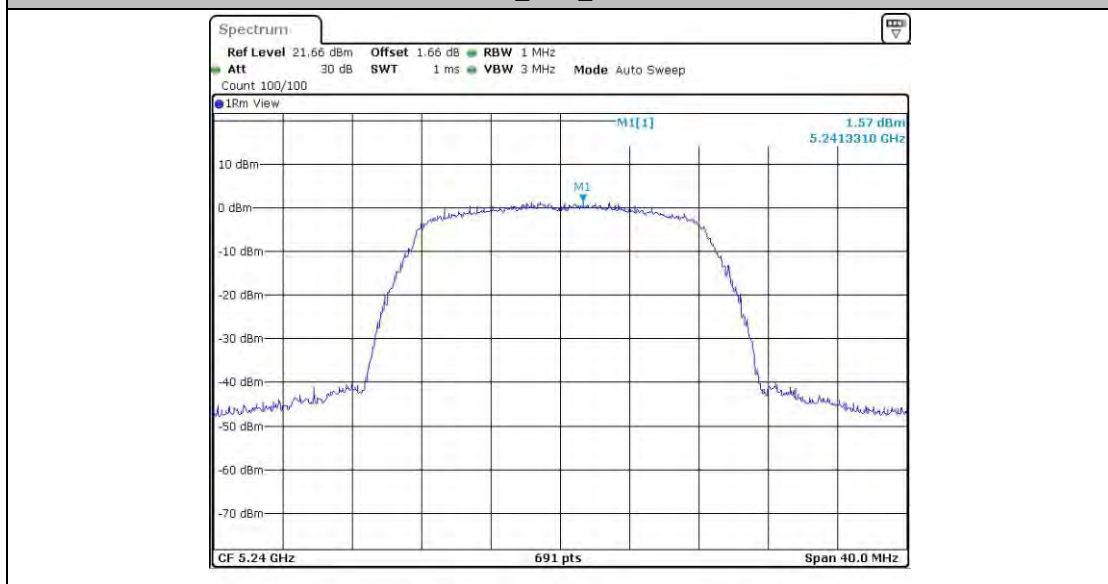
11A\_Ant1\_5200



11A\_Ant2\_5200

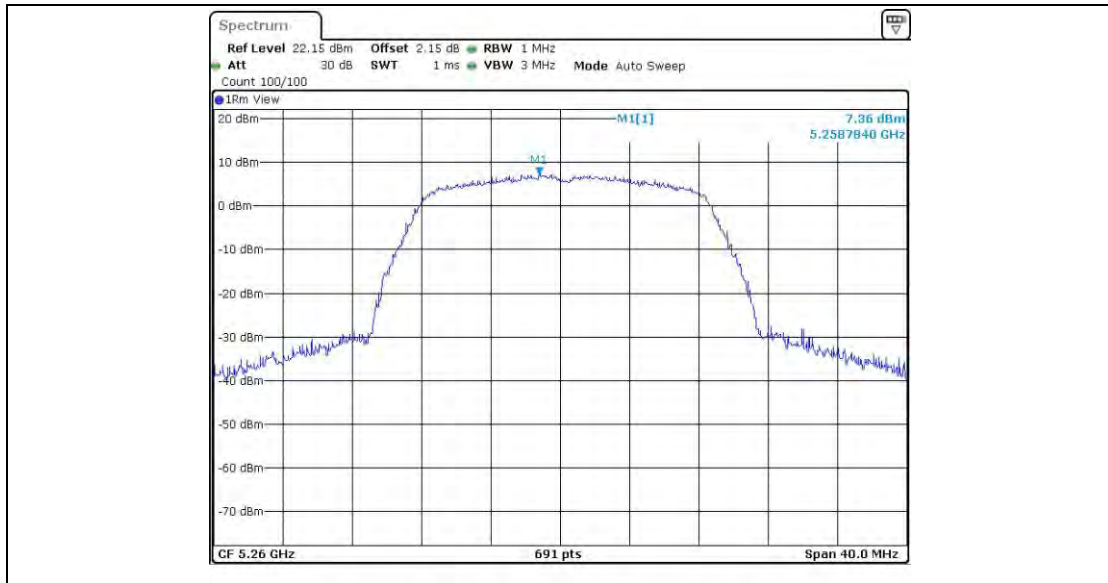


11A\_Ant1\_5240



11A\_Ant2\_5240

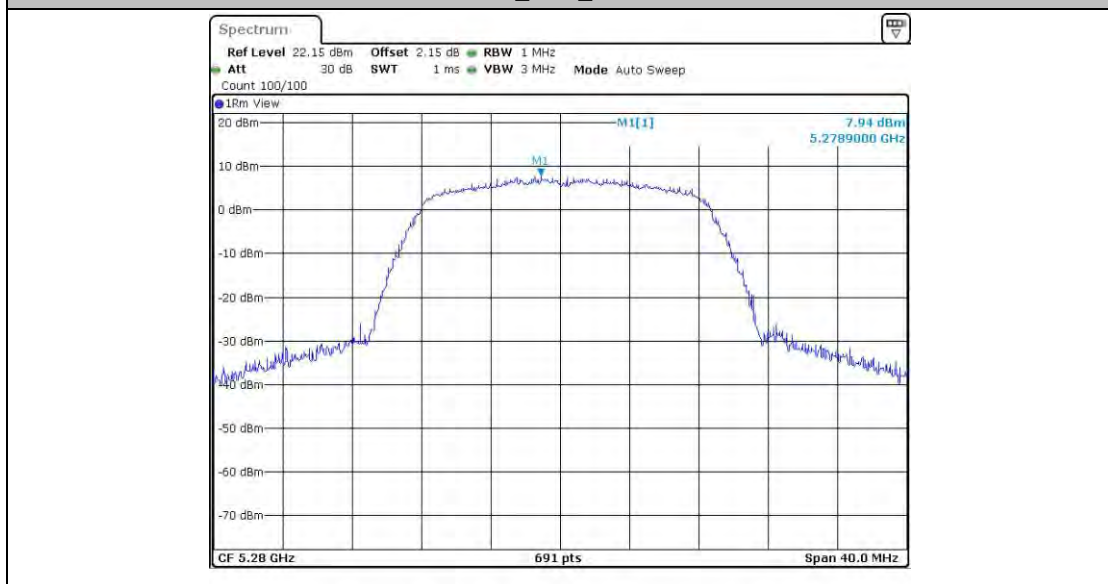




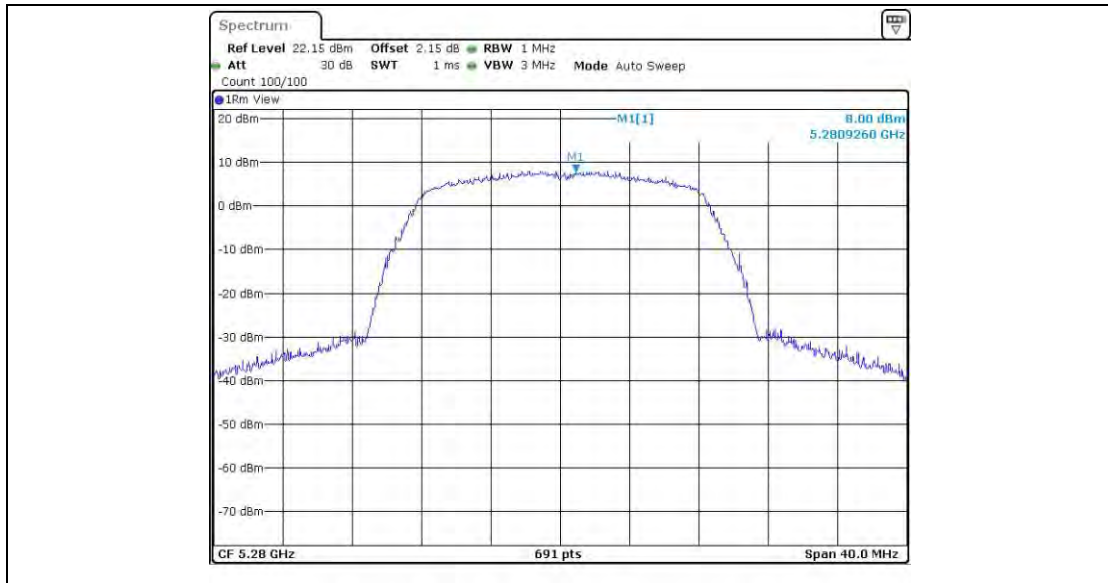
11A\_Ant1\_5260



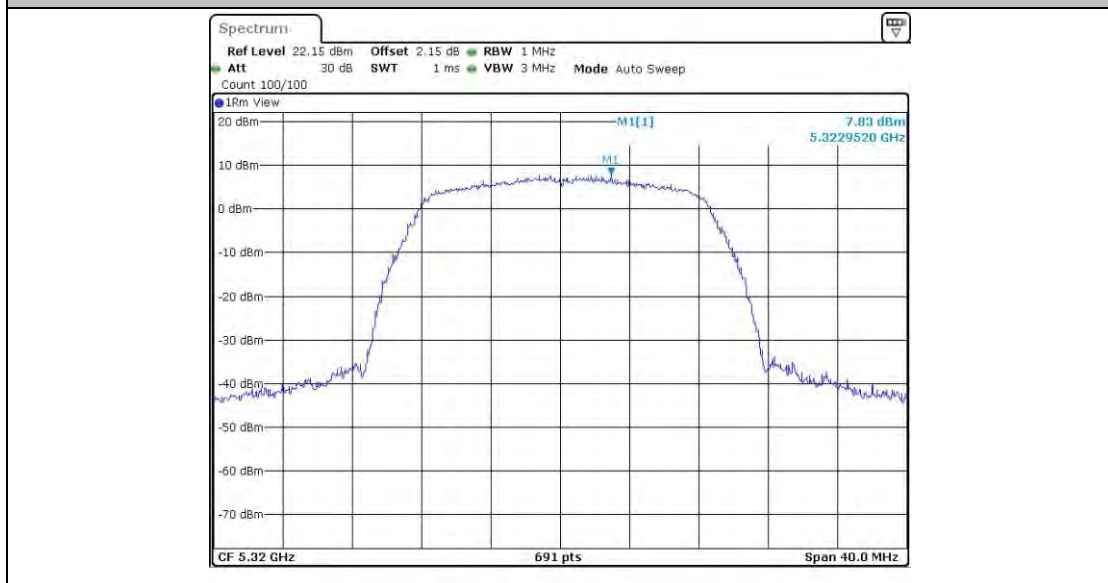
11A\_Ant2\_5260



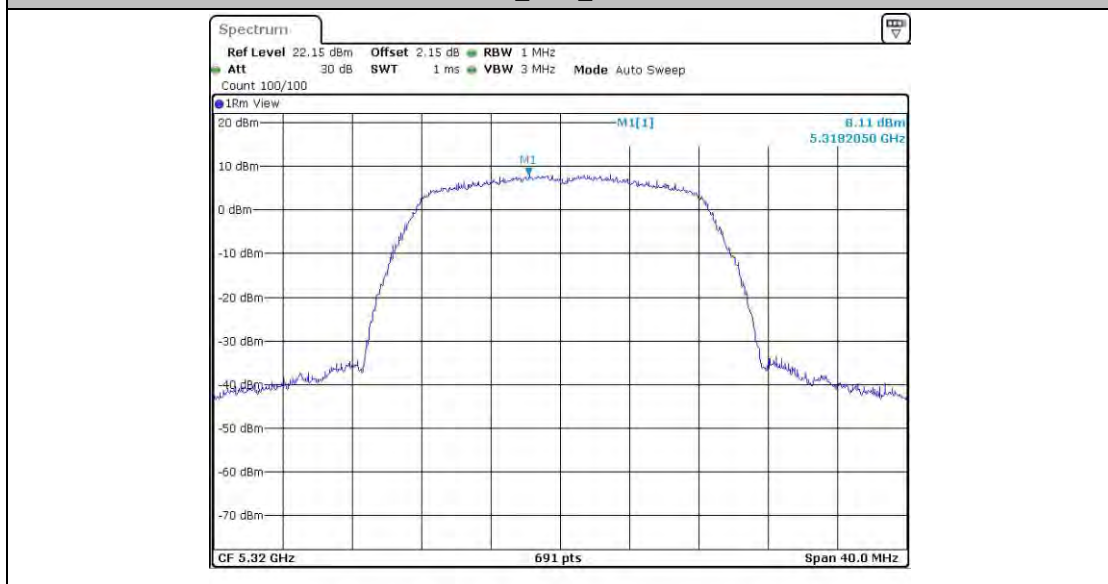
11A\_Ant1\_5280



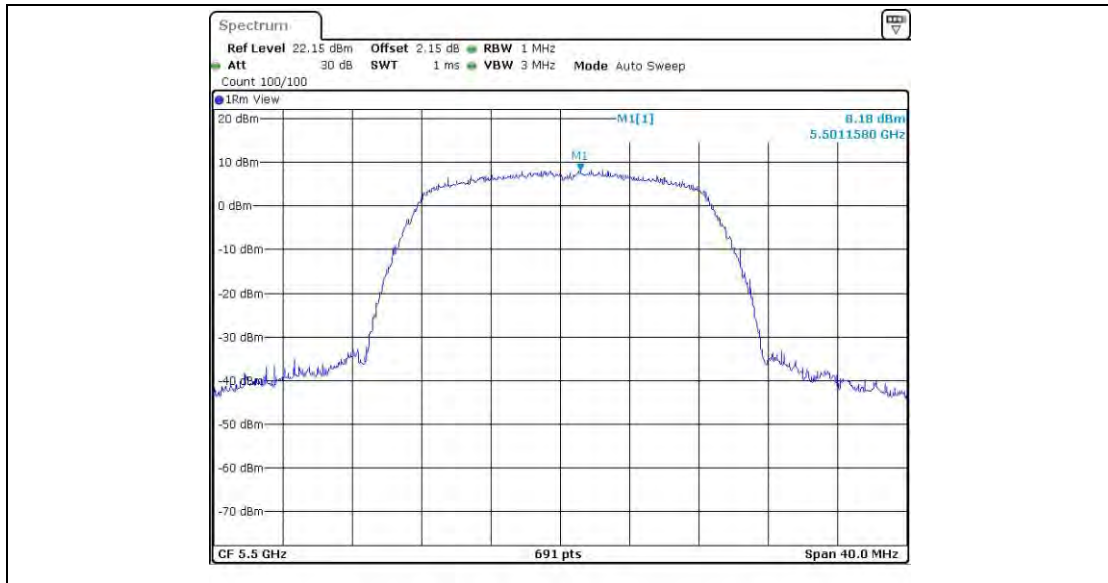
11A\_Ant2\_5280



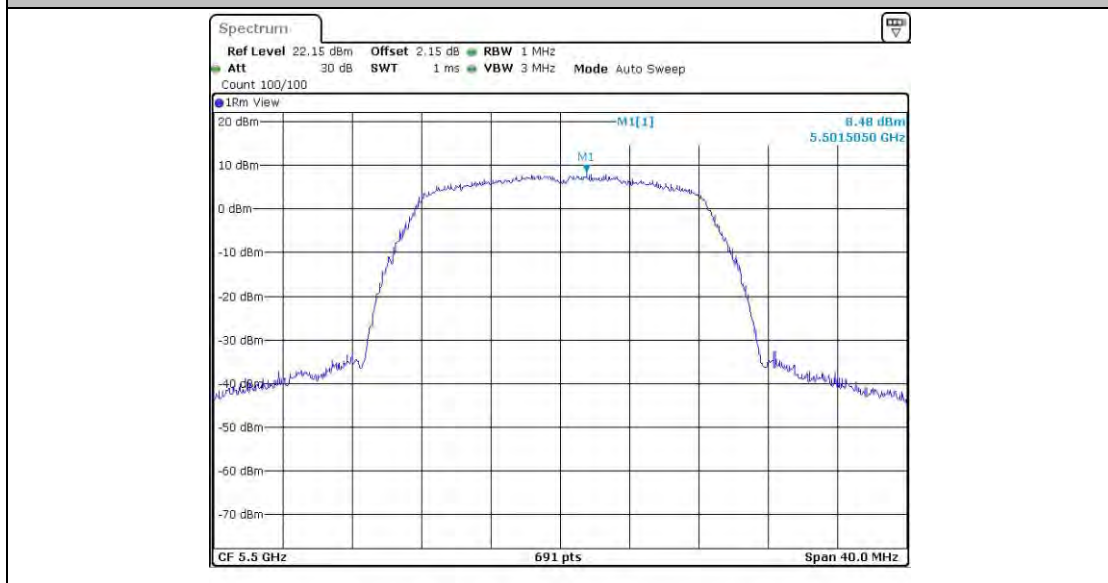
11A\_Ant1\_5320



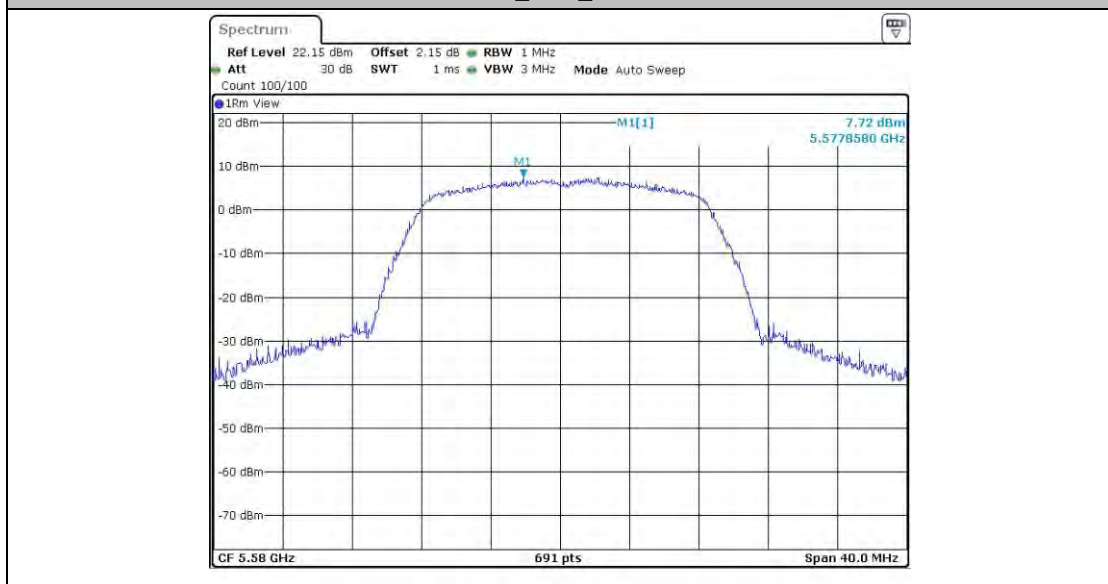
11A\_Ant2\_5320



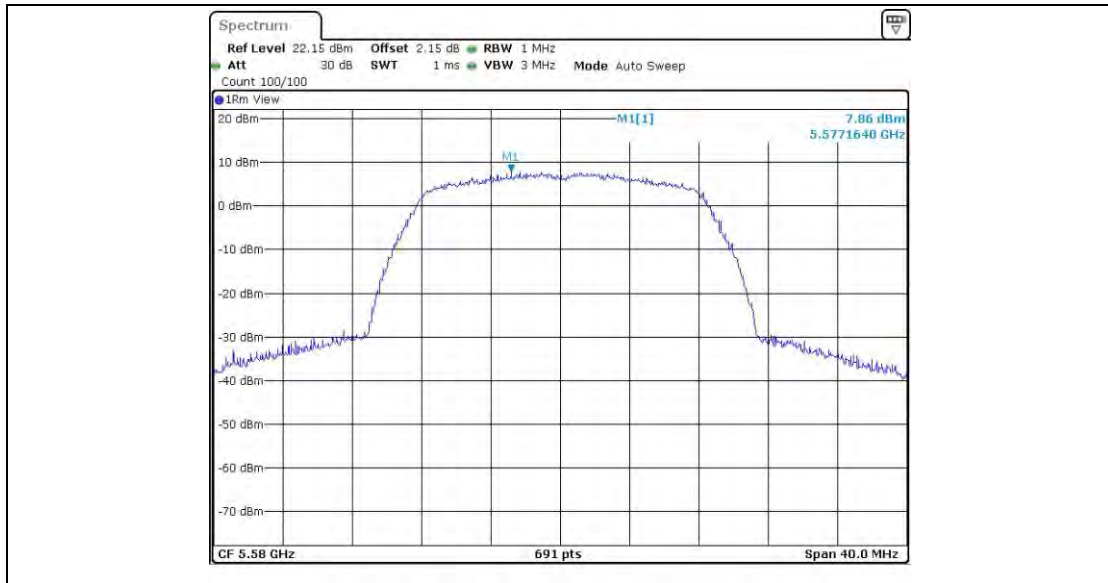
11A\_Ant1\_5500



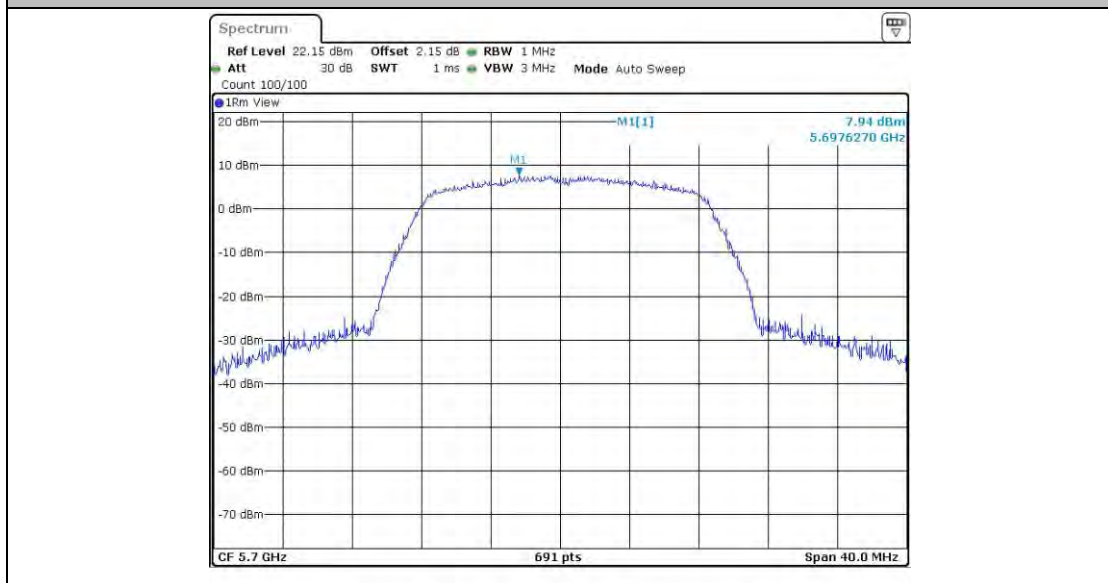
11A\_Ant2\_5500



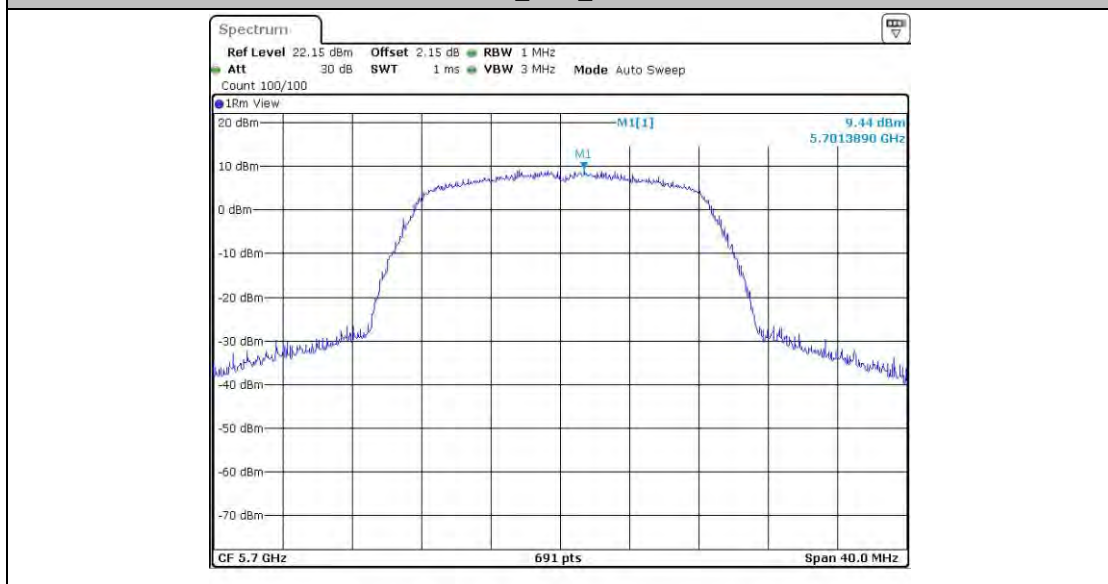
11A\_Ant1\_5580



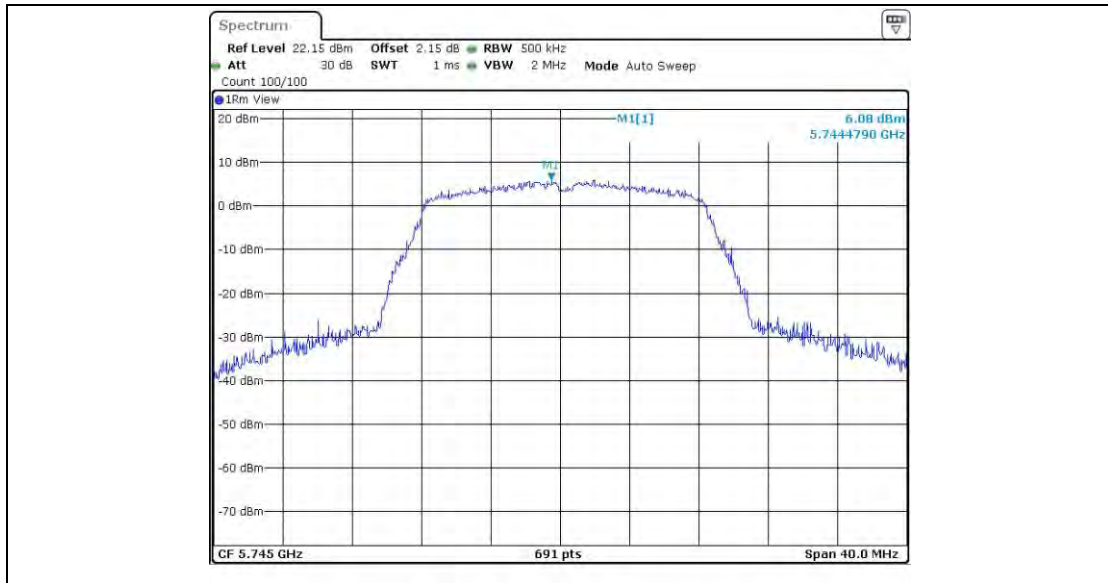
11A\_Ant2\_5580



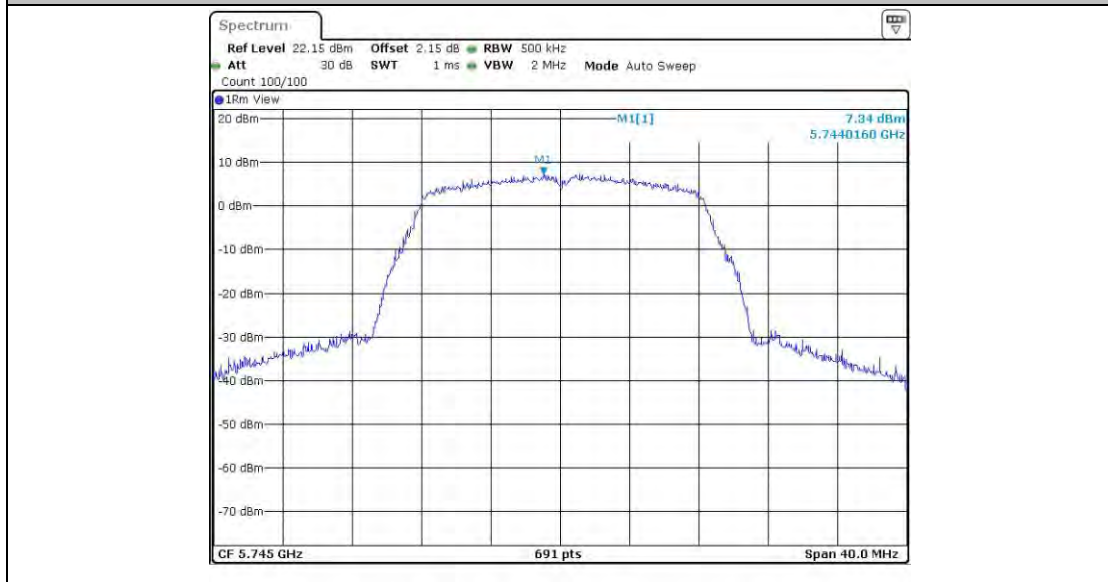
11A\_Ant1\_5700



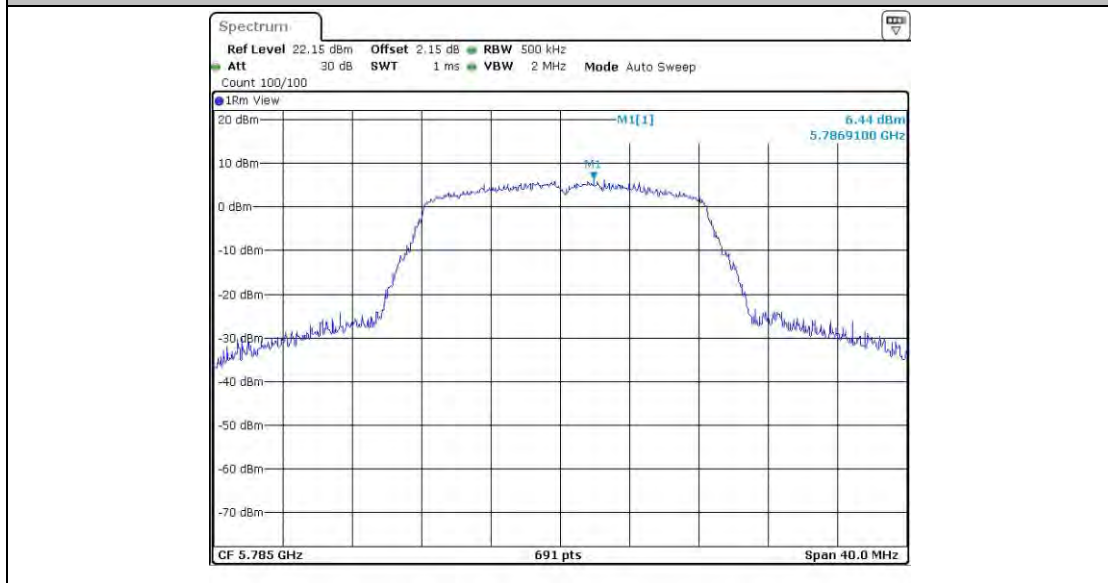
11A\_Ant2\_5700



11A\_Ant1\_5745

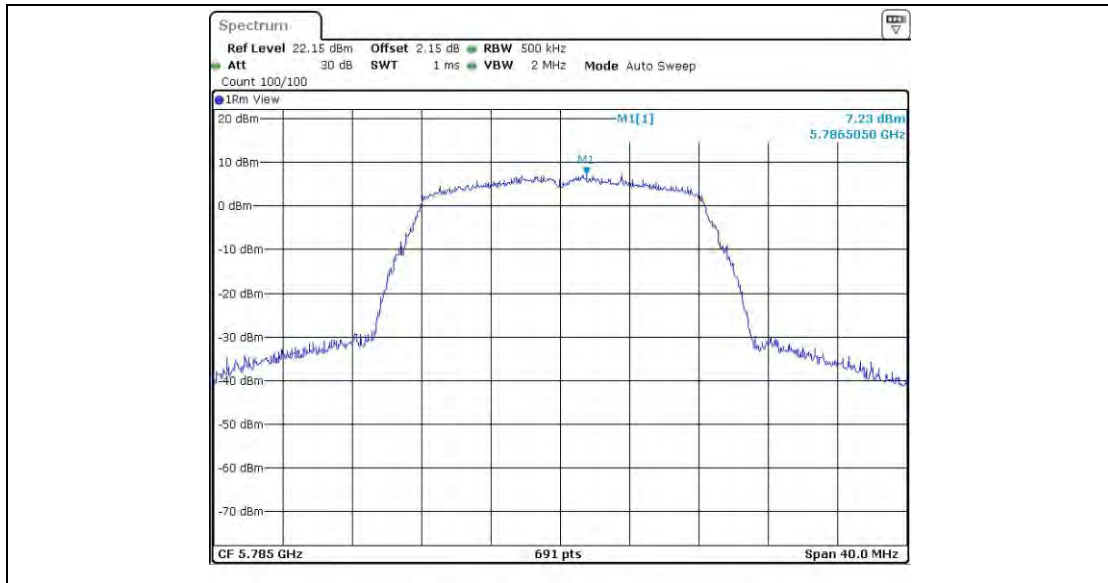


11A\_Ant2\_5745

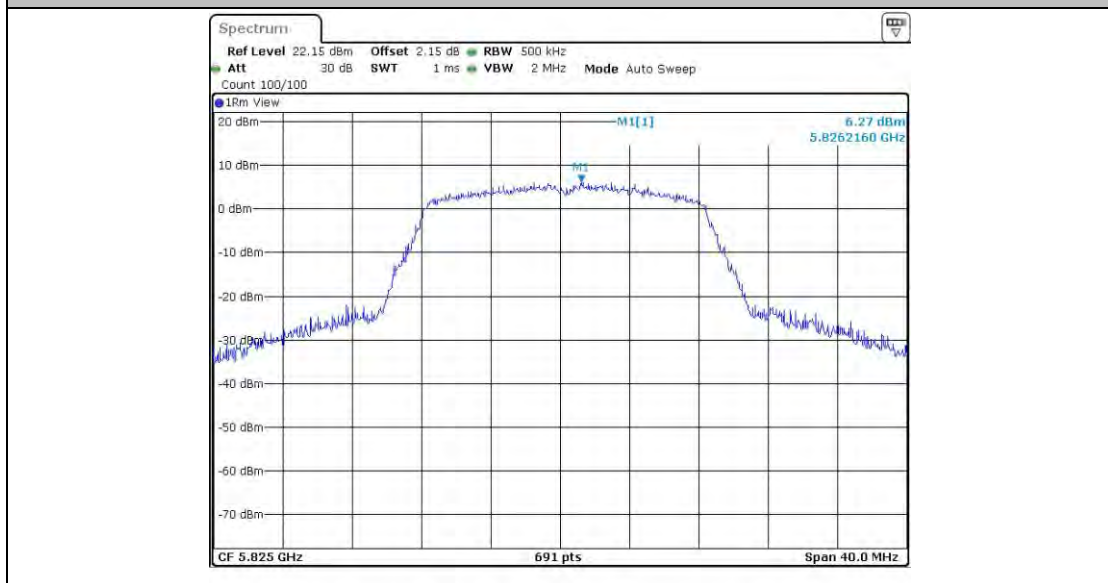


11A\_Ant1\_5785

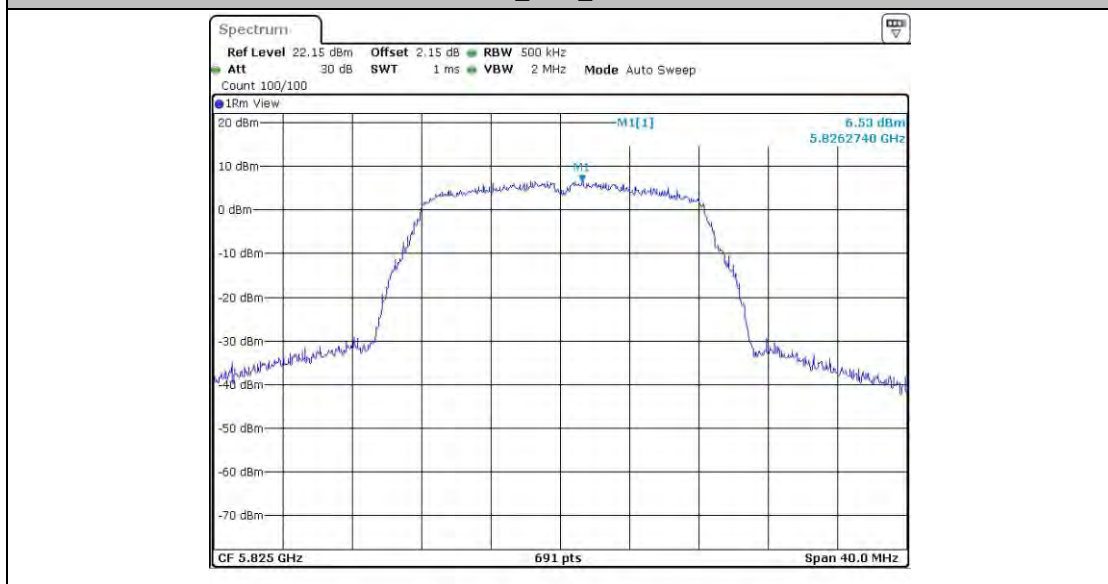




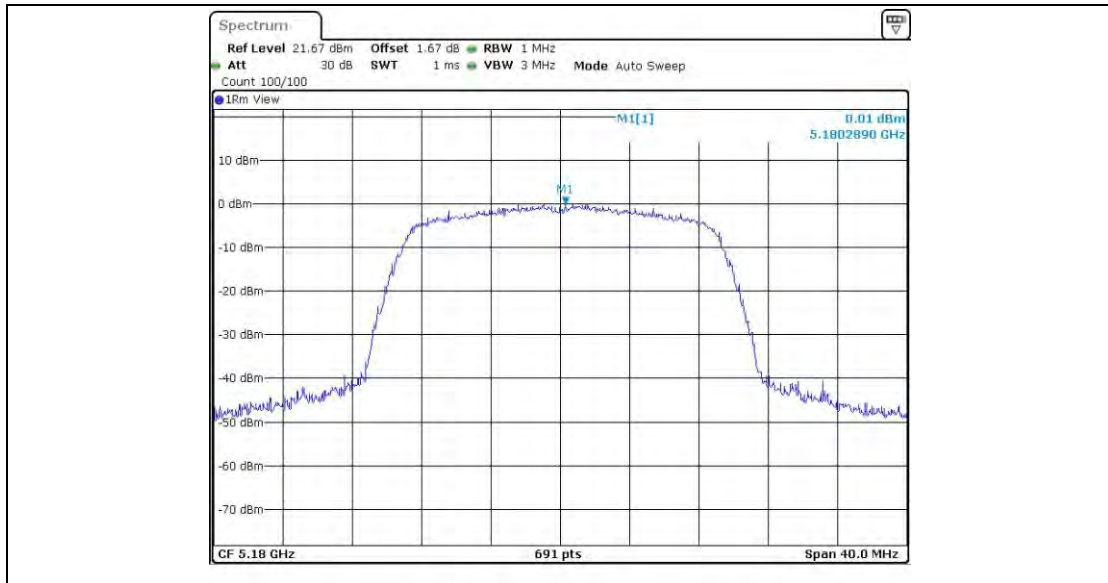
11A\_Ant2\_5785



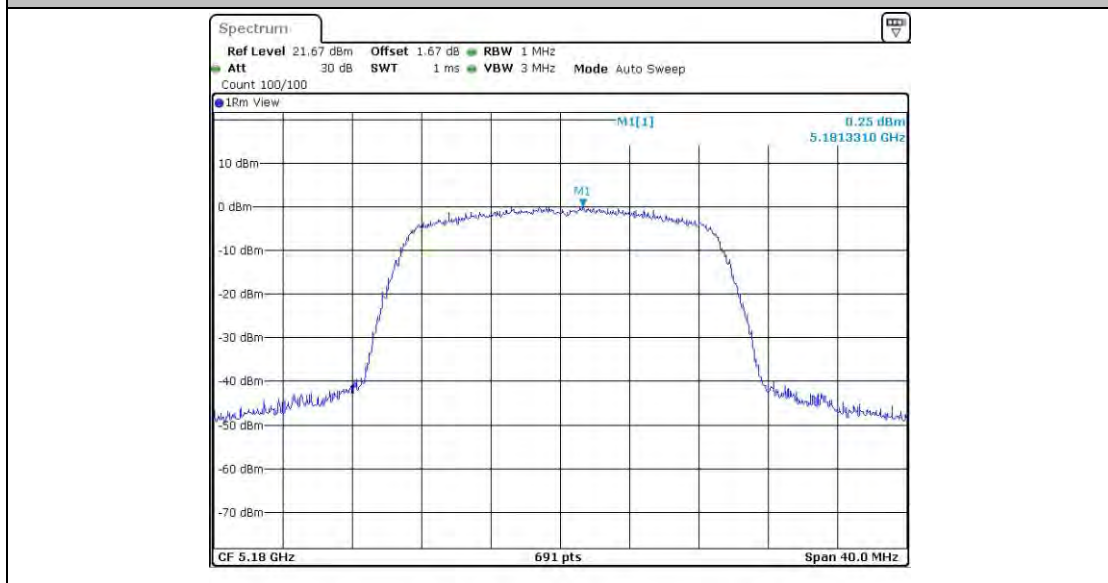
11A\_Ant1\_5825



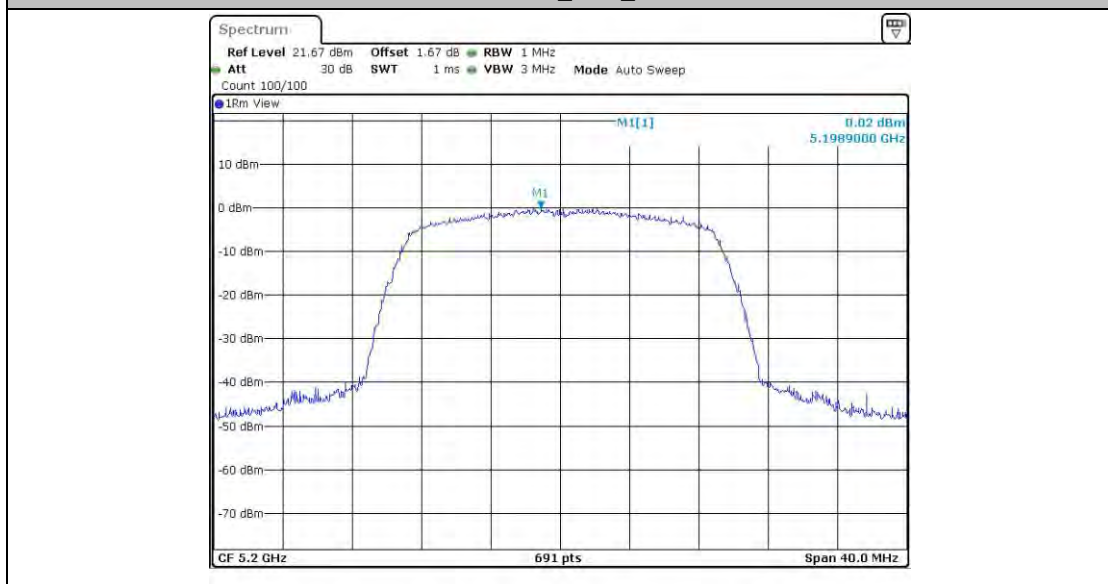
11A\_Ant2\_5825



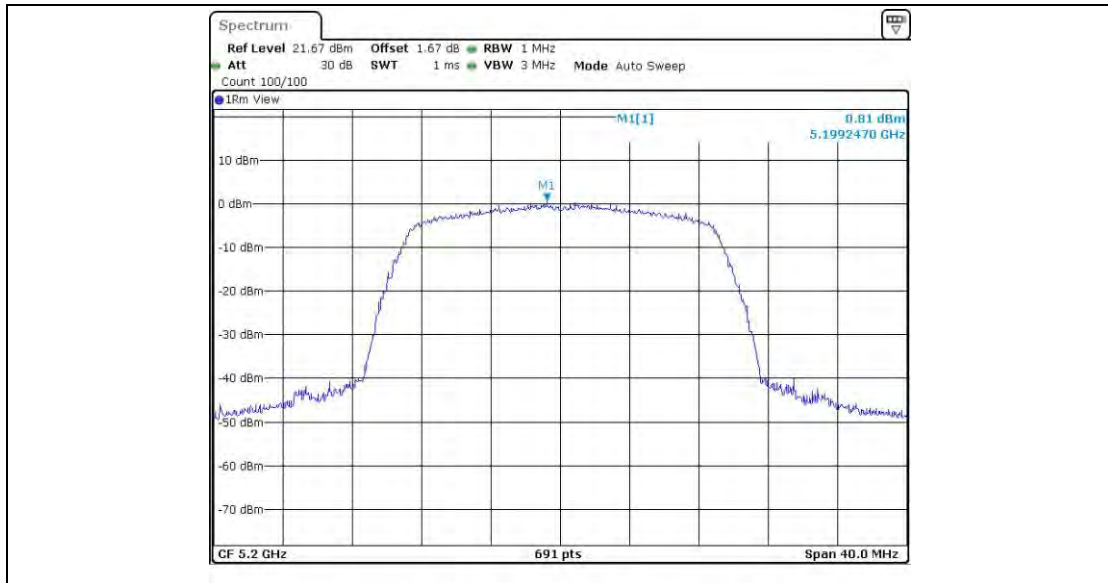
11N20SISO\_Ant1\_5180



11N20SISO\_Ant2\_5180



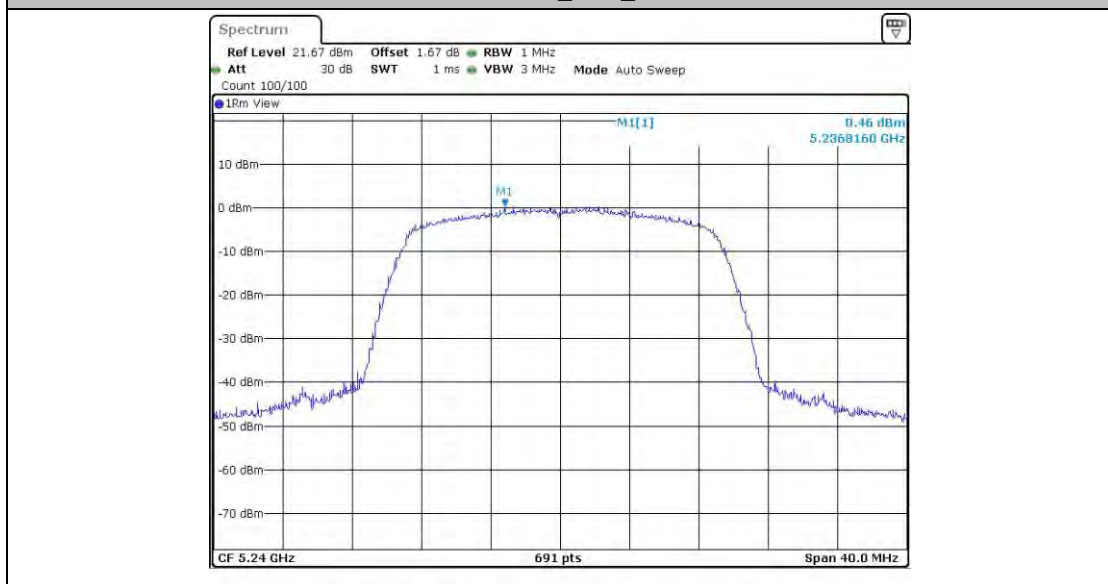
11N20SISO\_Ant1\_5200



11N20SISO\_Ant2\_5200

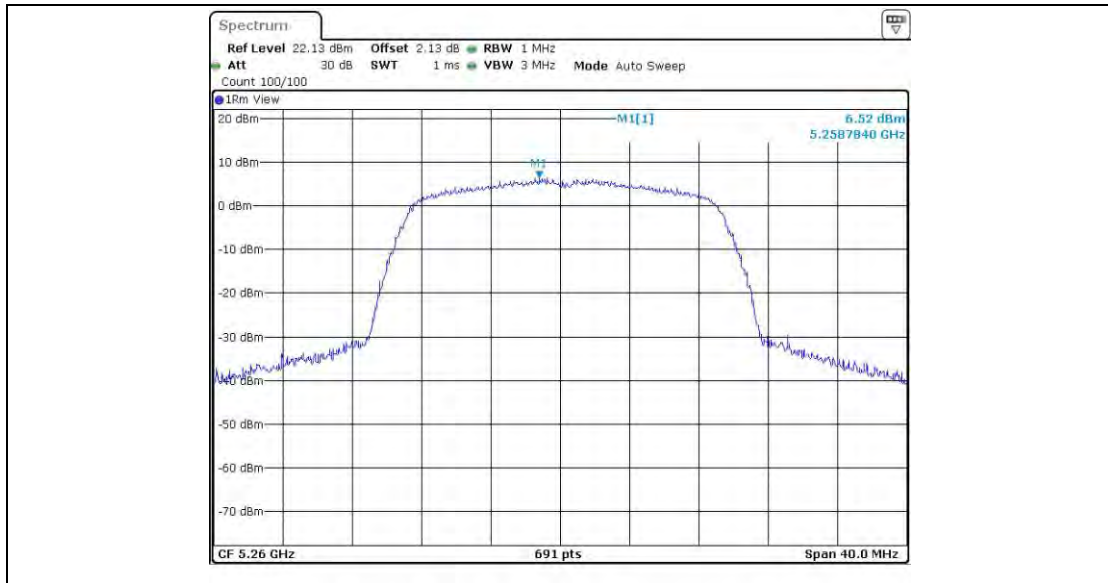


11N20SISO\_Ant1\_5240

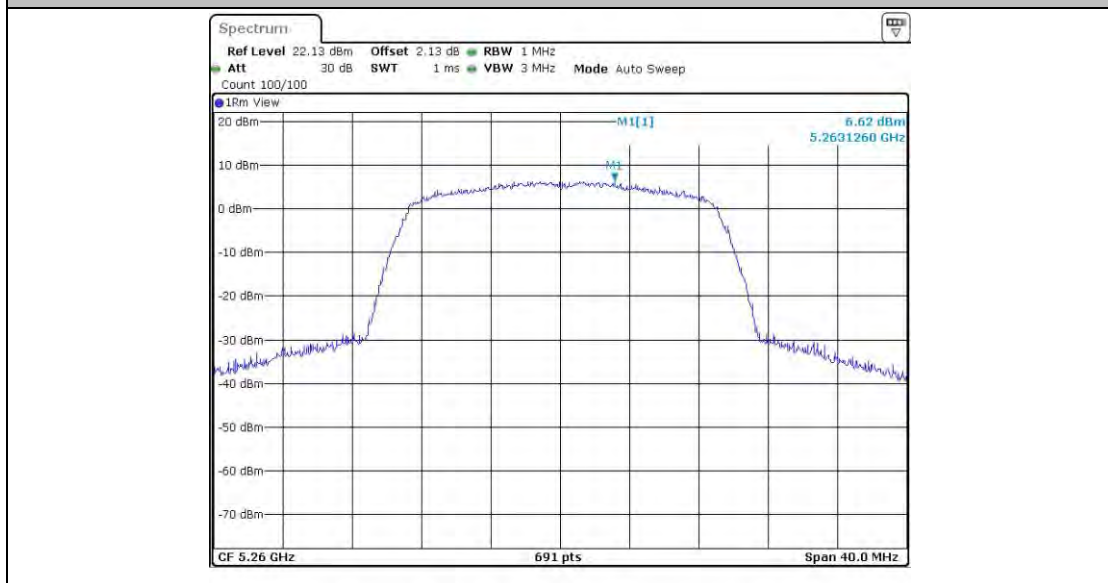


11N20SISO\_Ant2\_5240

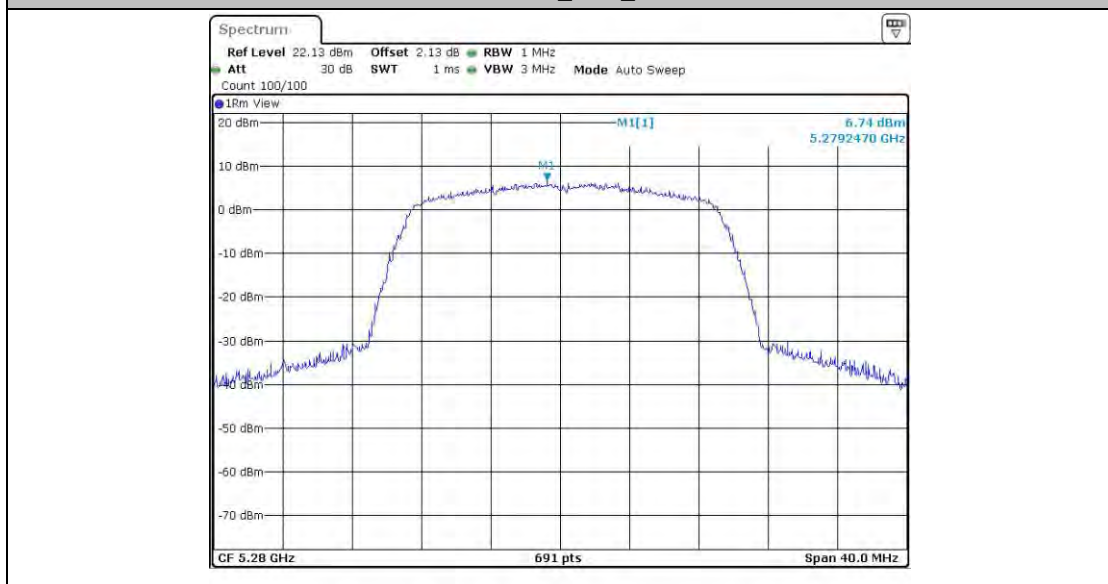




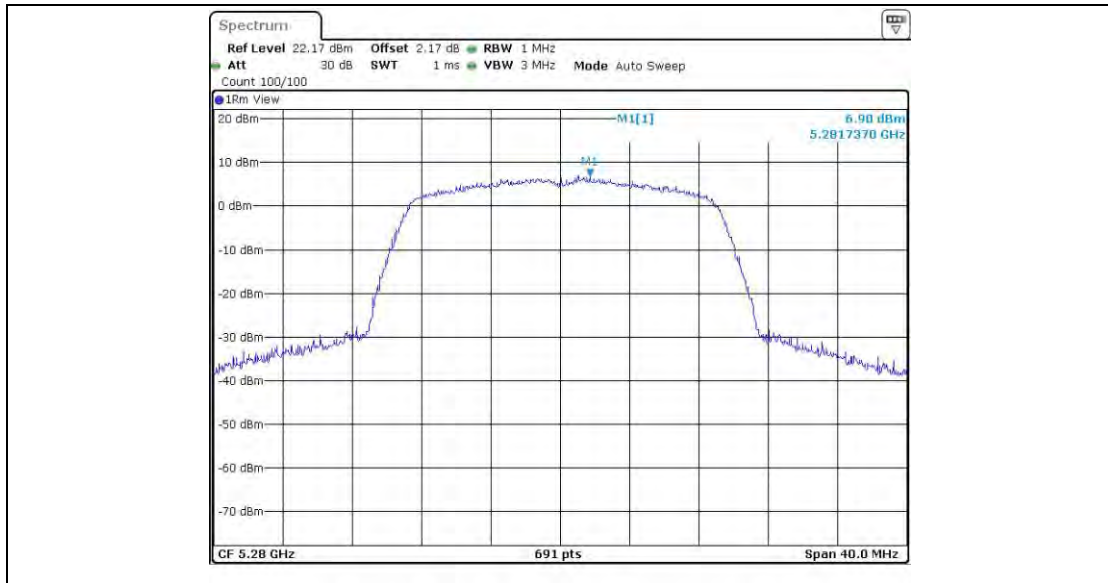
11N20SISO\_Ant1\_5260



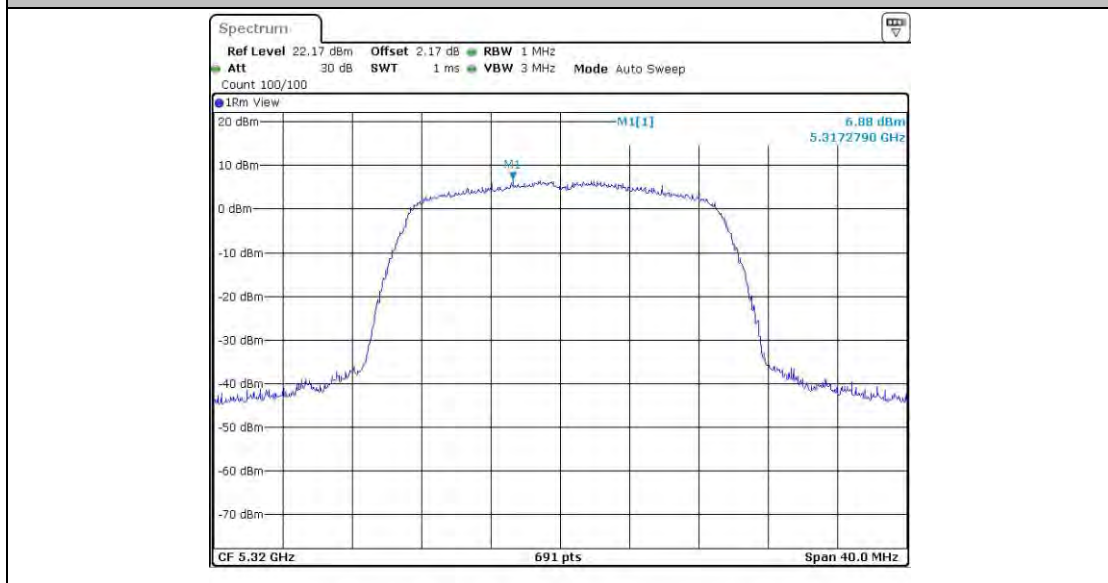
11N20SISO\_Ant2\_5260



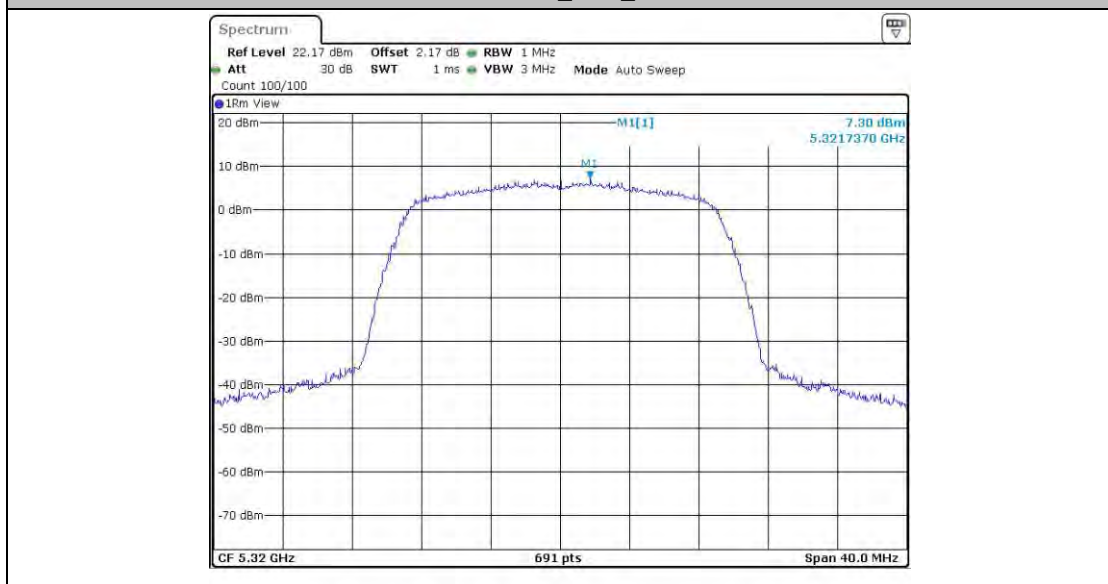
11N20SISO\_Ant1\_5280



11N20SISO\_Ant2\_5280



11N20SISO\_Ant1\_5320



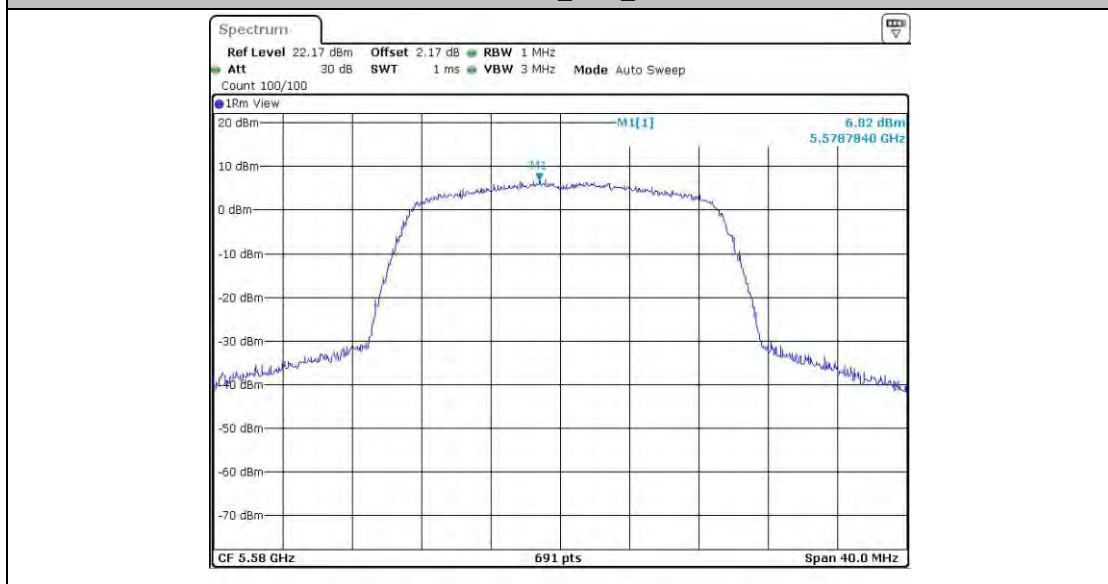
11N20SISO\_Ant2\_5320



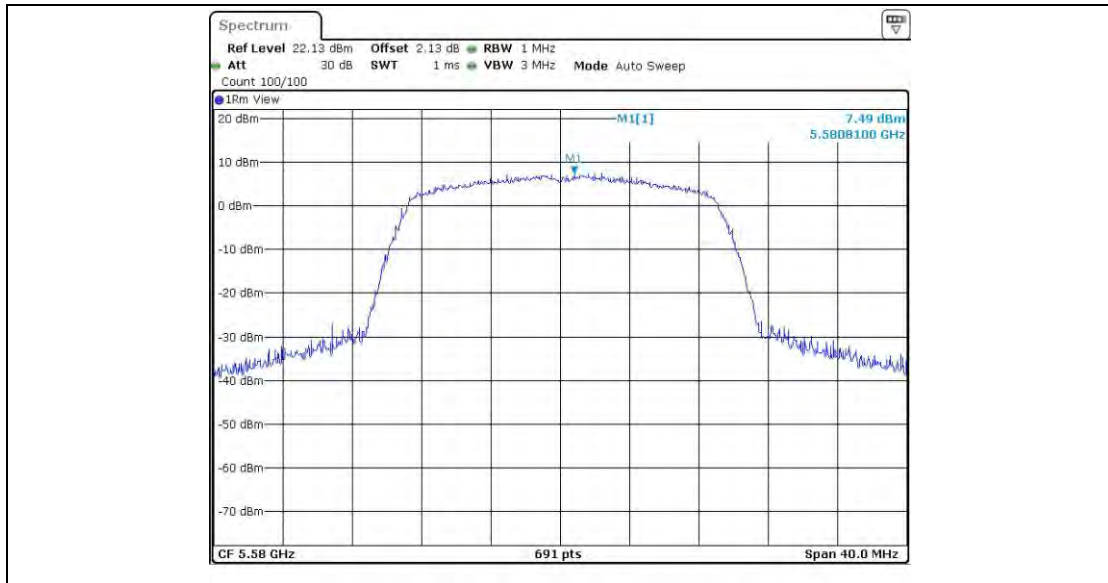
11N20SISO\_Ant1\_5500



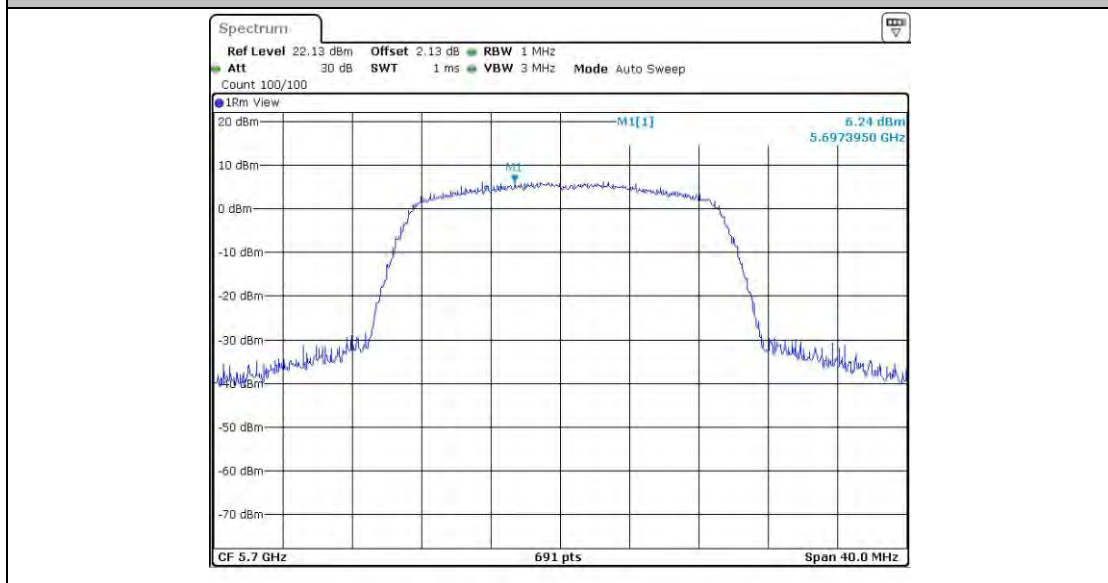
11N20SISO\_Ant2\_5500



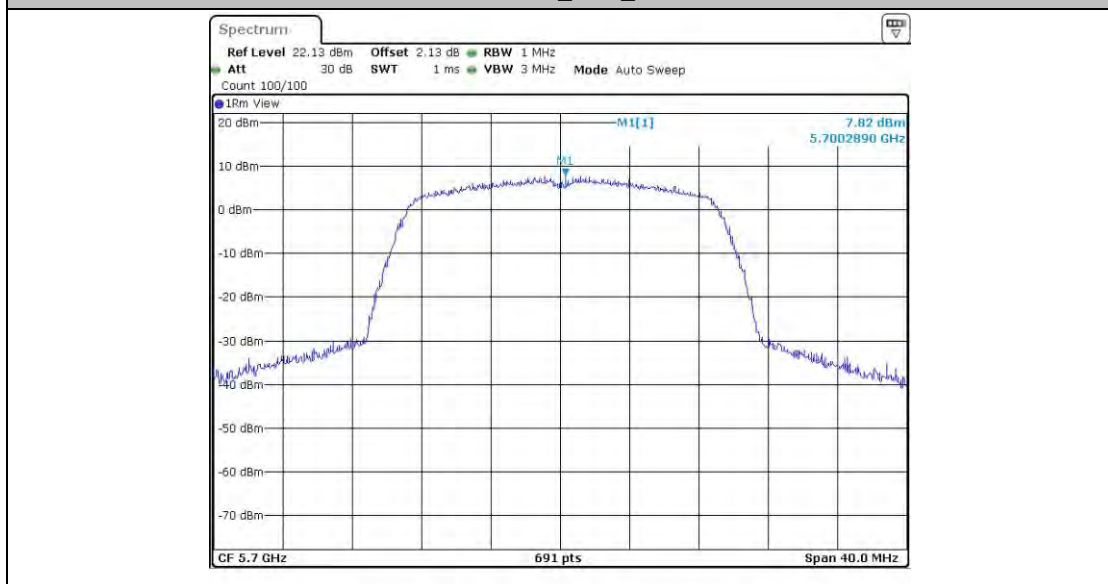
11N20SISO\_Ant1\_5580



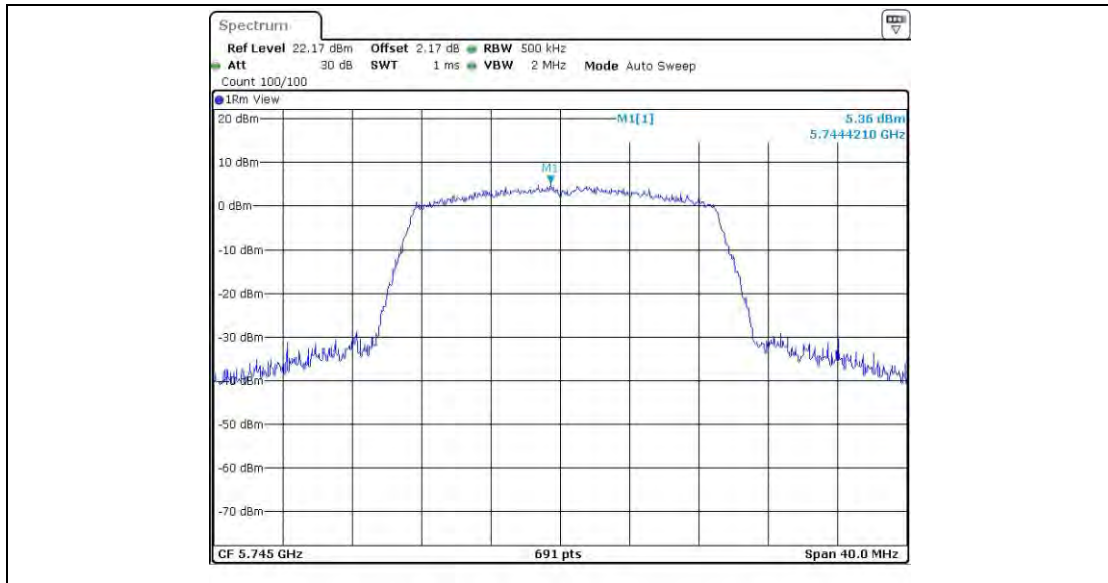
11N20SISO\_Ant2\_5580



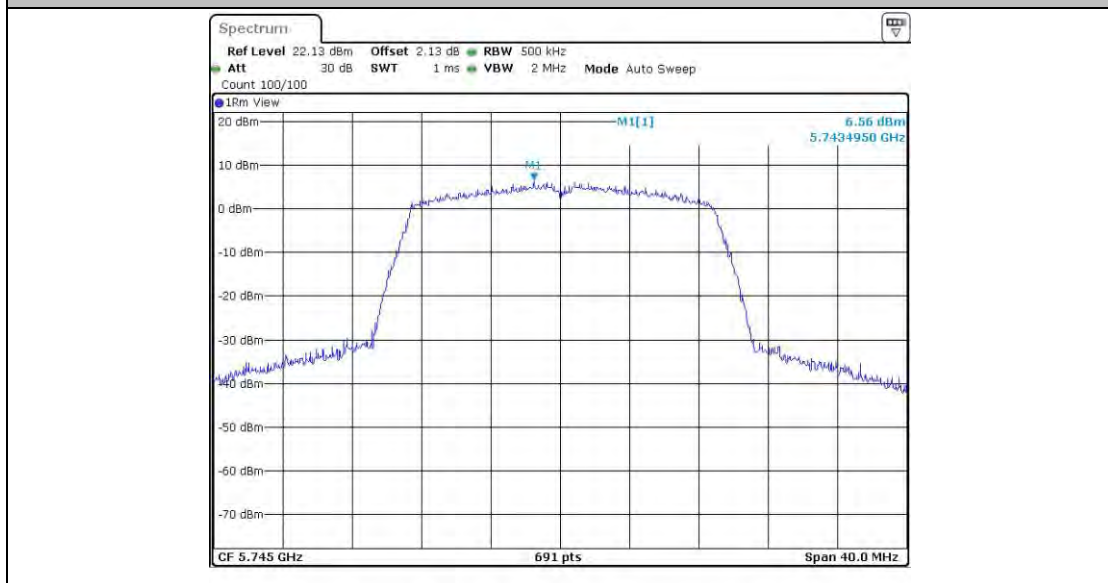
11N20SISO\_Ant1\_5700



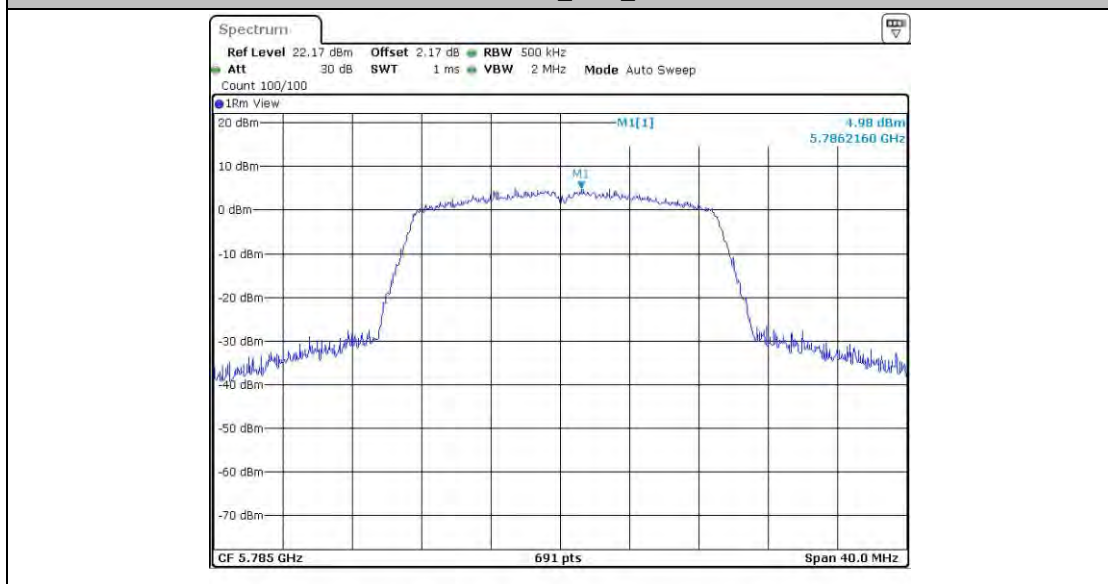
11N20SISO\_Ant2\_5700



11N20SISO\_Ant1\_5745

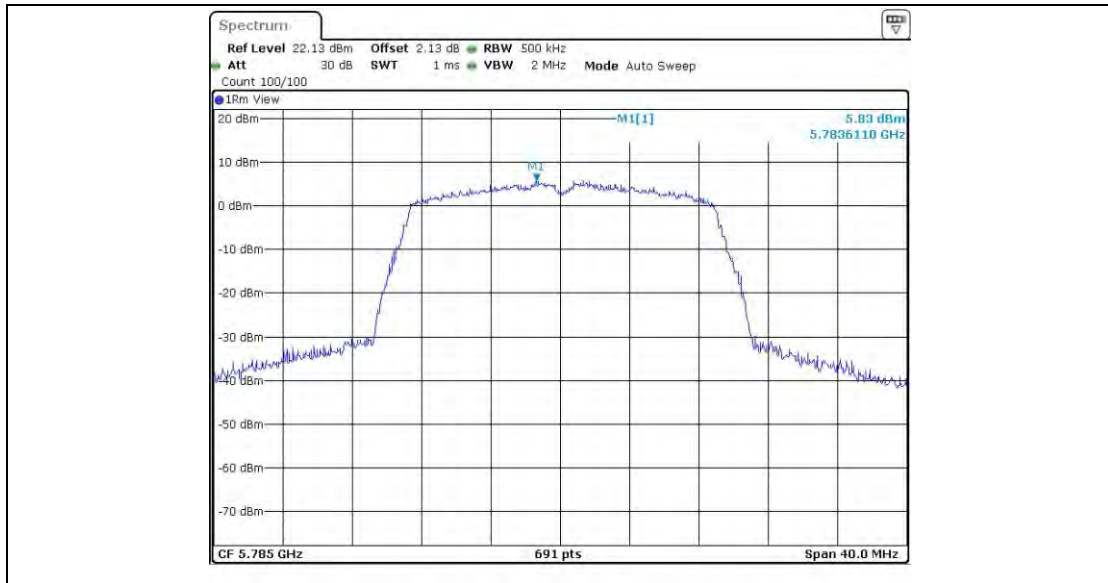


11N20SISO\_Ant2\_5745

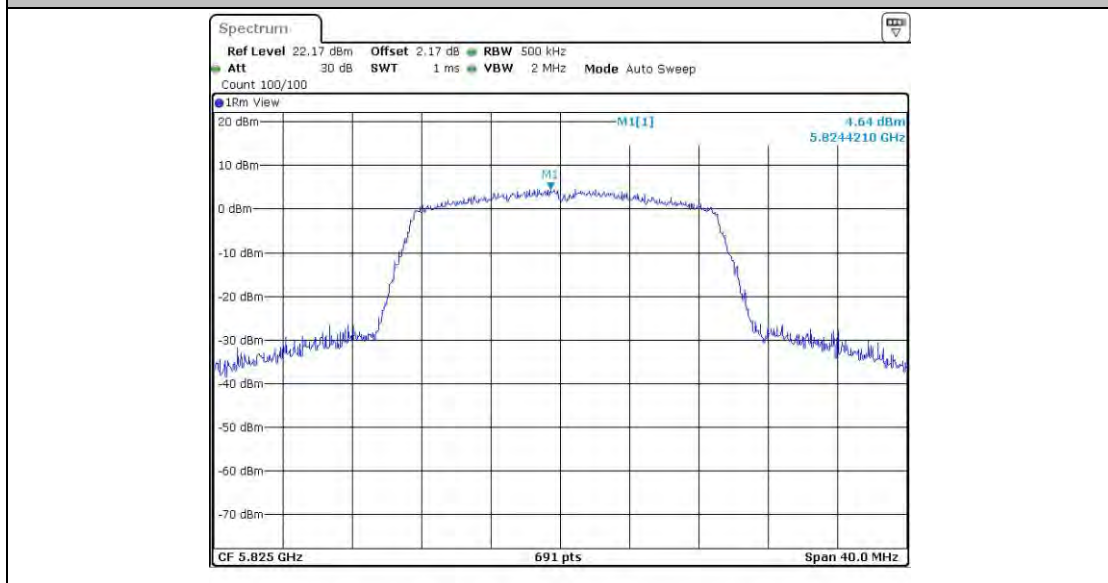


11N20SISO\_Ant1\_5785

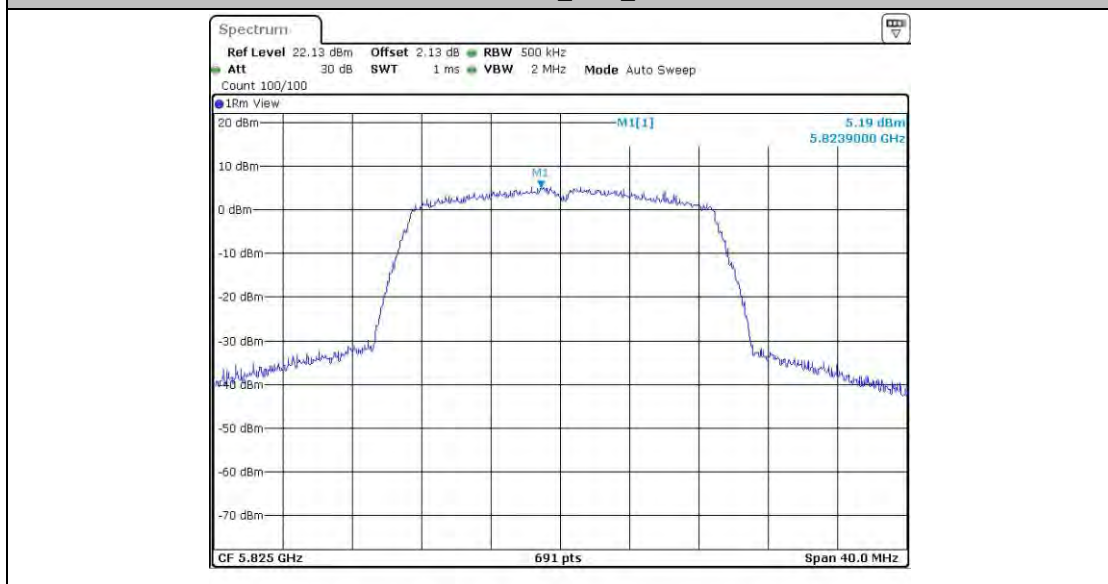




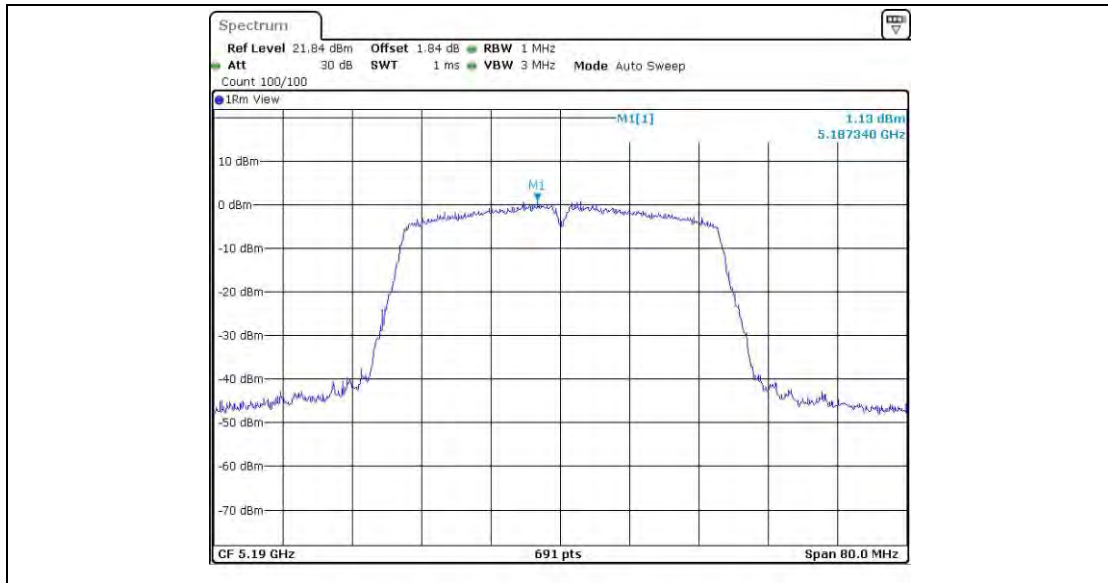
11N20SISO\_Ant2\_5785



11N20SISO\_Ant1\_5825



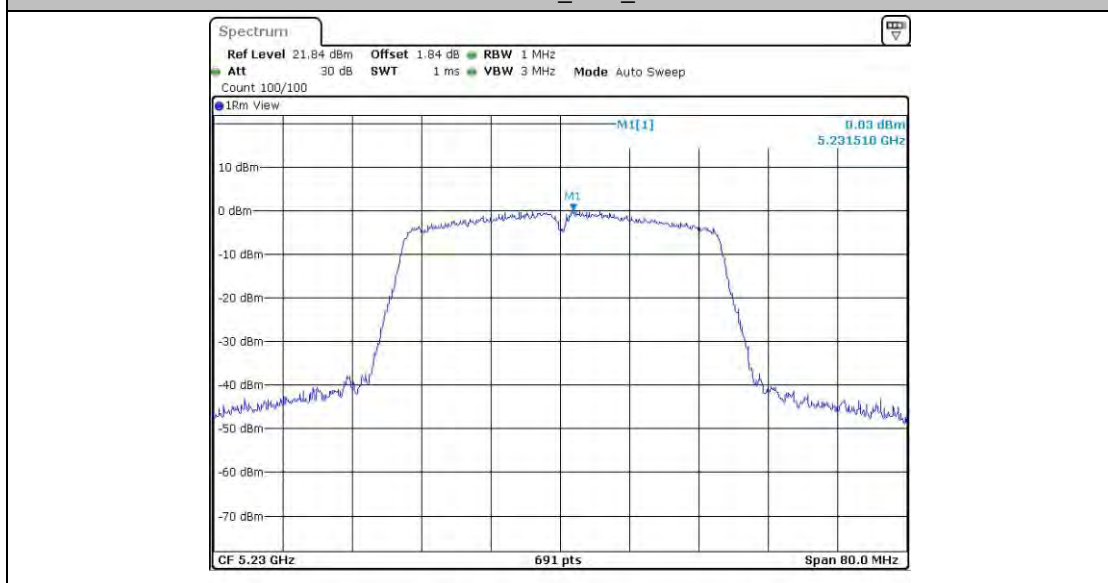
11N20SISO\_Ant2\_5825



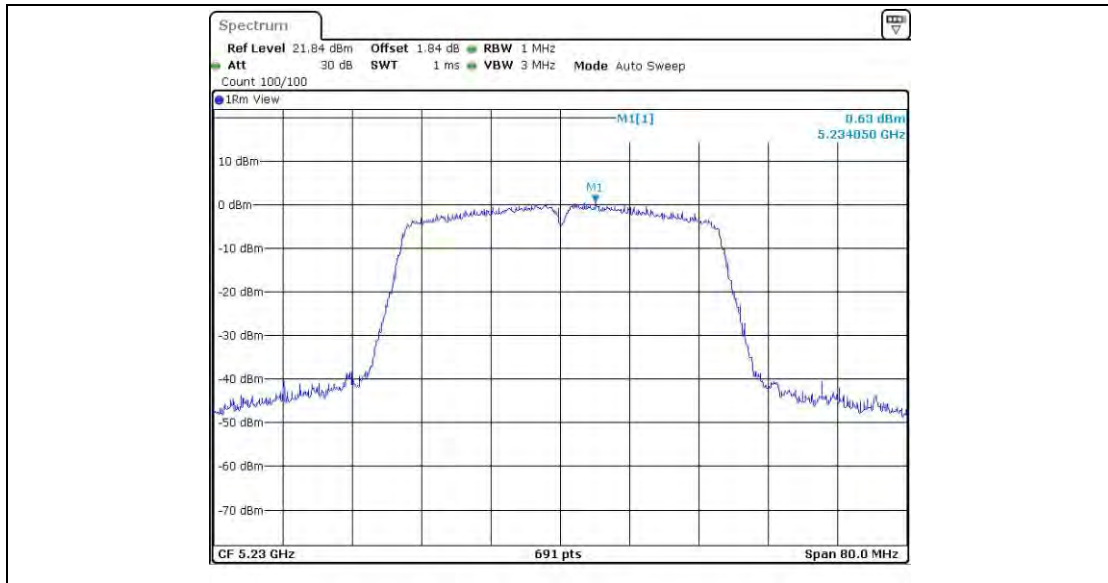
11N40SISO\_Ant1\_5190



11N40SISO\_Ant2\_5190



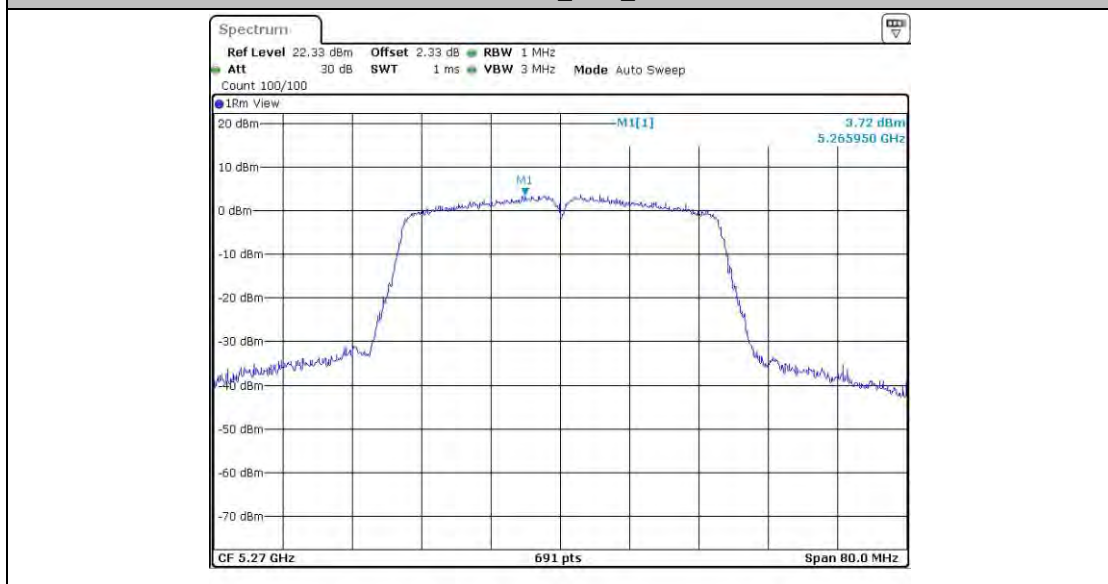
11N40SISO\_Ant1\_5230



11N40SISO\_Ant2\_5230

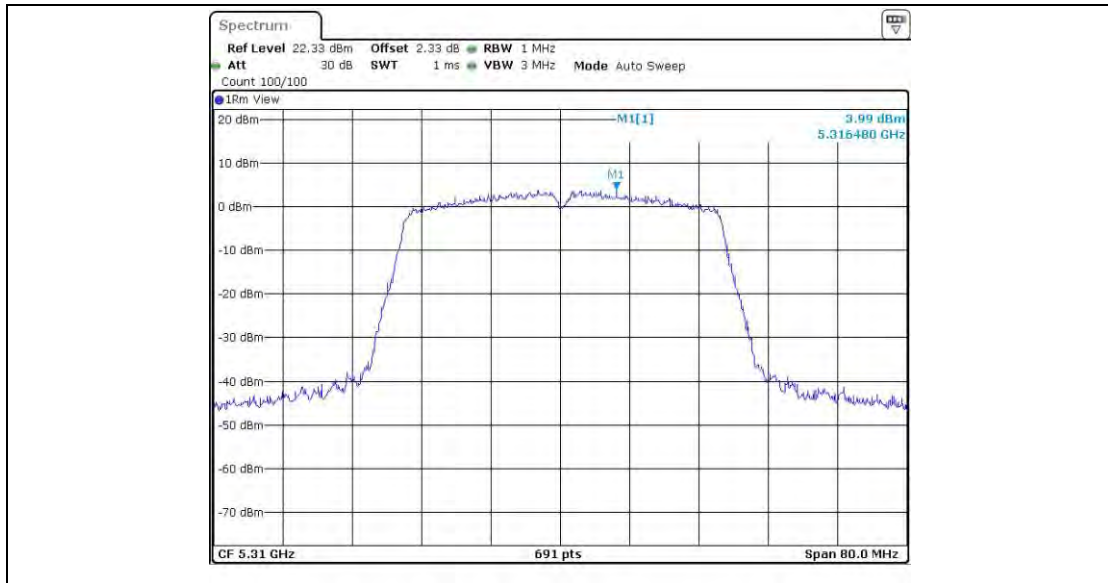


11N40SISO\_Ant1\_5270



11N40SISO\_Ant2\_5270

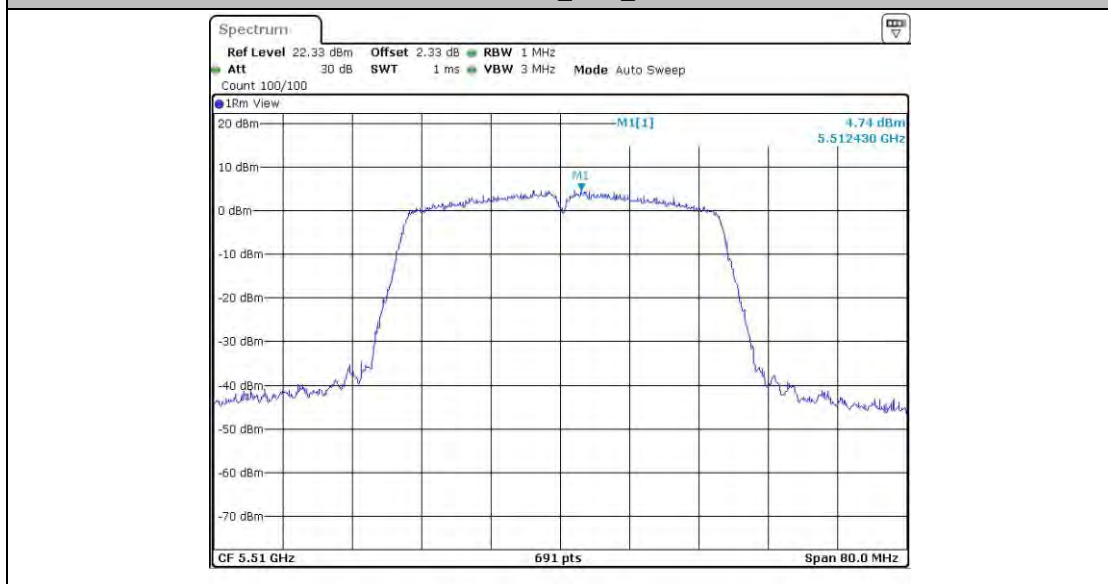




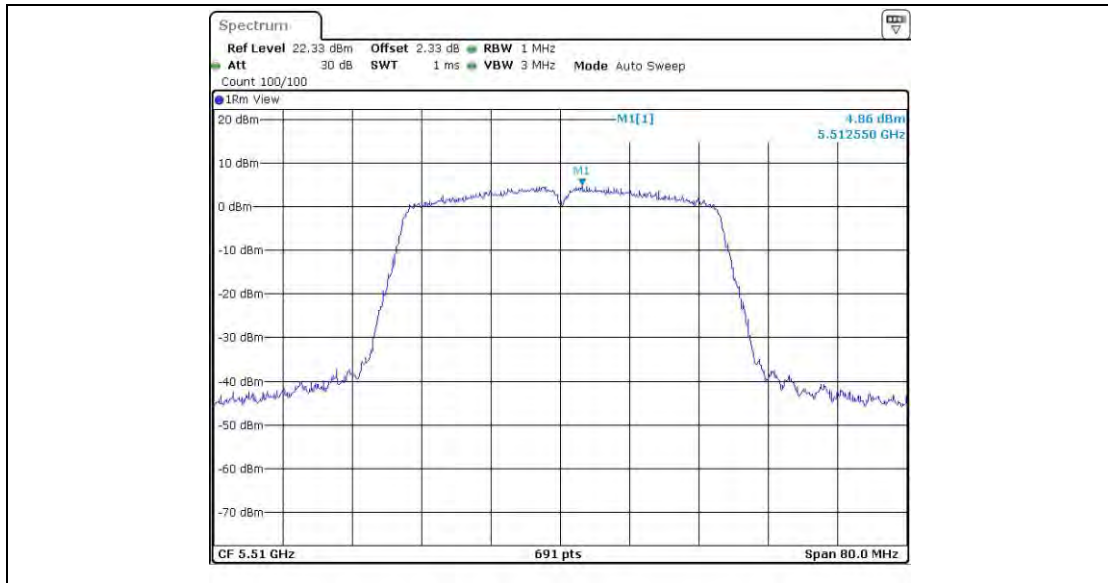
11N40SISO\_Ant1\_5310



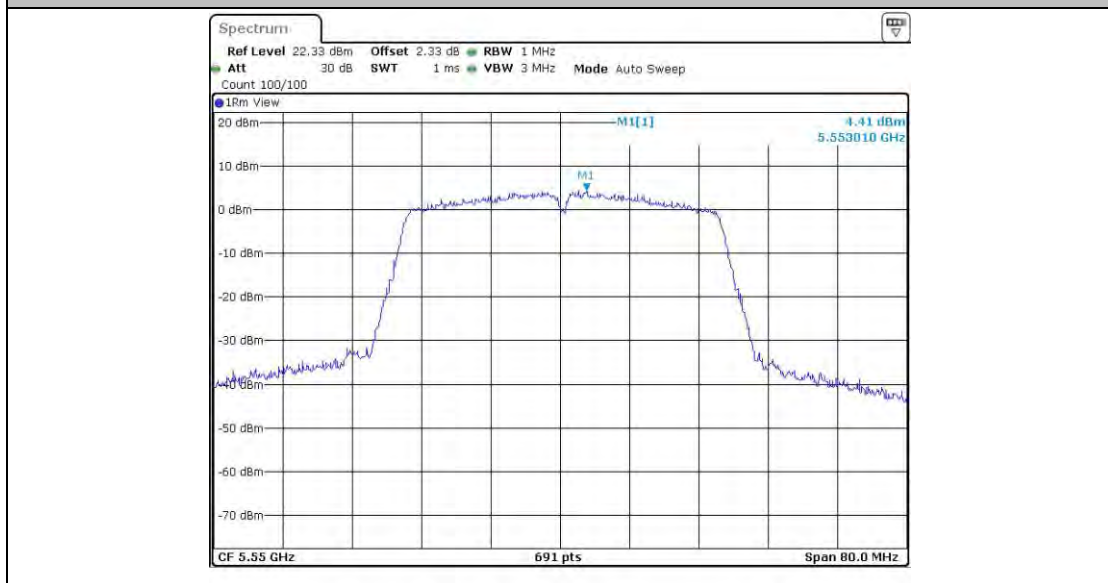
11N40SISO\_Ant2\_5310



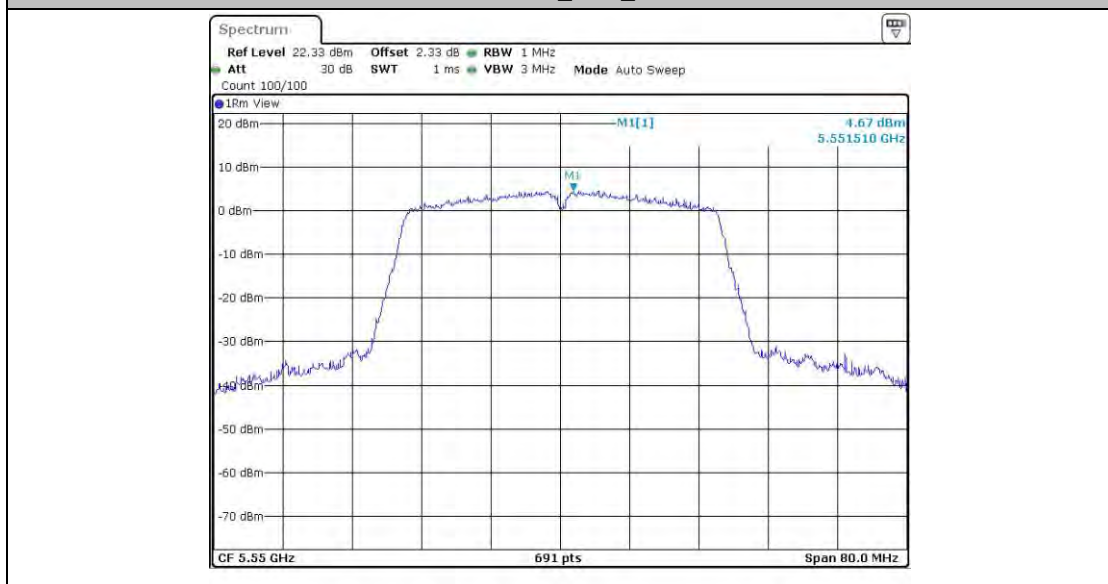
11N40SISO\_Ant1\_5510



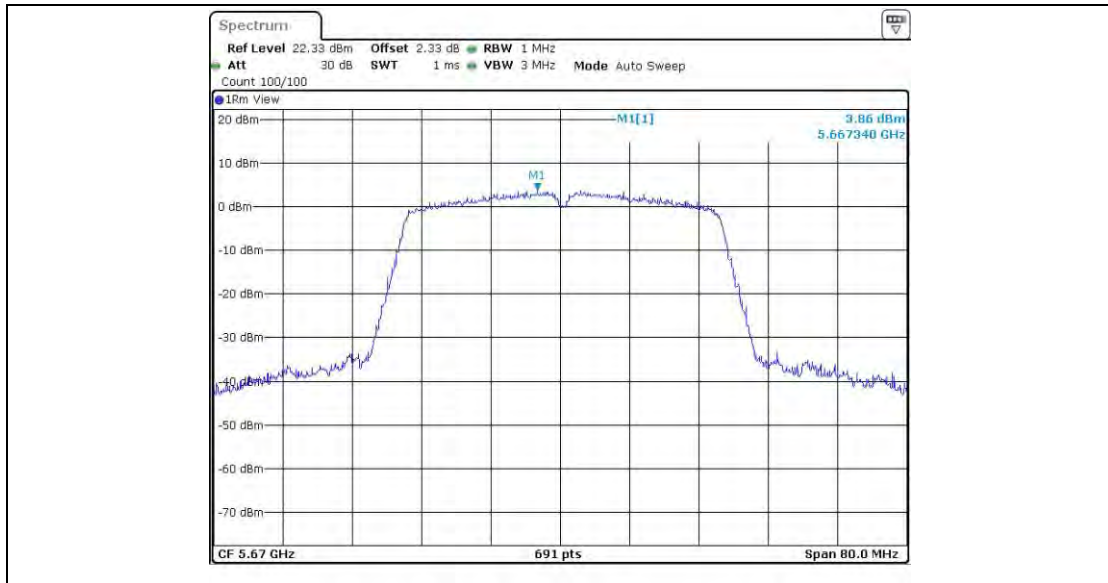
11N40SISO\_Ant2\_5510



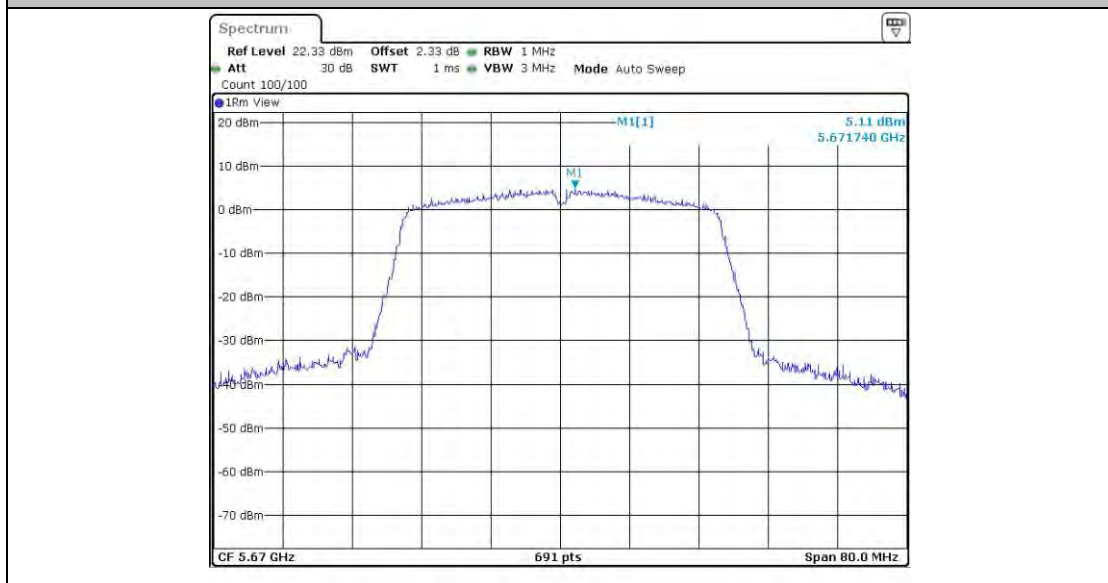
11N40SISO\_Ant1\_5550



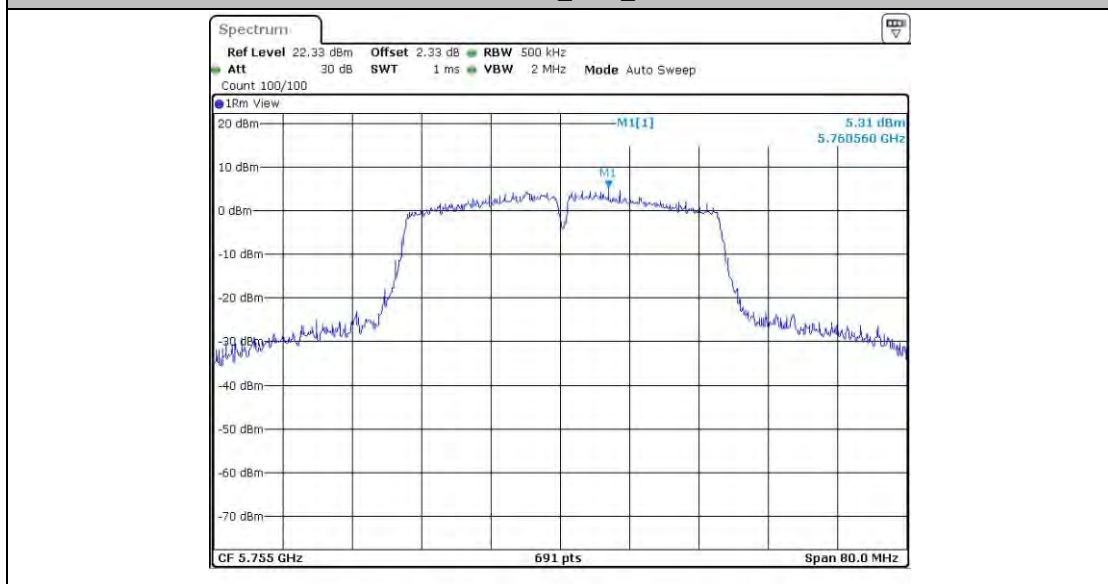
11N40SISO\_Ant2\_5550



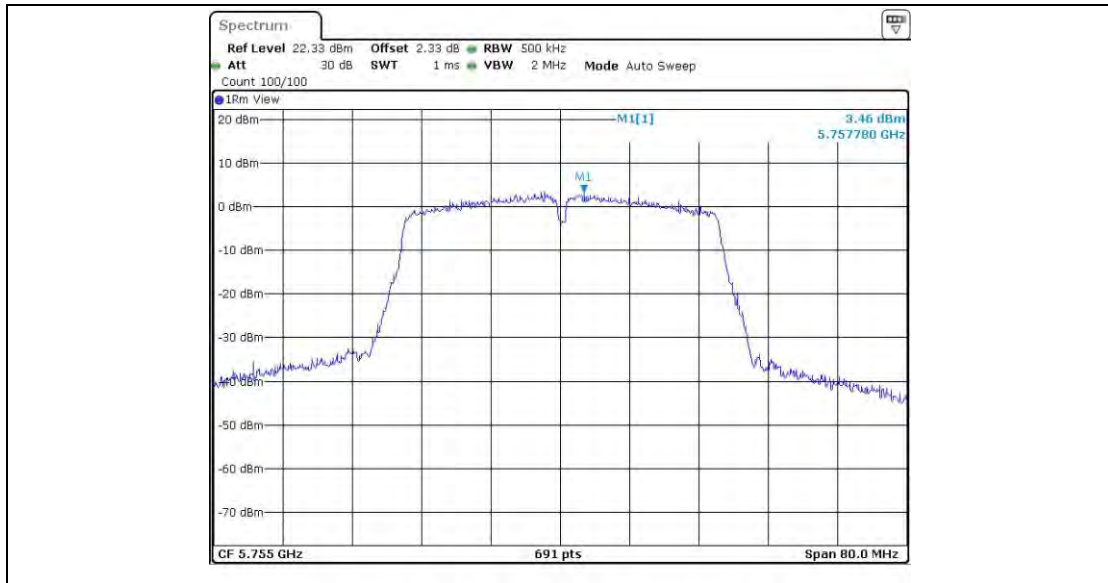
11N40SISO\_Ant1\_5670



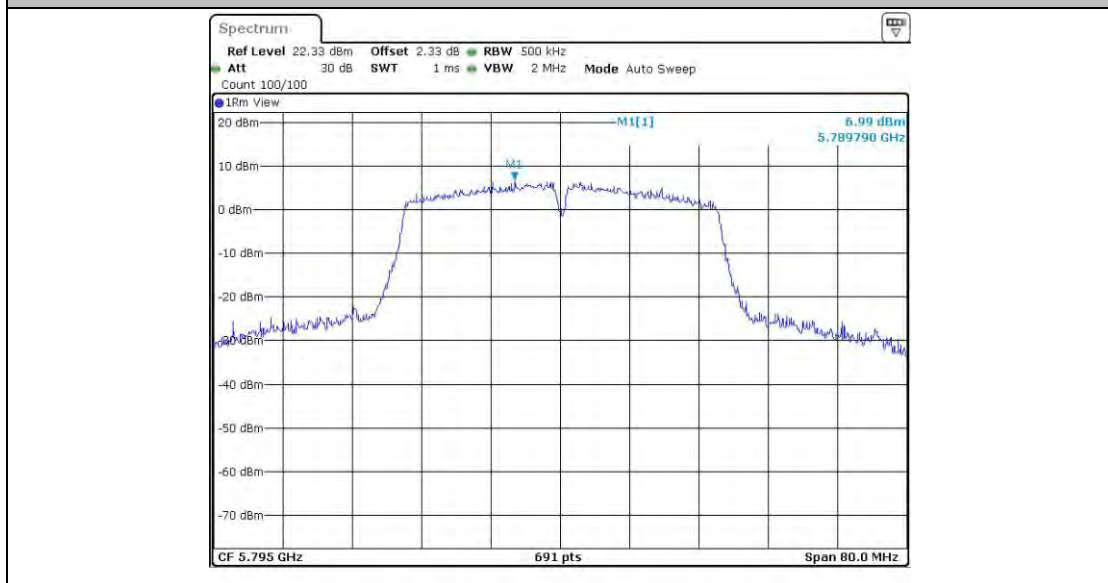
11N40SISO\_Ant2\_5670



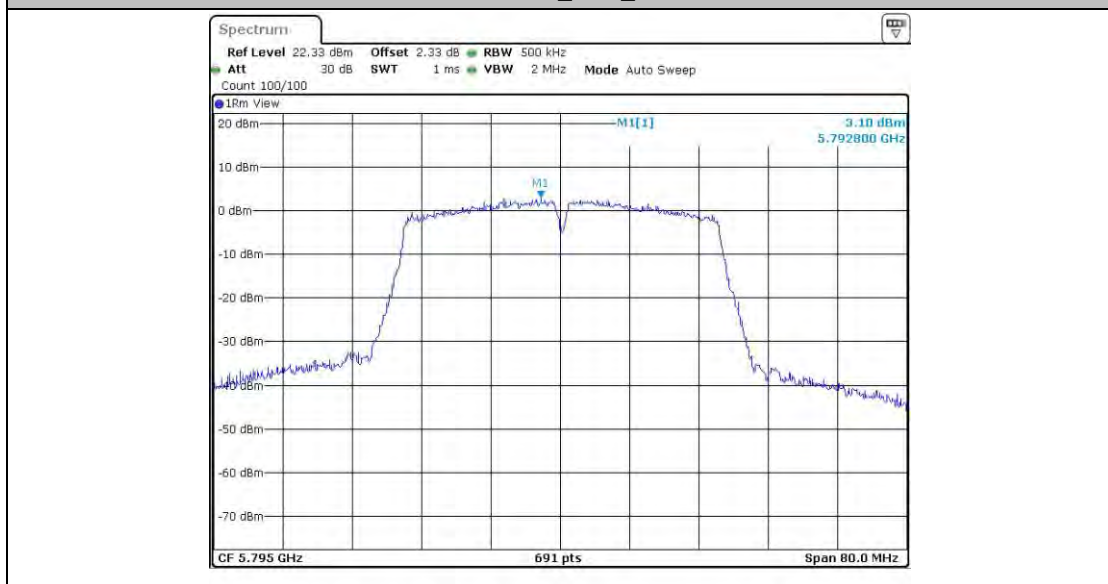
11N40SISO\_Ant1\_5755



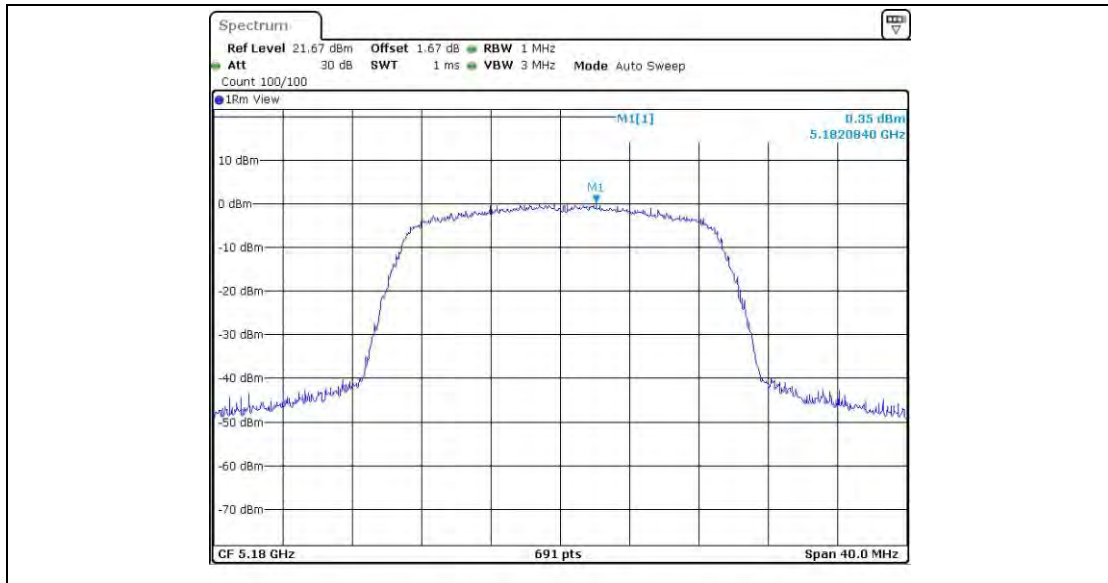
11N40SISO\_Ant2\_5755



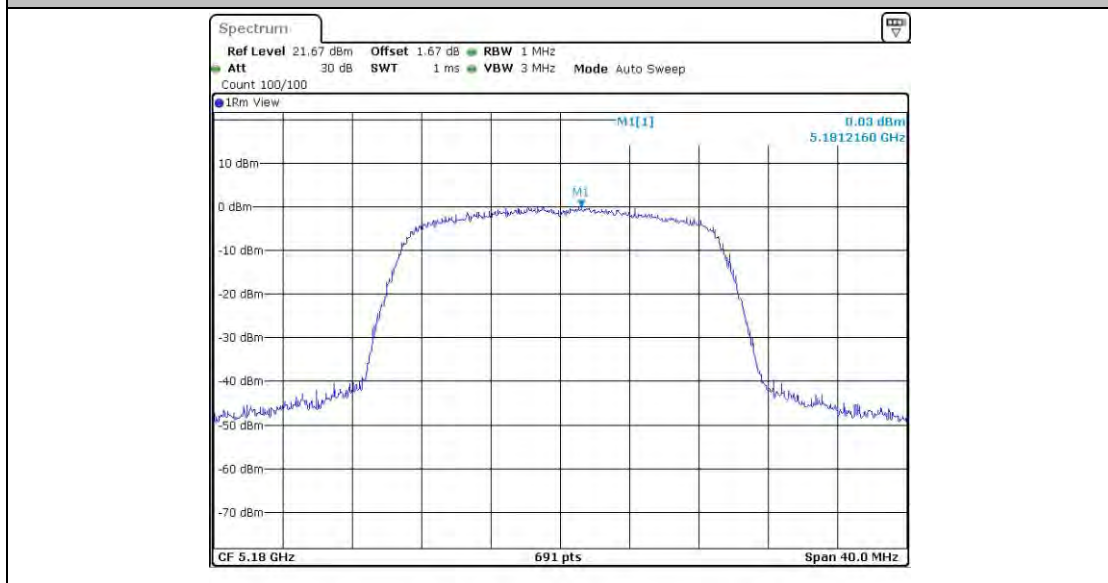
11N40SISO\_Ant1\_5795



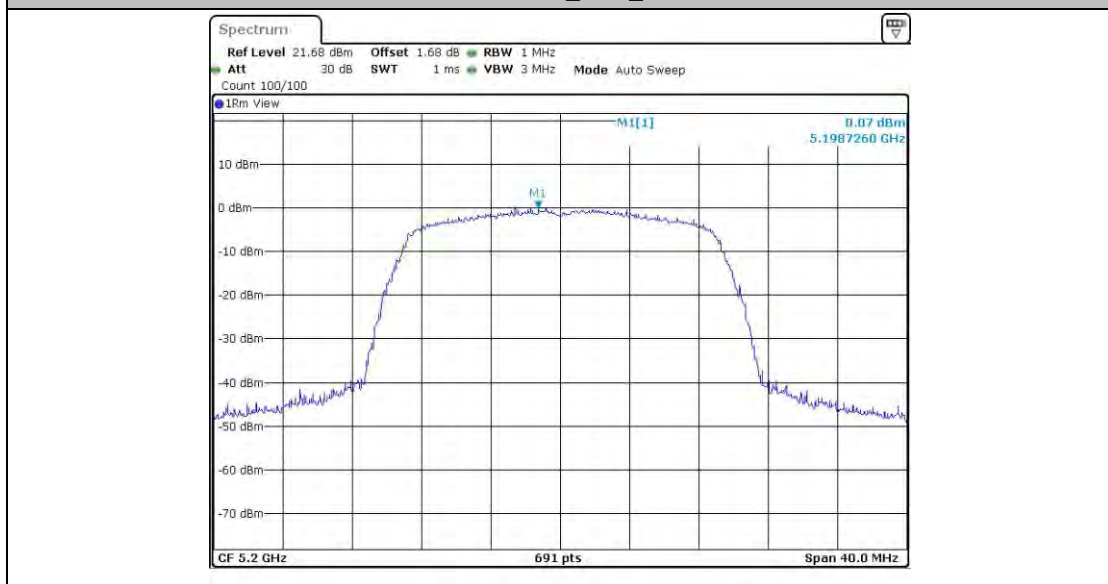
11N40SISO\_Ant2\_5795



11AC20SISO\_Ant1\_5180

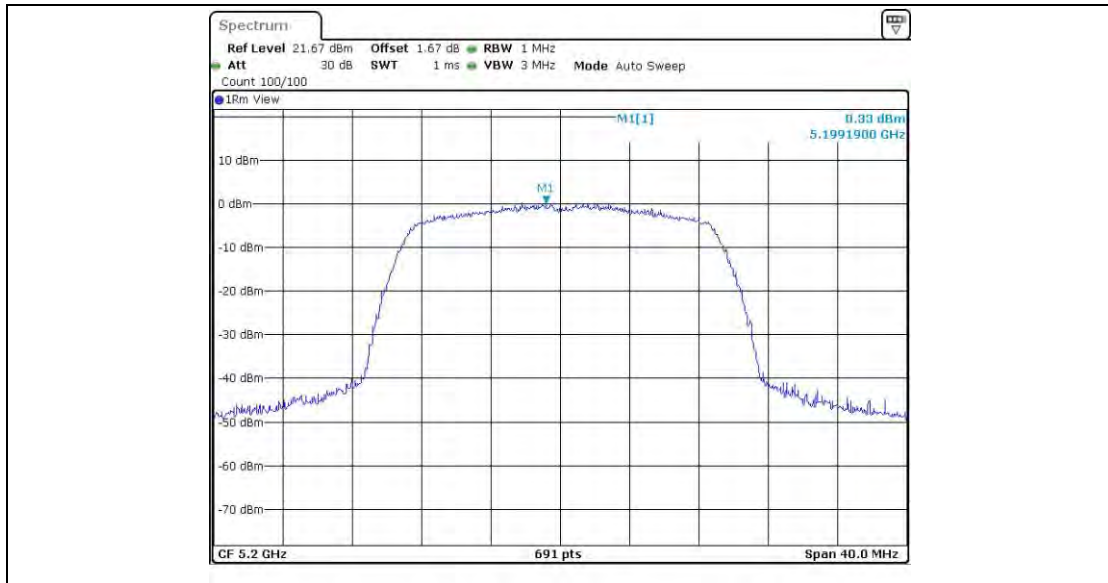


11AC20SISO\_Ant2\_5180

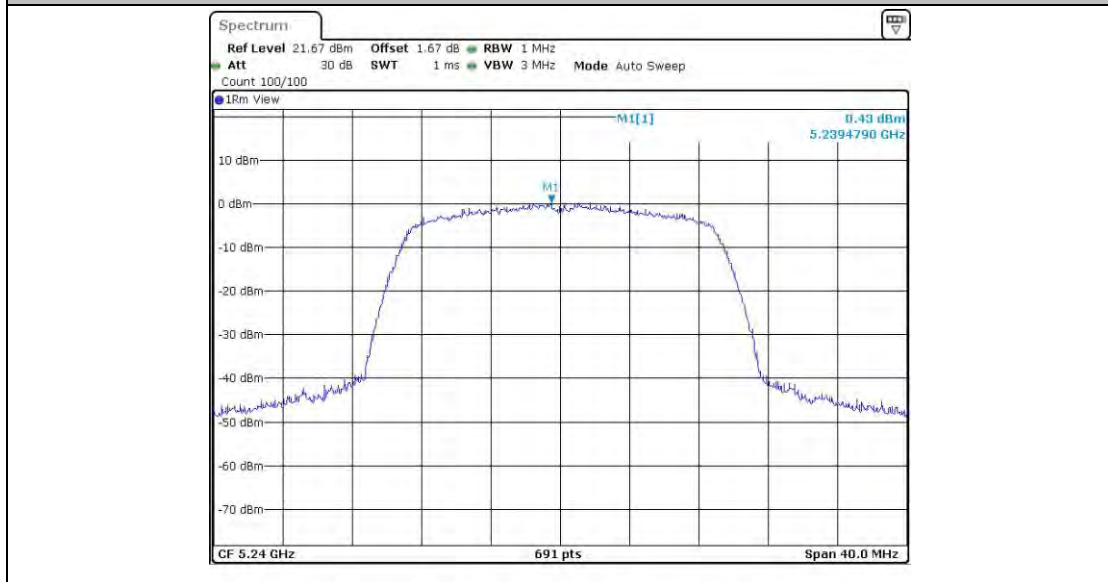


11AC20SISO\_Ant1\_5200





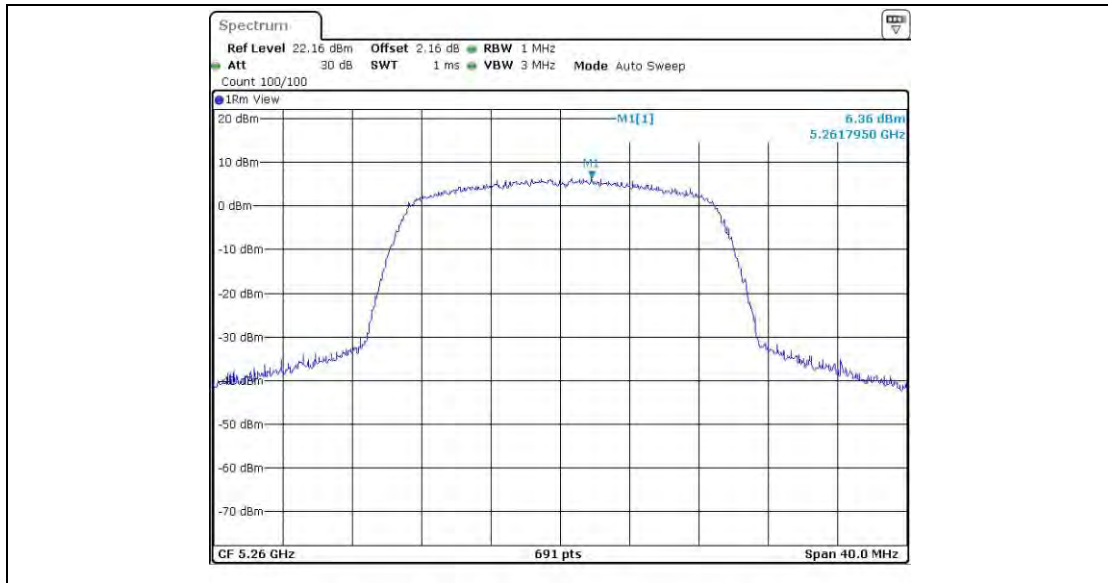
11AC20SISO\_Ant2\_5200



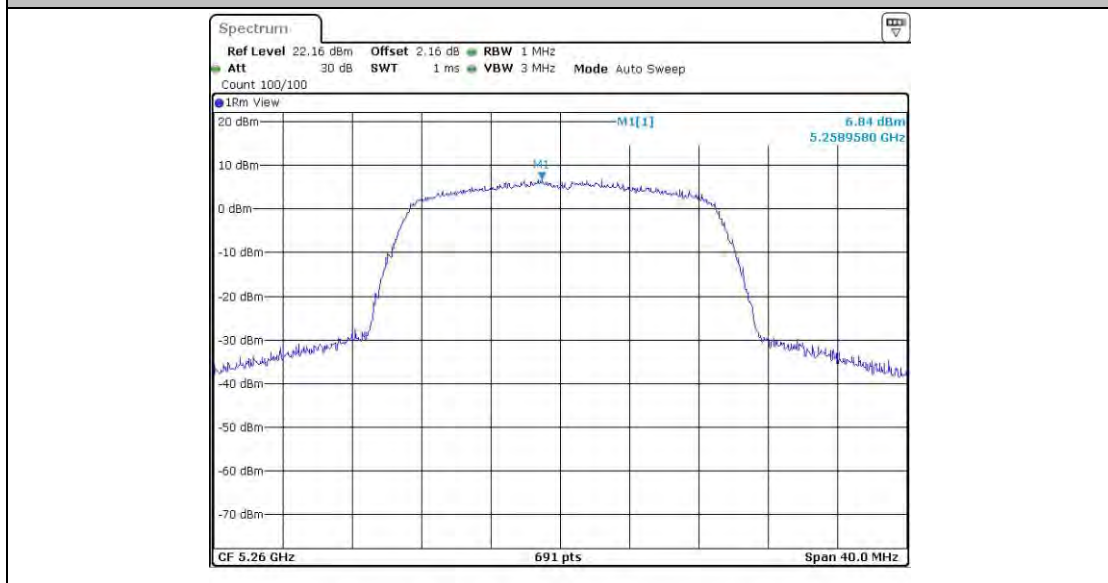
11AC20SISO\_Ant1\_5240



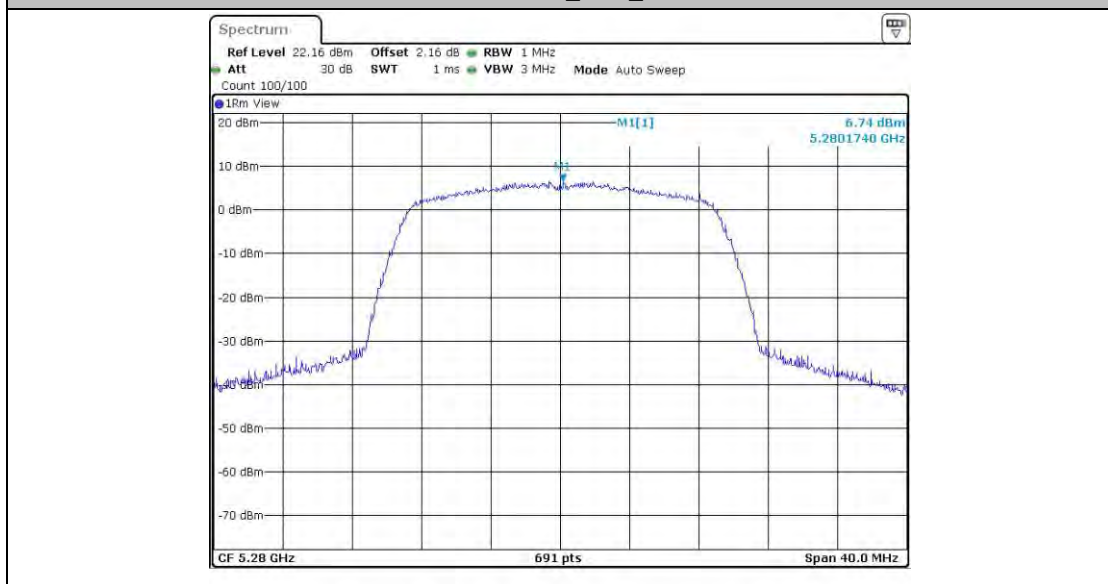
11AC20SISO\_Ant2\_5240



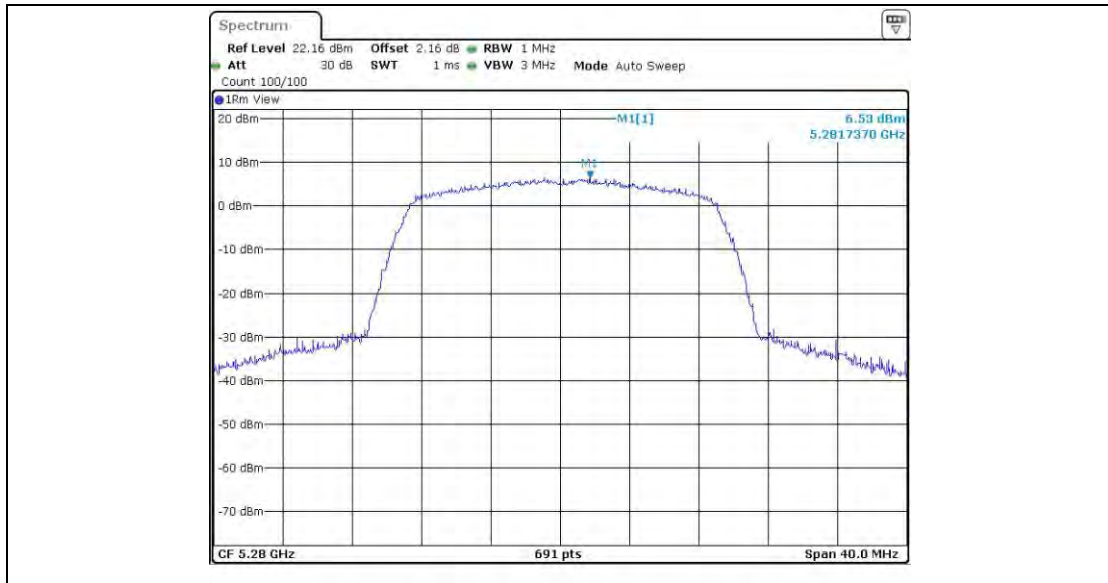
11AC20SISO\_Ant1\_5260



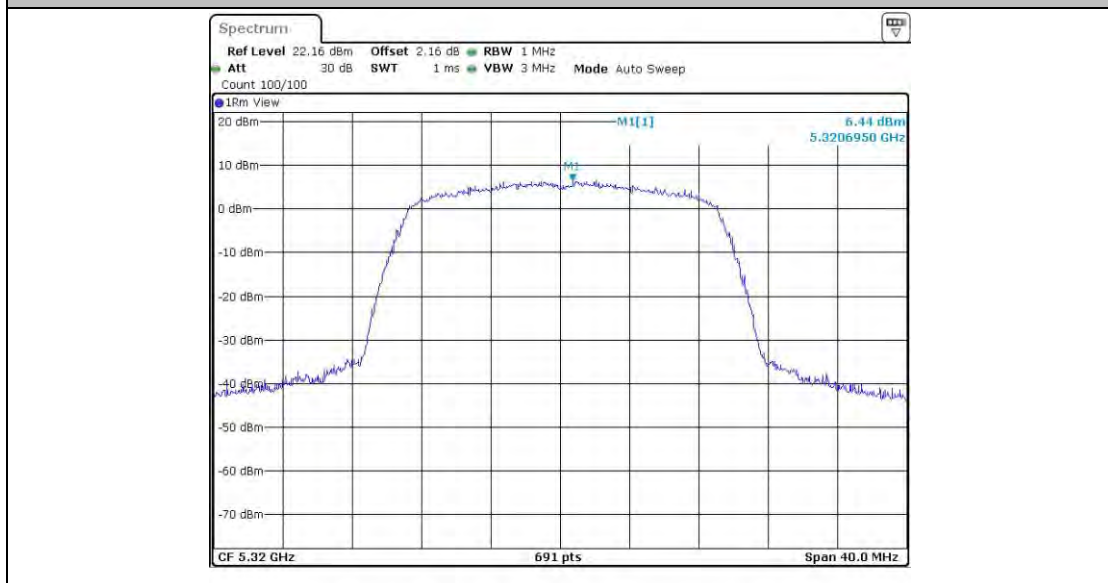
11AC20SISO\_Ant2\_5260



11AC20SISO\_Ant1\_5280



11AC20SISO\_Ant2\_5280

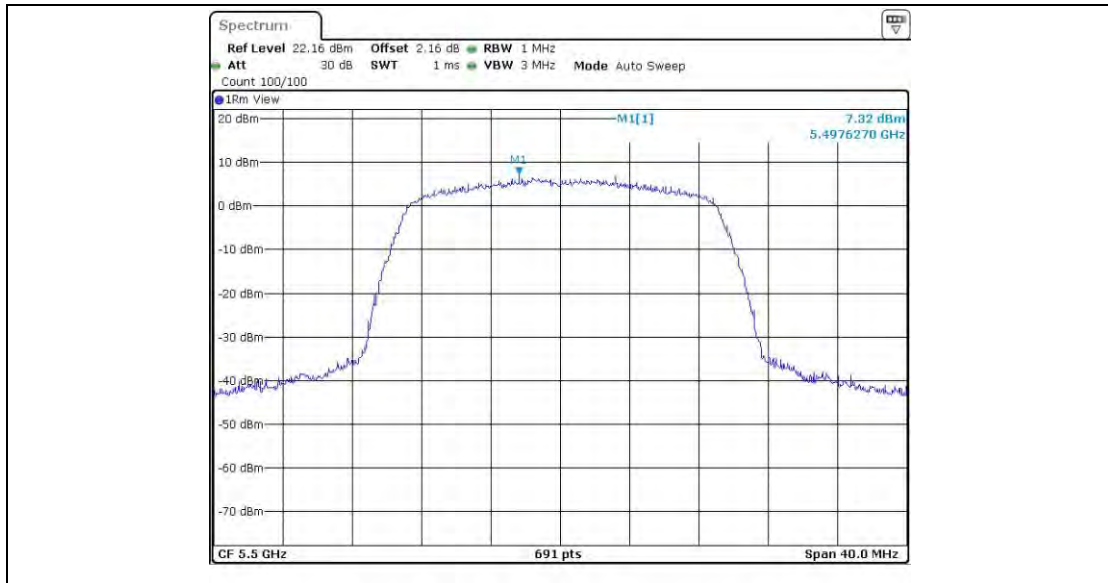


11AC20SISO\_Ant1\_5320

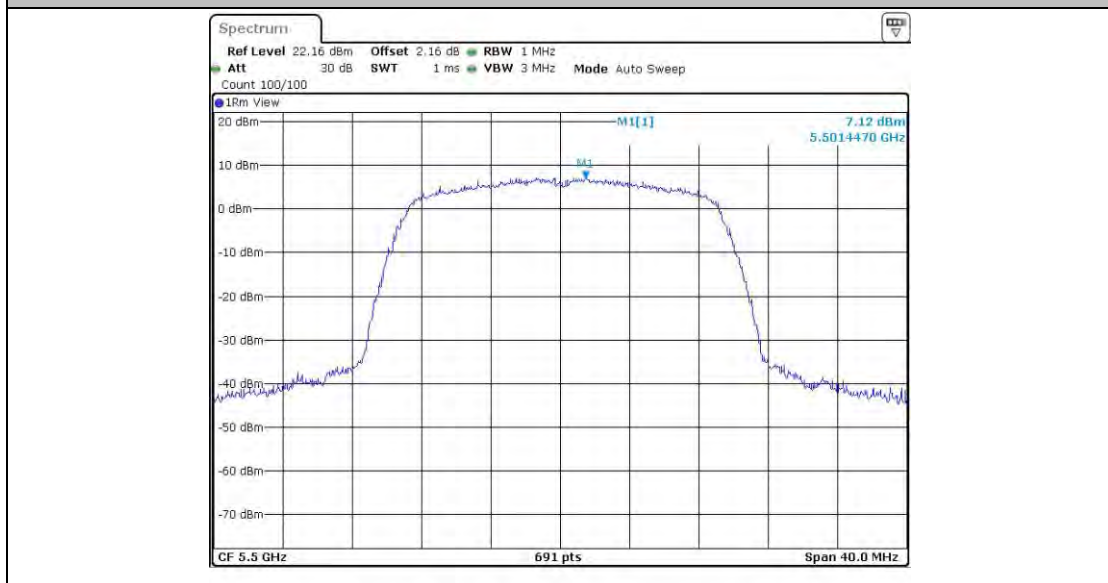


11AC20SISO\_Ant2\_5320

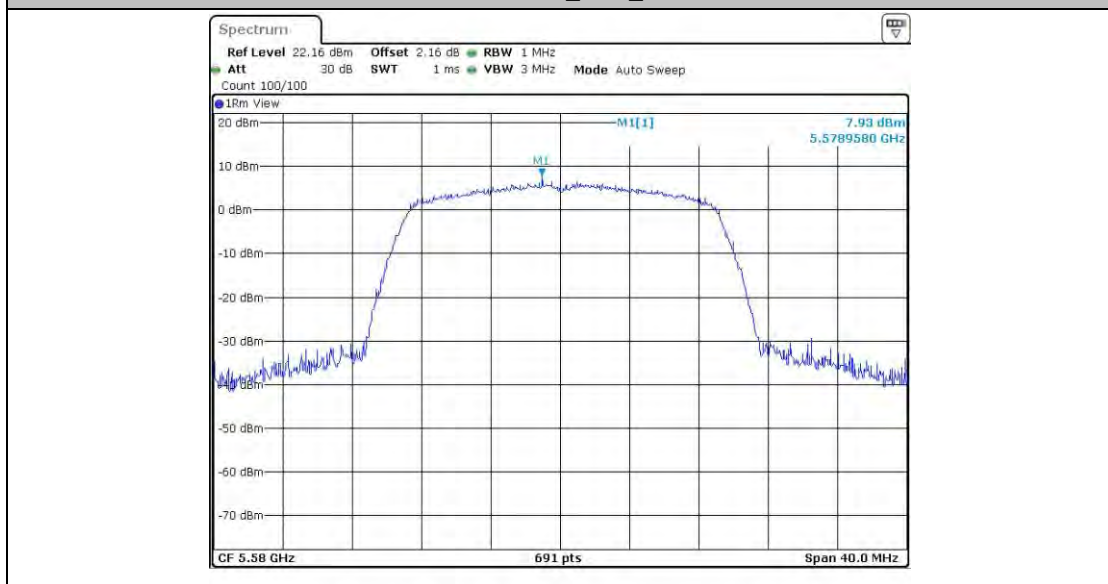




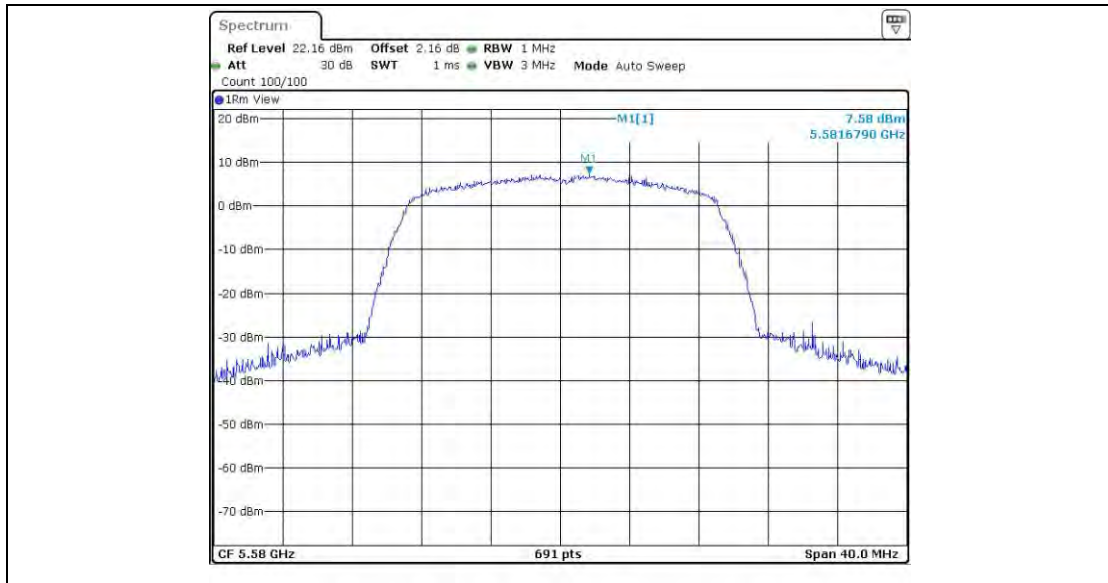
11AC20SISO\_Ant1\_5500



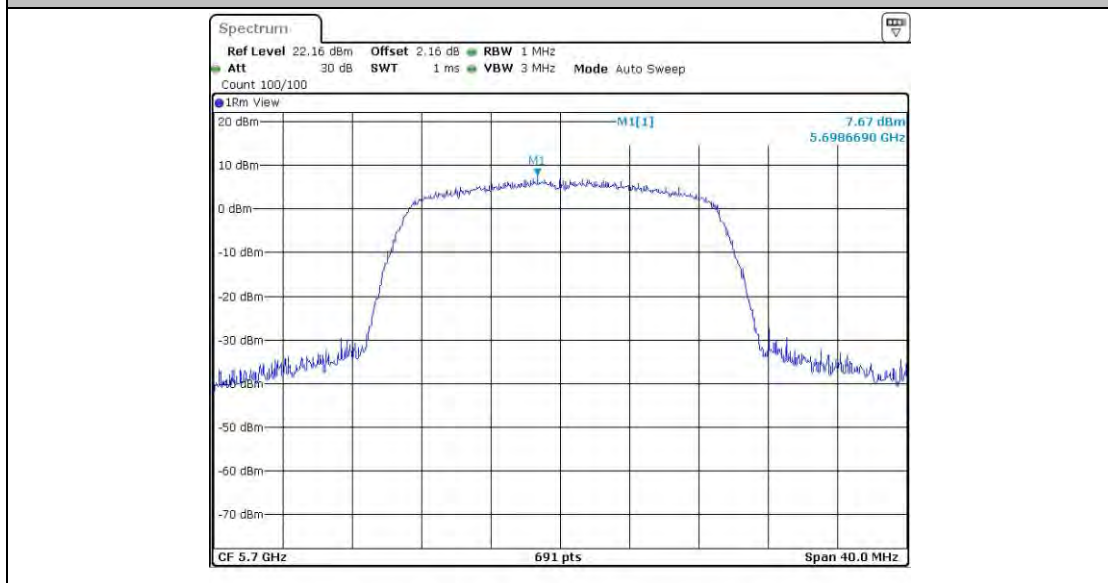
11AC20SISO\_Ant2\_5500



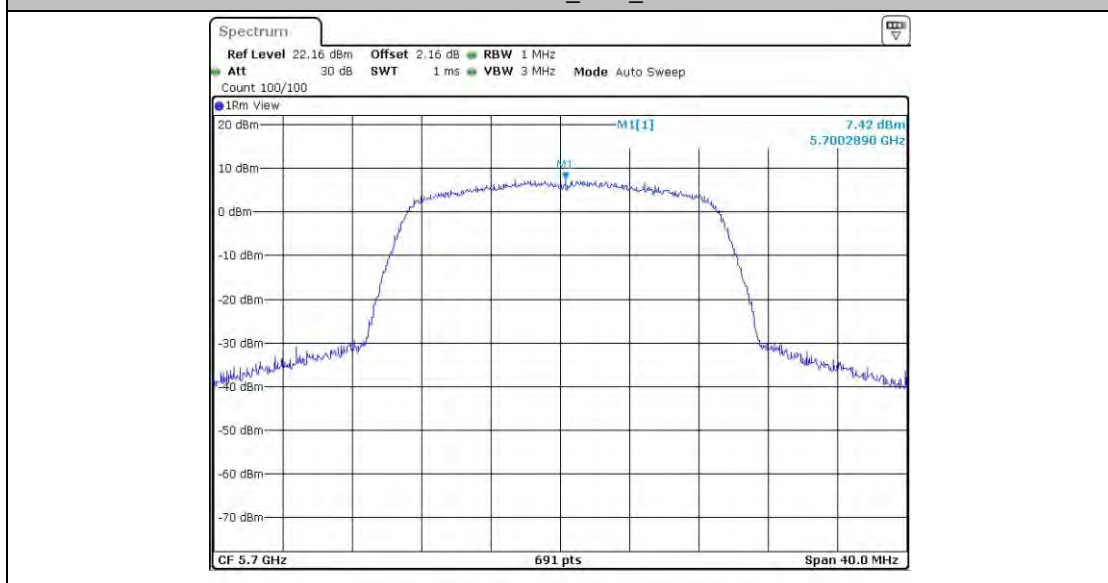
11AC20SISO\_Ant1\_5580



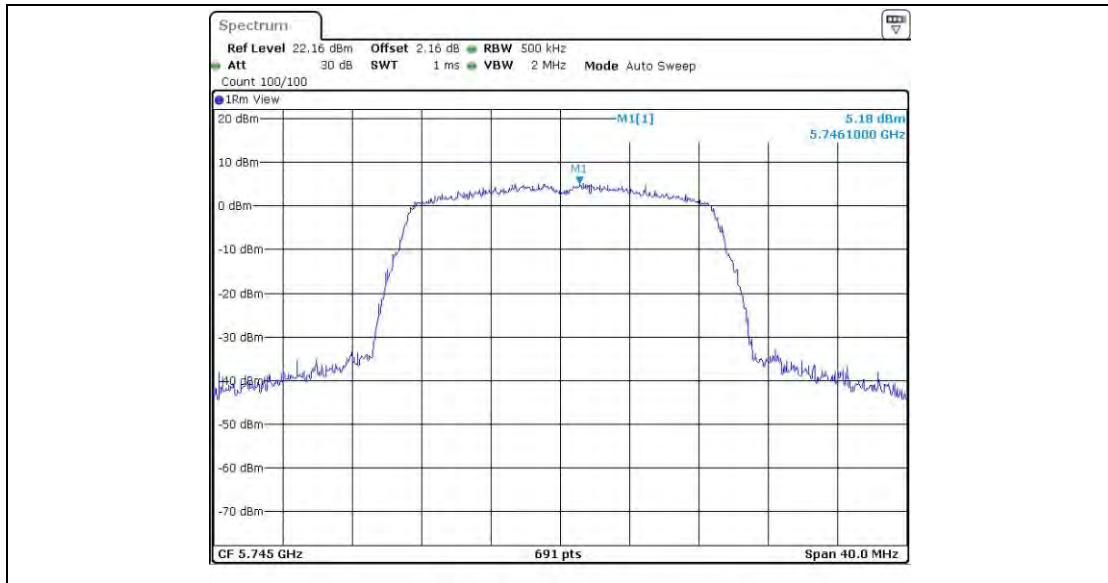
11AC20SISO\_Ant2\_5580



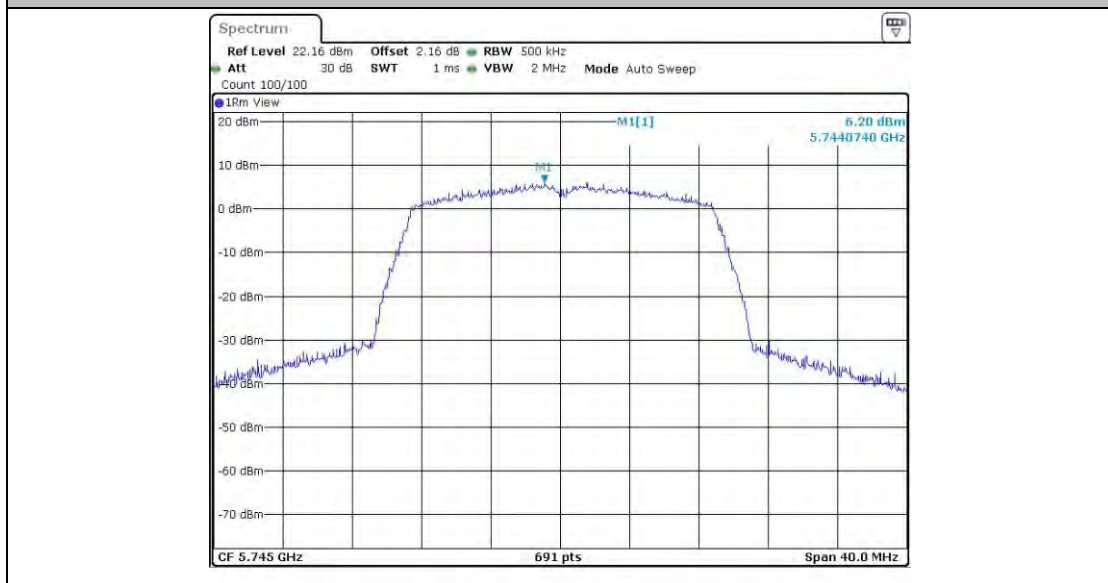
11AC20SISO\_Ant1\_5700



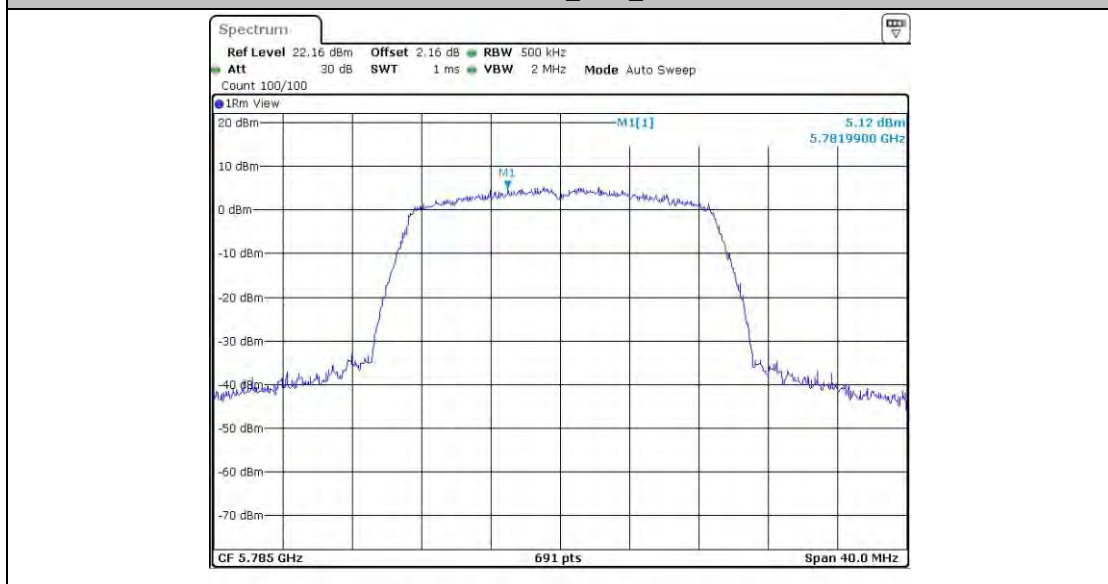
11AC20SISO\_Ant2\_5700



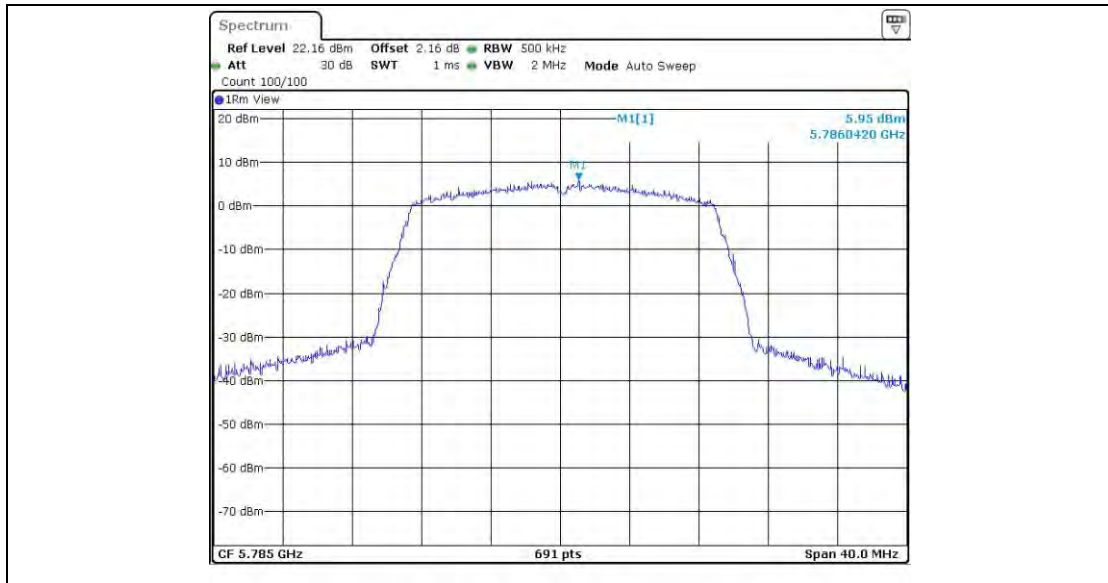
11AC20SISO\_Ant1\_5745



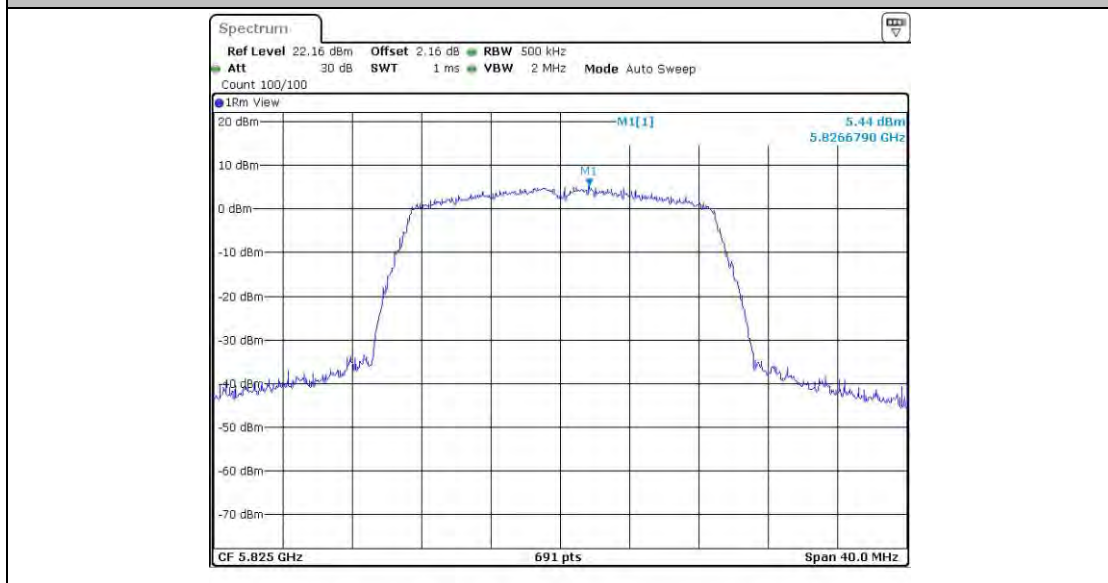
11AC20SISO\_Ant2\_5745



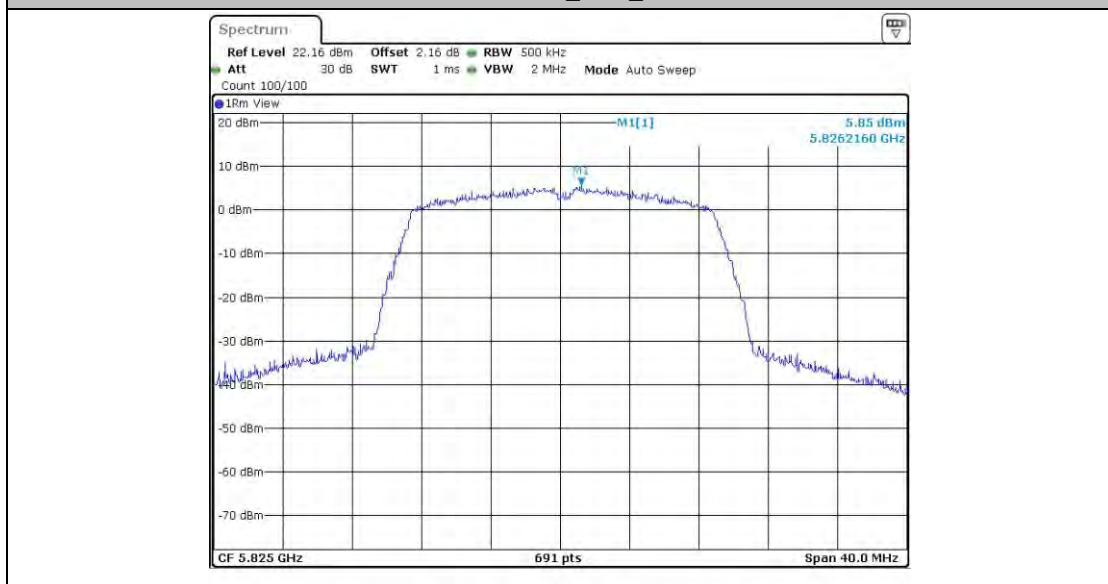
11AC20SISO\_Ant1\_5785



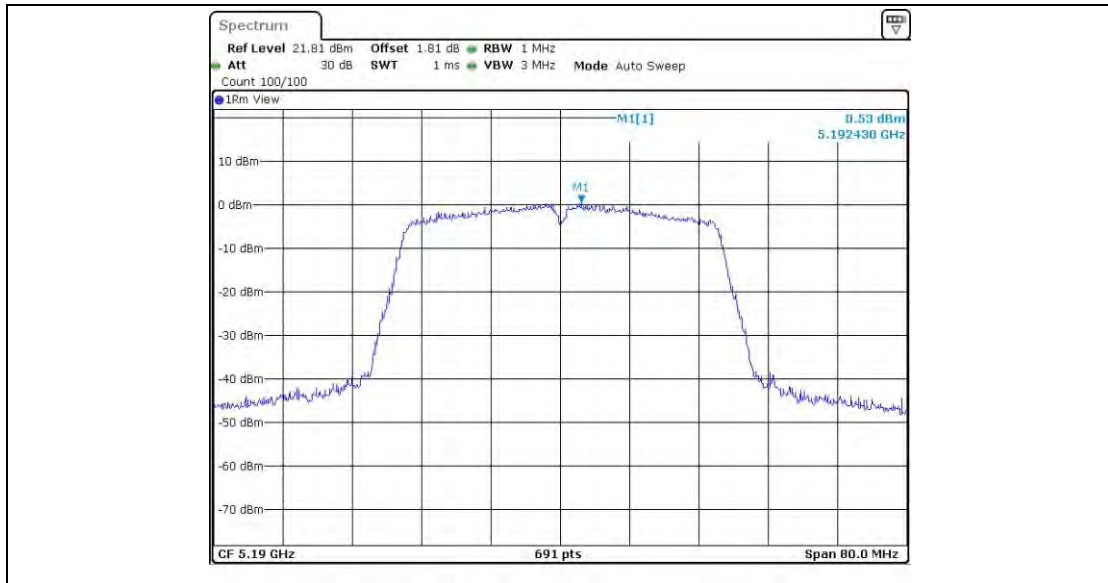
11AC20SISO\_Ant2\_5785



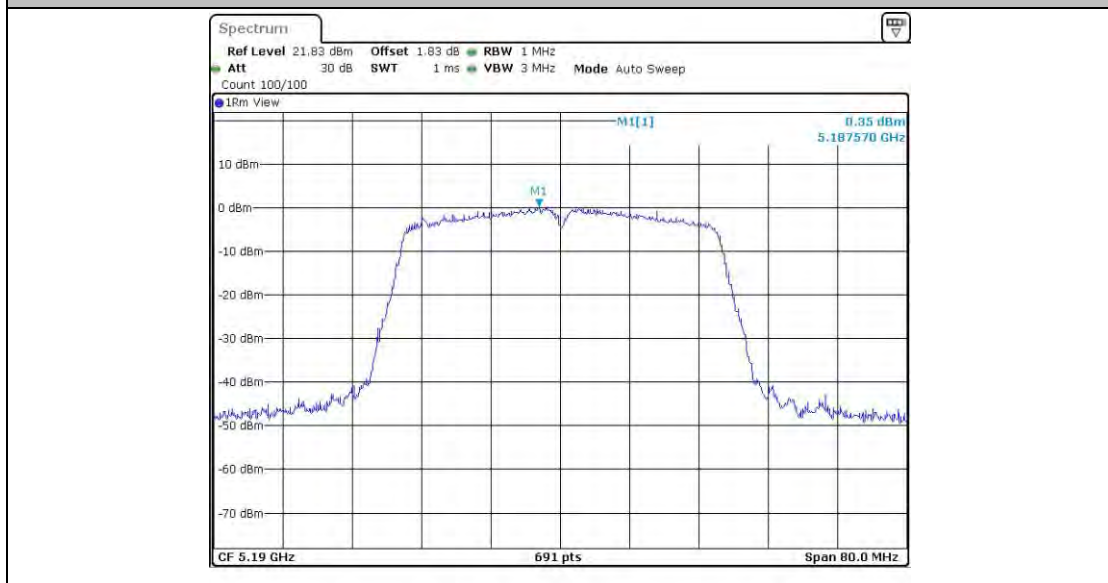
11AC20SISO\_Ant1\_5825



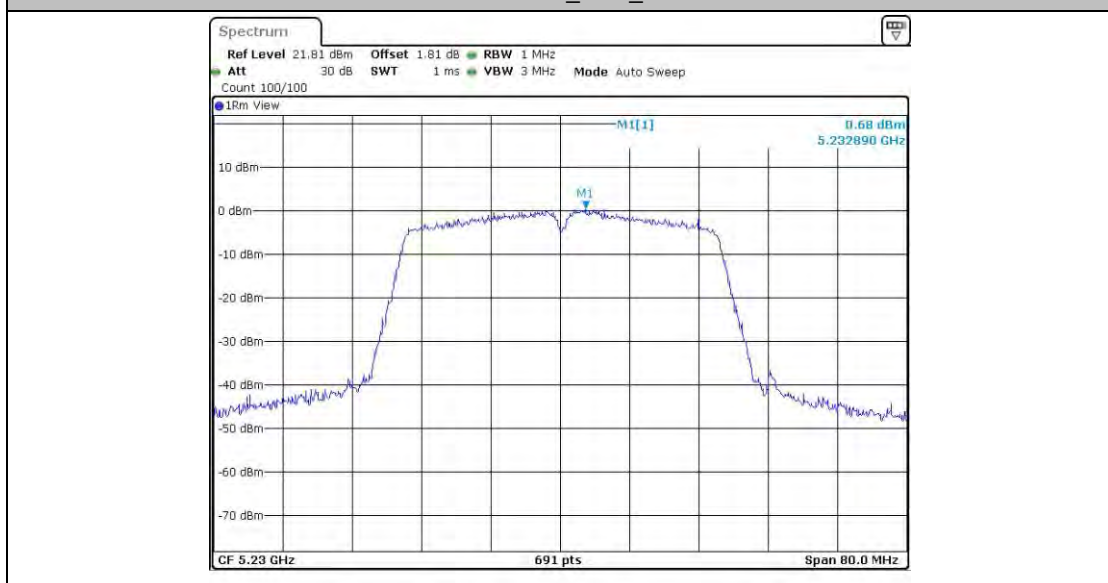
11AC20SISO\_Ant2\_5825



11AC40SISO\_Ant1\_5190

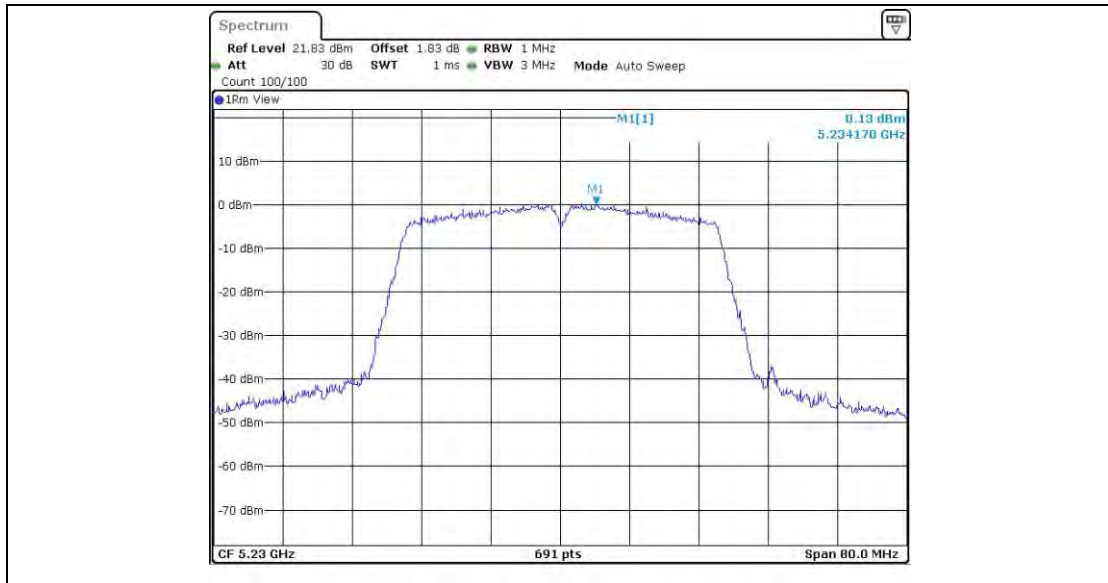


11AC40SISO\_Ant2\_5190



11AC40SISO\_Ant1\_5230

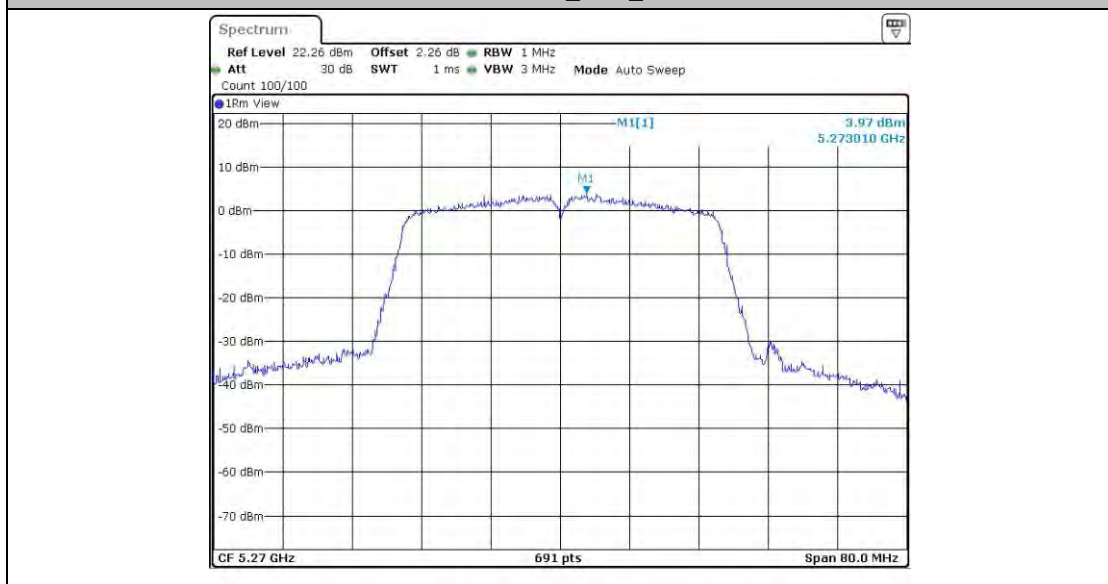




11AC40SISO\_Ant2\_5230



11AC40SISO\_Ant1\_5270

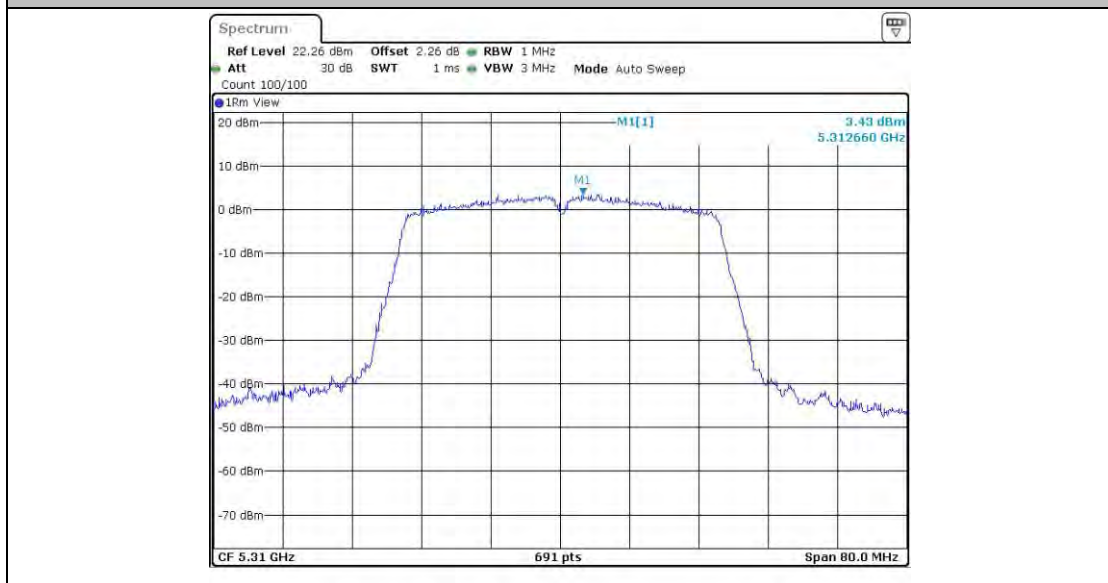


11AC40SISO\_Ant2\_5270

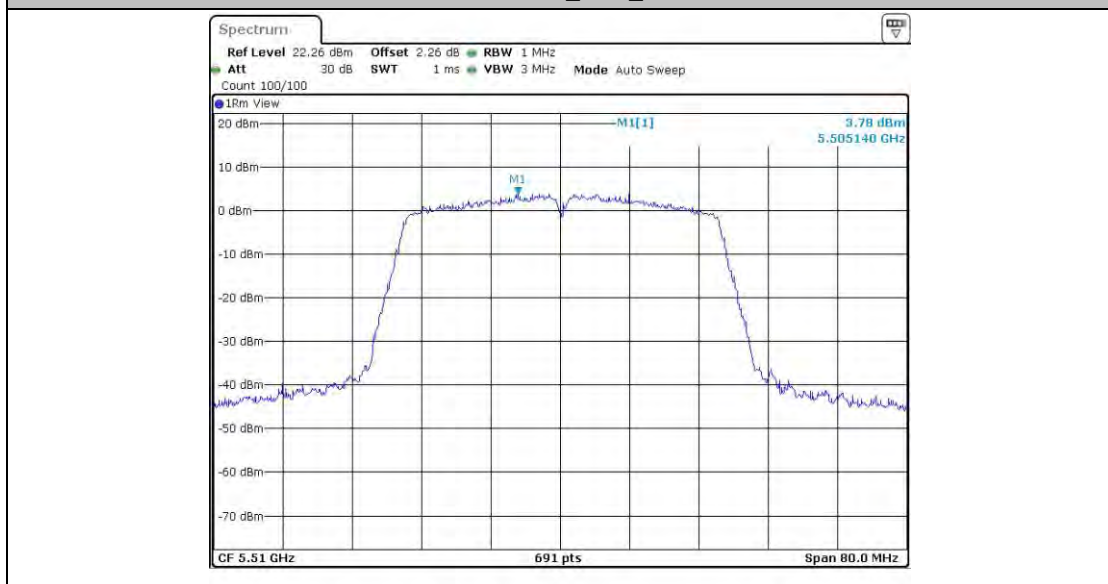




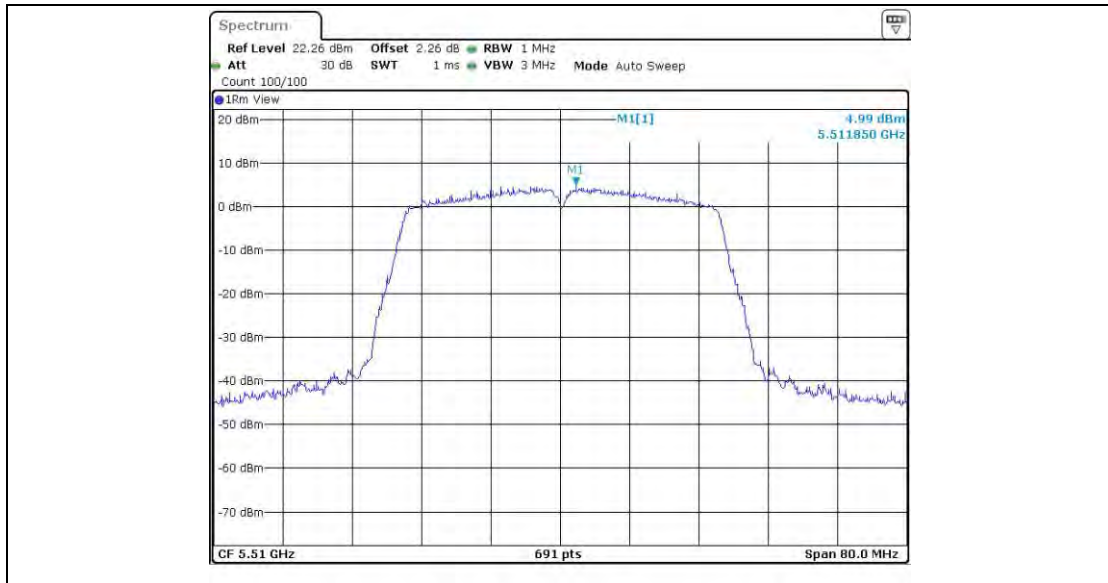
11AC40SISO\_Ant1\_5310



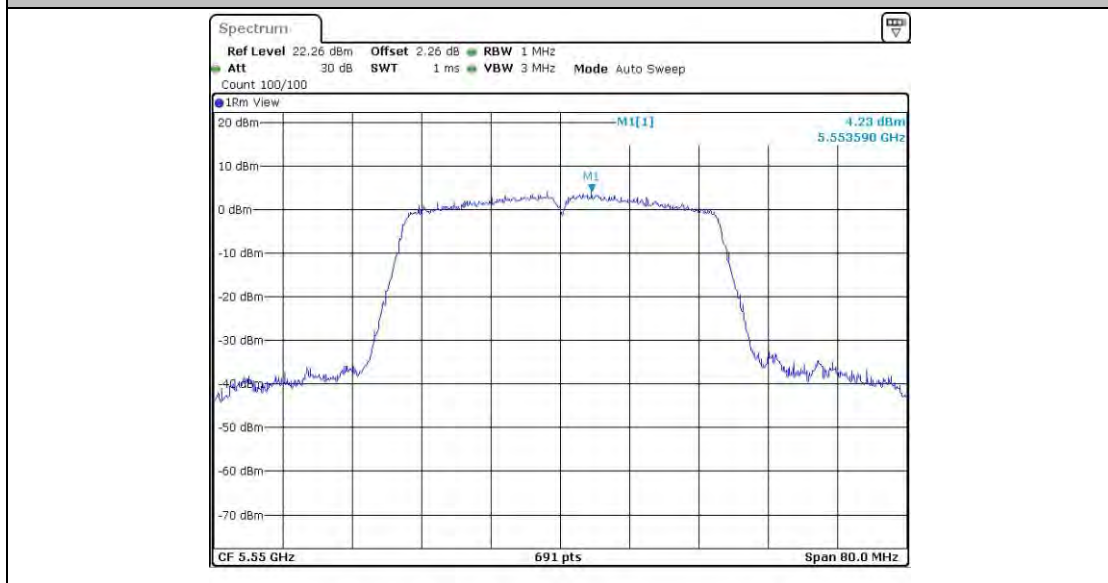
11AC40SISO\_Ant2\_5310



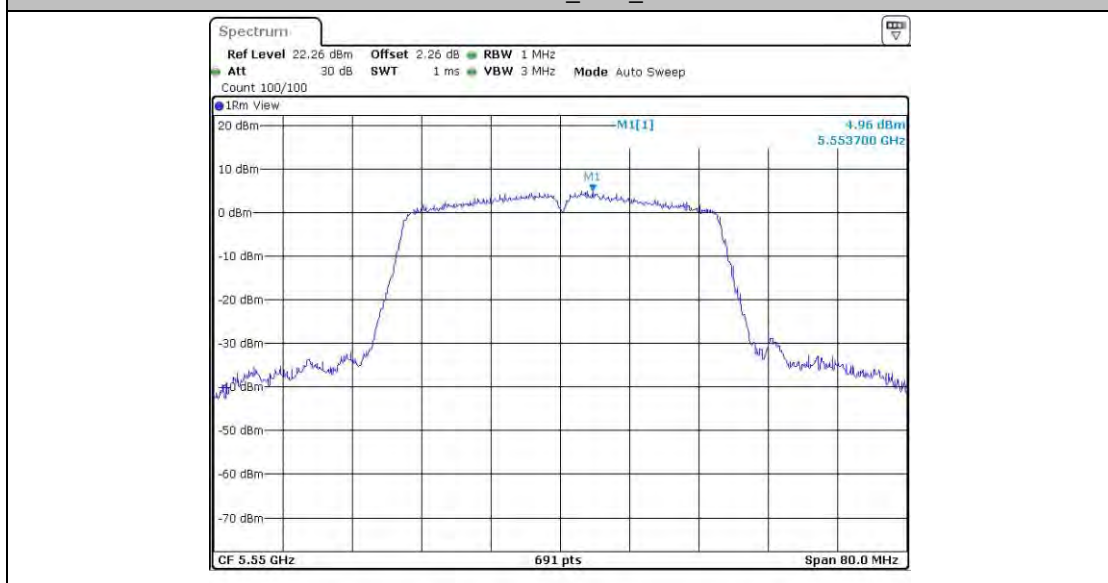
11AC40SISO\_Ant1\_5510



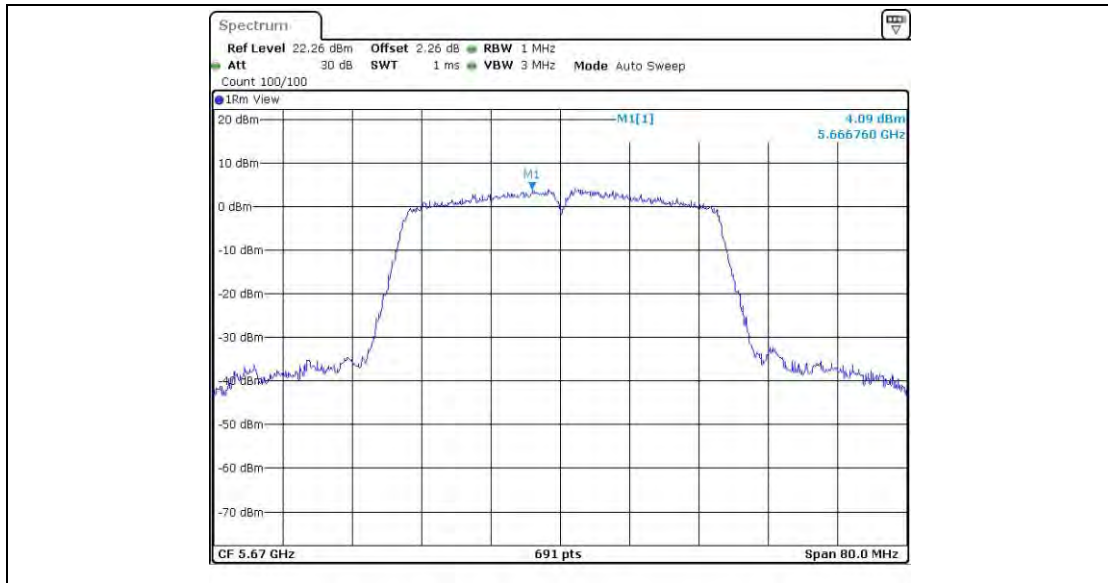
11AC40SISO\_Ant2\_5510



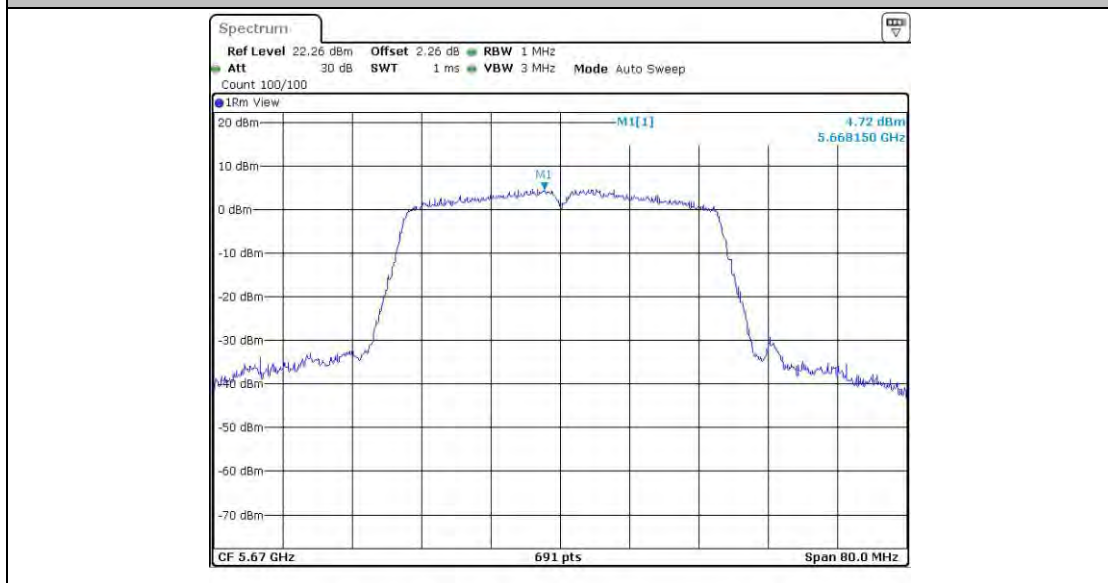
11AC40SISO\_Ant1\_5550



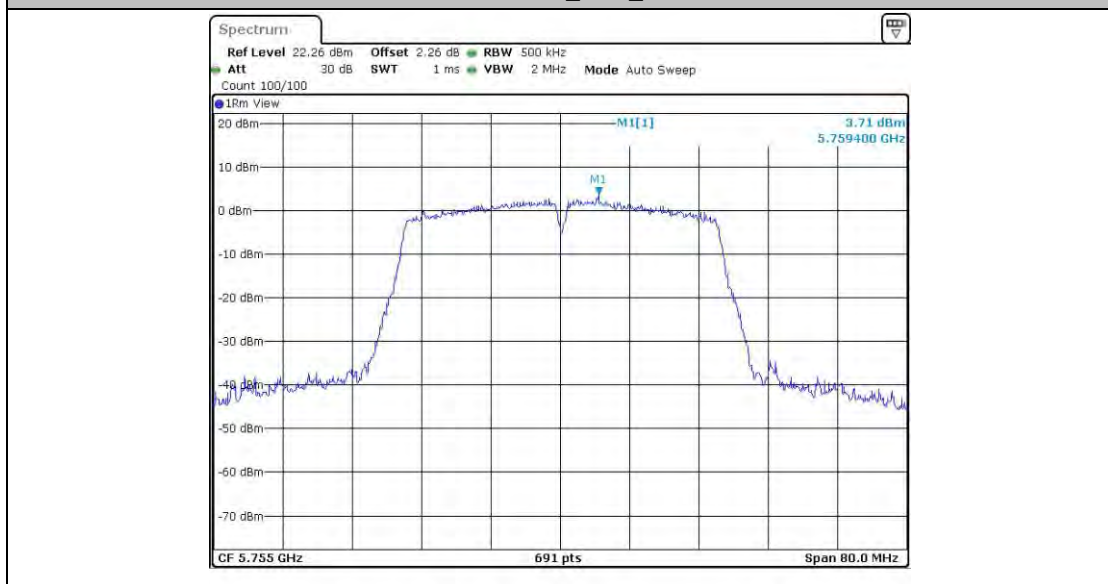
11AC40SISO\_Ant2\_5550



11AC40SISO\_Ant1\_5670

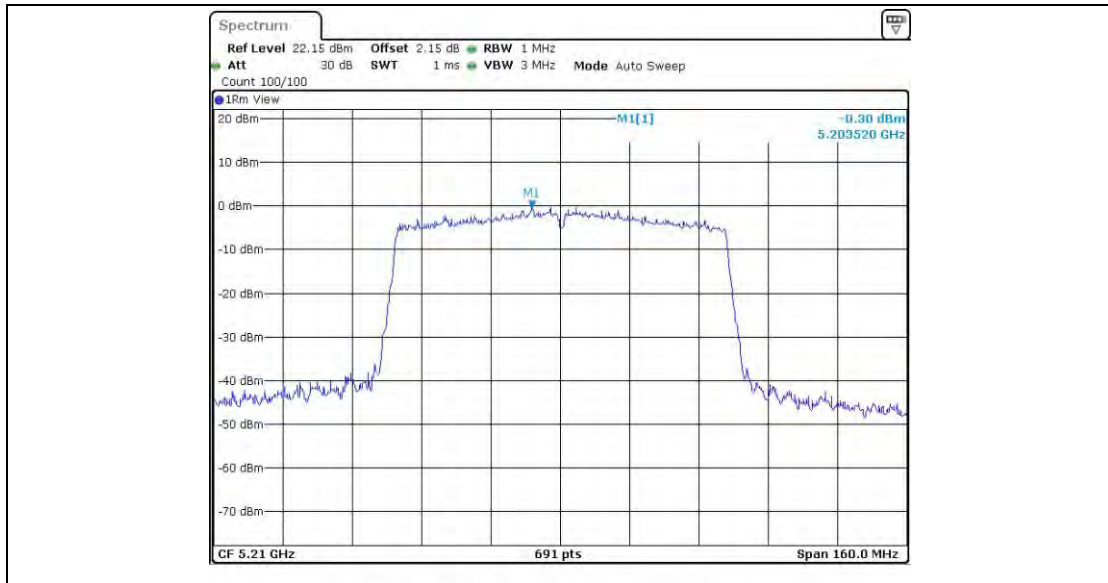


11AC40SISO\_Ant2\_5670

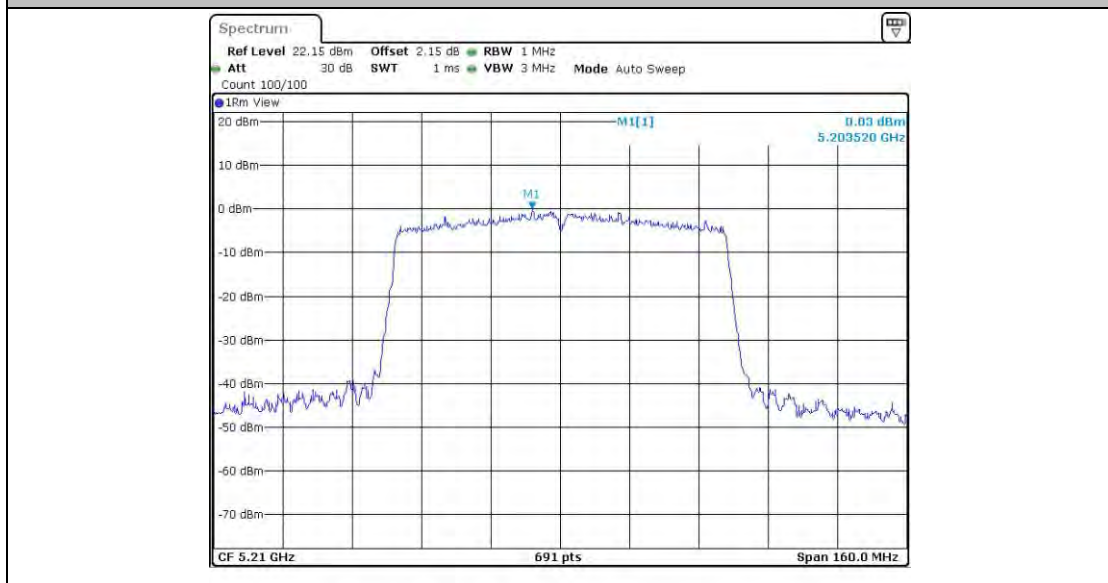


11AC40SISO\_Ant1\_5755

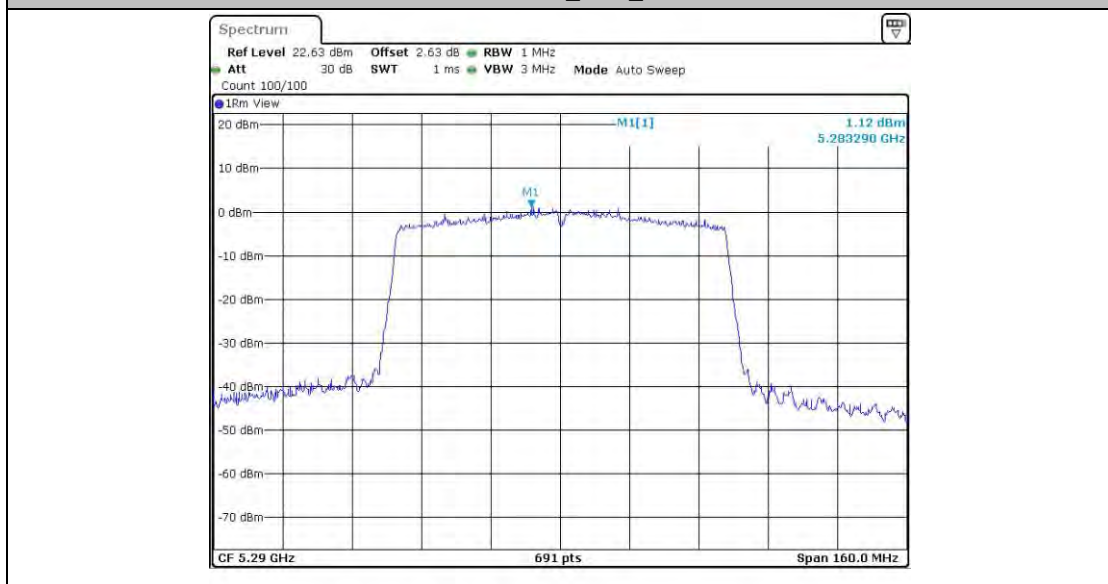




11AC80SISO\_Ant1\_5210

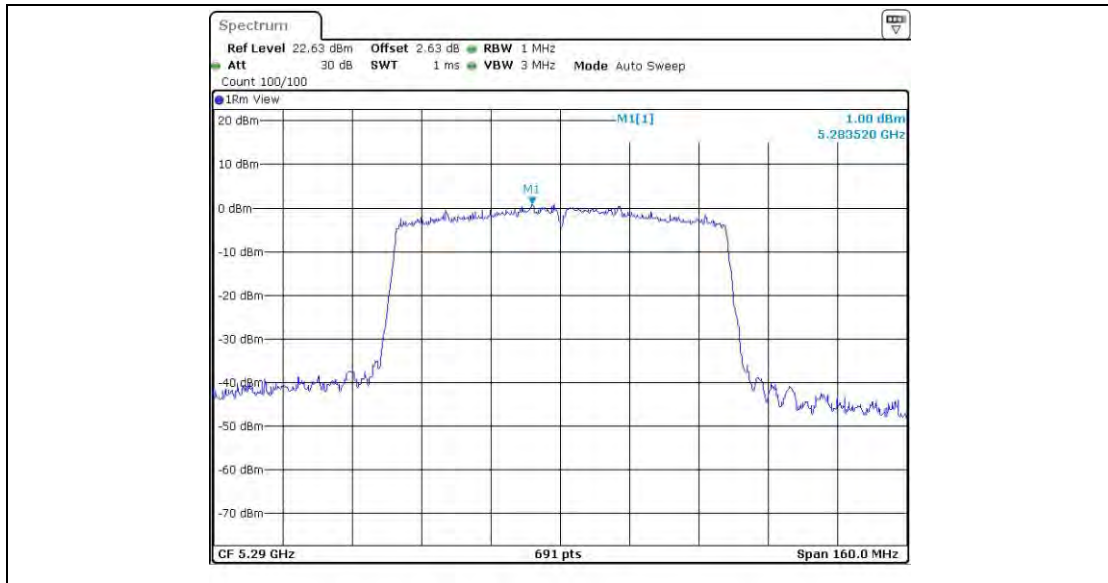


11AC80SISO\_Ant2\_5210

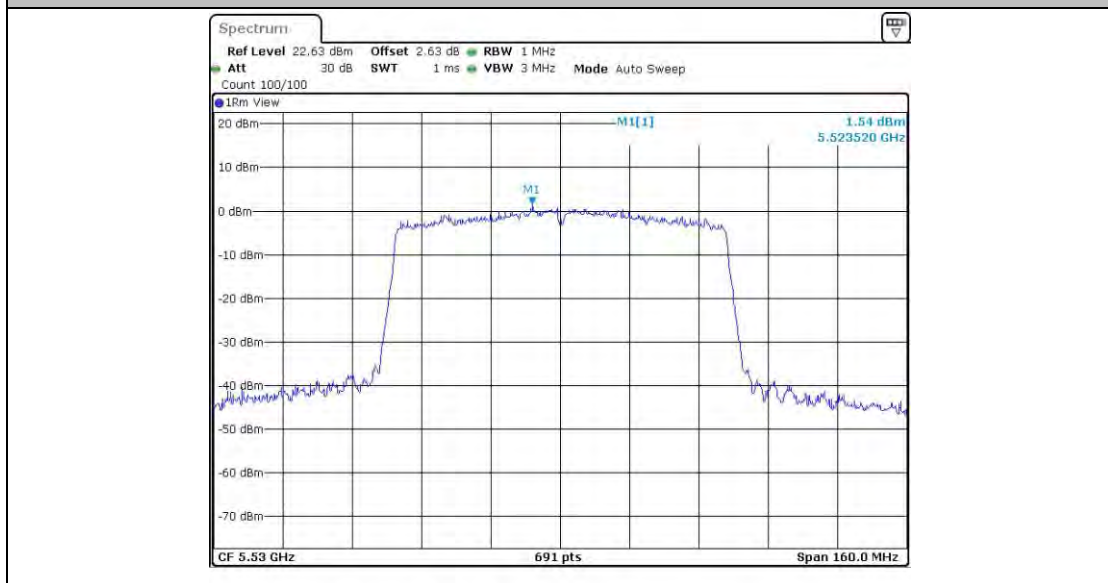


11AC80SISO\_Ant1\_5290

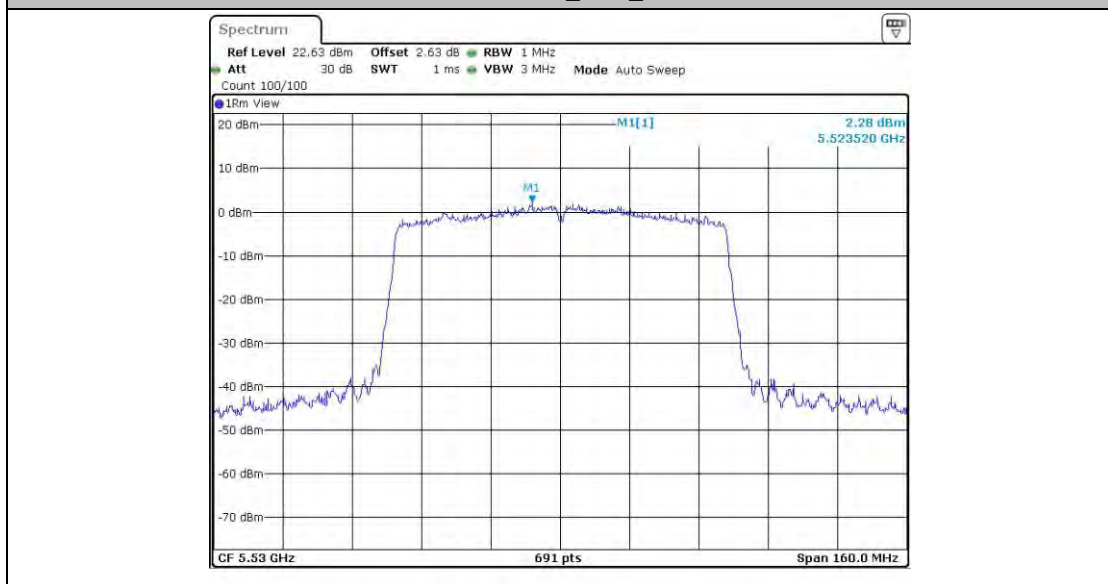




11AC80SISO\_Ant2\_5290

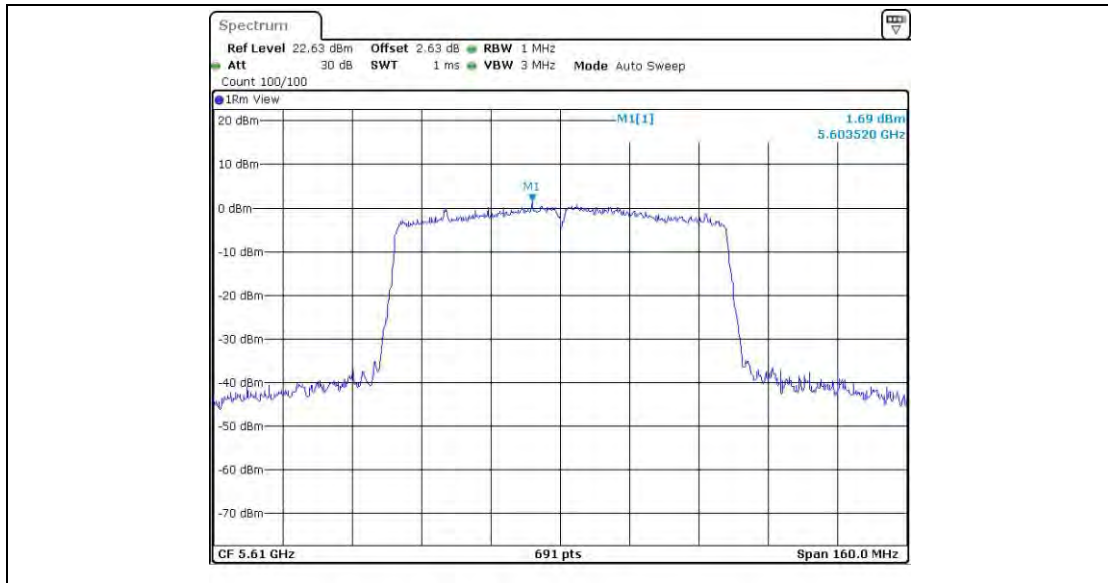


11AC80SISO\_Ant1\_5530

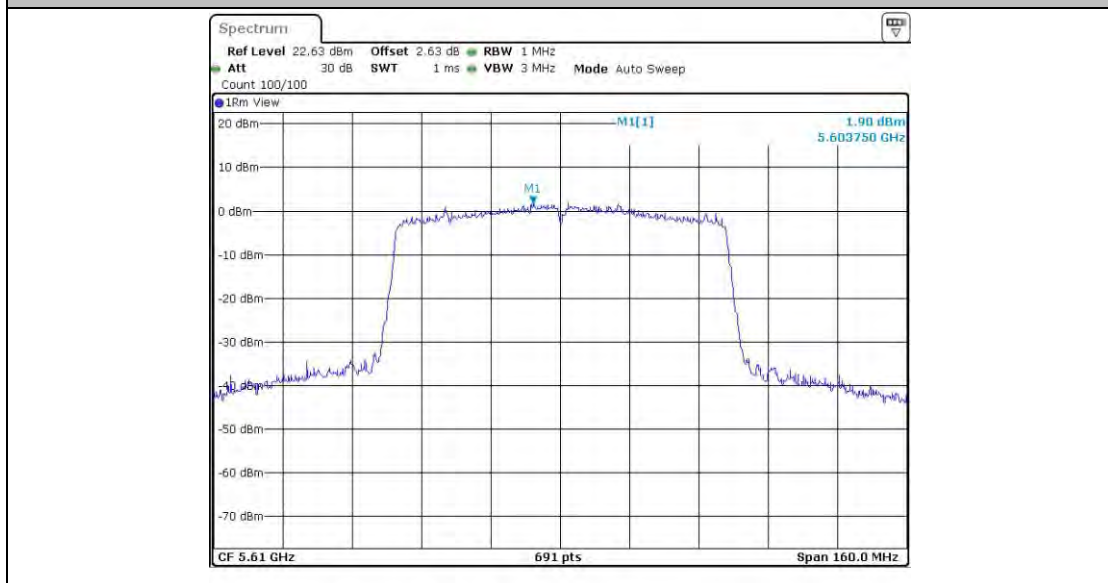


11AC80SISO\_Ant2\_5530

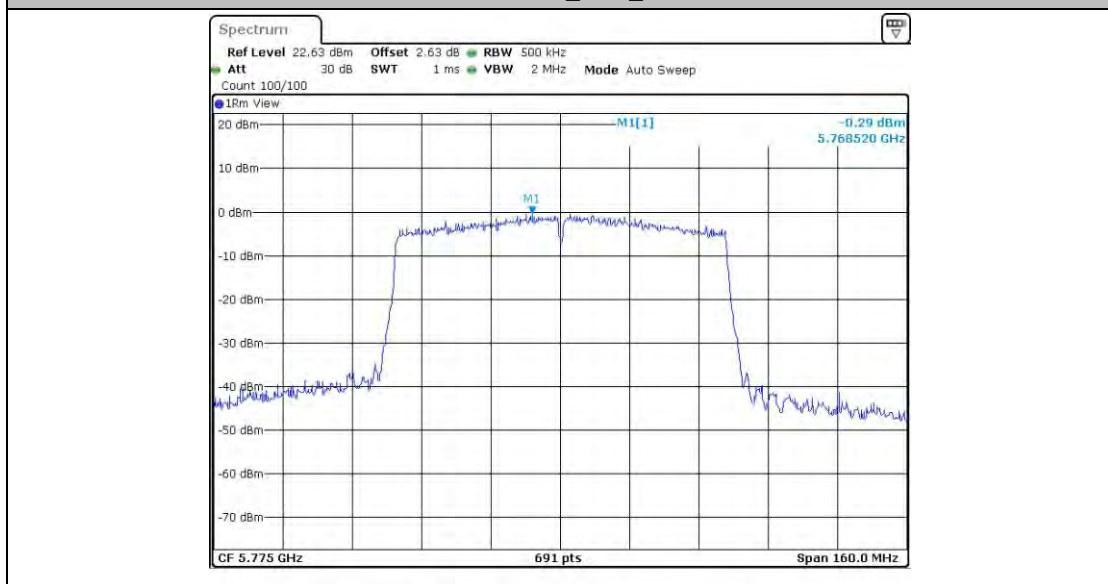




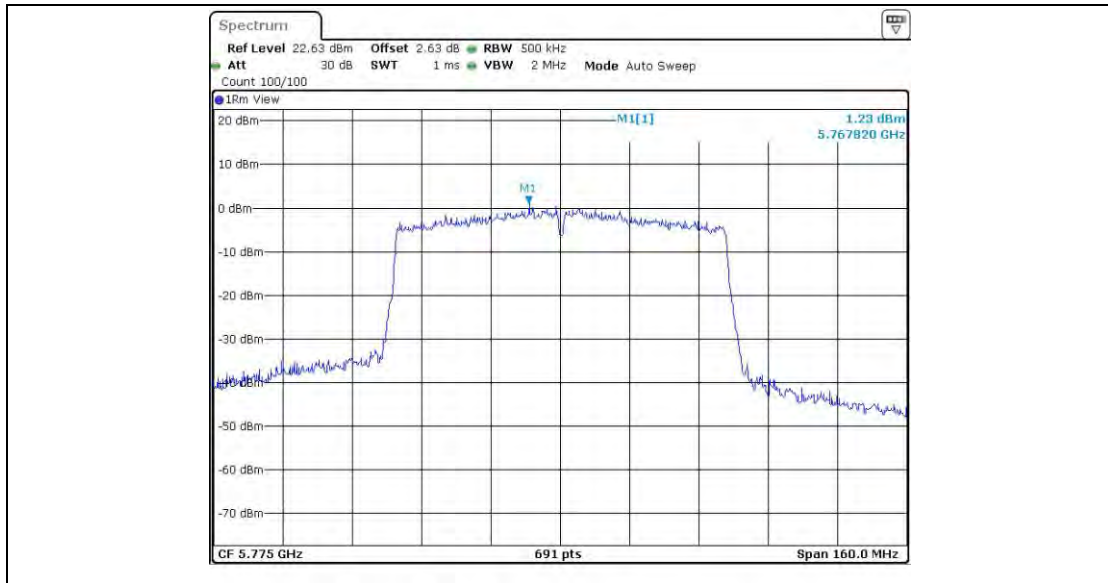
11AC80SISO\_Ant1\_5610



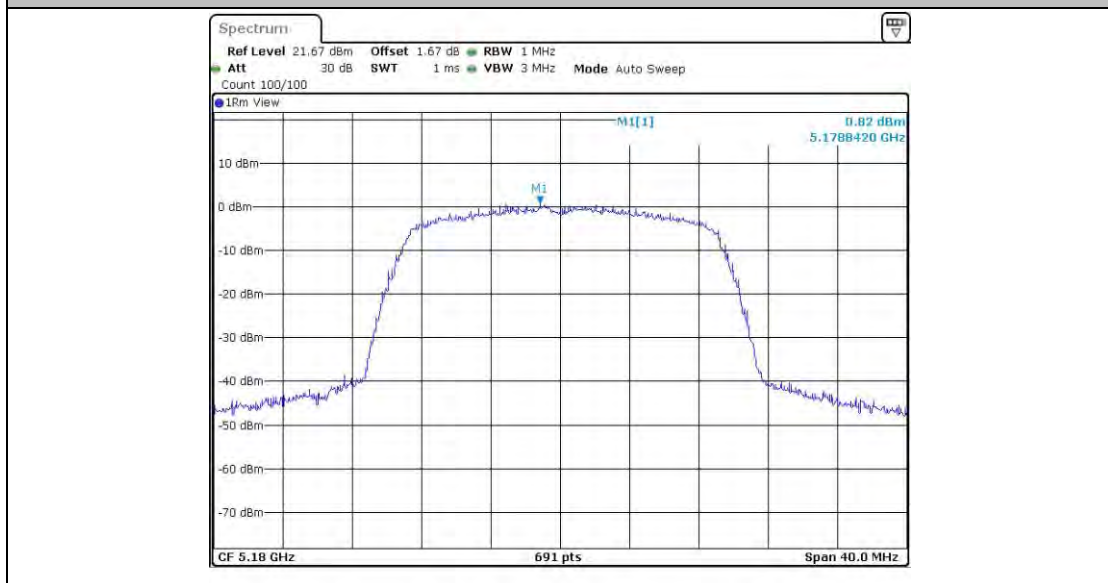
11AC80SISO\_Ant2\_5610



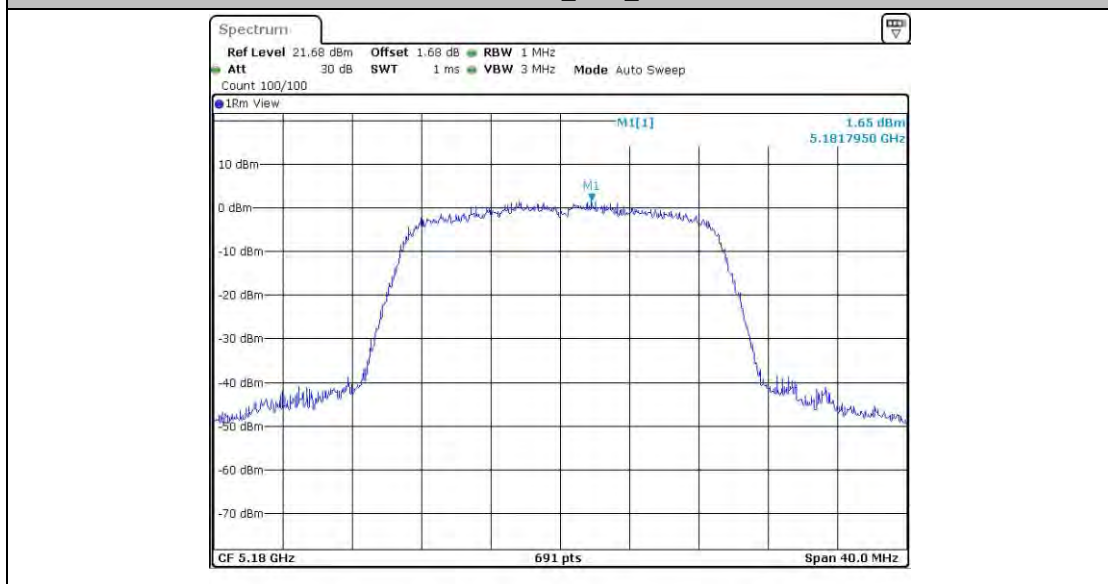
11AC80SISO\_Ant1\_5775



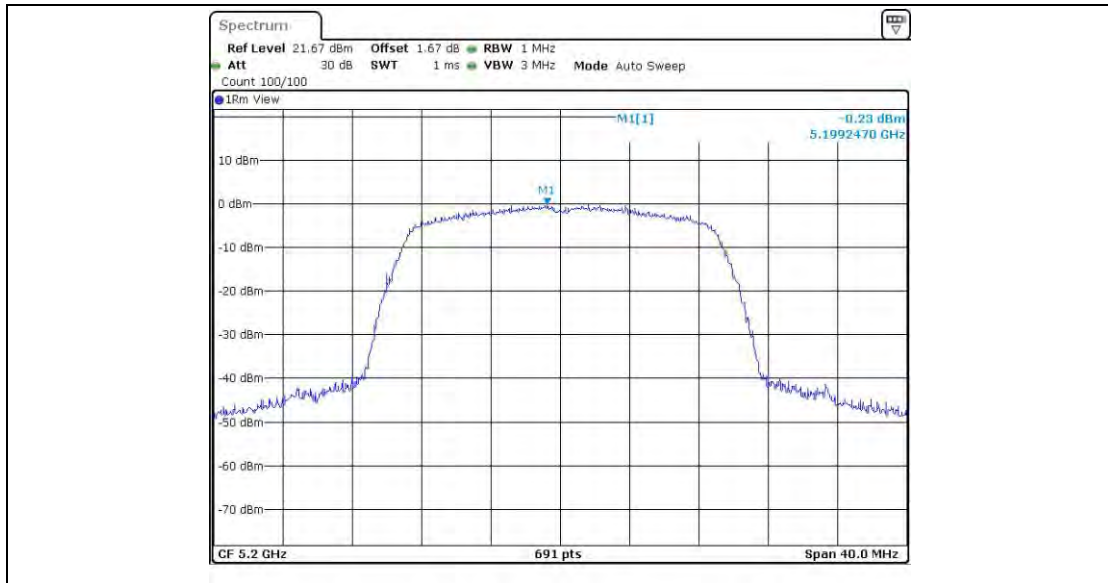
11AC80SISO\_Ant2\_5775



11N20MIMO\_Ant1\_5180



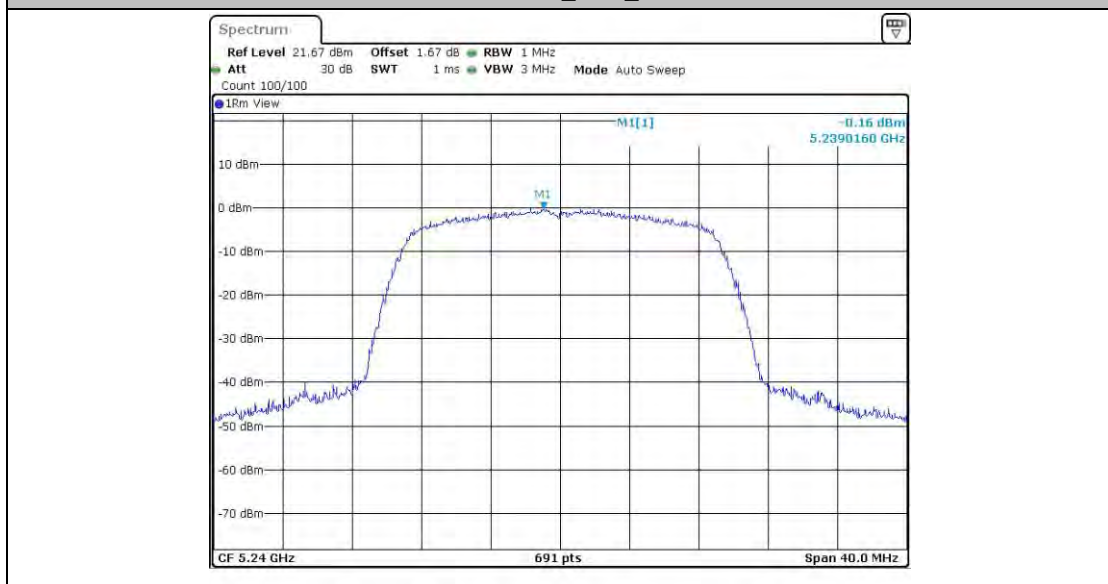
11N20MIMO\_Ant2\_5180



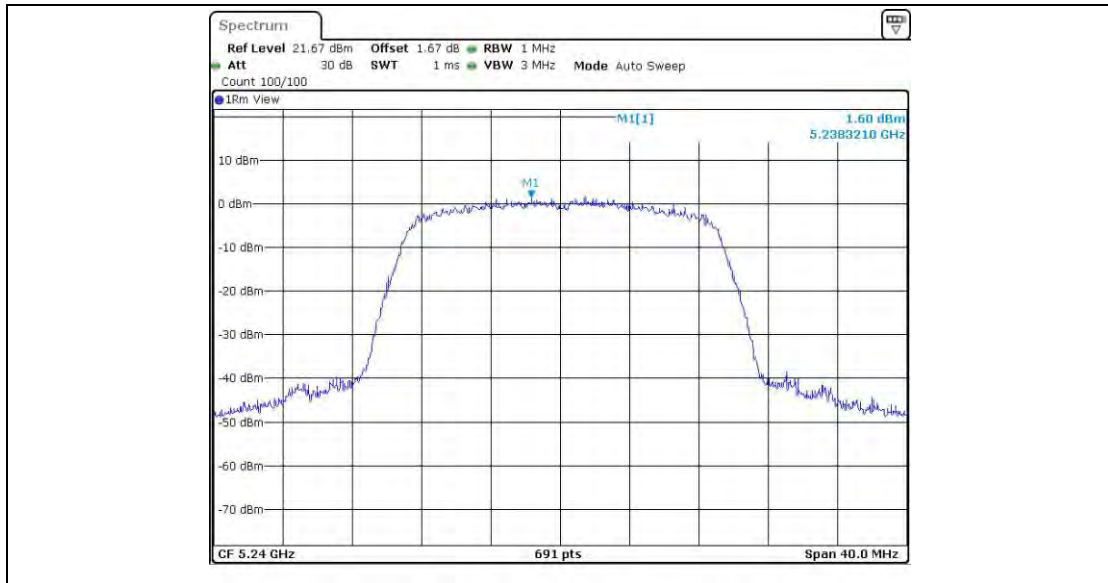
11N20MIMO\_Ant1\_5200



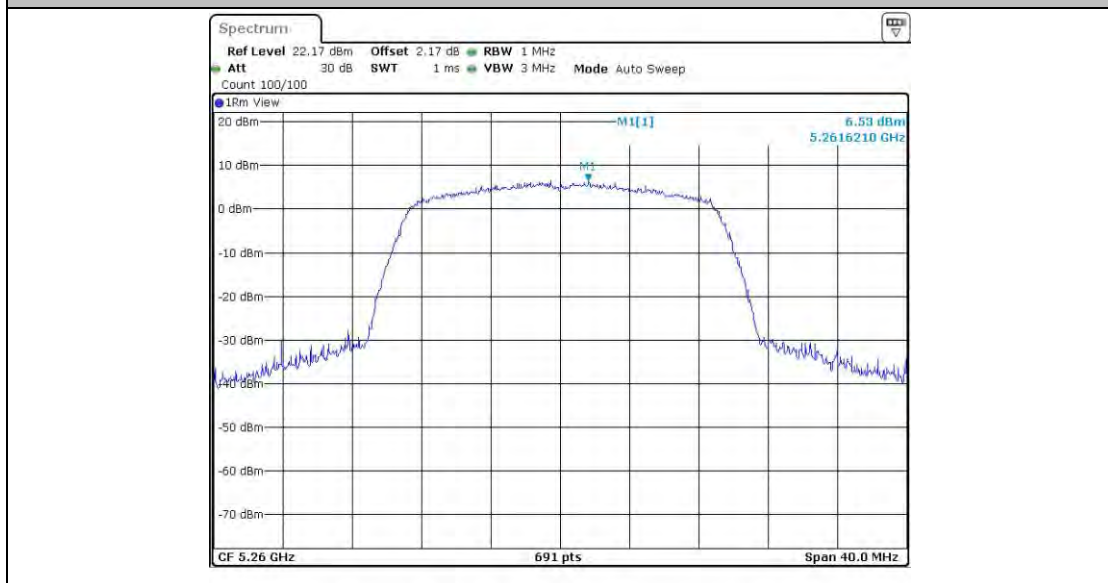
11N20MIMO\_Ant2\_5200



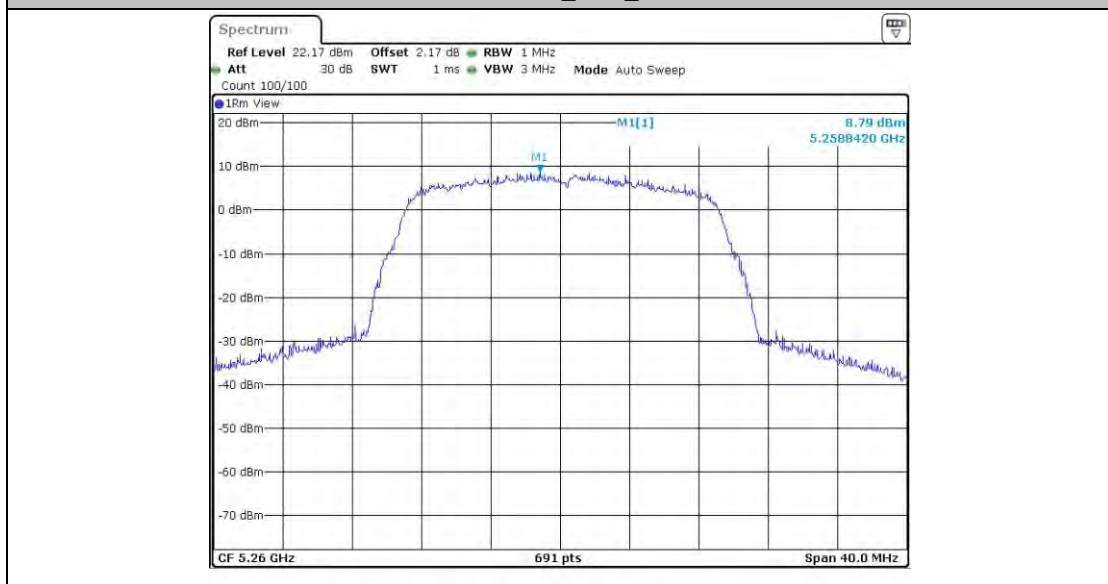
11N20MIMO\_Ant1\_5240



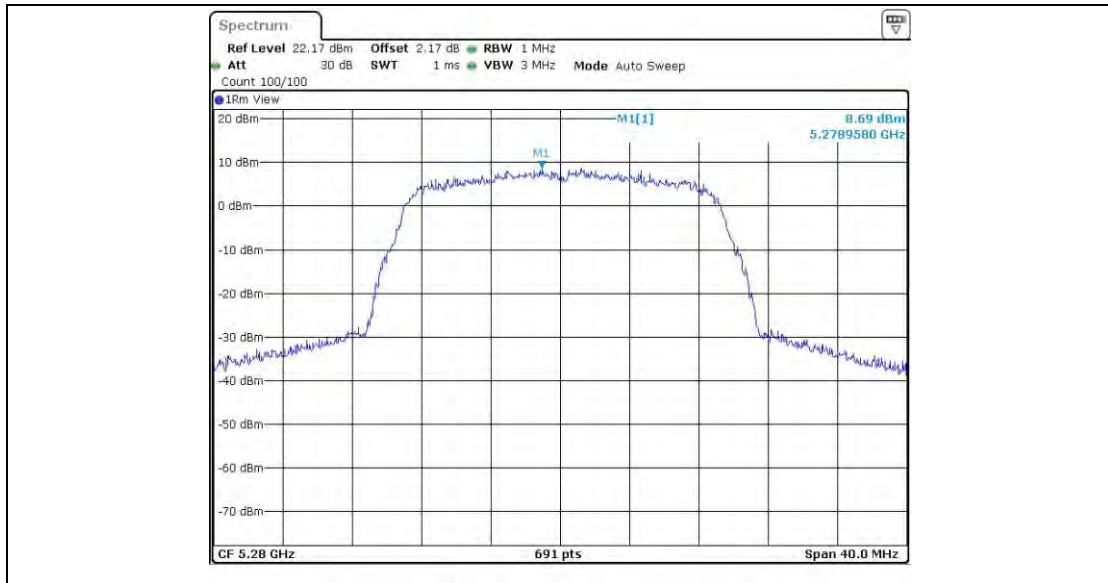
11N20MIMO\_Ant2\_5240



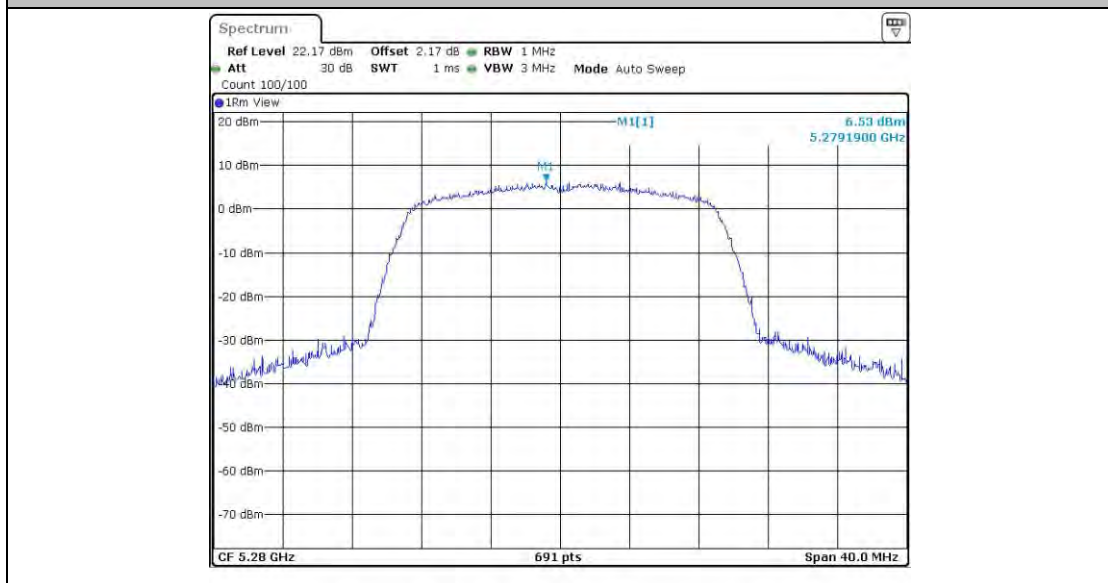
11N20MIMO\_Ant1\_5260



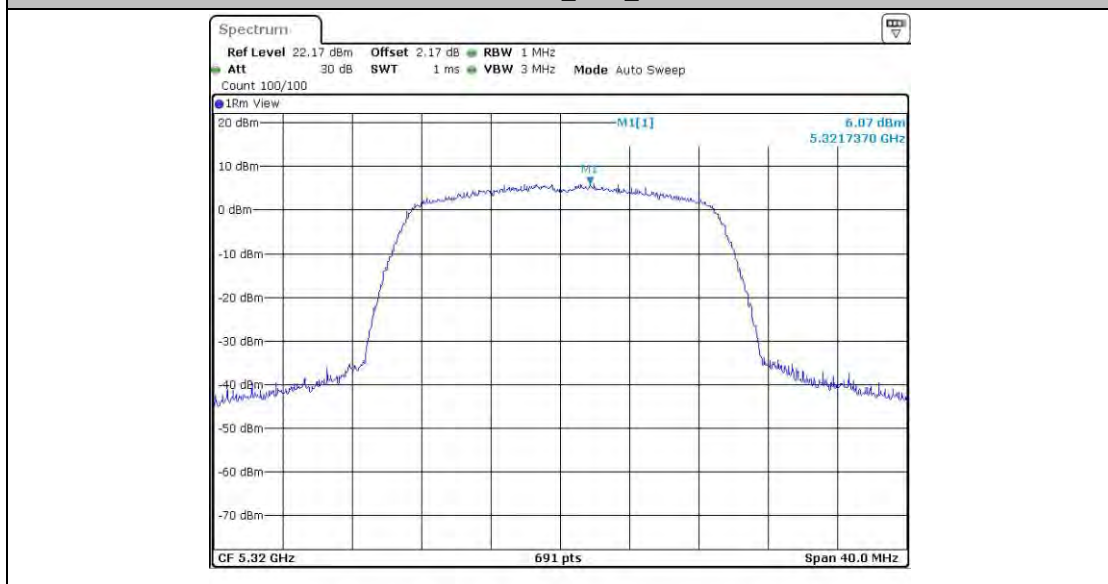
11N20MIMO\_Ant2\_5260



11N20MIMO\_Ant1\_5280

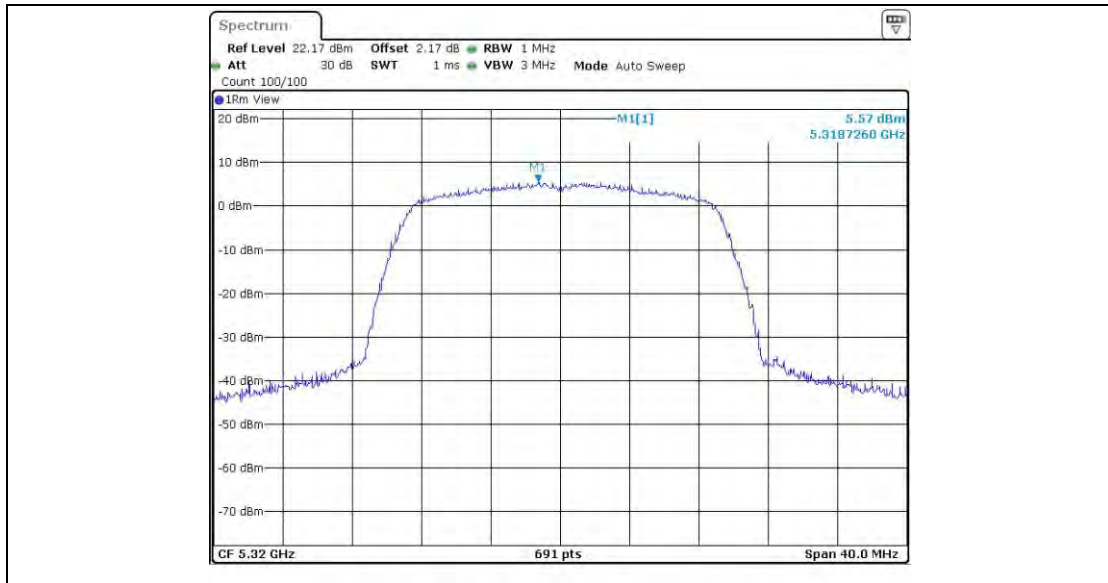


11N20MIMO\_Ant2\_5280

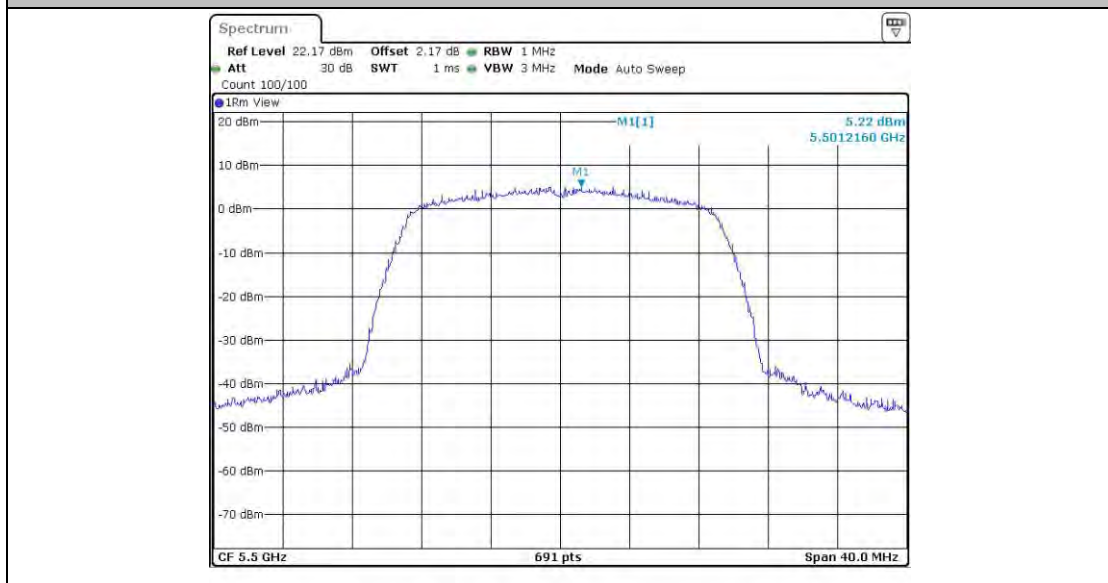


11N20MIMO\_Ant1\_5320

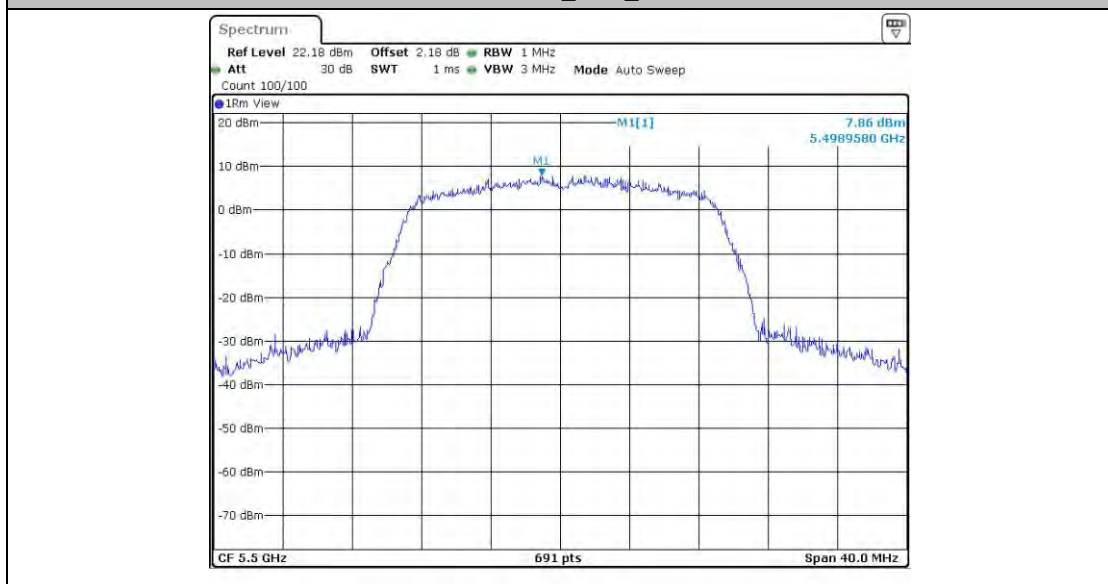




11N20MIMO\_Ant2\_5320

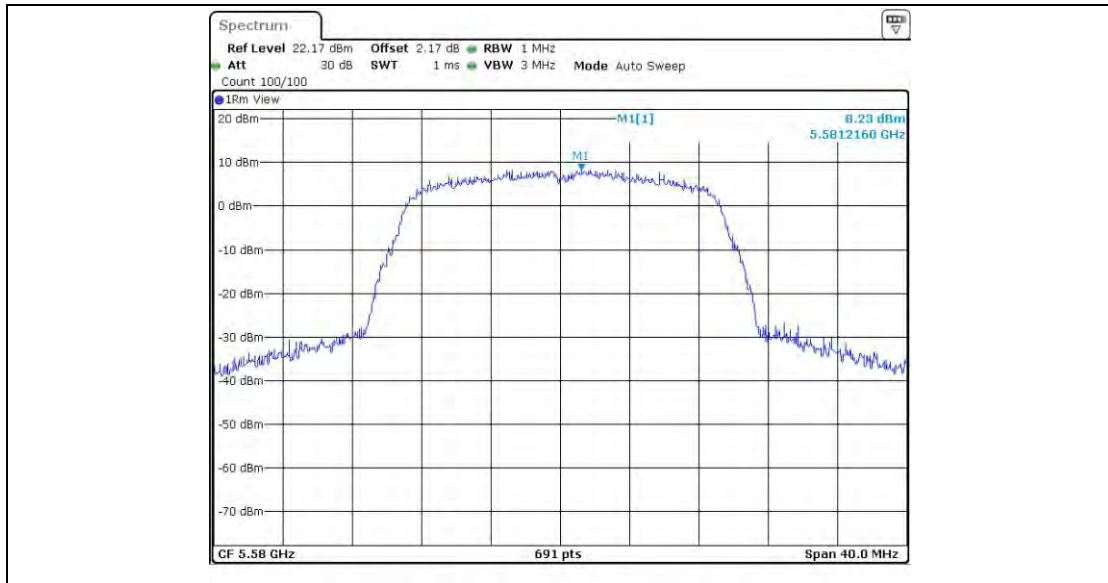


11N20MIMO\_Ant1\_5500

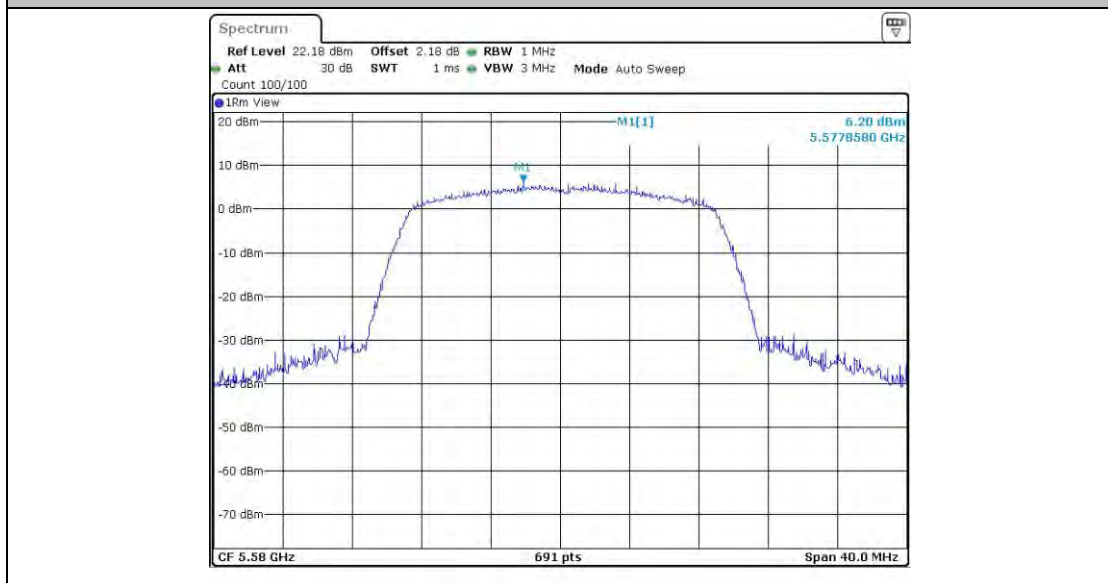


11N20MIMO\_Ant2\_5500

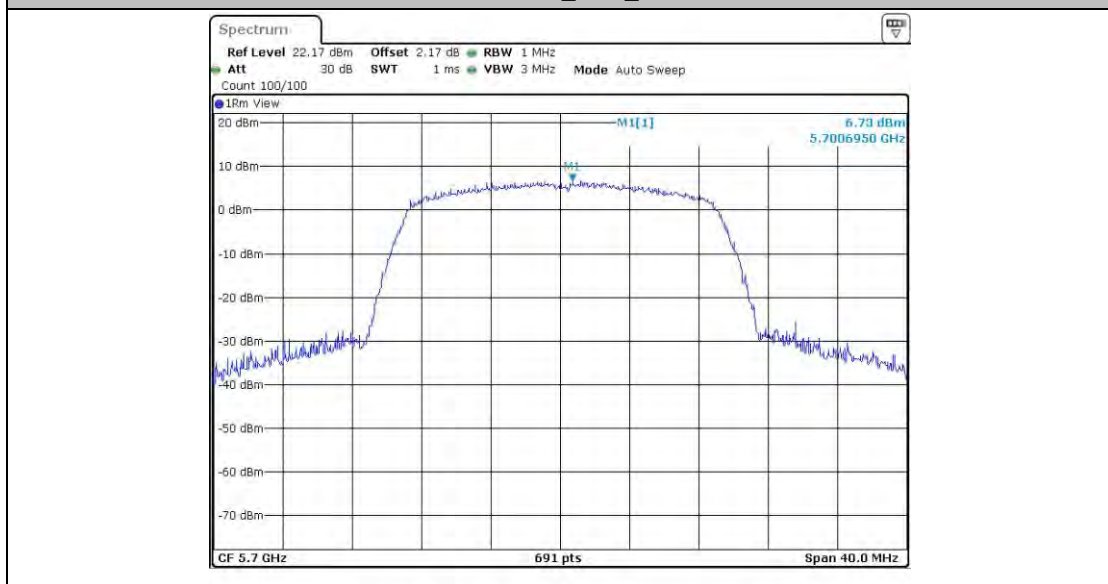




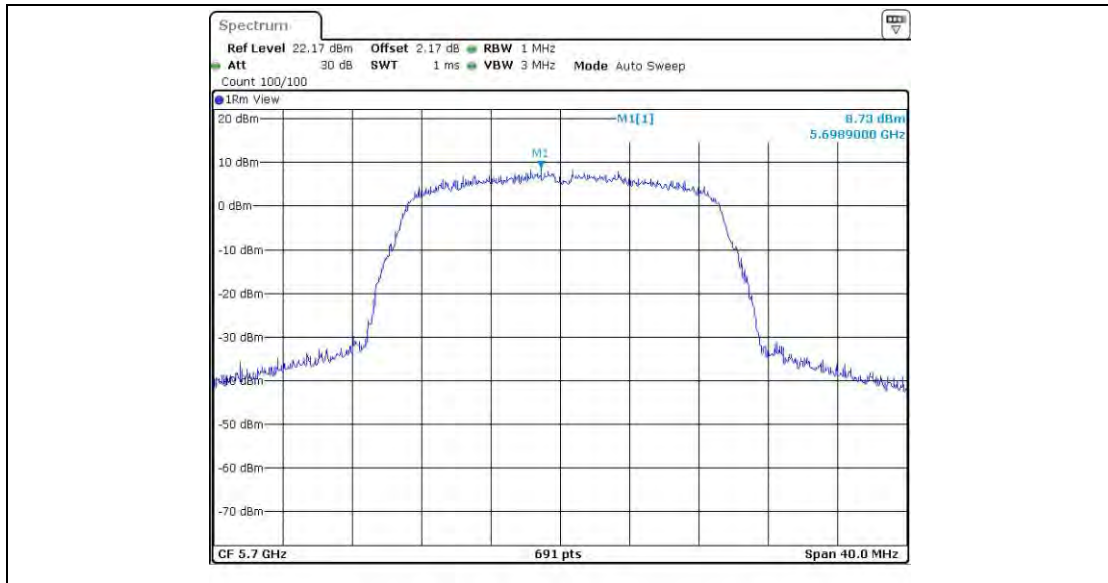
11N20MIMO\_Ant1\_5580



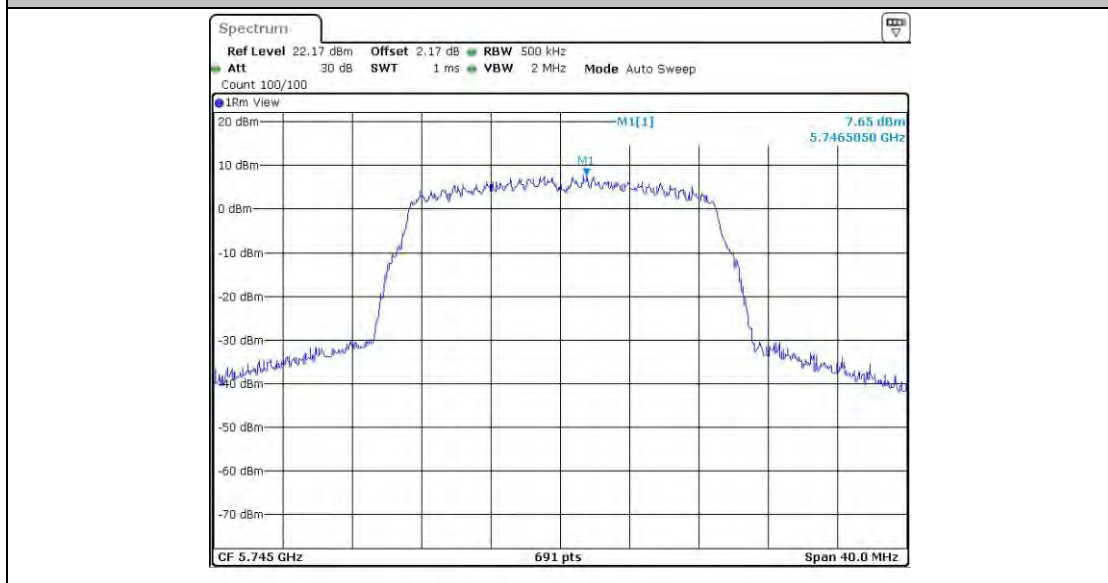
11N20MIMO\_Ant2\_5580



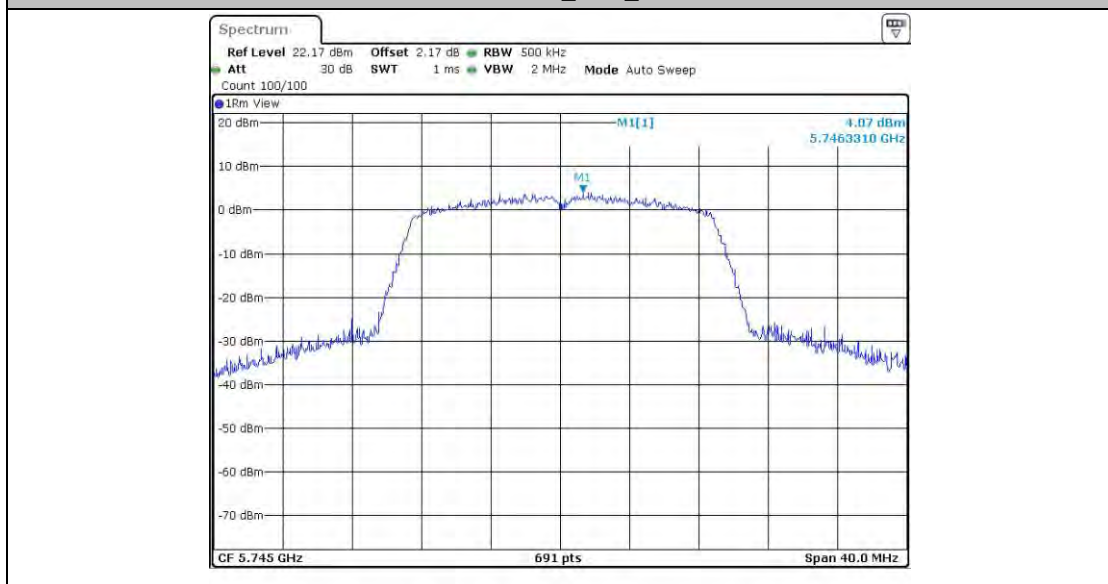
11N20MIMO\_Ant1\_5700



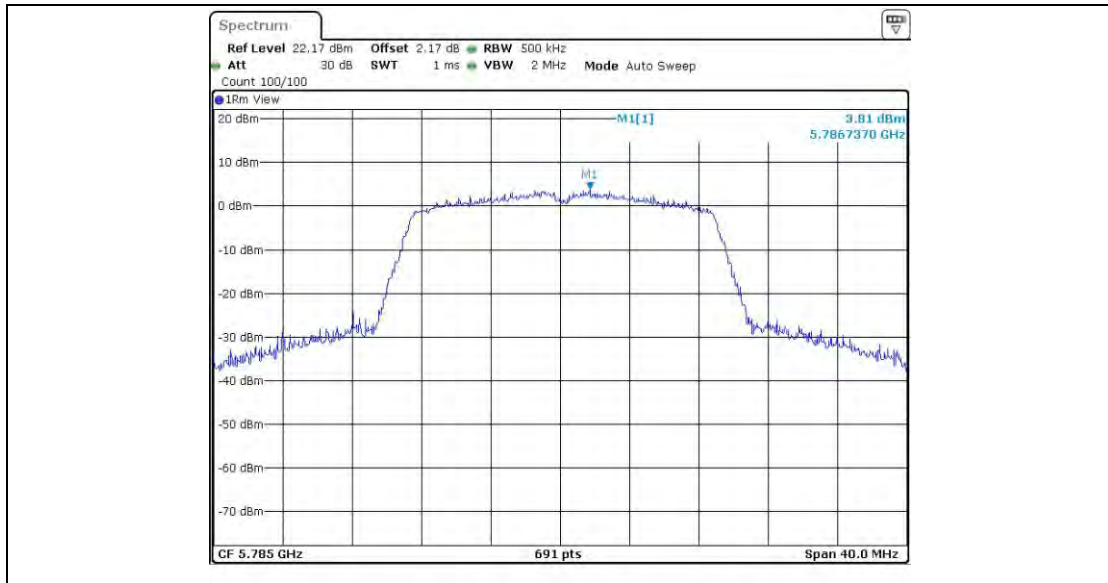
11N20MIMO\_Ant2\_5700



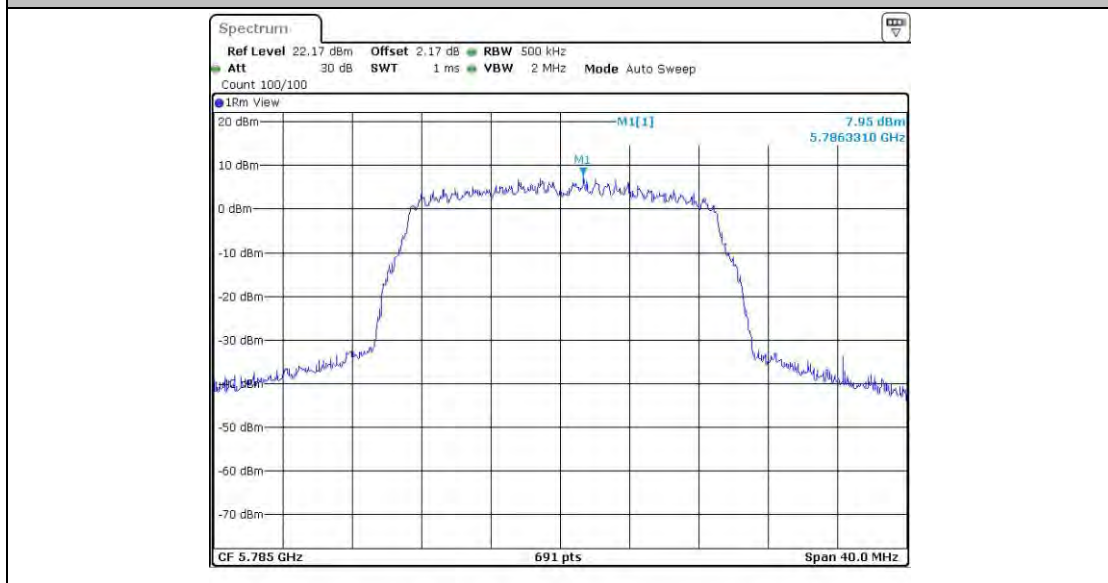
11N20MIMO\_Ant1\_5745



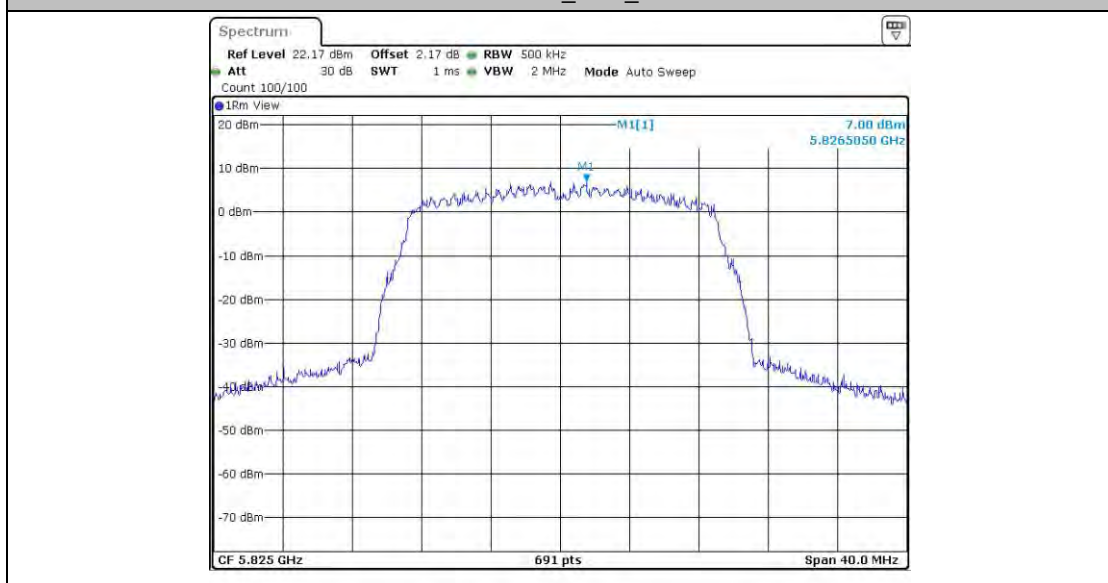
11N20MIMO\_Ant2\_5745



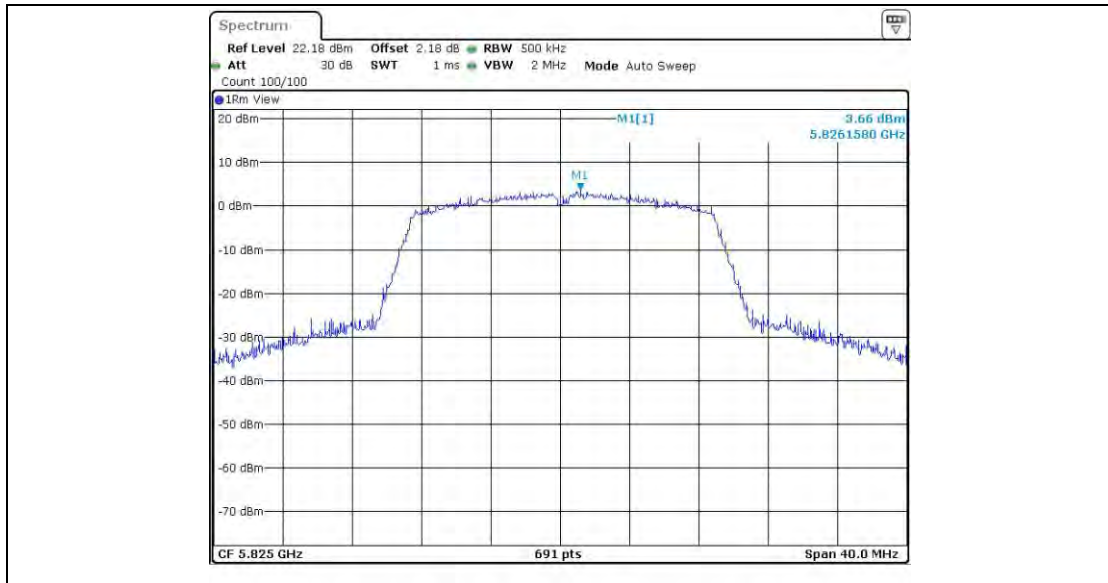
11N20MIMO\_Ant1\_5785



11N20MIMO\_Ant2\_5785



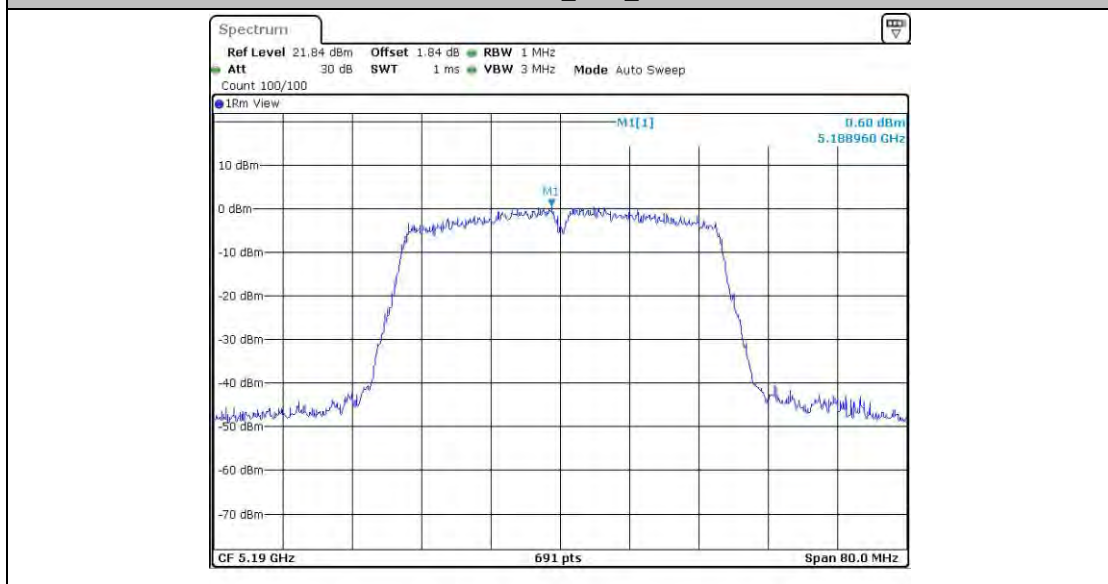
11N20MIMO\_Ant1\_5825



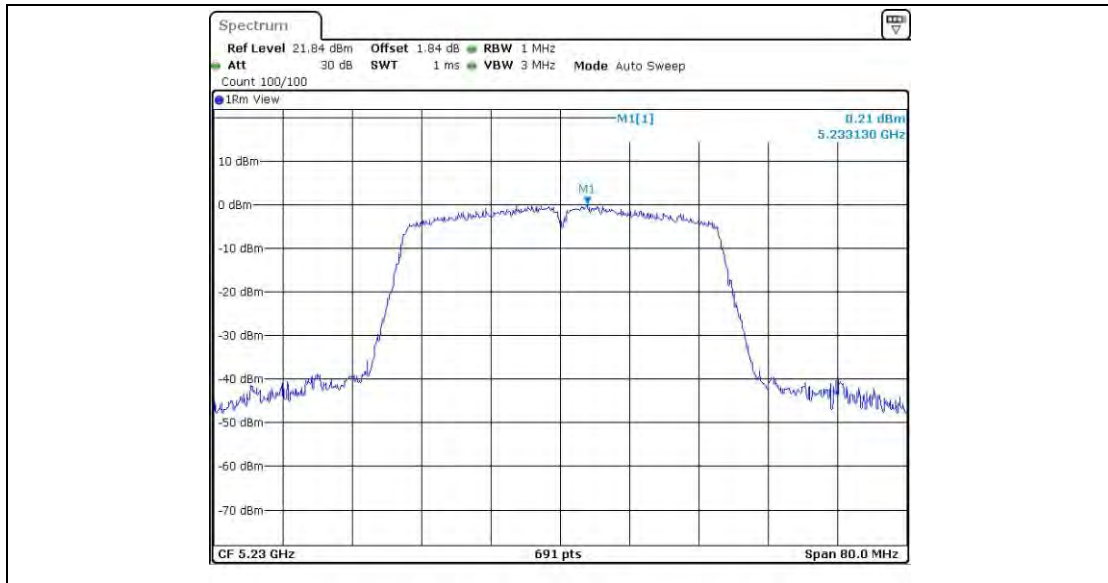
11N20MIMO\_Ant2\_5825



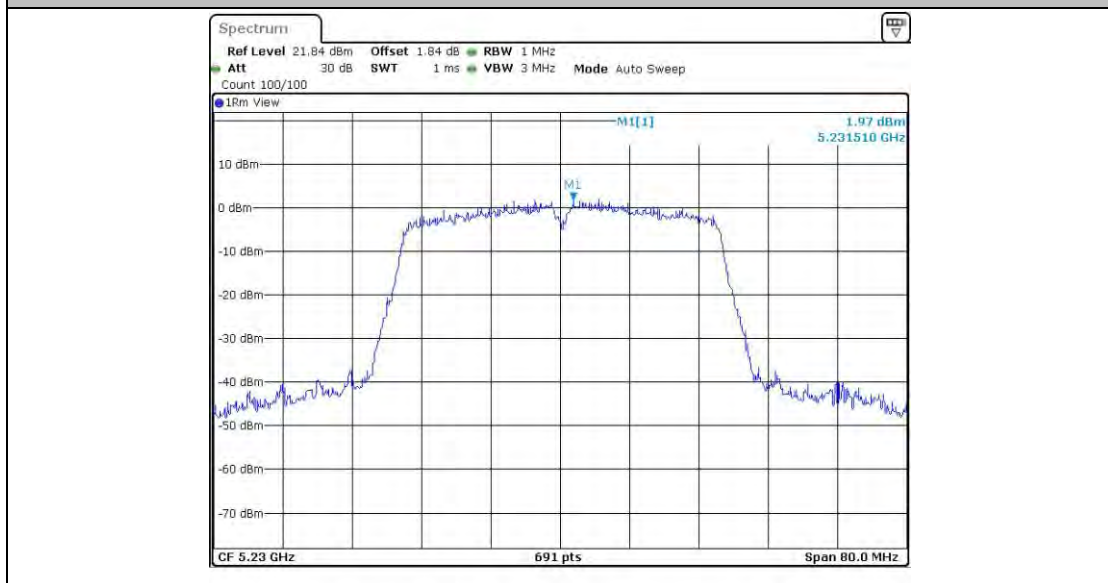
11N40MIMO\_Ant1\_5190



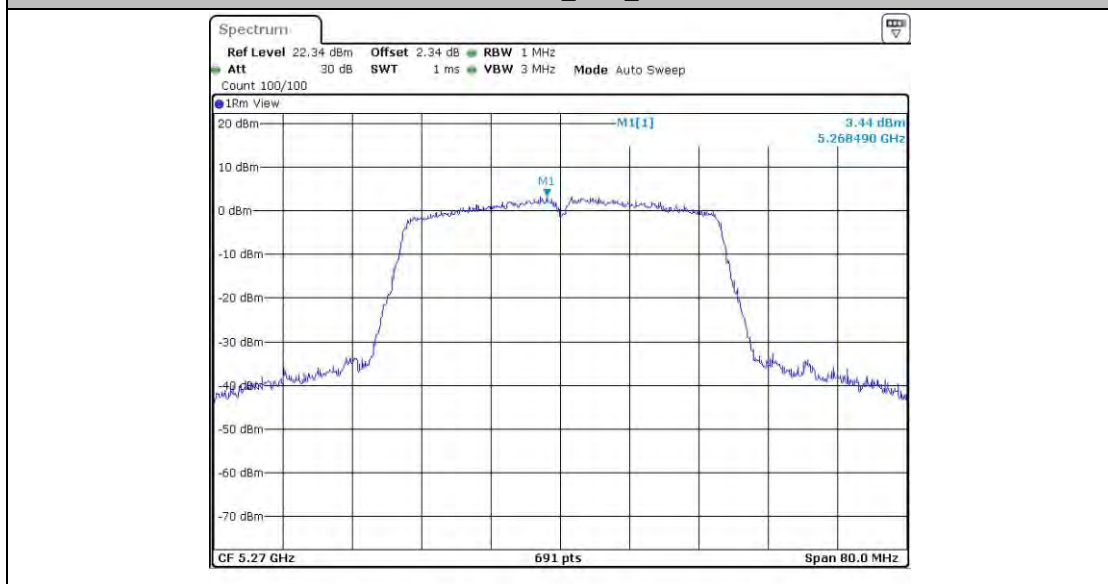
11N40MIMO\_Ant2\_5190



11N40MIMO\_Ant1\_5230

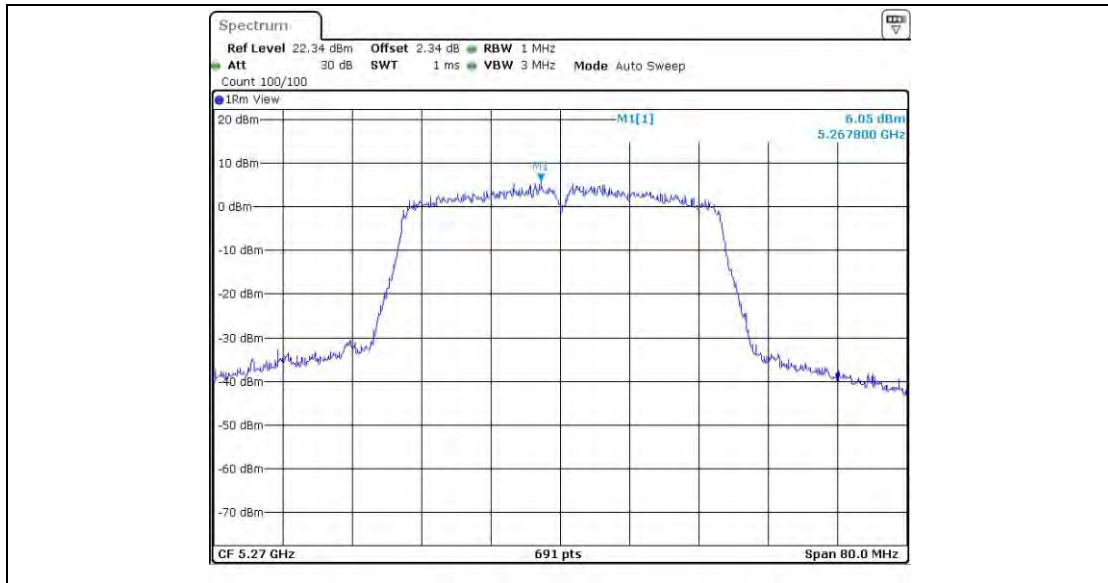


11N40MIMO\_Ant2\_5230

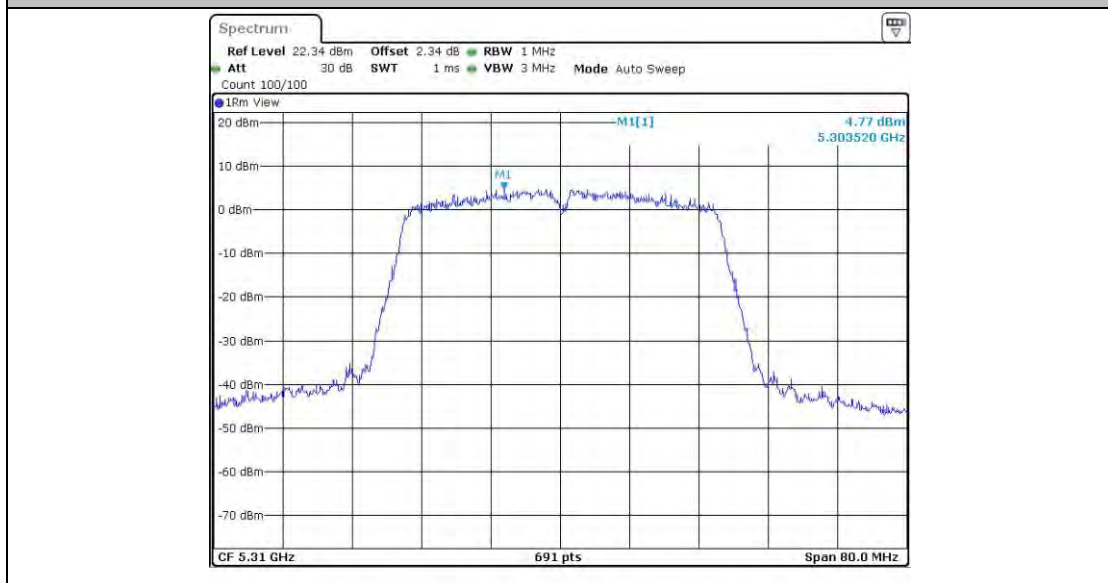


11N40MIMO\_Ant1\_5270

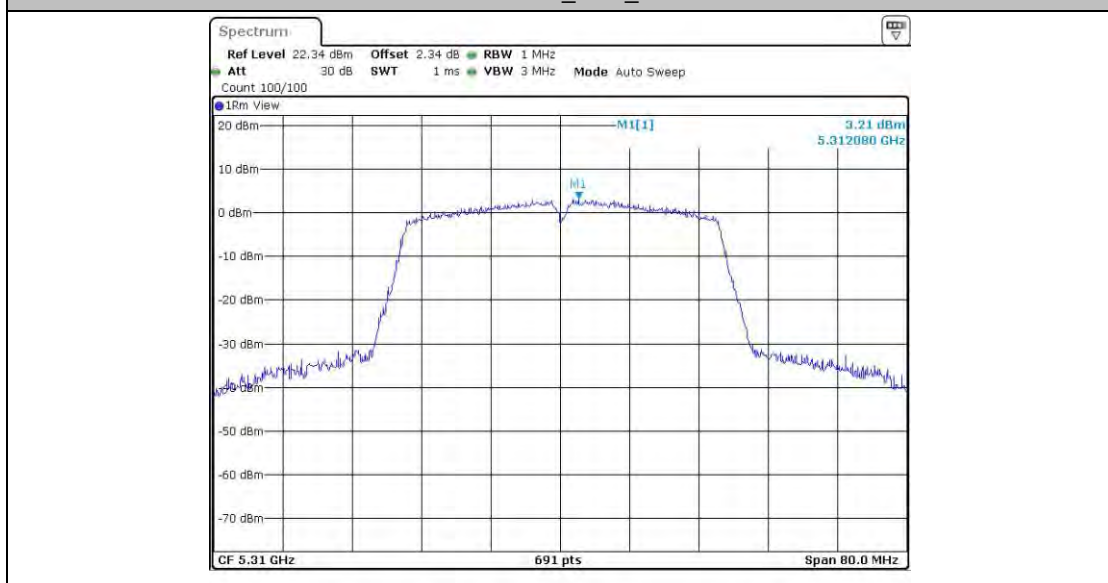




11N40MIMO\_Ant2\_5270



11N40MIMO\_Ant1\_5310



11N40MIMO\_Ant2\_5310