

## Appendix B: Maximum conducted output power

### Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	6.63	<=24	PASS
	Ant2	5180	6.68	<=24	PASS
	Ant1	5200	6.45	<=24	PASS
	Ant2	5200	6.58	<=24	PASS
	Ant1	5240	6.42	<=24	PASS
	Ant2	5240	6.59	<=24	PASS
	Ant1	5260	12.72	<=23.98	PASS
	Ant2	5260	13.36	<=23.98	PASS
	Ant1	5280	12.76	<=23.98	PASS
	Ant2	5280	13.47	<=23.98	PASS
	Ant1	5320	12.81	<=23.98	PASS
	Ant2	5320	13.42	<=23.98	PASS
	Ant1	5500	13.51	<=23.98	PASS
	Ant2	5500	13.31	<=23.98	PASS
	Ant1	5580	12.52	<=23.98	PASS
	Ant2	5580	13.16	<=23.98	PASS
	Ant1	5700	12.94	<=23.98	PASS
	Ant2	5700	14.12	<=23.98	PASS
	Ant1	5745	12.67	<=30	PASS
	Ant2	5745	14.07	<=30	PASS
	Ant1	5785	12.66	<=30	PASS
	Ant2	5785	13.71	<=30	PASS
	Ant1	5825	12.68	<=30	PASS
	Ant2	5825	13.38	<=30	PASS
11N20SISO	Ant1	5180	5.60	<=24	PASS
	Ant2	5180	5.83	<=24	PASS
	Ant1	5200	5.52	<=24	PASS
	Ant2	5200	5.68	<=24	PASS
	Ant1	5240	5.54	<=24	PASS
	Ant2	5240	5.73	<=24	PASS
	Ant1	5260	11.72	<=23.98	PASS
	Ant2	5260	12.00	<=23.98	PASS
	Ant1	5280	11.84	<=23.98	PASS
	Ant2	5280	12.05	<=23.98	PASS
	Ant1	5320	11.90	<=23.98	PASS
	Ant2	5320	12.05	<=23.98	PASS
	Ant1	5500	12.53	<=23.98	PASS
	Ant2	5500	12.90	<=23.98	PASS
	Ant1	5580	12.10	<=23.98	PASS
	Ant2	5580	12.79	<=23.98	PASS

	Ant1	5700	11.79	<=23.98	PASS
	Ant2	5700	12.87	<=23.98	PASS
	Ant1	5745	11.72	<=30	PASS
	Ant2	5745	12.77	<=30	PASS
	Ant1	5785	11.57	<=30	PASS
	Ant2	5785	12.56	<=30	PASS
	Ant1	5825	11.51	<=30	PASS
	Ant2	5825	12.14	<=30	PASS
11N40SISO	Ant1	5190	8.62	<=24	PASS
	Ant2	5190	8.65	<=24	PASS
	Ant1	5230	8.68	<=24	PASS
	Ant2	5230	8.74	<=24	PASS
	Ant1	5270	12.03	<=23.98	PASS
	Ant2	5270	12.26	<=23.98	PASS
	Ant1	5310	12.09	<=23.98	PASS
	Ant2	5310	12.07	<=23.98	PASS
	Ant1	5510	12.70	<=23.98	PASS
	Ant2	5510	13.10	<=23.98	PASS
	Ant1	5550	12.50	<=23.98	PASS
	Ant2	5550	12.99	<=23.98	PASS
	Ant1	5670	12.09	<=23.98	PASS
	Ant2	5670	13.09	<=23.98	PASS
	Ant1	5755	11.94	<=30	PASS
	Ant2	5755	12.84	<=30	PASS
	Ant1	5795	12.99	<=30	PASS
	Ant2	5795	12.53	<=30	PASS
11AC20SISO	Ant1	5180	5.73	<=24	PASS
	Ant2	5180	5.85	<=24	PASS
	Ant1	5200	5.55	<=24	PASS
	Ant2	5200	5.71	<=24	PASS
	Ant1	5240	5.58	<=24	PASS
	Ant2	5240	5.66	<=24	PASS
	Ant1	5260	11.85	<=23.98	PASS
	Ant2	5260	12.03	<=23.98	PASS
	Ant1	5280	11.92	<=23.98	PASS
	Ant2	5280	11.89	<=23.98	PASS
	Ant1	5320	11.96	<=23.98	PASS
	Ant2	5320	11.92	<=23.98	PASS
	Ant1	5500	11.90	<=23.98	PASS
	Ant2	5500	12.80	<=23.98	PASS
	Ant1	5580	11.79	<=23.98	PASS
	Ant2	5580	12.68	<=23.98	PASS
	Ant1	5700	12.16	<=23.98	PASS
	Ant2	5700	12.79	<=23.98	PASS
	Ant1	5745	12.14	<=30	PASS

	Ant2	5745	12.63	<=30	PASS
	Ant1	5785	12.16	<=30	PASS
	Ant2	5785	12.40	<=30	PASS
	Ant1	5825	12.06	<=30	PASS
	Ant2	5825	12.10	<=30	PASS
11AC40SISO	Ant1	5190	8.73	<=24	PASS
	Ant2	5190	8.68	<=24	PASS
	Ant1	5230	8.54	<=24	PASS
	Ant2	5230	8.70	<=24	PASS
	Ant1	5270	12.09	<=23.98	PASS
	Ant2	5270	12.01	<=23.98	PASS
	Ant1	5310	11.99	<=23.98	PASS
	Ant2	5310	11.92	<=23.98	PASS
	Ant1	5510	12.08	<=23.98	PASS
	Ant2	5510	12.76	<=23.98	PASS
	Ant1	5550	11.98	<=23.98	PASS
	Ant2	5550	12.86	<=23.98	PASS
	Ant1	5670	12.12	<=23.98	PASS
	Ant2	5670	12.88	<=23.98	PASS
	Ant1	5755	12.37	<=30	PASS
	Ant2	5755	12.66	<=30	PASS
	Ant1	5795	12.23	<=30	PASS
	Ant2	5795	12.46	<=30	PASS
	11AC80SISO	Ant1	5210	10.61	<=24
Ant2		5210	10.63	<=24	PASS
Ant1		5290	12.12	<=23.98	PASS
Ant2		5290	12.02	<=23.98	PASS
Ant1		5530	12.00	<=23.98	PASS
Ant2		5530	12.89	<=23.98	PASS
Ant1		5610	11.98	<=23.98	PASS
Ant2		5610	12.89	<=23.98	PASS
Ant1		5775	12.32	<=30	PASS
Ant2	5775	12.66	<=30	PASS	
11N20MIMO	Ant1	5180	6.12	<=24	PASS
	Ant2	5180	5.56	<=24	PASS
	total	5180	8.9	<=24	PASS
	Ant1	5200	5.38	<=24	PASS
	Ant2	5200	5.43	<=24	PASS
	total	5200	8.4	<=24	PASS
	Ant1	5240	5.32	<=24	PASS
	Ant2	5240	5.33	<=24	PASS
	total	5240	8.3	<=24	PASS
	Ant1	5260	11.81	<=23.98	PASS
	Ant2	5260	12.34	<=23.98	PASS
	total	5260	15.1	<=23.98	PASS

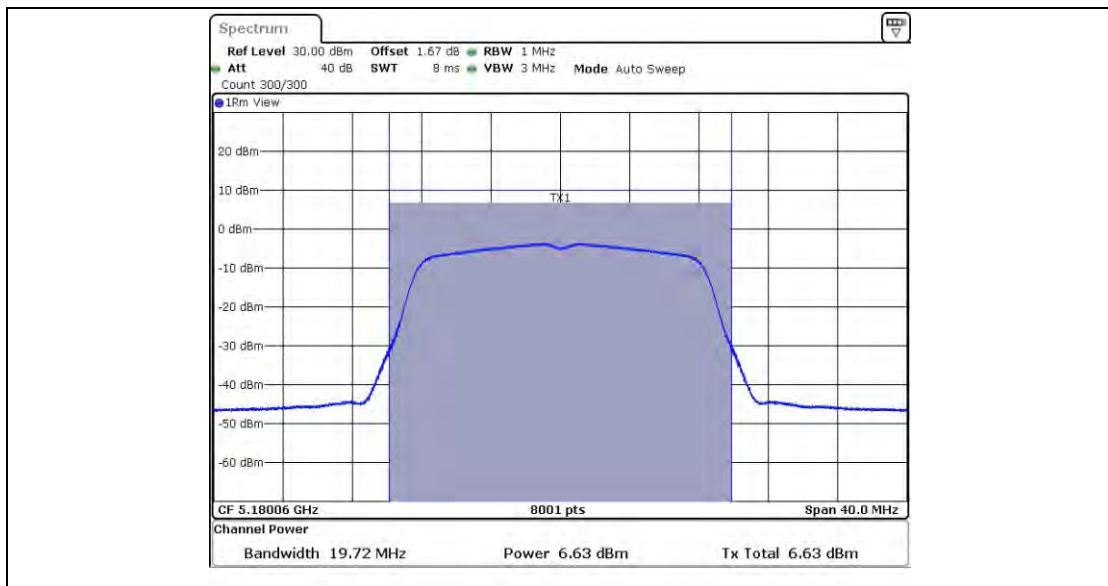
	Ant1	5280	12.37	<=23.98	PASS
	Ant2	5280	11.44	<=23.98	PASS
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	Ant1	5320	11.36	<=23.98	PASS
	Ant2	5320	10.99	<=23.98	PASS
	total	5320	14.2	<=23.98	PASS
	Ant1	5500	10.31	<=23.98	PASS
	Ant2	5500	11.39	<=23.98	PASS
	total	5500	13.9	<=23.98	PASS
	Ant1	5580	12.37	<=23.98	PASS
	Ant2	5580	11.97	<=23.98	PASS
	total	5580	15.2	<=23.98	PASS
	Ant1	5700	12.25	<=23.98	PASS
	Ant2	5700	11.55	<=23.98	PASS
	total	5700	14.9	<=23.98	PASS
	Ant1	5745	13.15	<=30	PASS
	Ant2	5745	10.67	<=30	PASS
	total	5745	15.1	<=30	PASS
	Ant1	5785	10.43	<=30	PASS
	Ant2	5785	12.06	<=30	PASS
	total	5785	14.3	<=30	PASS
	Ant1	5825	12.06	<=30	PASS
	Ant2	5825	11.18	<=30	PASS
	total	5825	14.7	<=30	PASS
11N40MIMO	Ant1	5190	7.42	<=24	PASS
	Ant2	5190	7.36	<=24	PASS
	total	5190	10.4	<=24	PASS
	Ant1	5230	8.41	<=24	PASS
	Ant2	5230	8.42	<=24	PASS
	total	5230	11.4	<=24	PASS
	Ant1	5270	11.60	<=23.98	PASS
	Ant2	5270	12.08	<=23.98	PASS
	total	5270	14.9	<=23.98	PASS
	Ant1	5310	12.13	<=23.98	PASS
	Ant2	5310	11.50	<=23.98	PASS
	total	5310	14.8	<=23.98	PASS
	Ant1	5510	11.01	<=23.98	PASS
	Ant2	5510	12.72	<=23.98	PASS
	total	5510	15.0	<=23.98	PASS
	Ant1	5550	11.44	<=23.98	PASS
	Ant2	5550	12.12	<=23.98	PASS
	total	5550	14.8	<=23.98	PASS
	Ant1	5670	11.49	<=23.98	PASS
	Ant2	5670	13.27	<=23.98	PASS
total	5670	15.5	<=23.98	PASS	

	Ant1	5755	13.06	<=30	PASS
	Ant2	5755	12.09	<=30	PASS
	total	5755	15.6	<=30	PASS
	Ant1	5795	12.06	<=30	PASS
	Ant2	5795	12.08	<=30	PASS
	total	5795	15.1	<=30	PASS
11AC20MIMO	Ant1	5180	5.58	<=24	PASS
	Ant2	5180	5.57	<=24	PASS
	total	5180	8.6	<=24	PASS
	Ant1	5200	5.48	<=24	PASS
	Ant2	5200	5.56	<=24	PASS
	total	5200	8.5	<=24	PASS
	Ant1	5240	5.54	<=24	PASS
	Ant2	5240	5.45	<=24	PASS
	total	5240	8.5	<=24	PASS
	Ant1	5260	11.95	<=23.98	PASS
	Ant2	5260	12.21	<=23.98	PASS
	total	5260	15.1	<=23.98	PASS
	Ant1	5280	12.04	<=23.98	PASS
	Ant2	5280	11.74	<=23.98	PASS
	total	5280	14.9	<=23.98	PASS
	Ant1	5320	11.98	<=23.98	PASS
	Ant2	5320	11.64	<=23.98	PASS
	total	5320	14.8	<=23.98	PASS
	Ant1	5500	11.84	<=23.98	PASS
	Ant2	5500	11.79	<=23.98	PASS
	total	5500	14.8	<=23.98	PASS
	Ant1	5580	11.33	<=23.98	PASS
	Ant2	5580	11.49	<=23.98	PASS
	total	5580	14.4	<=23.98	PASS
	Ant1	5700	11.57	<=23.98	PASS
	Ant2	5700	12.82	<=23.98	PASS
	total	5700	15.3	<=23.98	PASS
	Ant1	5745	11.50	<=30	PASS
	Ant2	5745	12.40	<=30	PASS
	total	5745	15.0	<=30	PASS
	Ant1	5785	11.82	<=30	PASS
	Ant2	5785	12.55	<=30	PASS
	total	5785	15.2	<=30	PASS
Ant1	5825	10.34	<=30	PASS	
Ant2	5825	12.55	<=30	PASS	
total	5825	14.6	<=30	PASS	
11AC40MIMO	Ant1	5190	8.44	<=24	PASS
	Ant2	5190	8.60	<=24	PASS
	total	5190	11.5	<=24	PASS

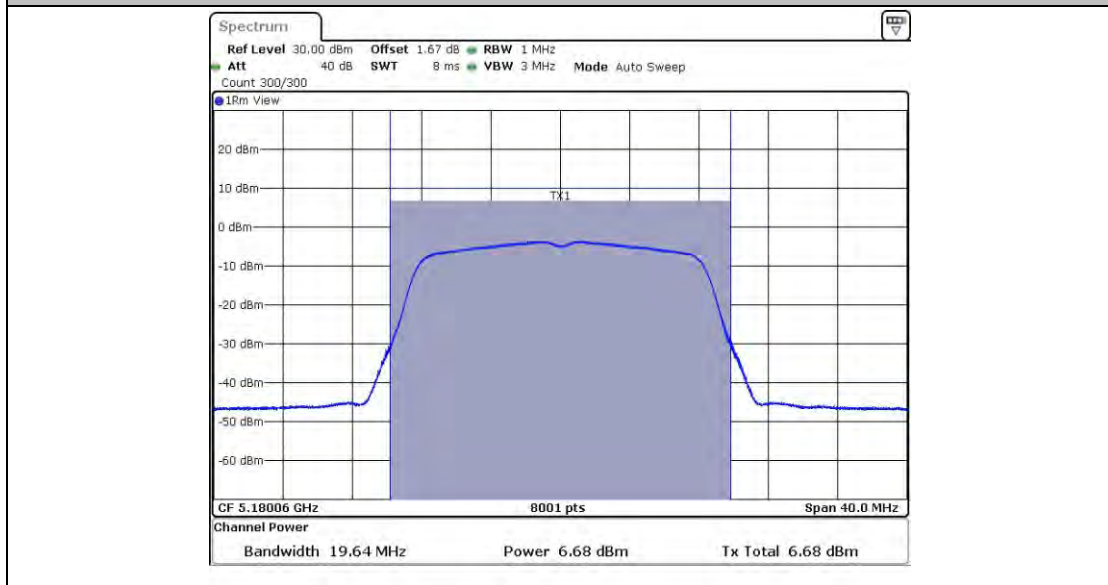
	Ant1	5230	8.45	<=24	PASS
	Ant2	5230	8.50	<=24	PASS
	total	5230	11.5	<=24	PASS
	Ant1	5270	11.61	<=23.98	PASS
	Ant2	5270	12.37	<=23.98	PASS
	total	5270	15.0	<=23.98	PASS
	Ant1	5310	12.23	<=23.98	PASS
	Ant2	5310	12.26	<=23.98	PASS
	total	5310	15.3	<=23.98	PASS
	Ant1	5510	12.87	<=23.98	PASS
	Ant2	5510	13.27	<=23.98	PASS
	total	5510	16.1	<=23.98	PASS
	Ant1	5550	11.87	<=23.98	PASS
	Ant2	5550	11.48	<=23.98	PASS
	total	5550	14.7	<=23.98	PASS
	Ant1	5670	11.68	<=23.98	PASS
	Ant2	5670	13.39	<=23.98	PASS
	total	5670	15.6	<=23.98	PASS
	Ant1	5755	11.62	<=30	PASS
	Ant2	5755	13.19	<=30	PASS
	total	5755	15.5	<=30	PASS
	Ant1	5795	10.46	<=30	PASS
	Ant2	5795	11.04	<=30	PASS
	total	5795	13.8	<=30	PASS
11AC80MIMO	Ant1	5210	10.56	<=24	PASS
	Ant2	5210	10.55	<=24	PASS
	total	5210	13.6	<=24	PASS
	Ant1	5290	12.13	<=23.98	PASS
	Ant2	5290	11.59	<=23.98	PASS
	total	5290	14.9	<=23.98	PASS
	Ant1	5530	10.03	<=23.98	PASS
	Ant2	5530	12.24	<=23.98	PASS
	total	5530	14.3	<=23.98	PASS
	Ant1	5610	11.12	<=23.98	PASS
	Ant2	5610	11.71	<=23.98	PASS
	total	5610	14.4	<=23.98	PASS
	Ant1	5775	10.17	<=30	PASS
	Ant2	5775	12.03	<=30	PASS
	total	5775	14.2	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.

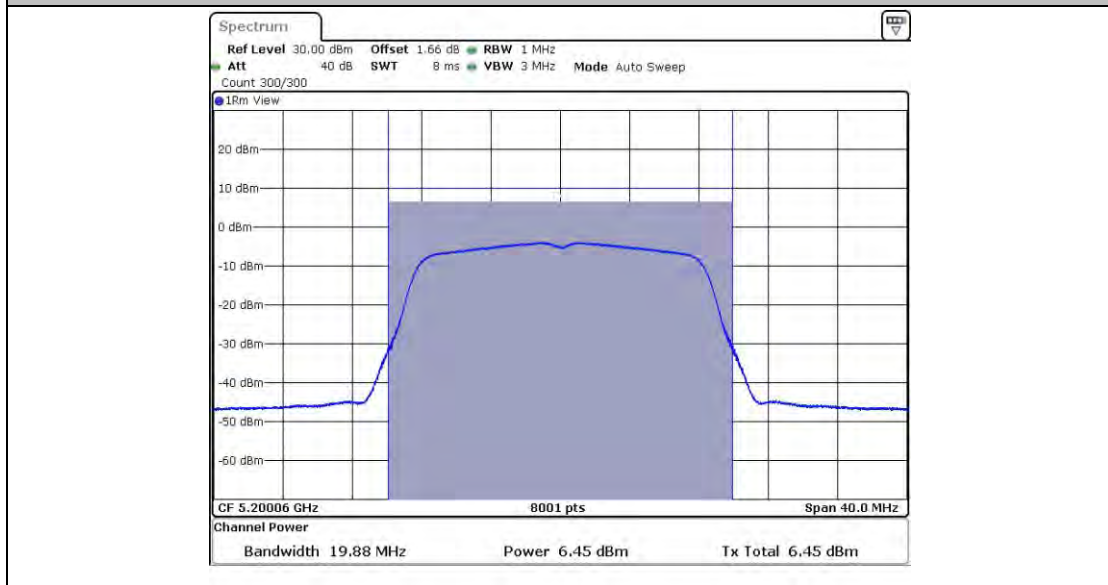
## Test Graphs



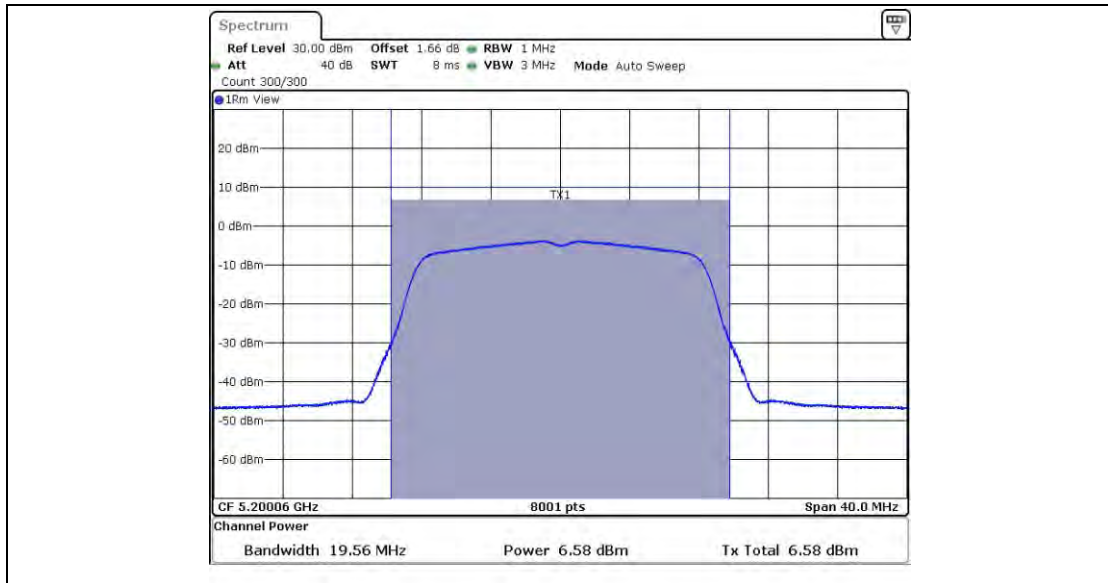
11A\_Ant1\_5180



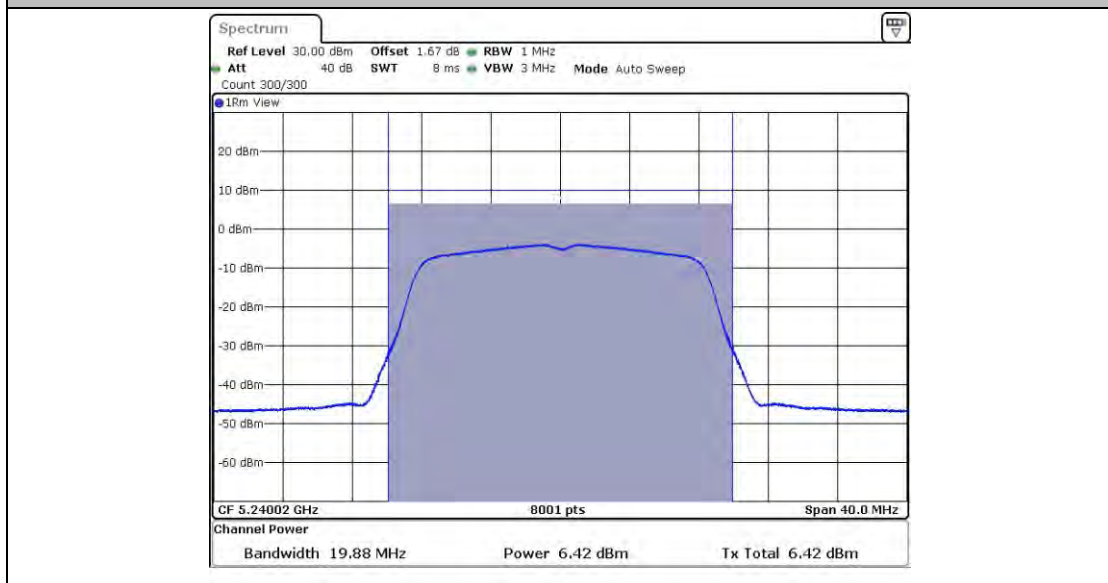
11A\_Ant2\_5180



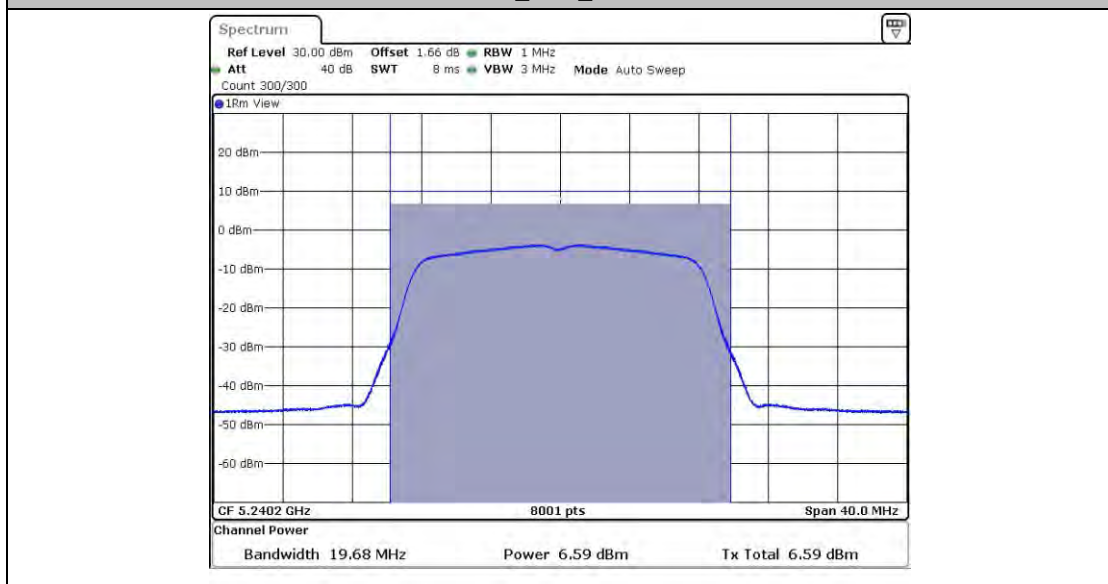
11A\_Ant1\_5200



11A\_Ant2\_5200

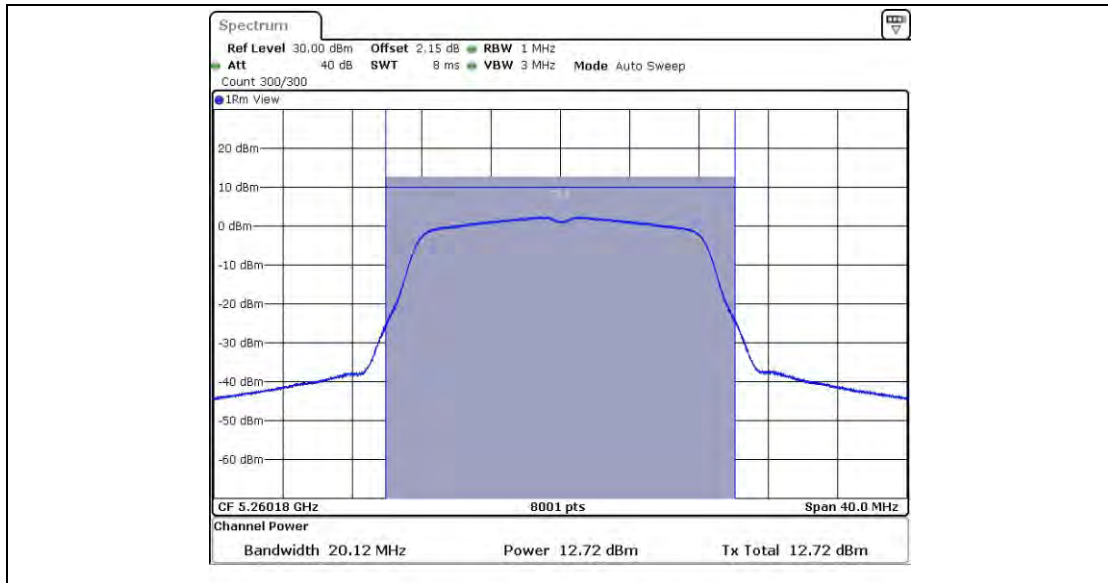


11A\_Ant1\_5240

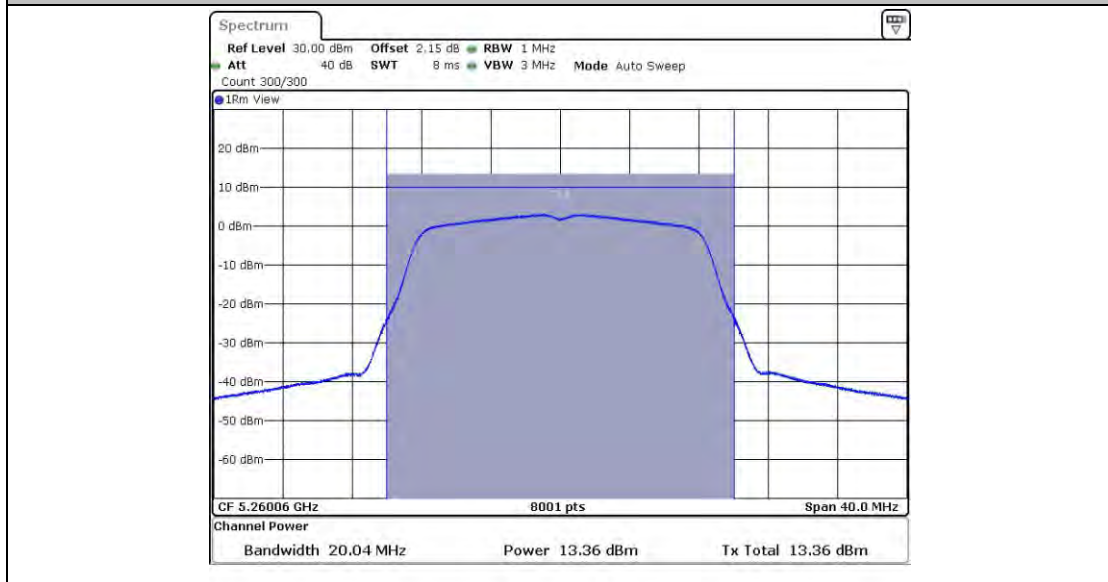


11A\_Ant2\_5240

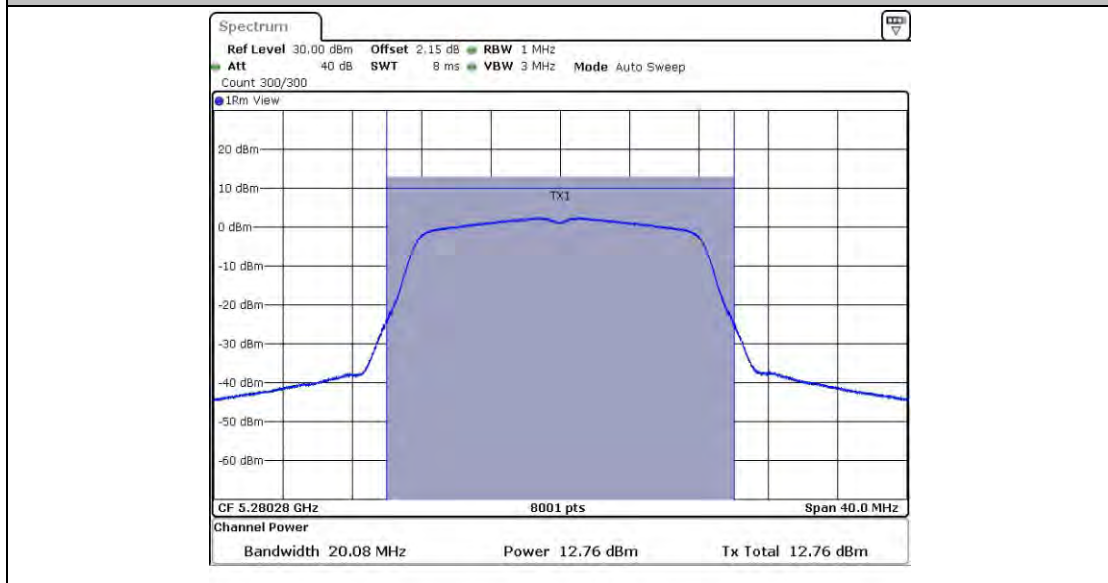




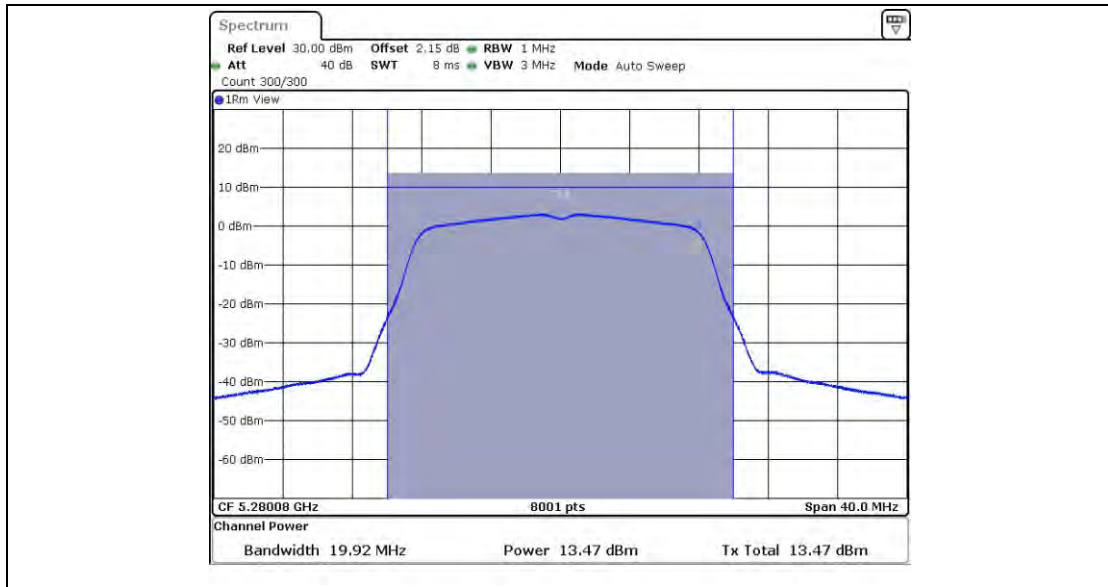
11A\_Ant1\_5260



11A\_Ant2\_5260



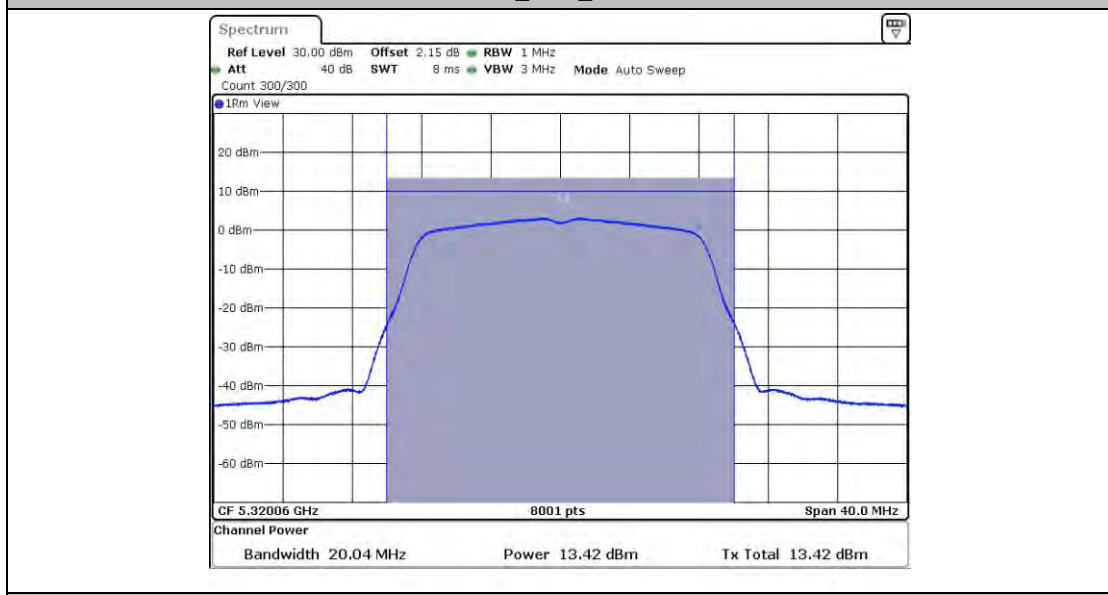
11A\_Ant1\_5280



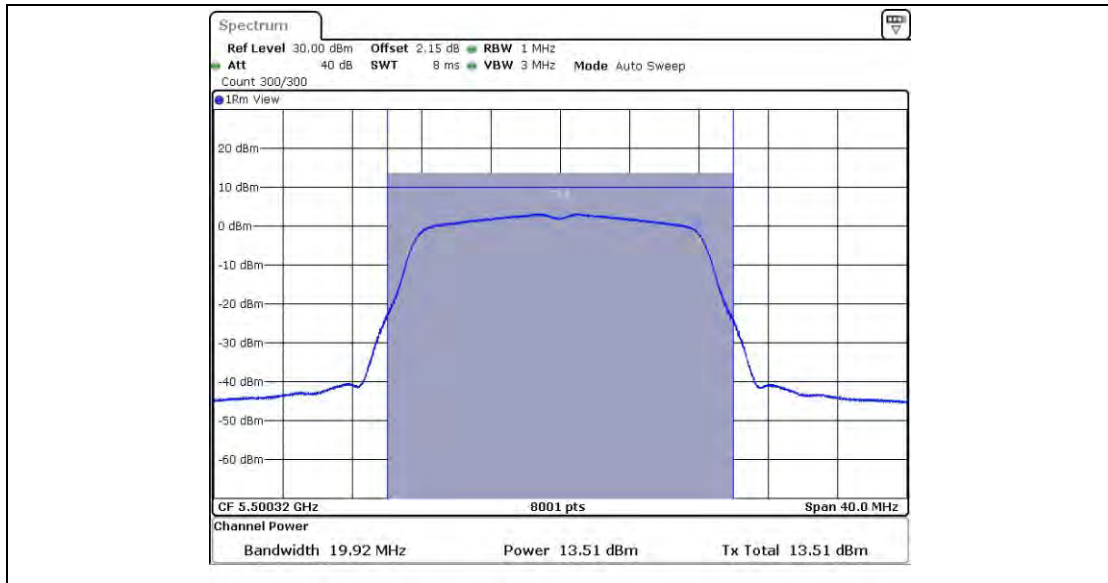
11A\_Ant2\_5280



11A\_Ant1\_5320



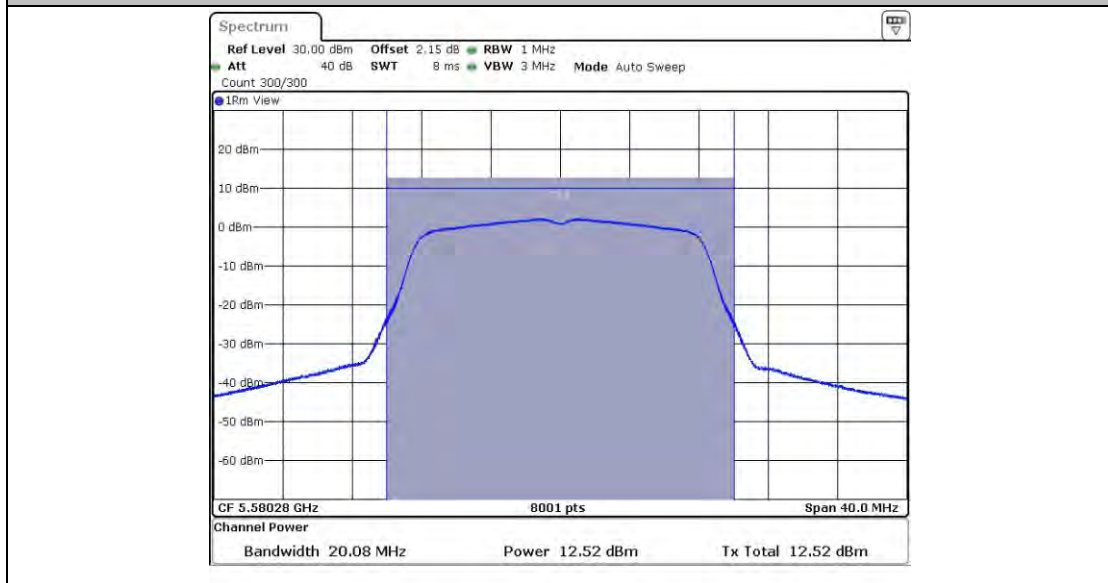
11A\_Ant2\_5320



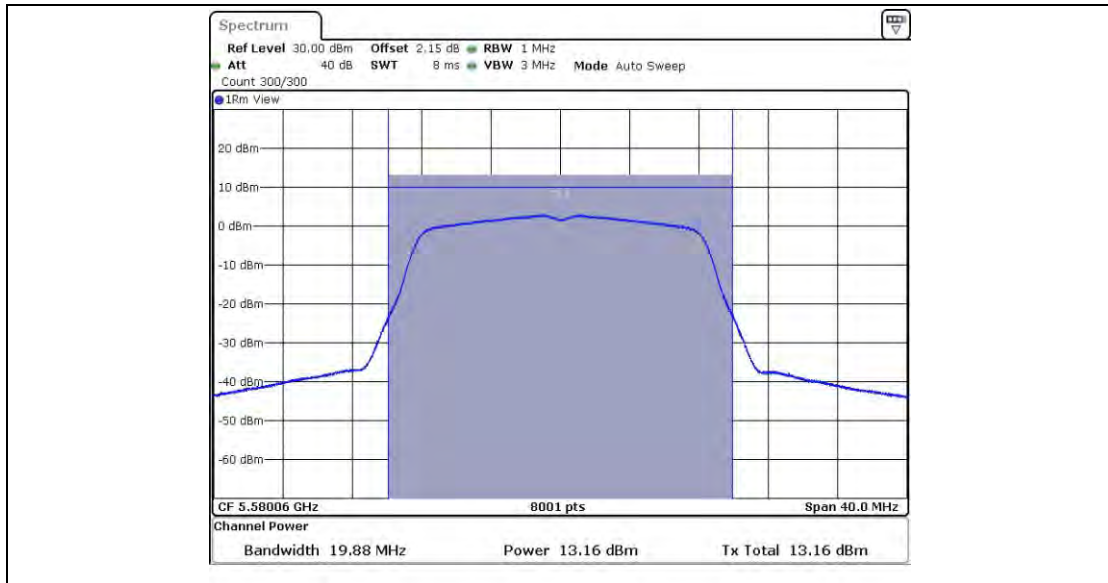
11A\_Ant1\_5500



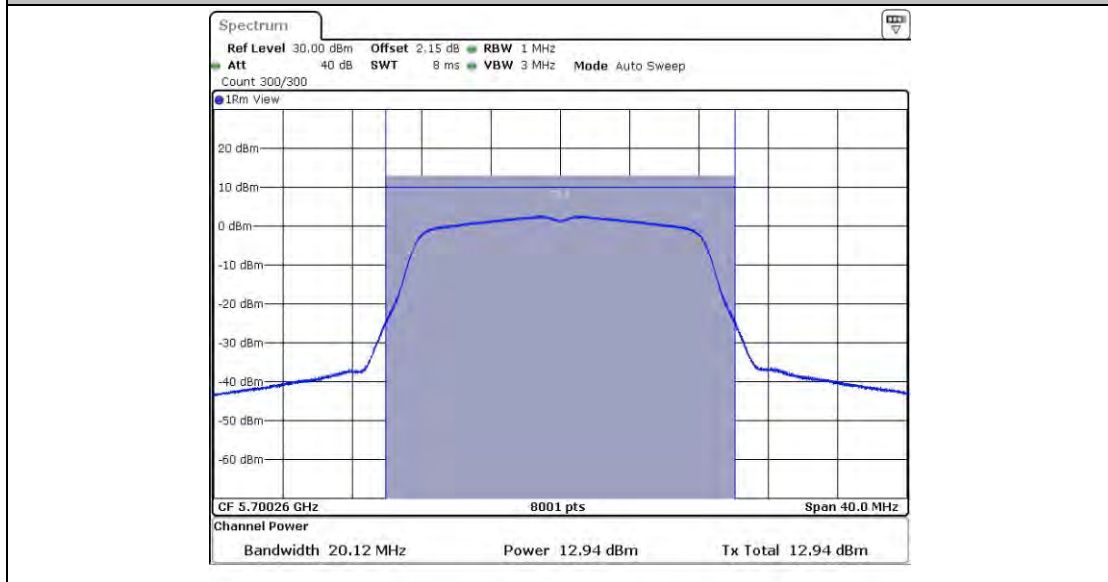
11A\_Ant2\_5500



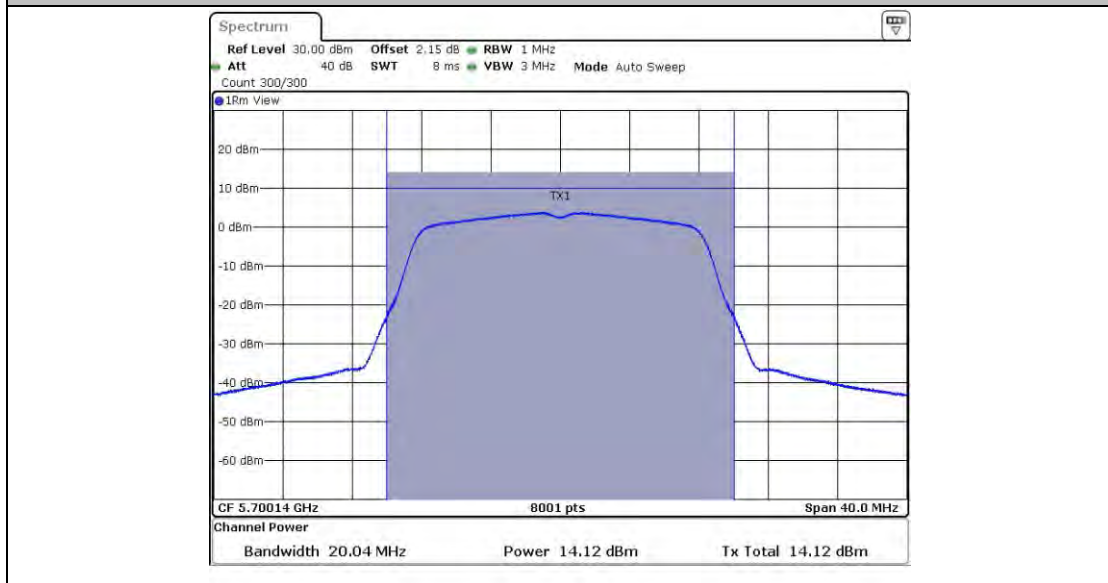
11A\_Ant1\_5580



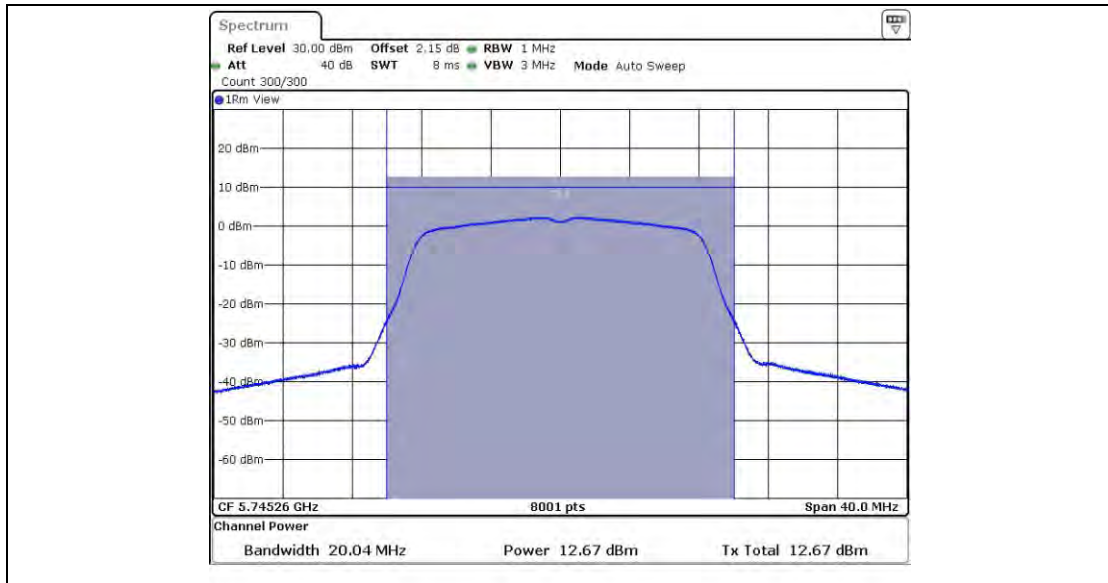
11A\_Ant2\_5580



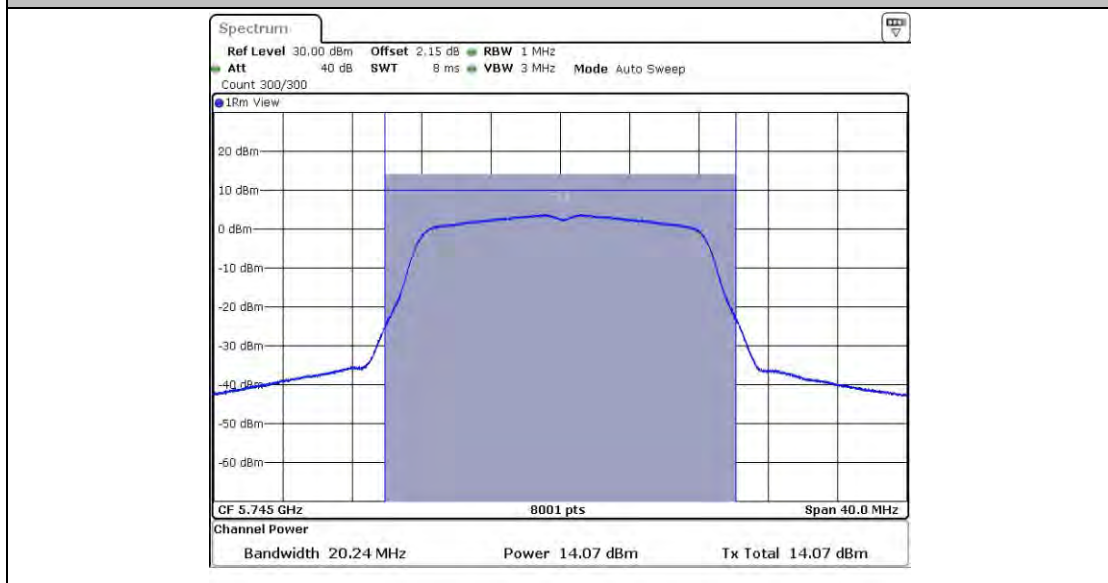
11A\_Ant1\_5700



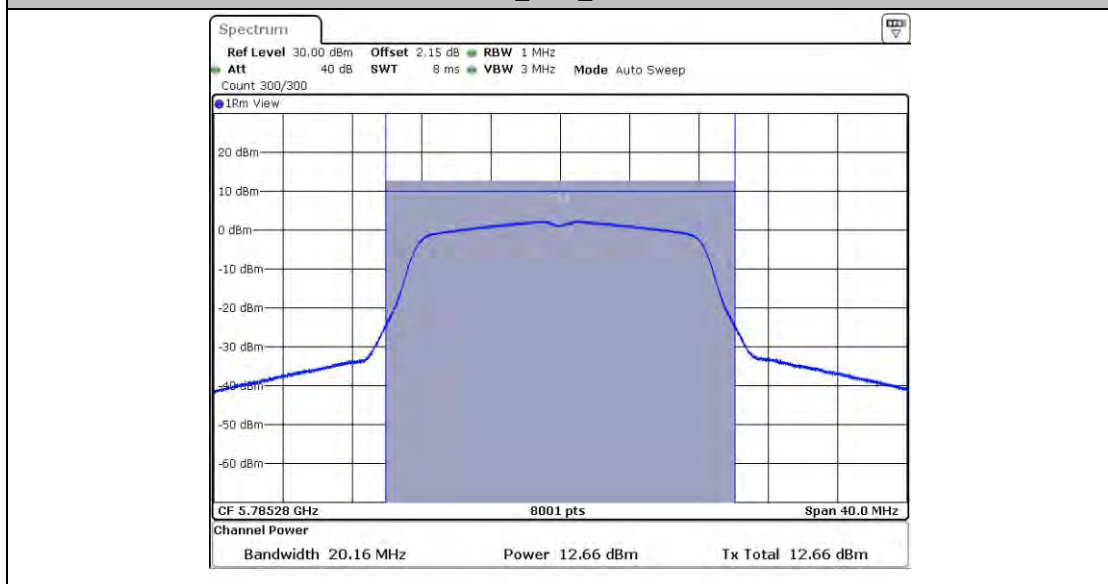
11A\_Ant2\_5700



11A\_Ant1\_5745

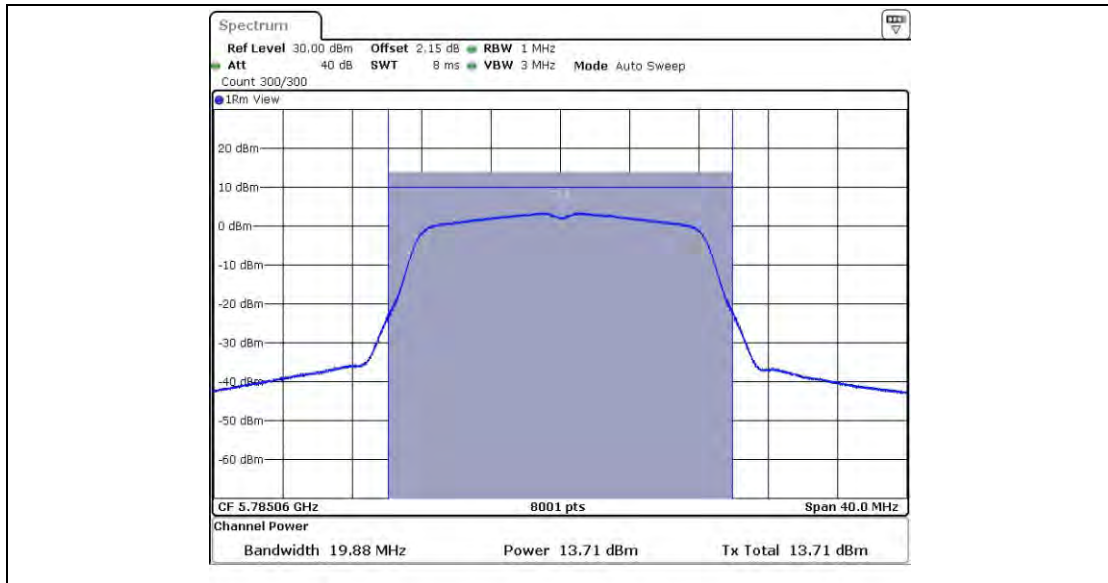


11A\_Ant2\_5745

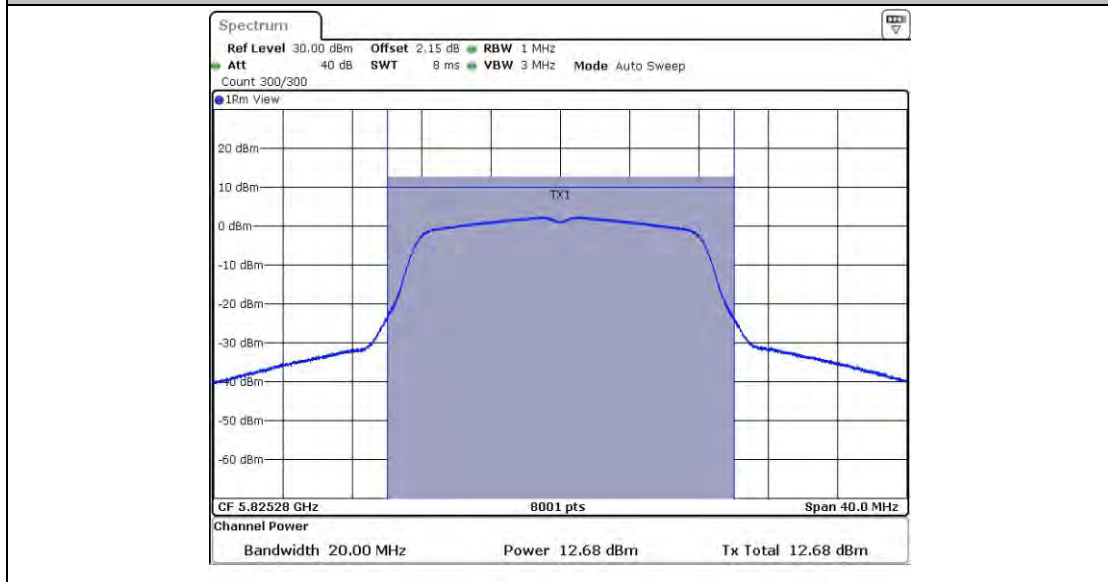


11A\_Ant1\_5785

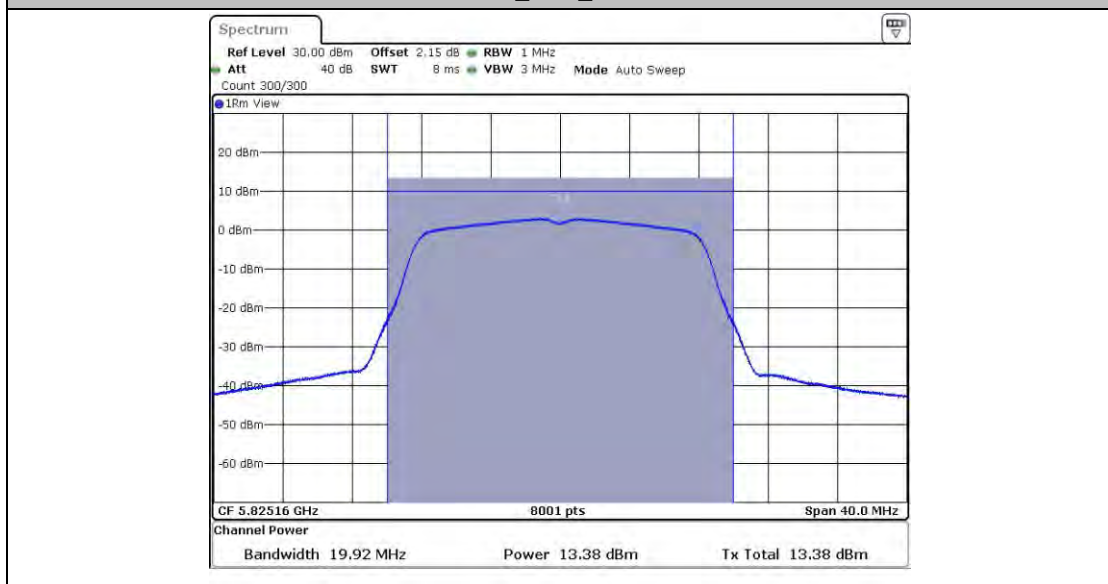




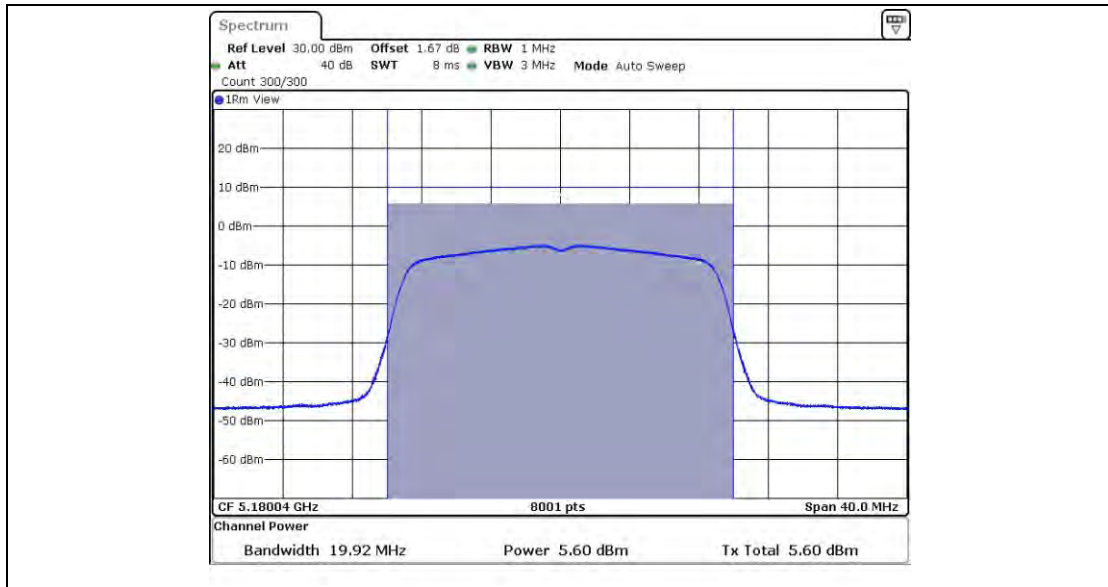
11A\_Ant2\_5785



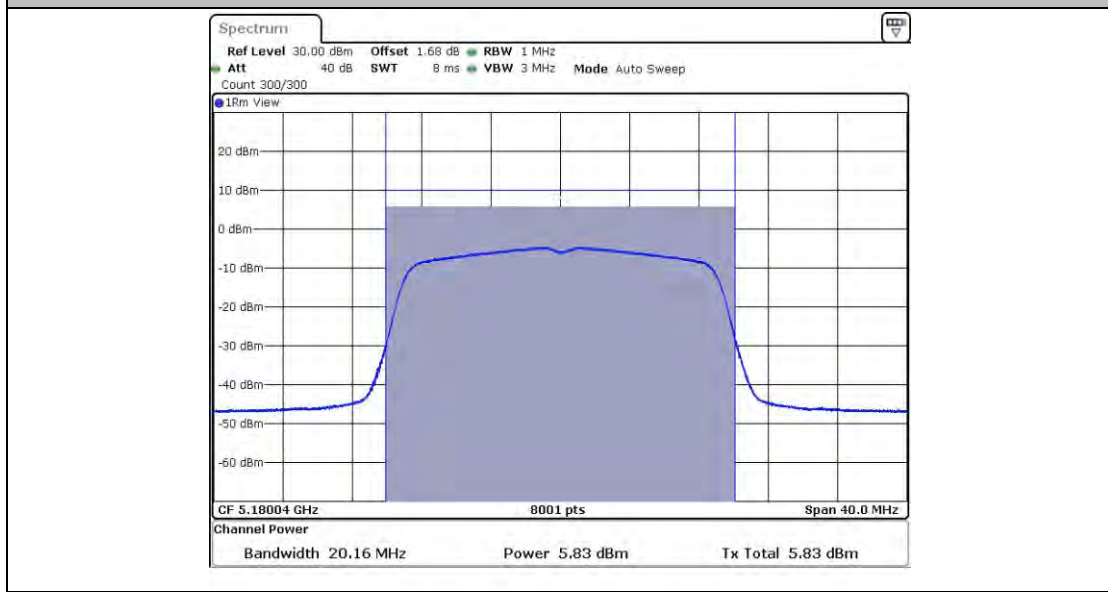
11A\_Ant1\_5825



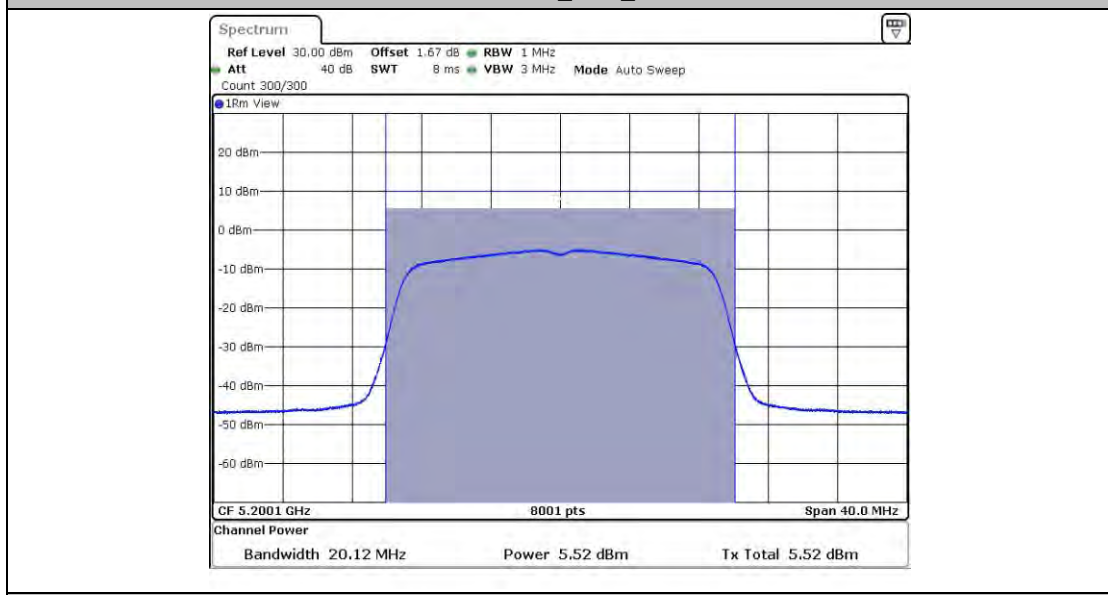
11A\_Ant2\_5825



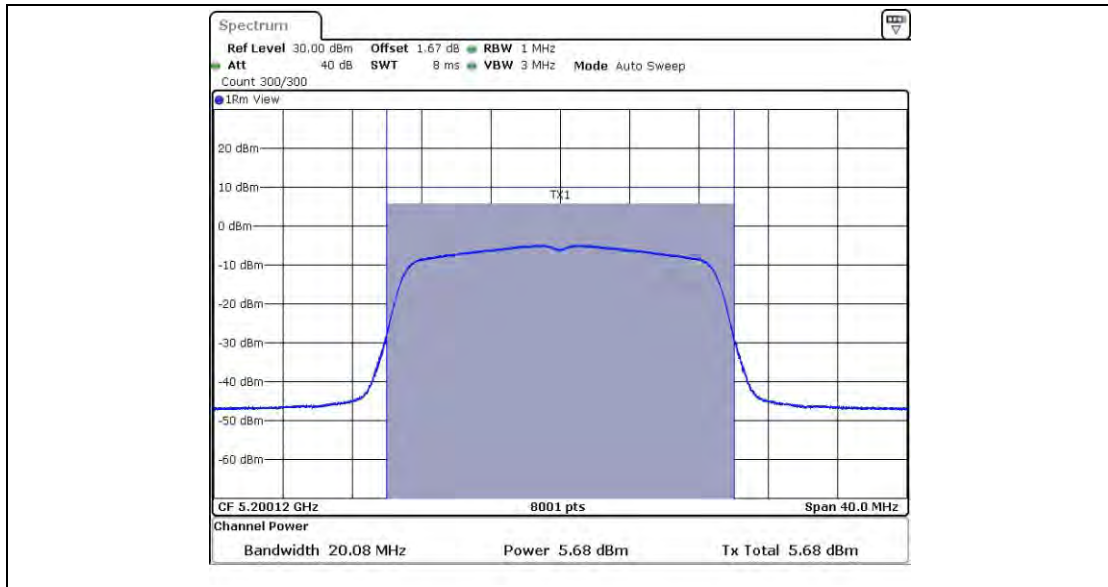
11N20SISO\_Ant1\_5180



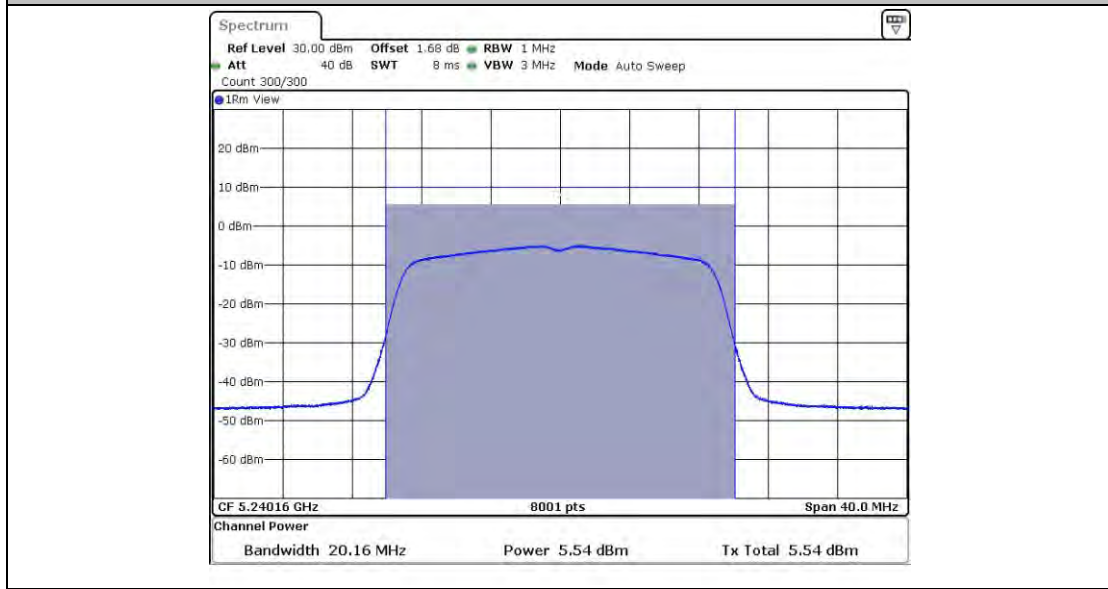
11N20SISO\_Ant2\_5180



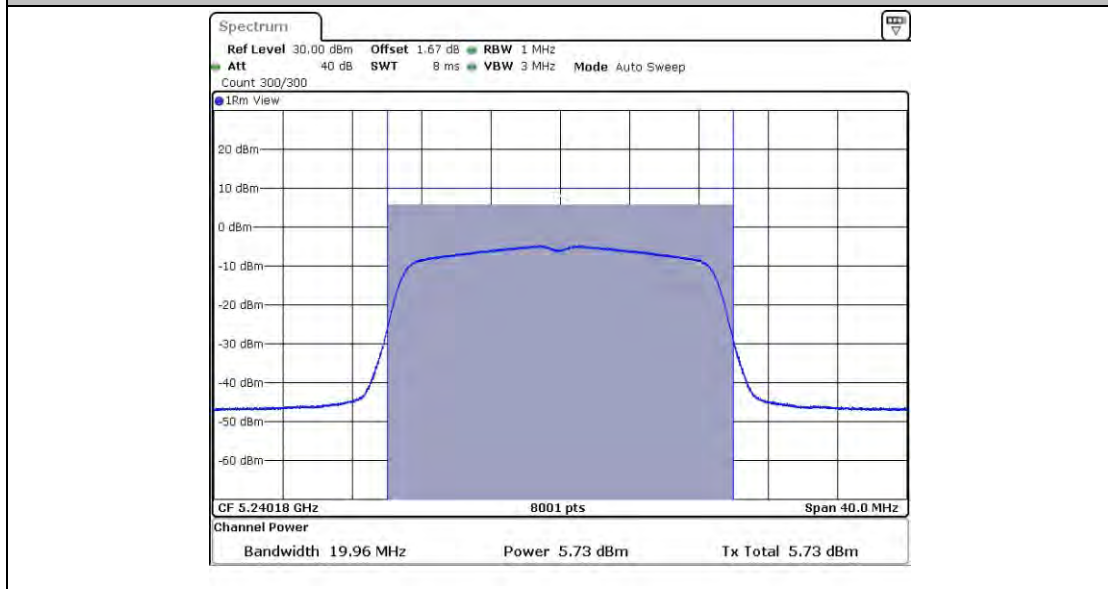
11N20SISO\_Ant1\_5200



11N20SISO\_Ant2\_5200

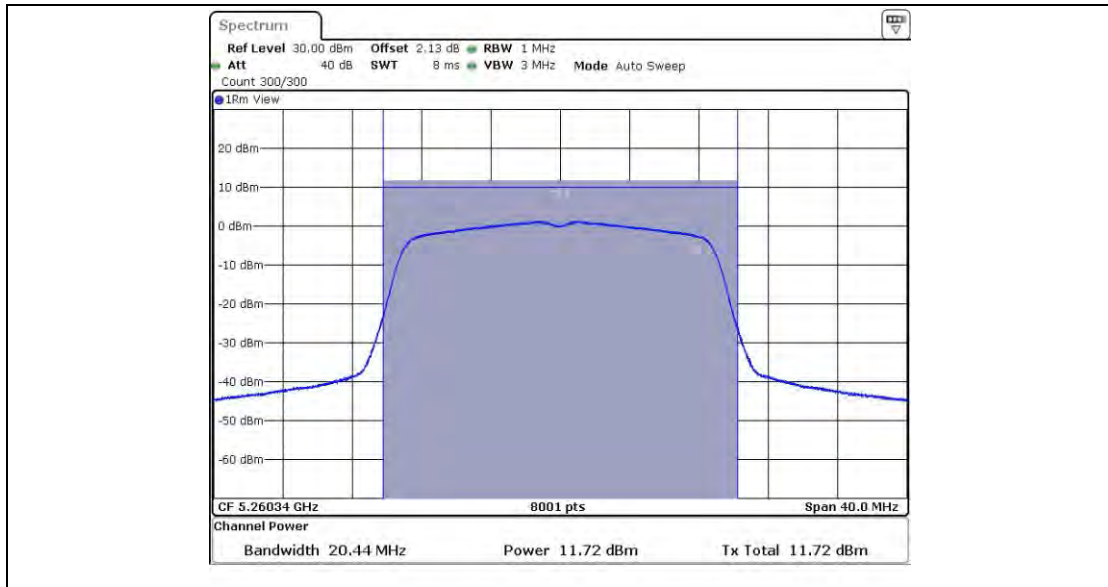


11N20SISO\_Ant1\_5240



11N20SISO\_Ant2\_5240

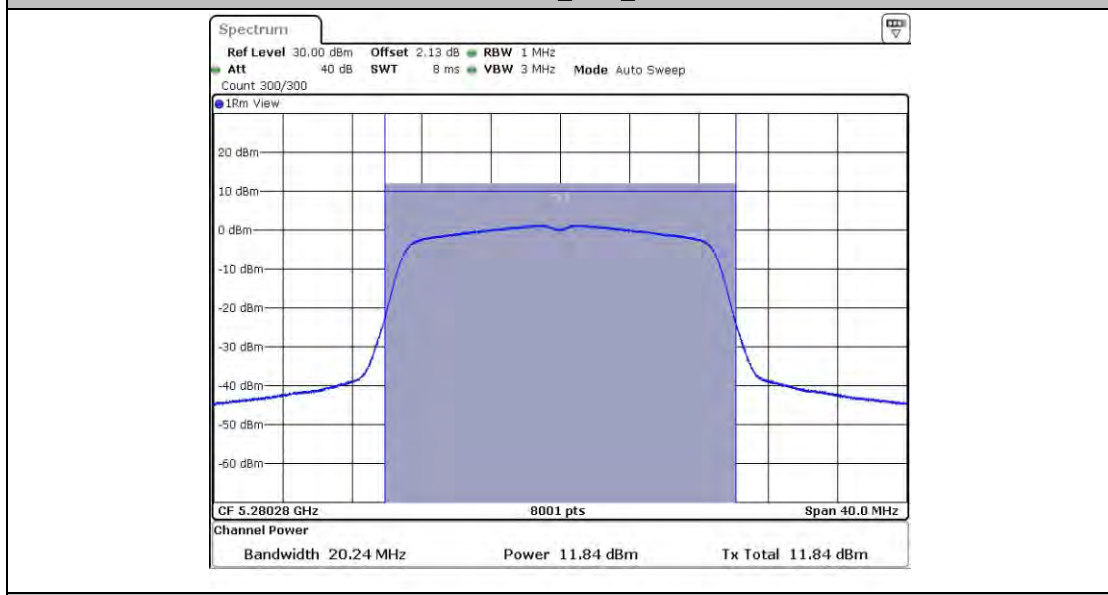




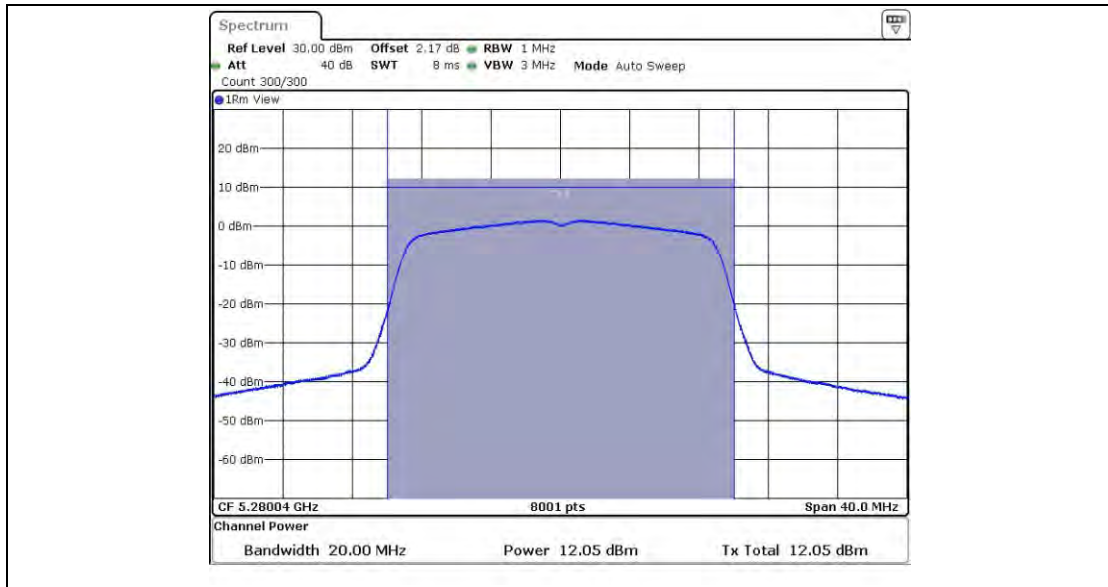
11N20SISO\_Ant1\_5260



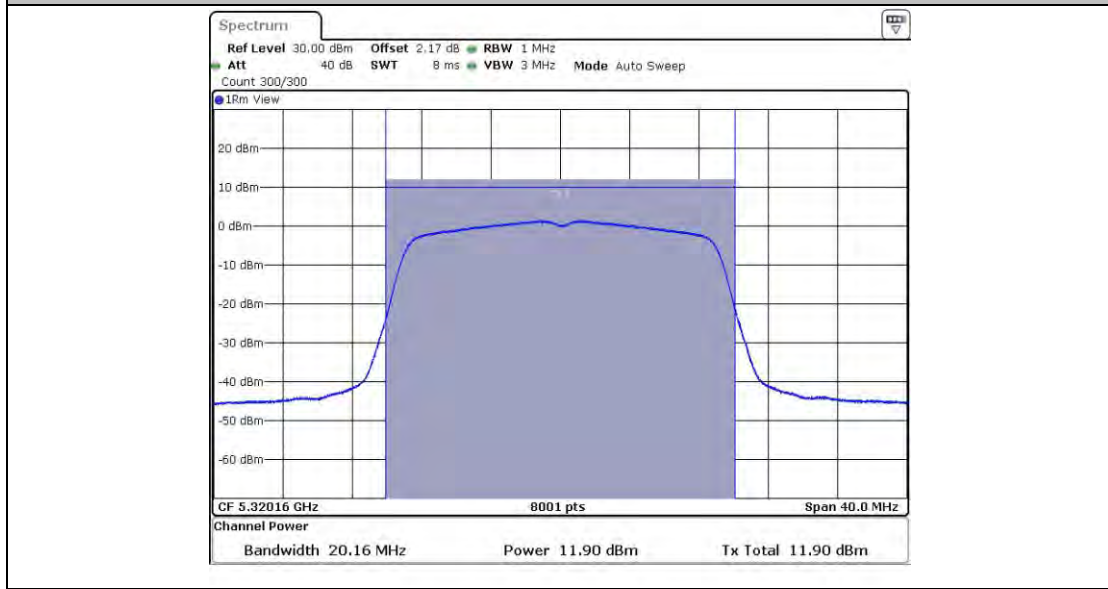
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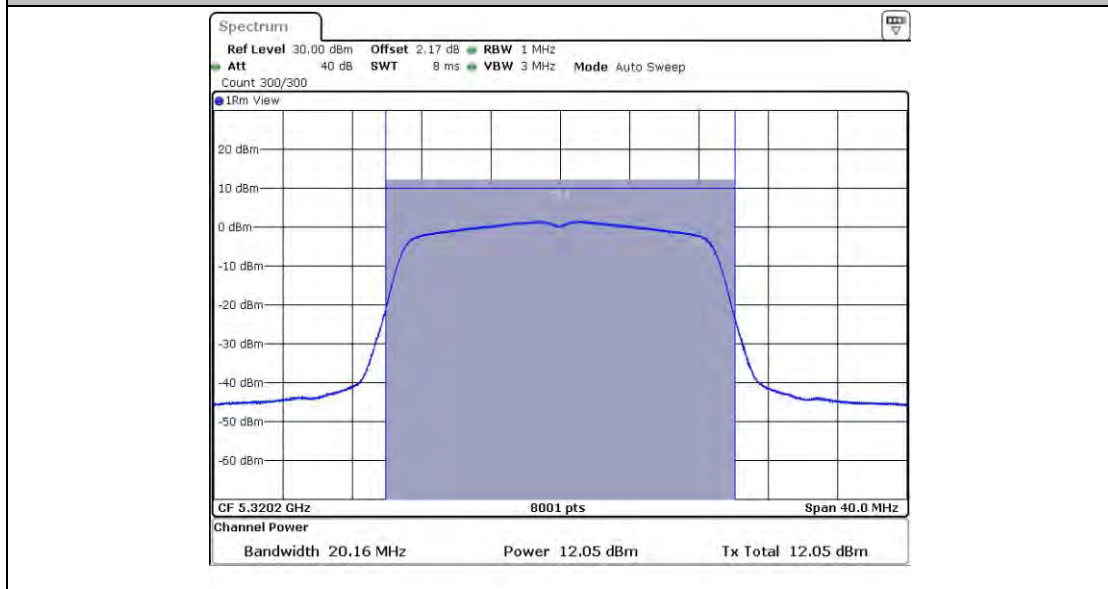
11N20SISO\_Ant1\_5280



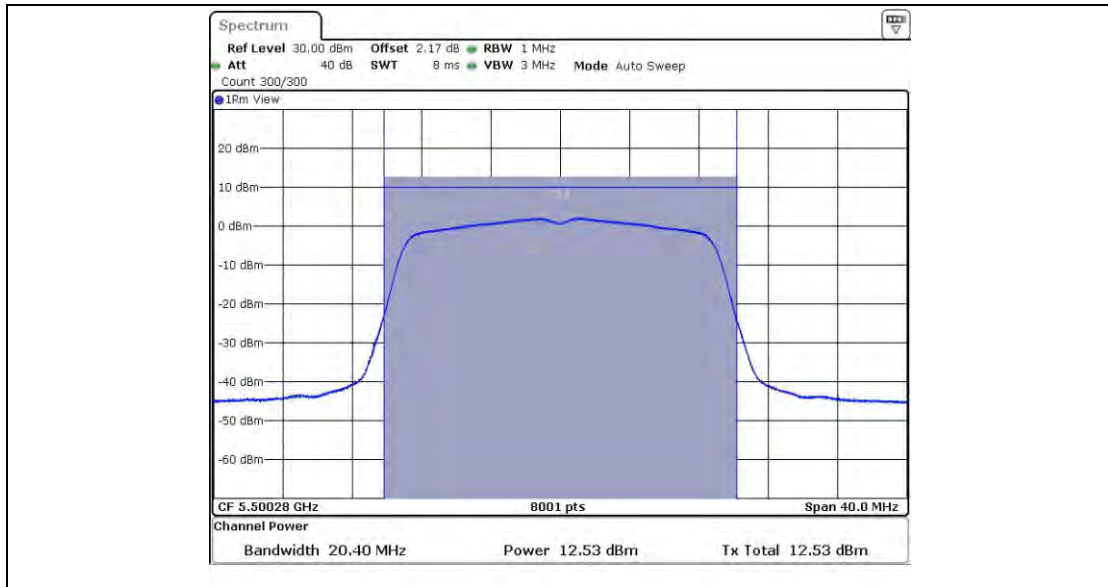
11N20SISO\_Ant2\_5280



11N20SISO\_Ant1\_5320



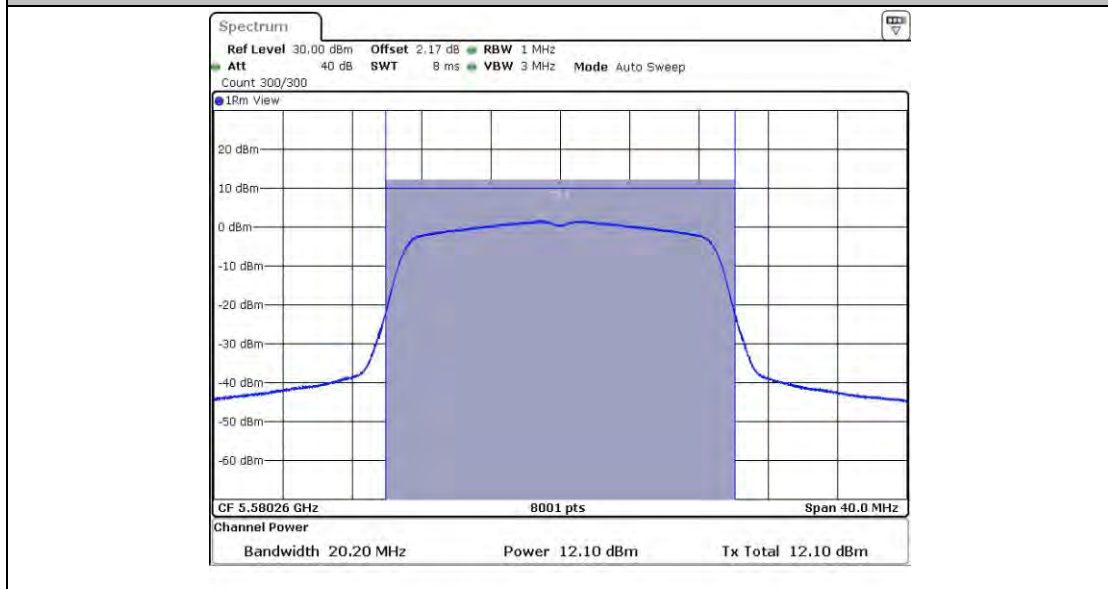
11N20SISO\_Ant2\_5320



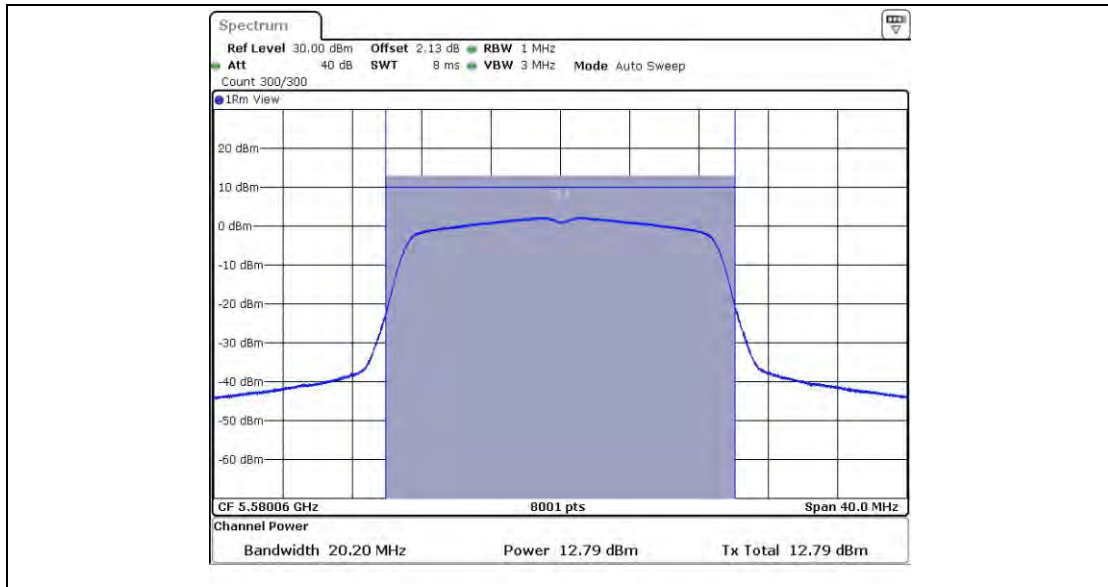
11N20SISO\_Ant1\_5500



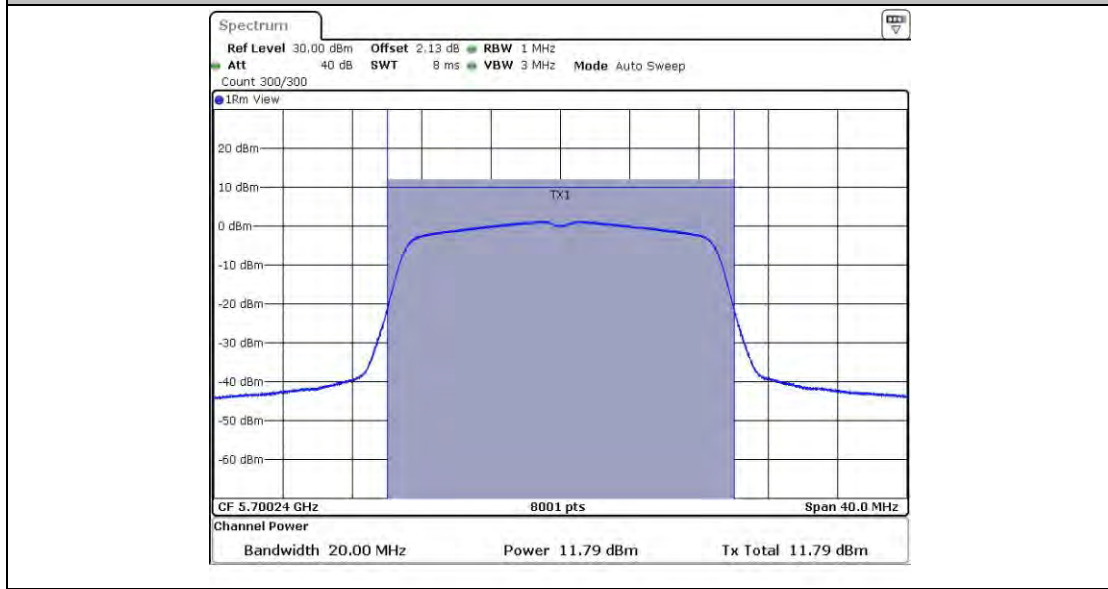
11N20SISO\_Ant2\_5500



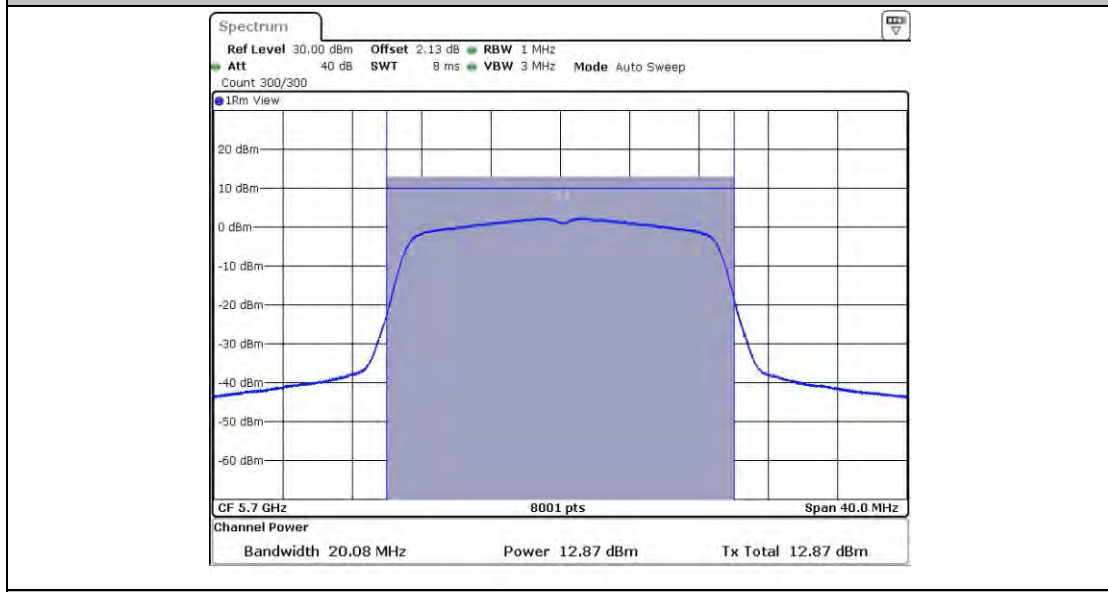
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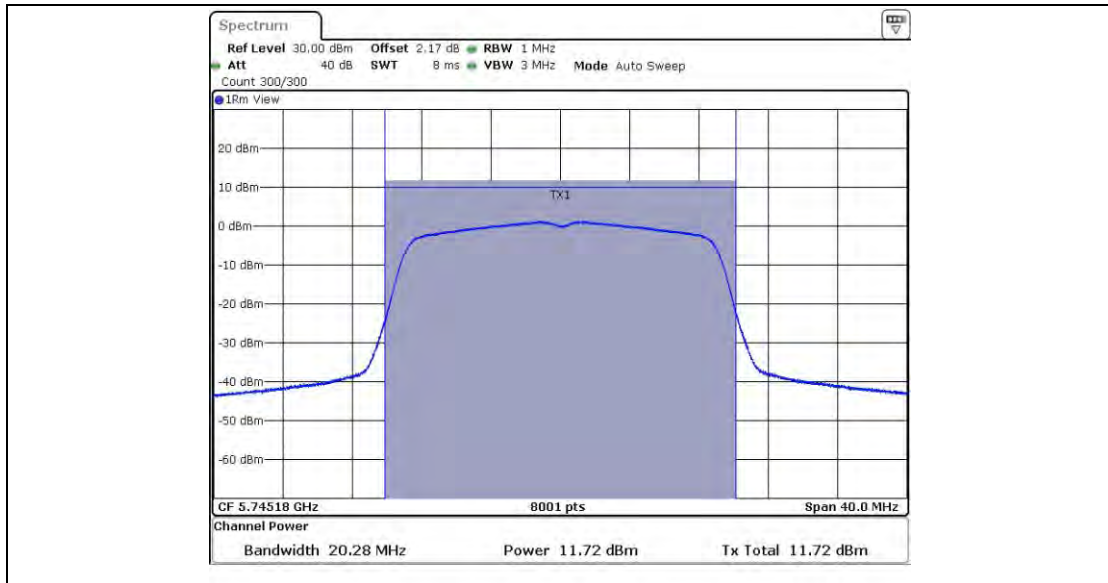
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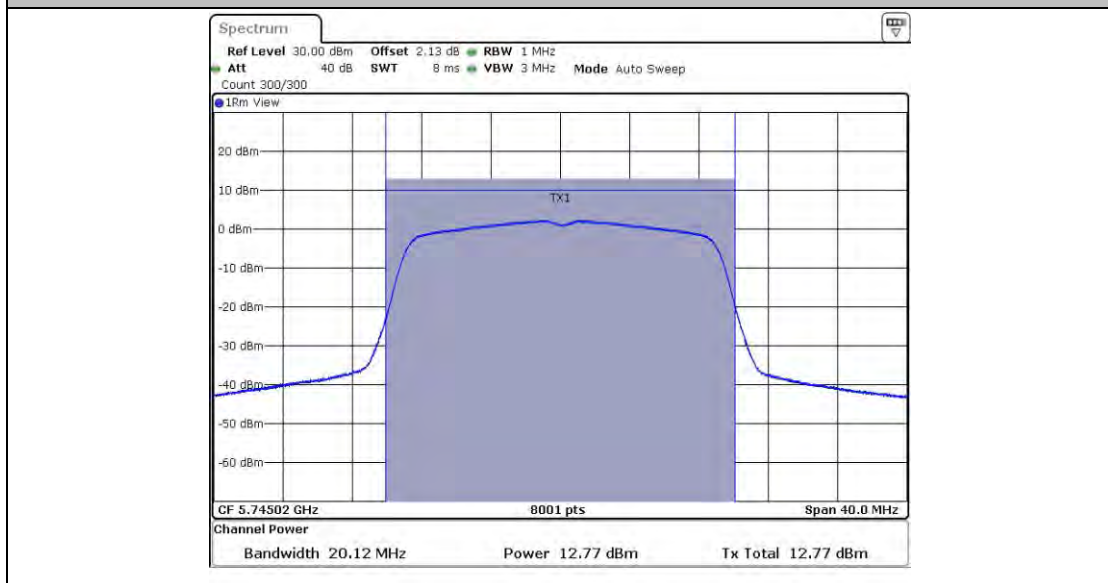
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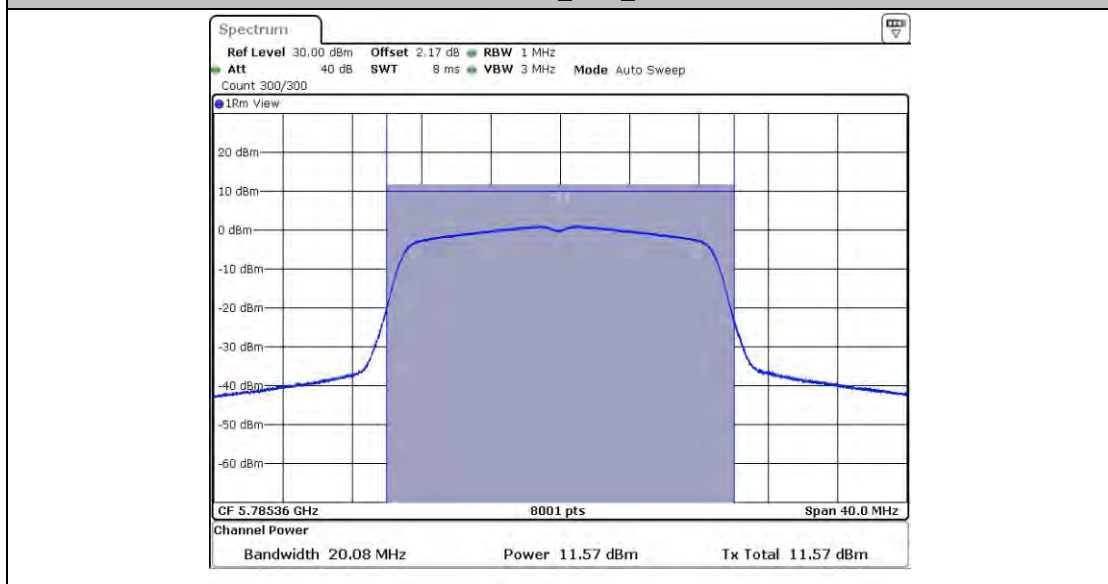
11N20SISO\_Ant2\_5700



11N20SISO\_Ant1\_5745

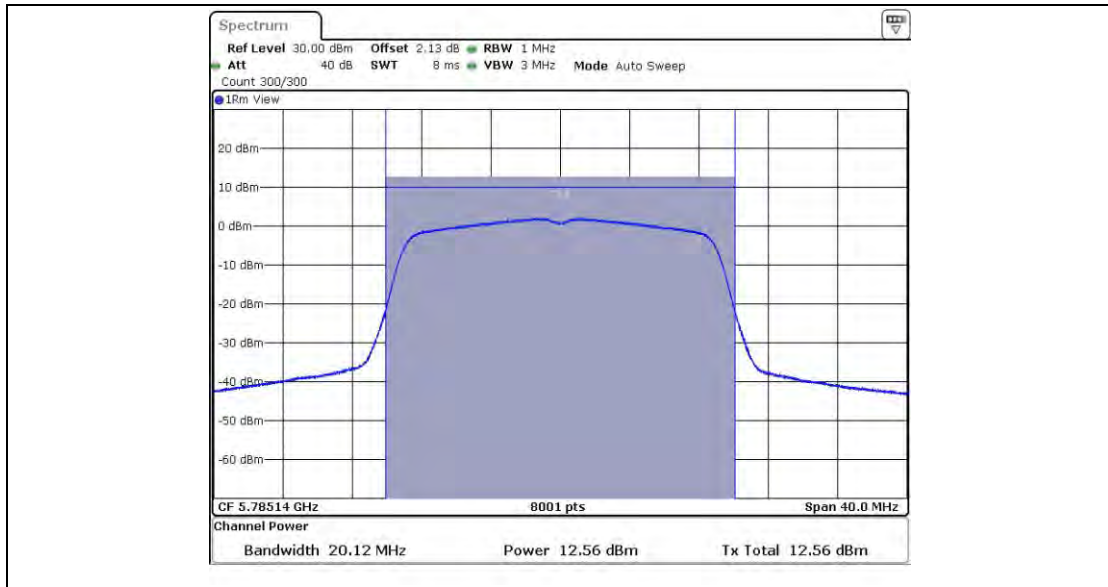


11N20SISO\_Ant2\_5745

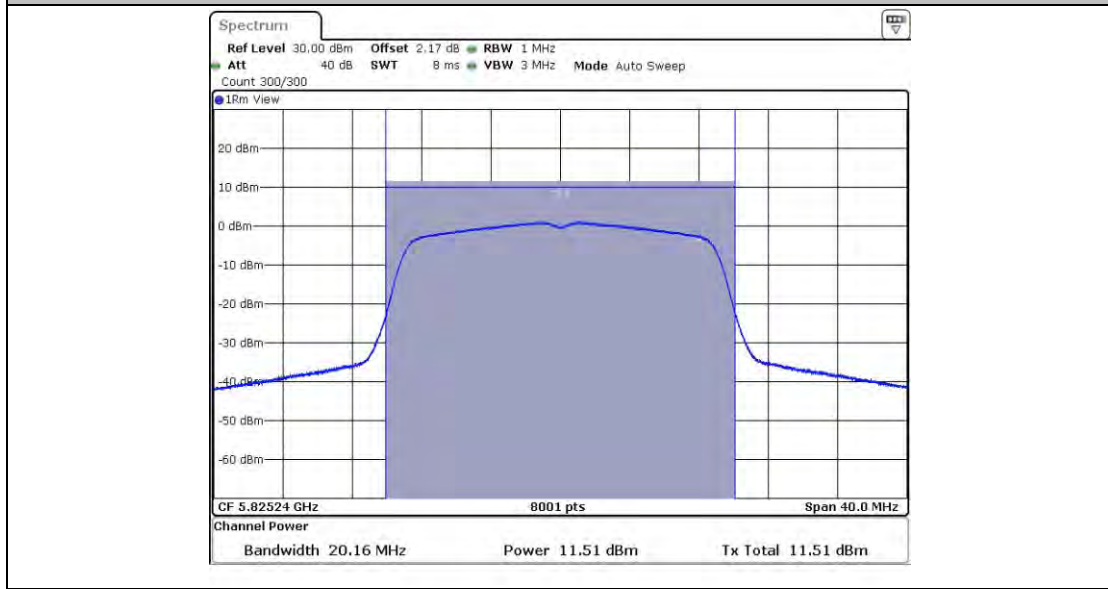


11N20SISO\_Ant1\_5785

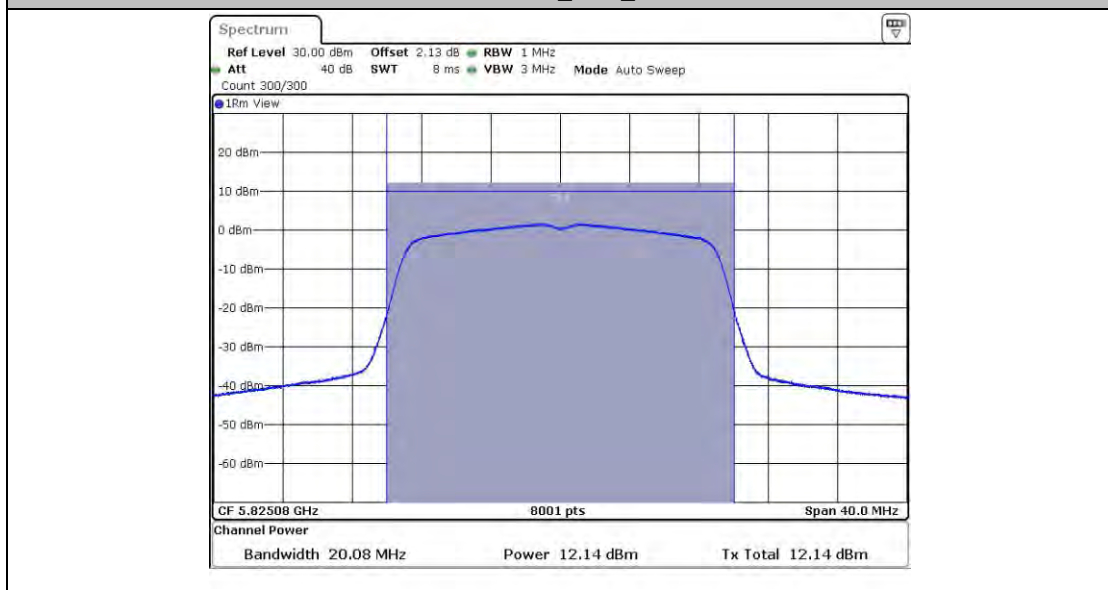




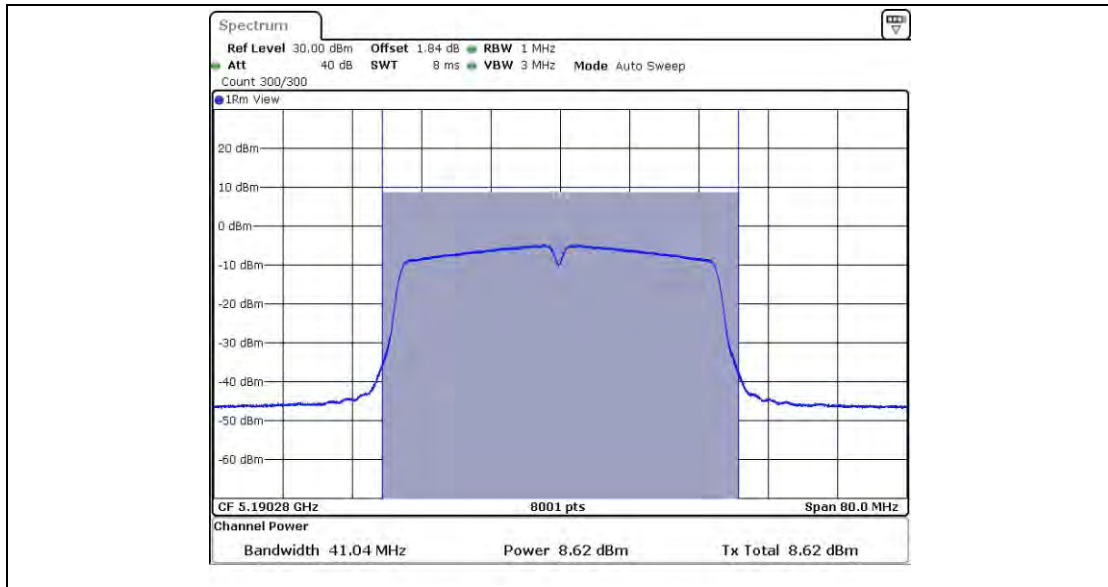
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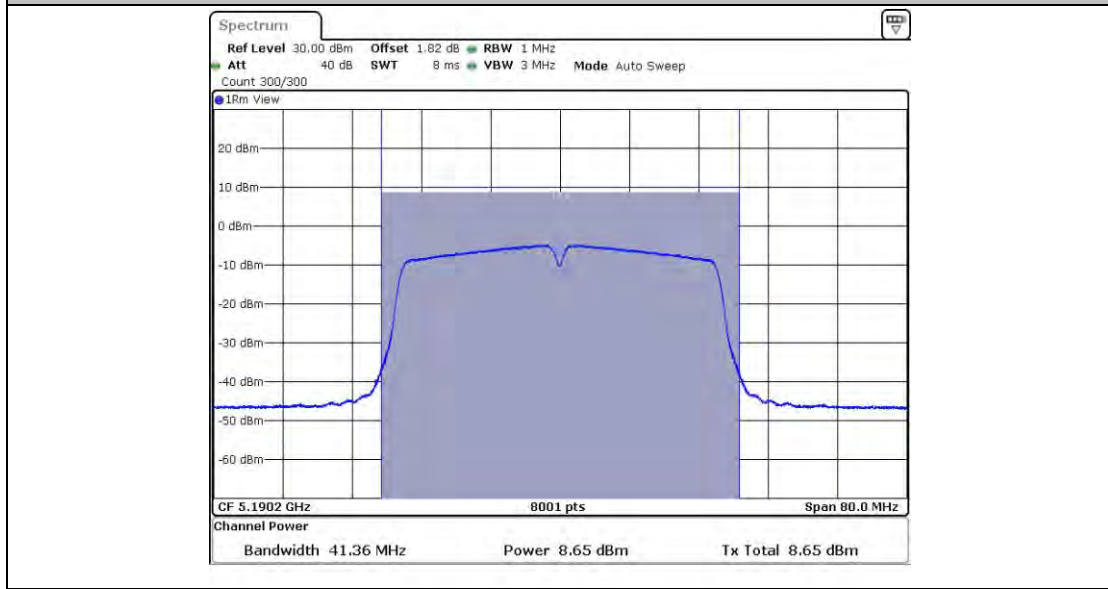
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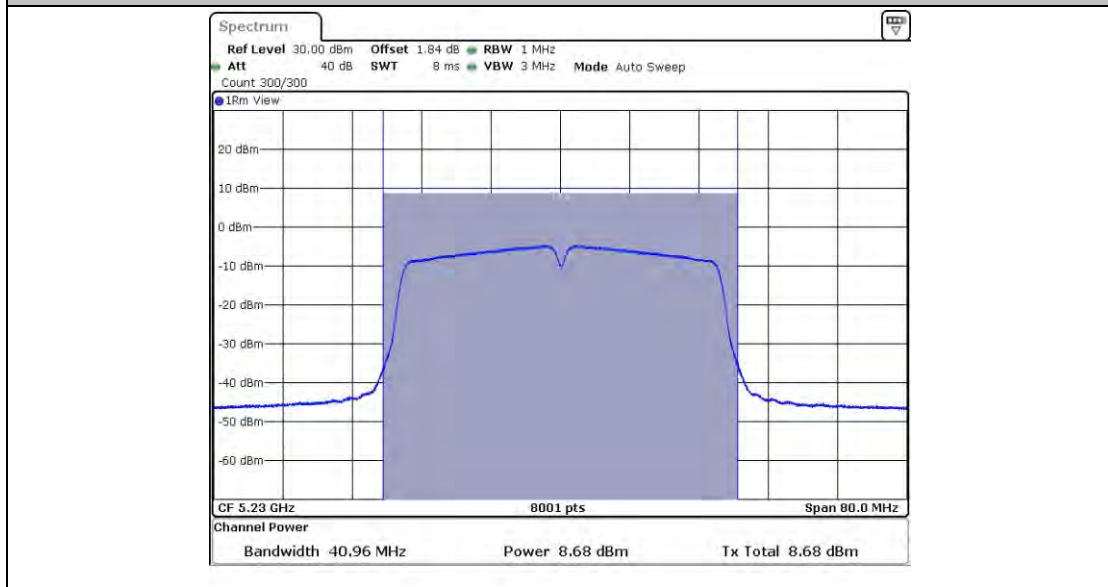
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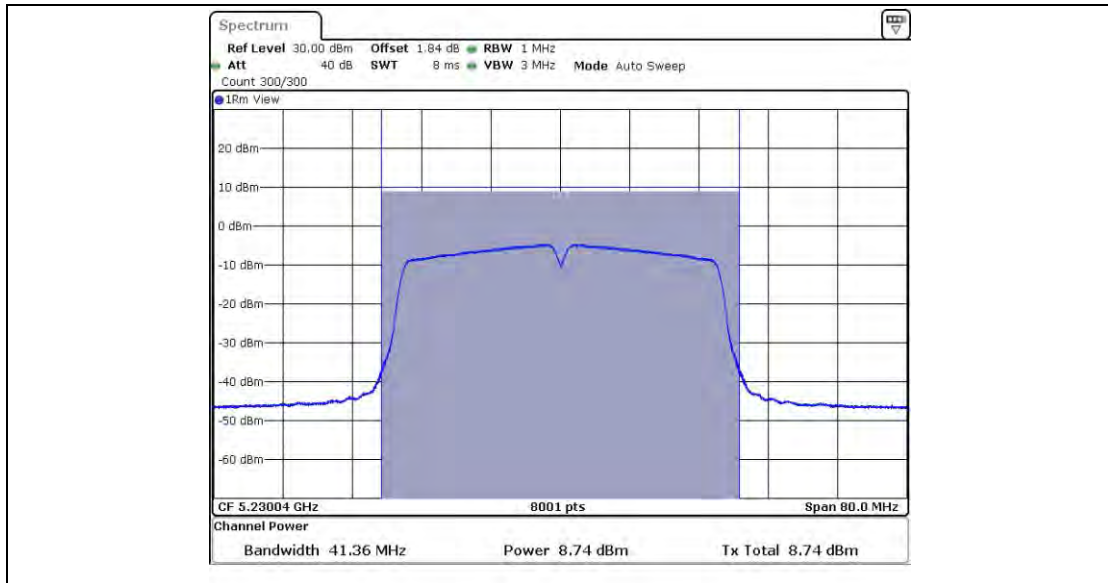
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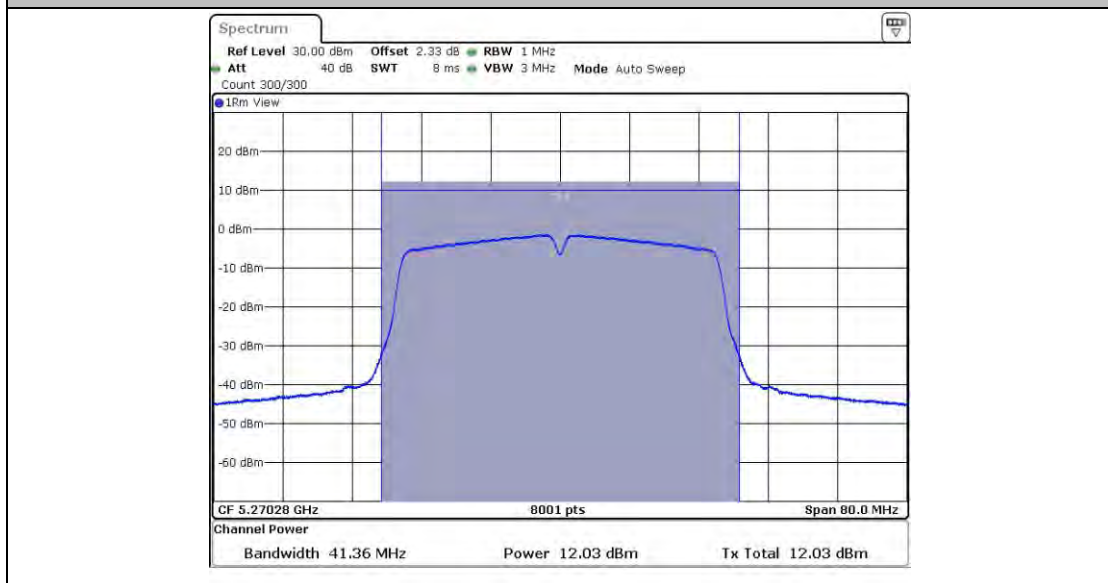
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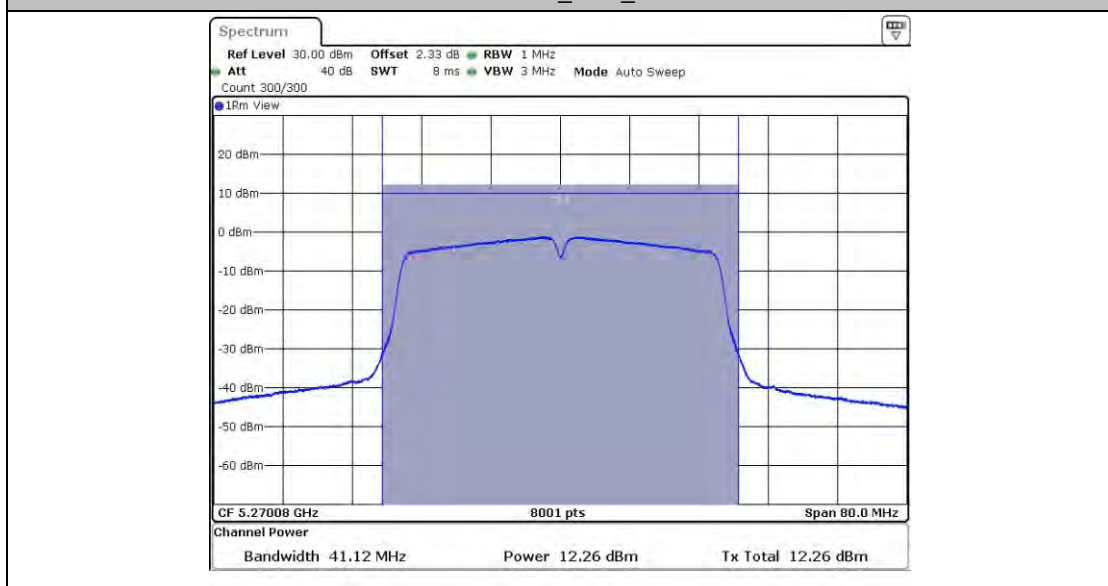
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11N40SISO\_Ant2\_5230

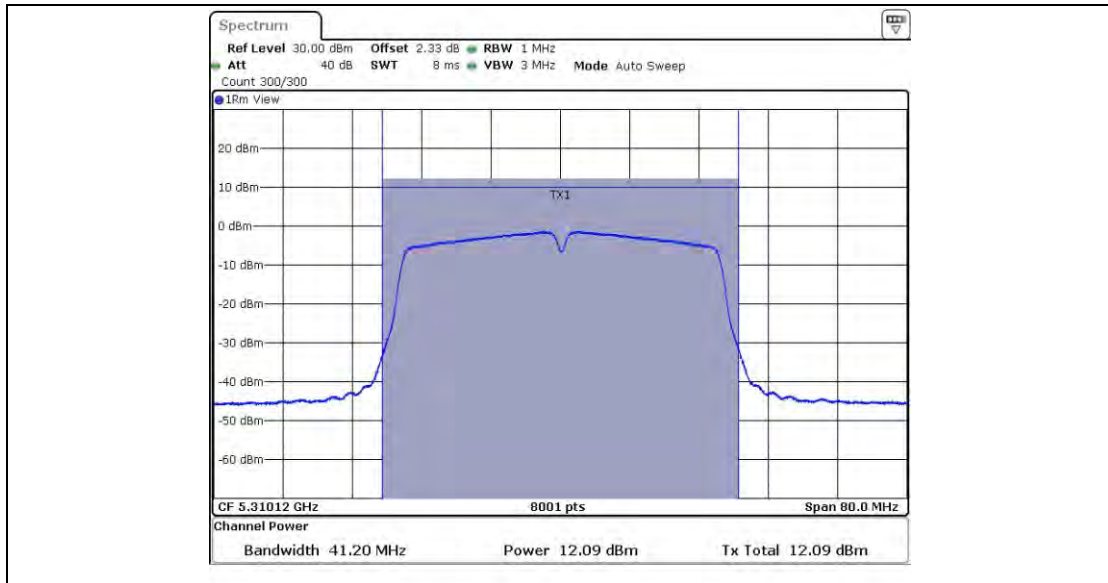


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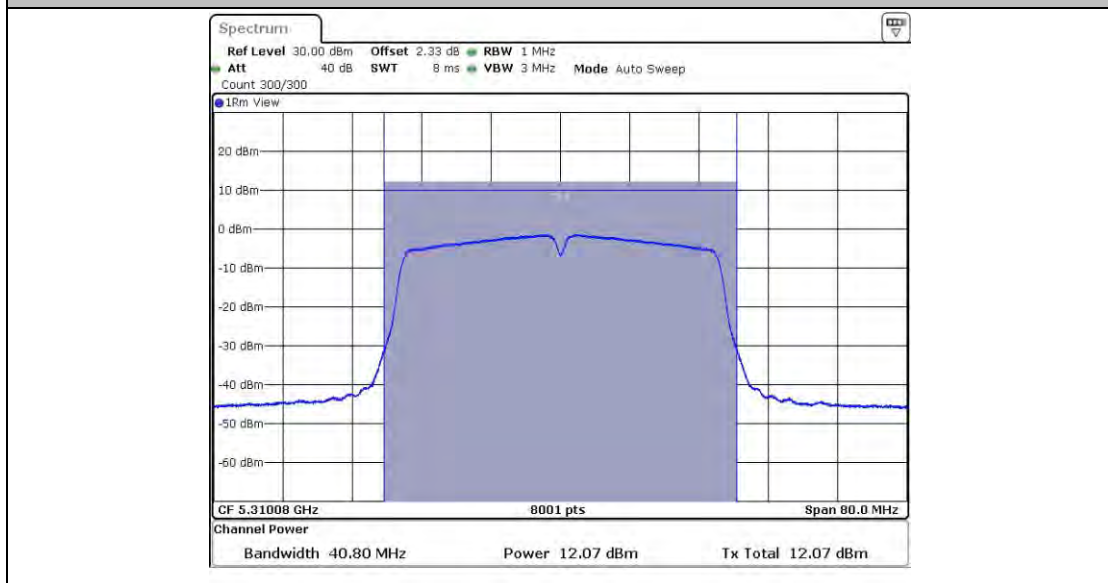


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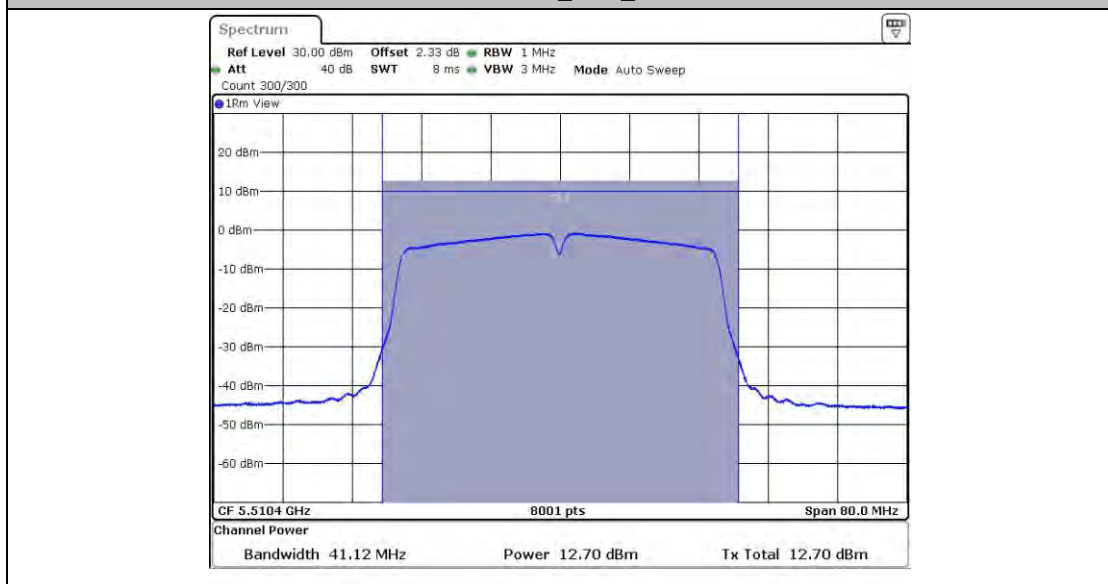




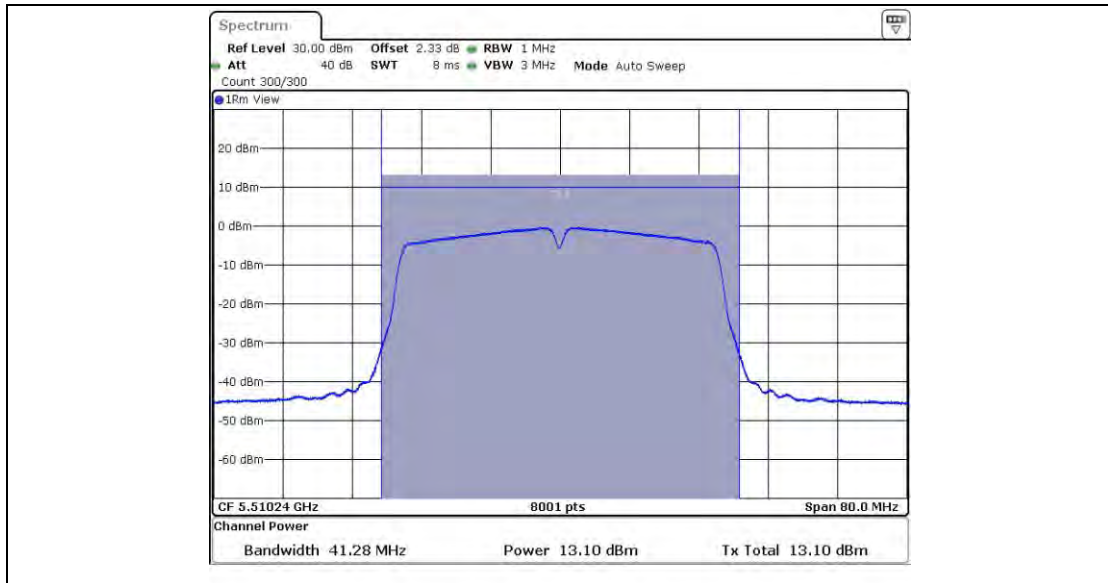
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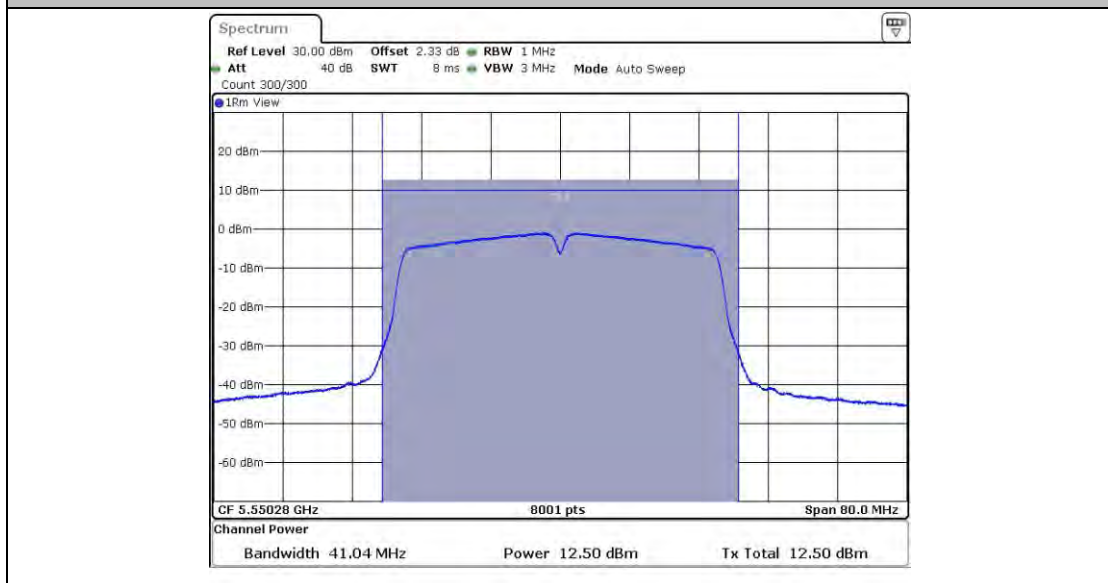
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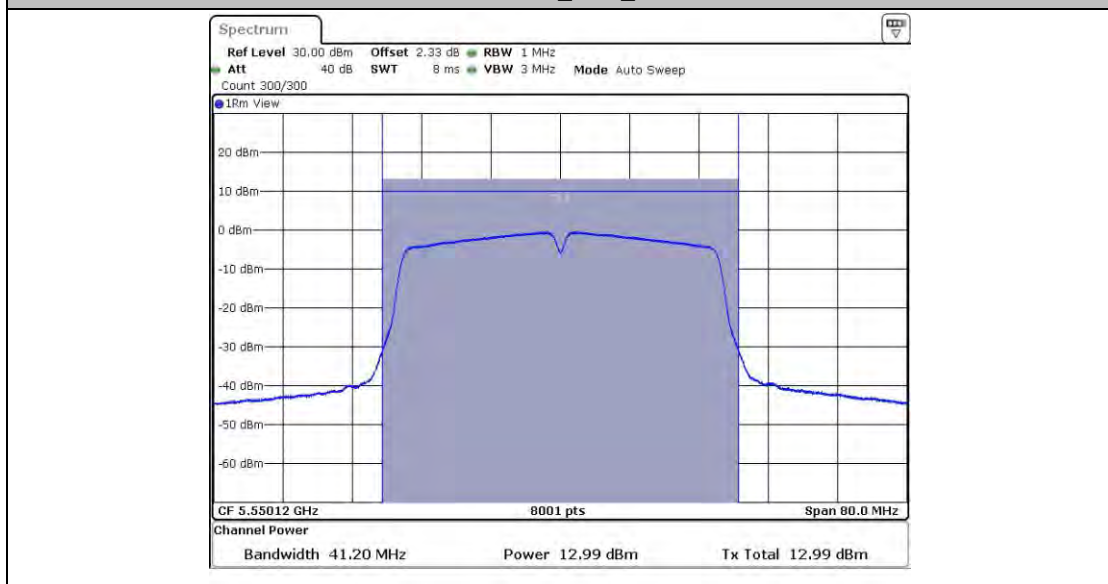
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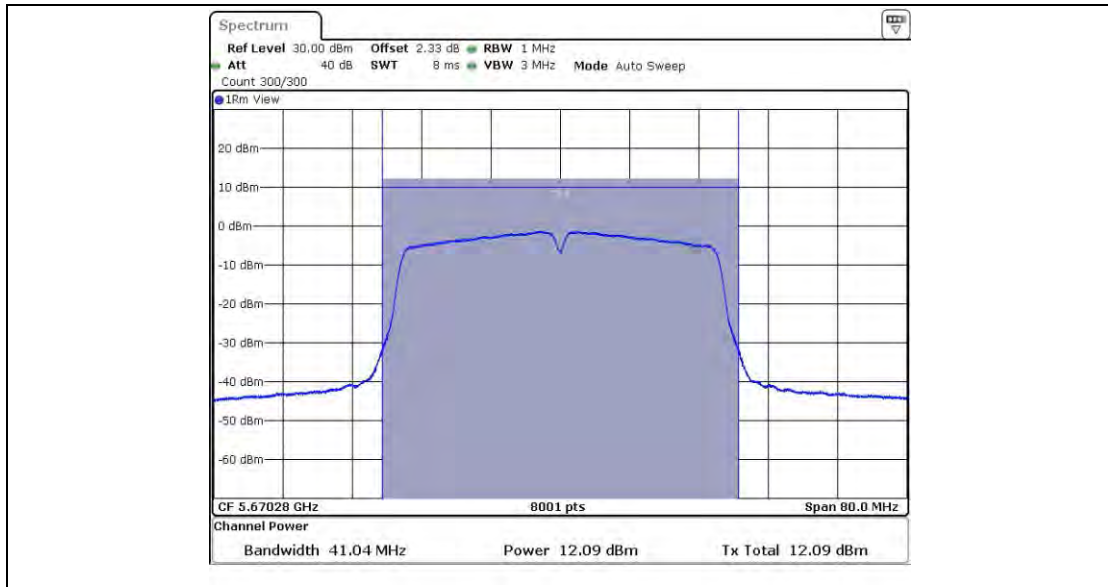
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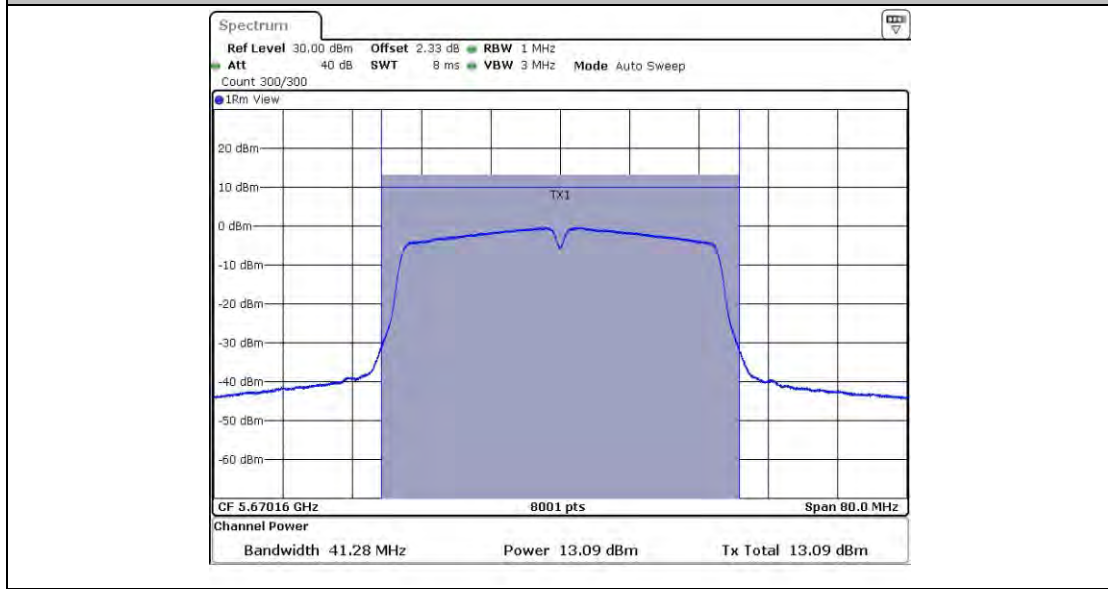
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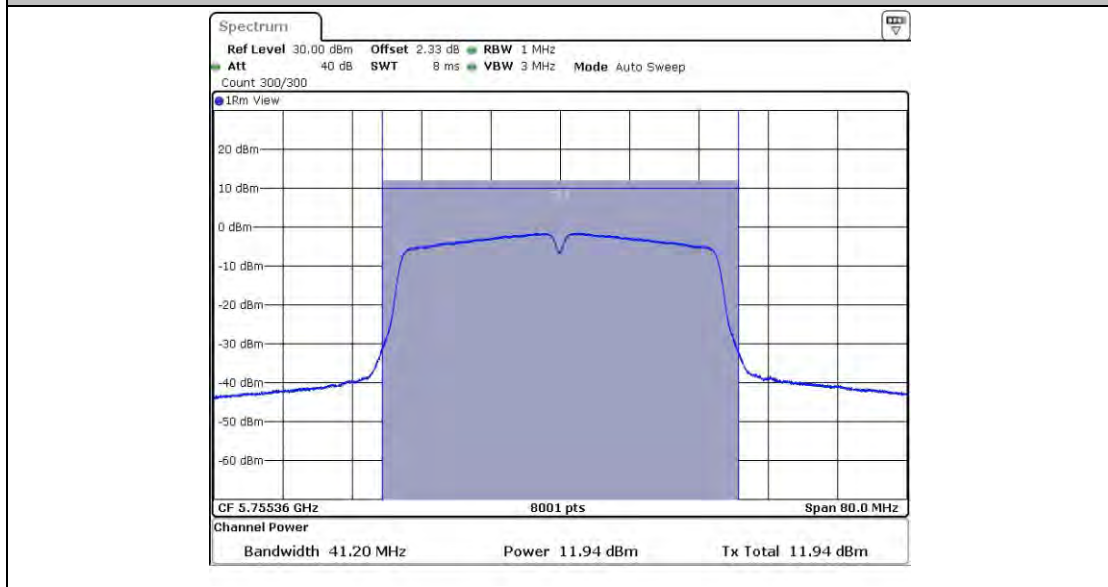
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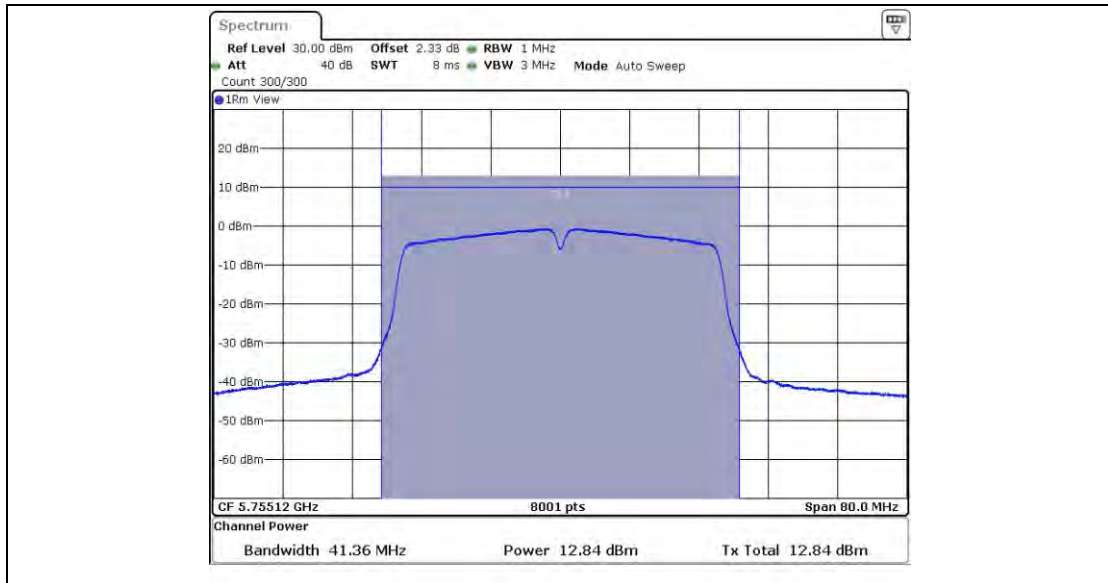
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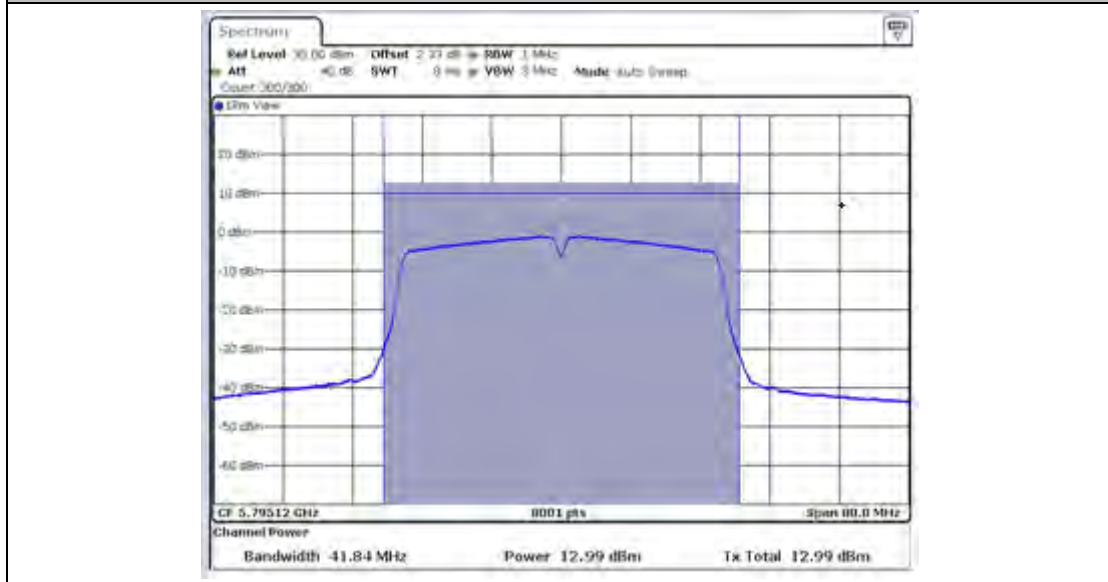
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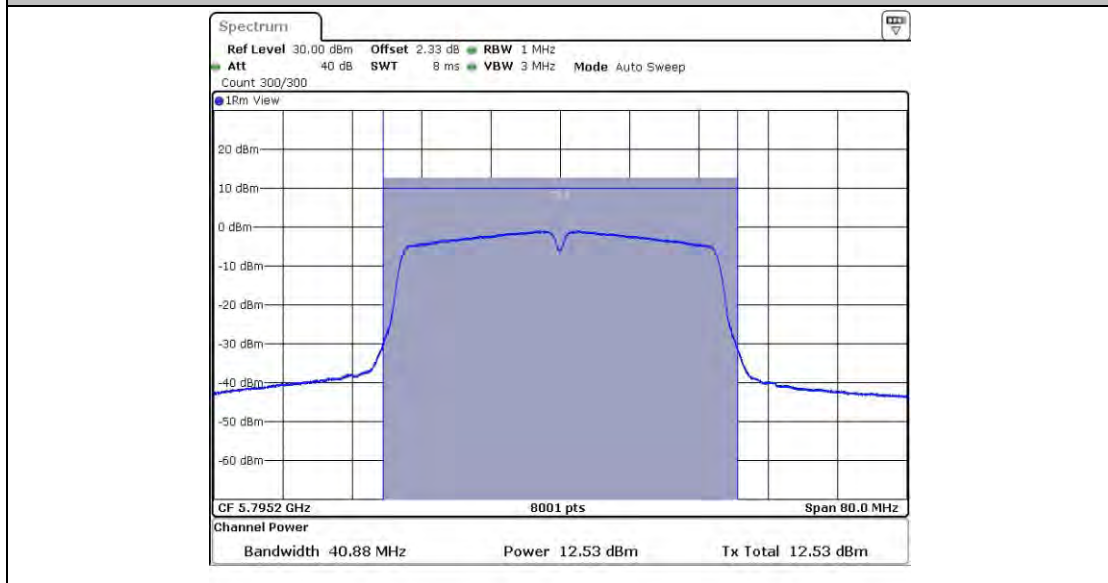
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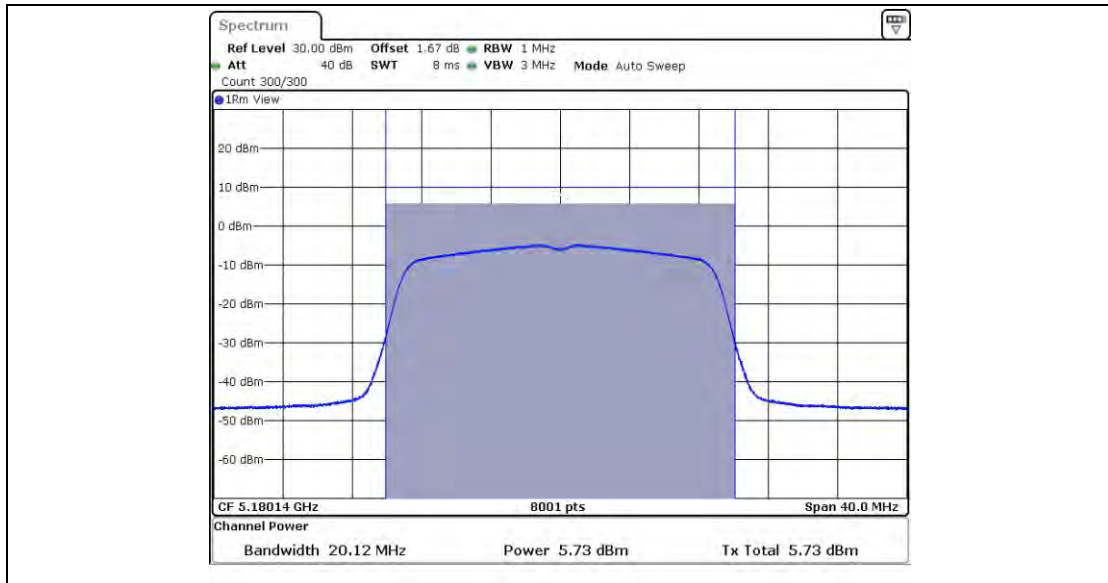
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11N40SISO\_Ant1\_5795



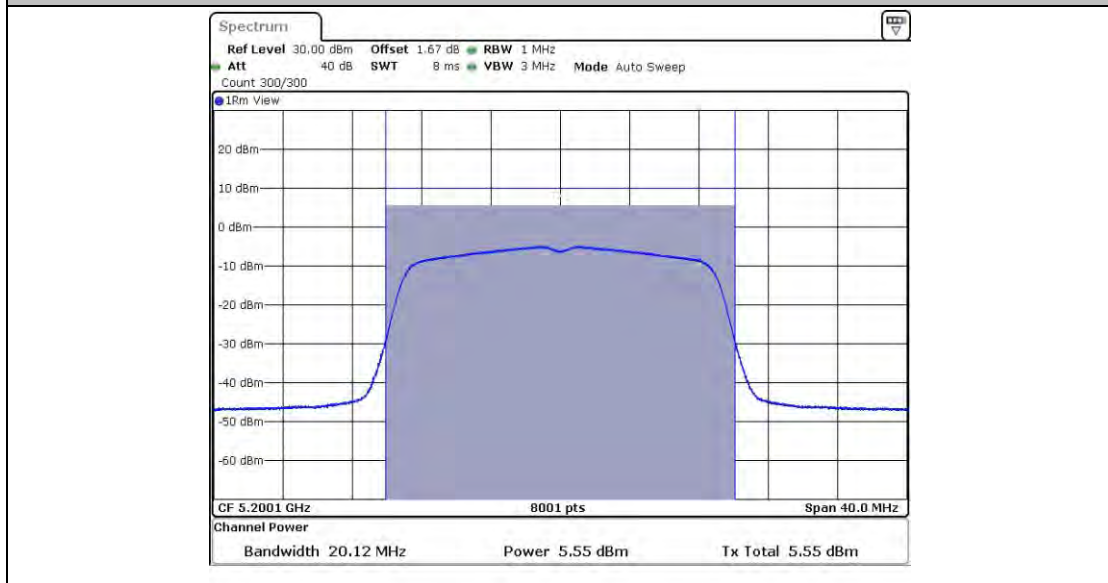
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11AC20SISO\_Ant1\_5180

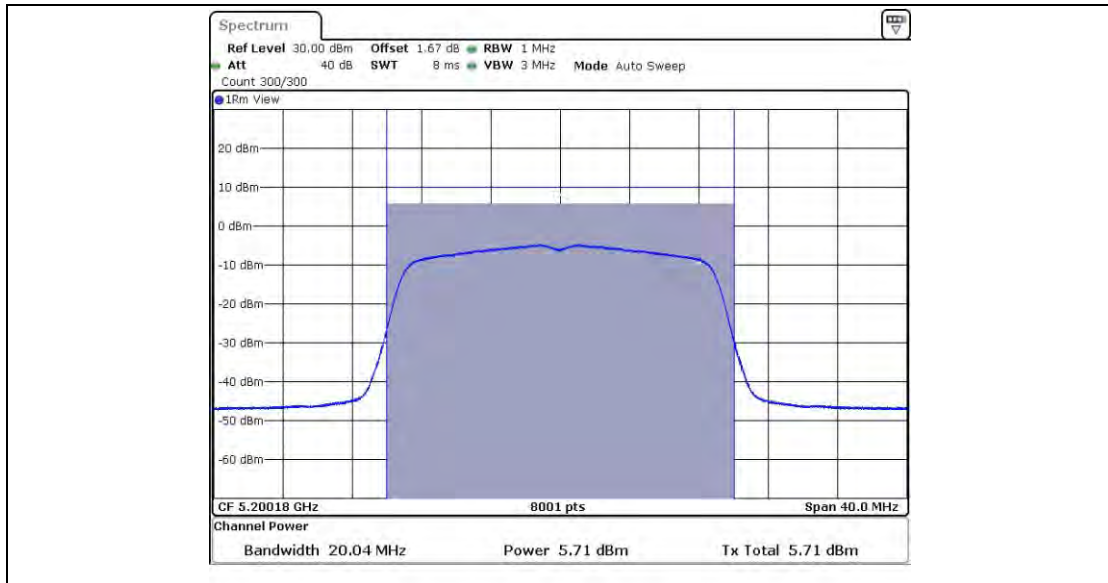


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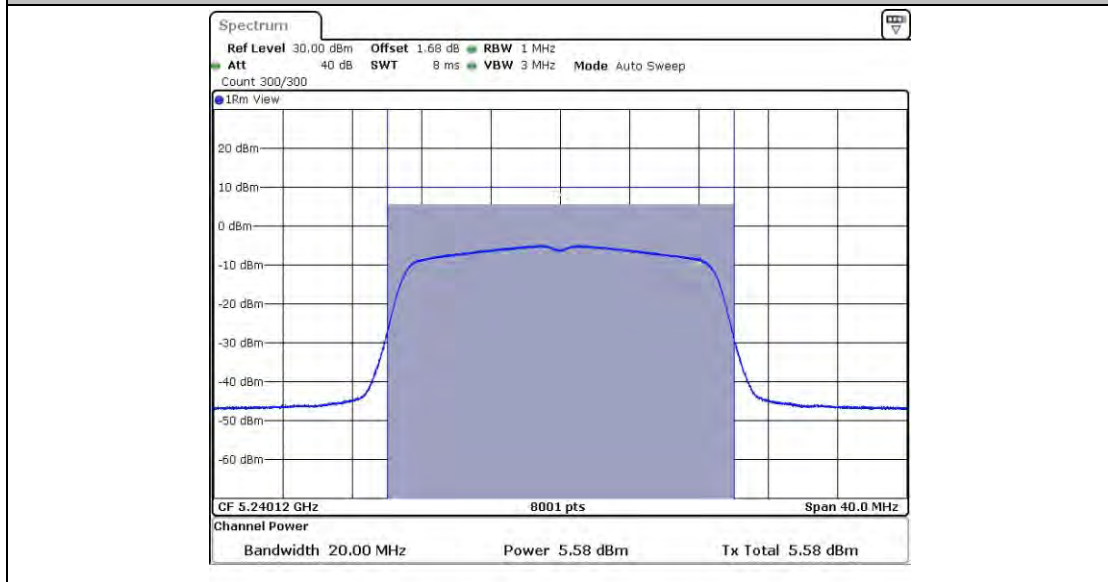


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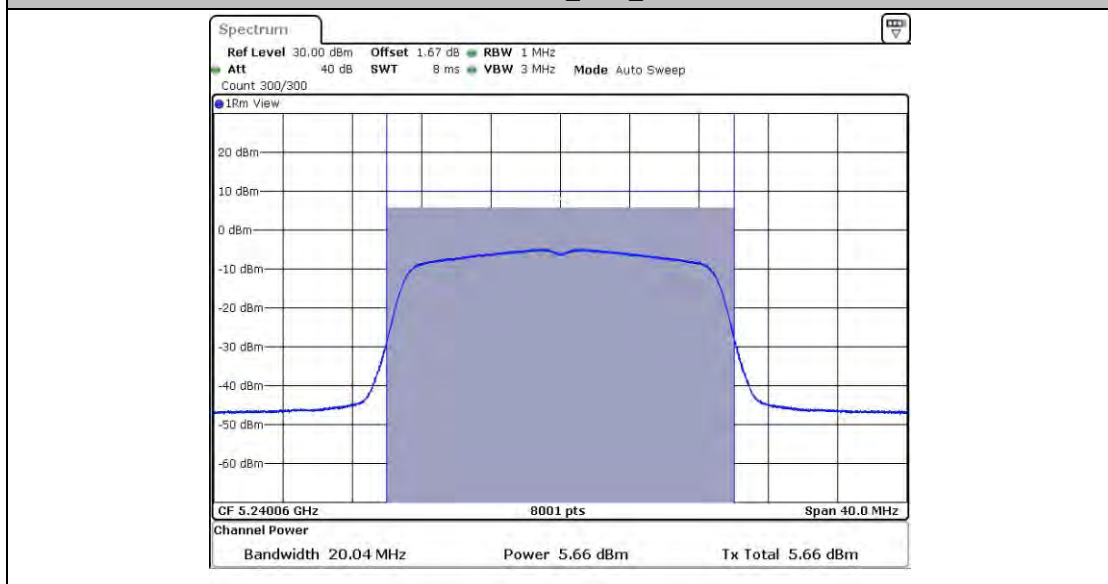




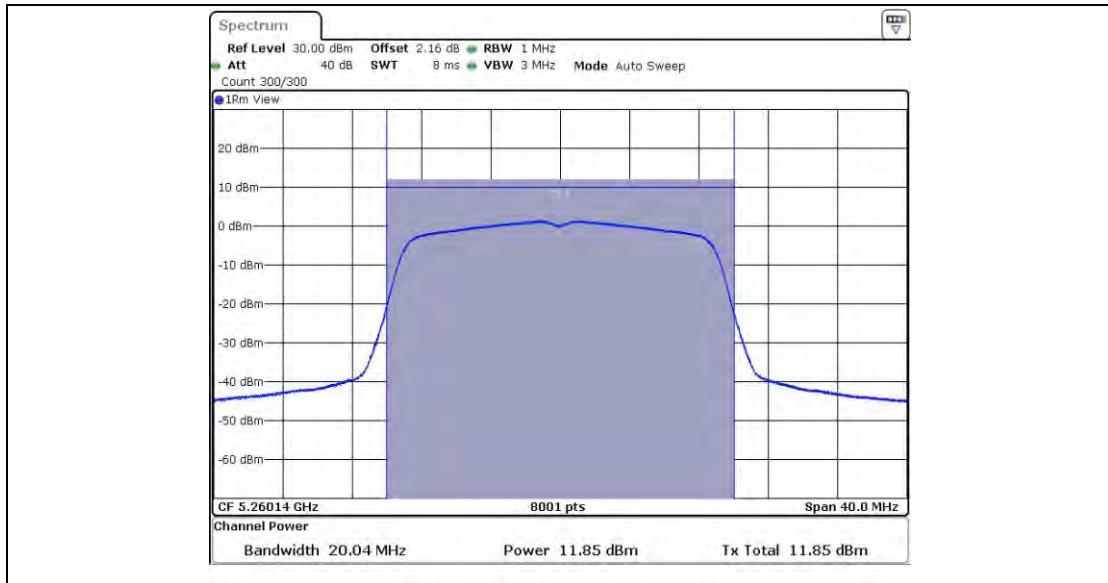
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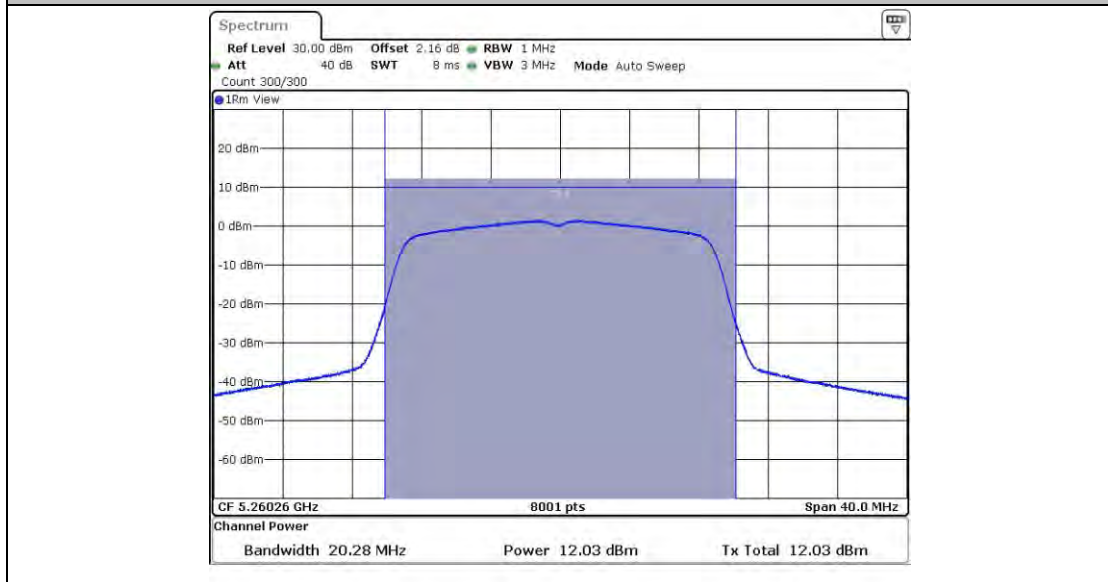
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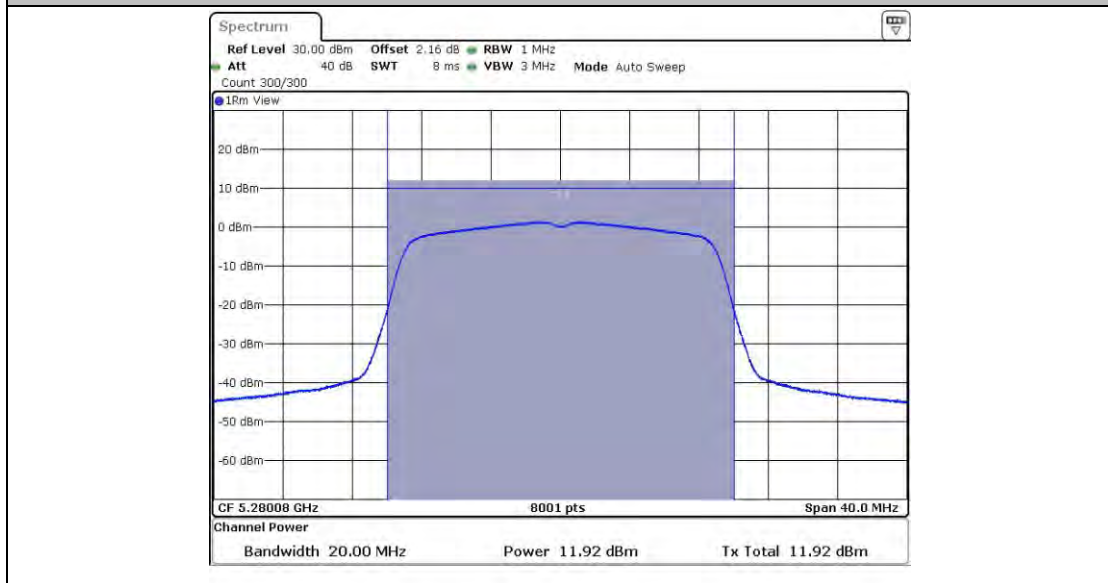
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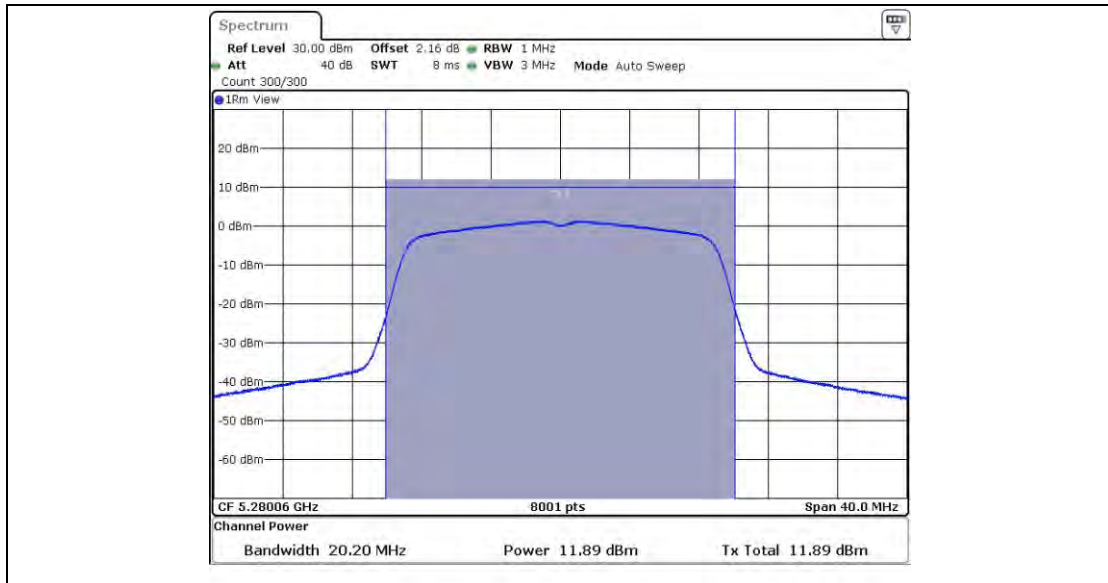
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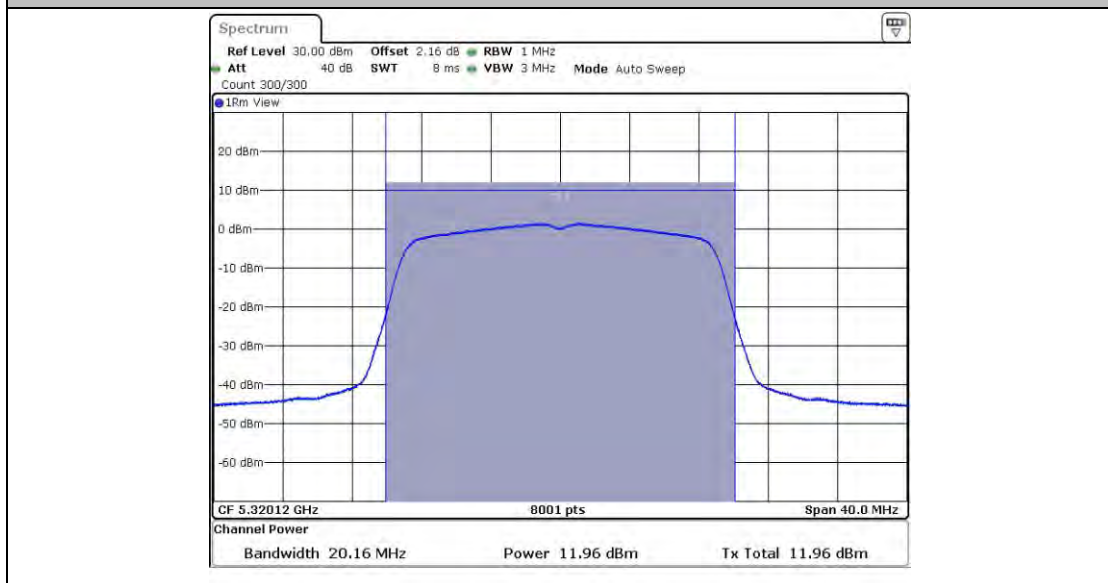
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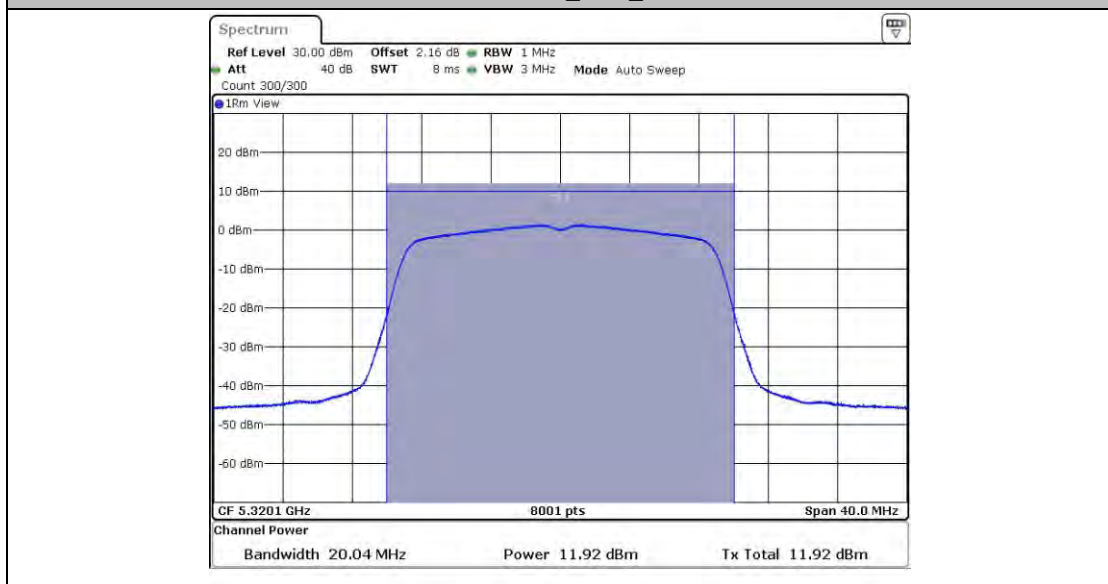
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11AC20SISO\_Ant2\_5280

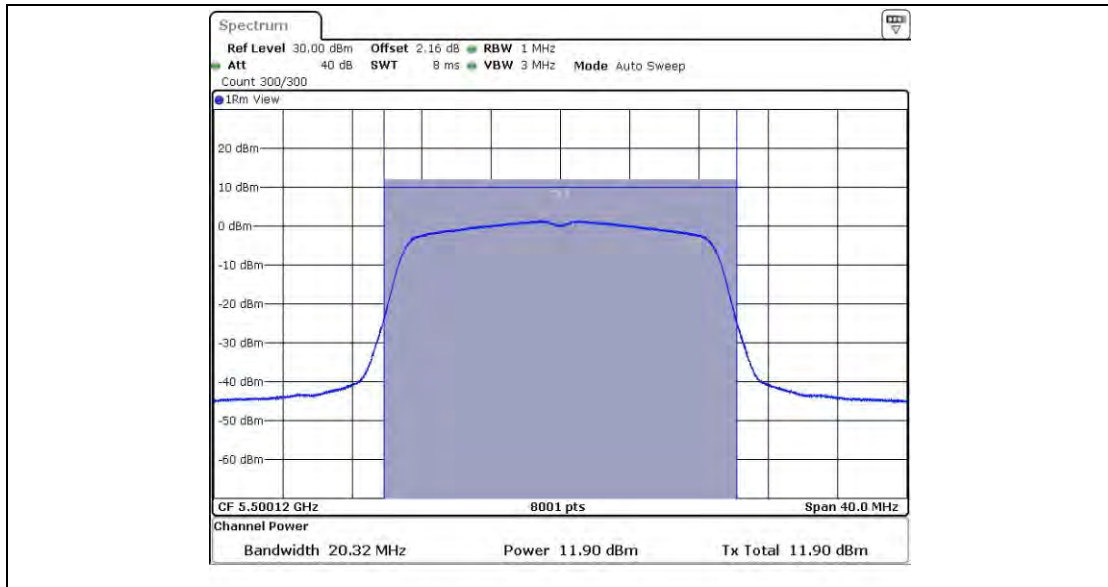


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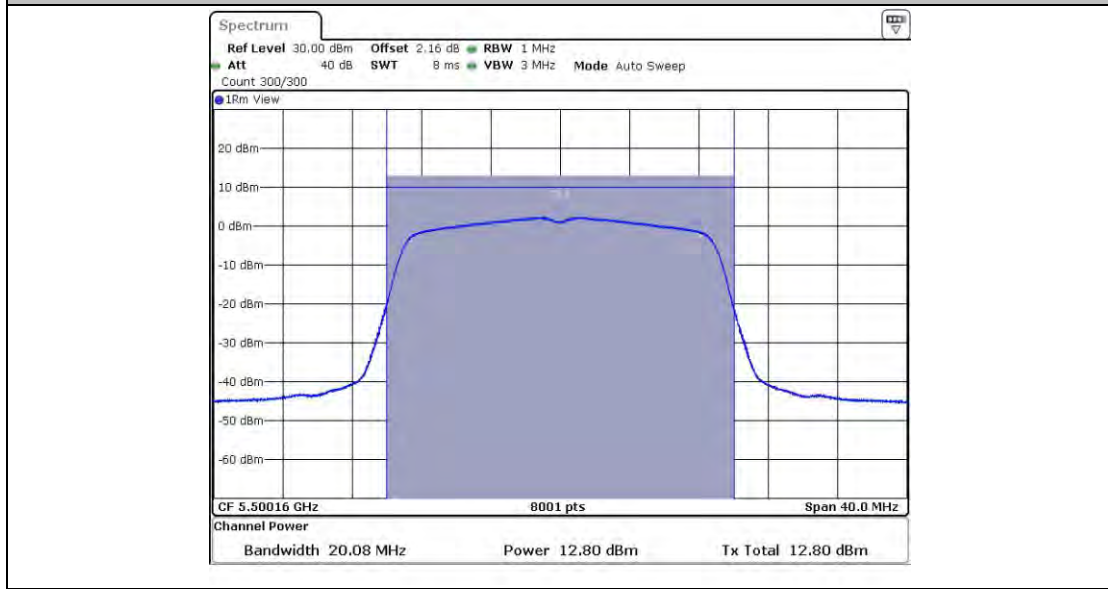


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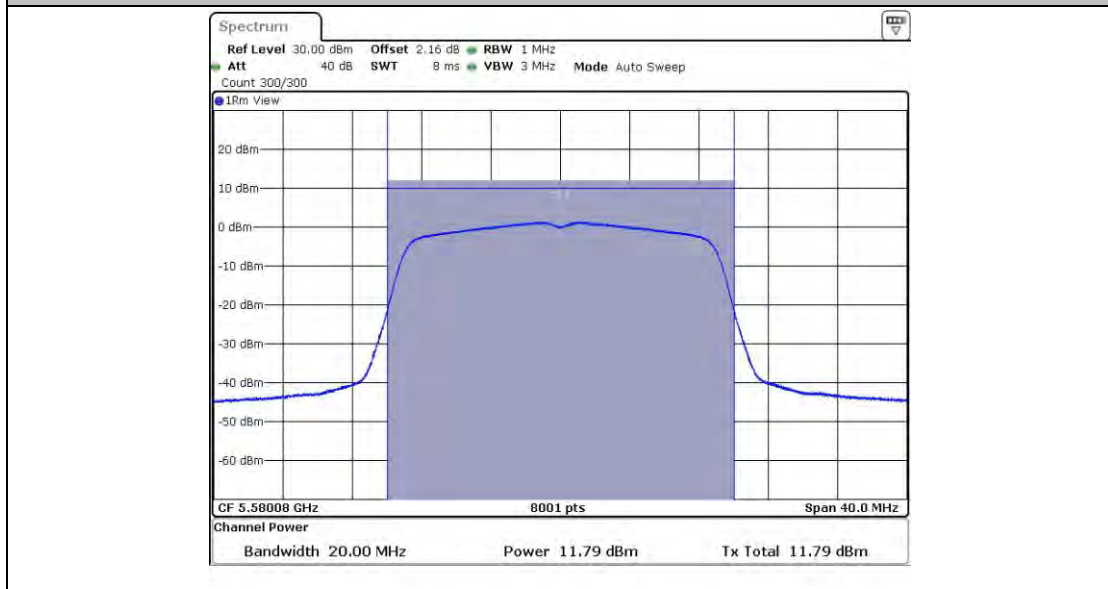




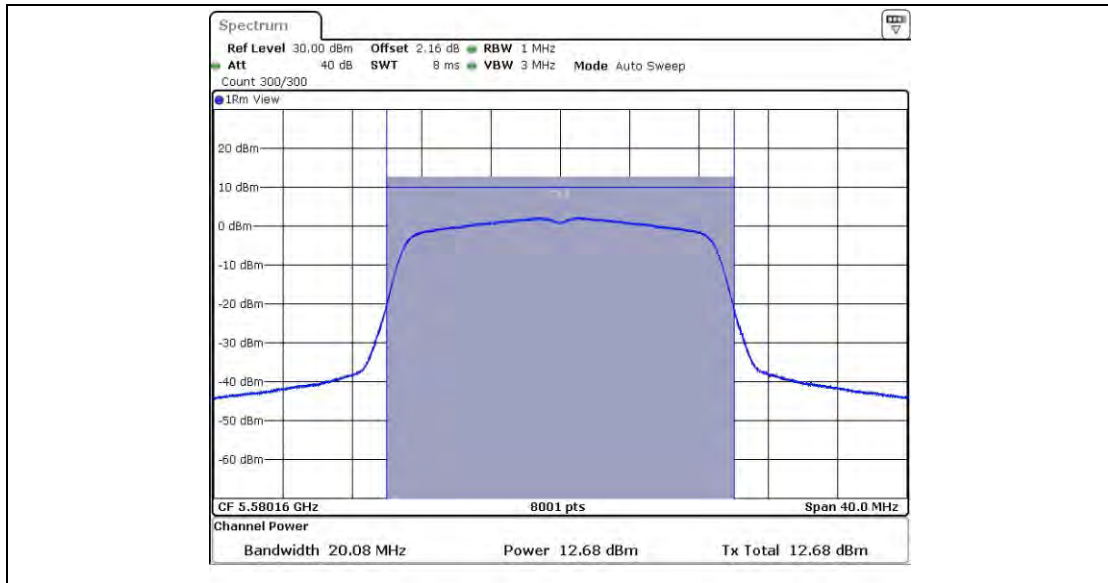
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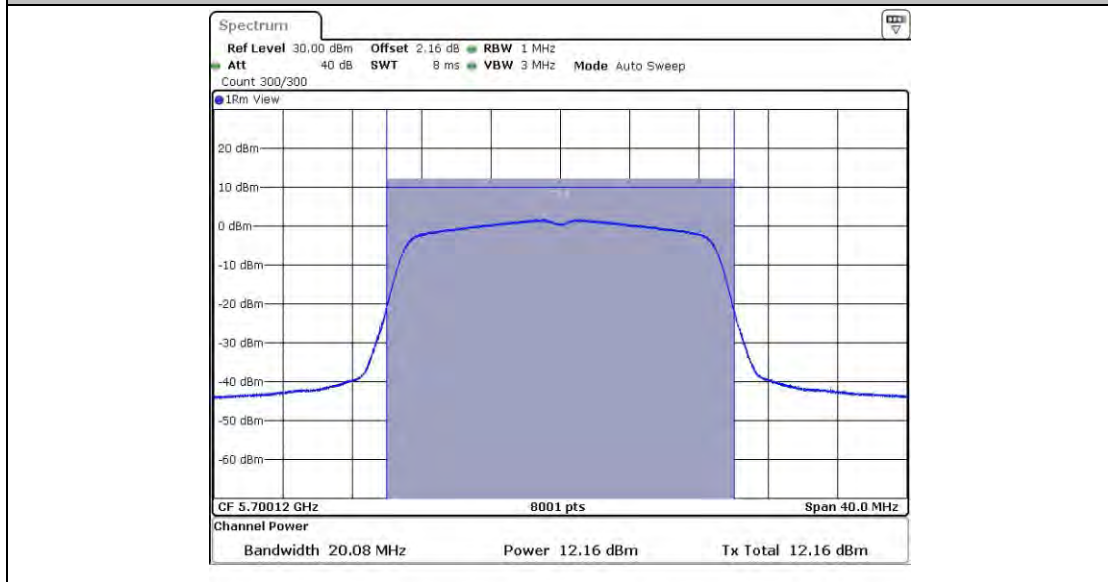
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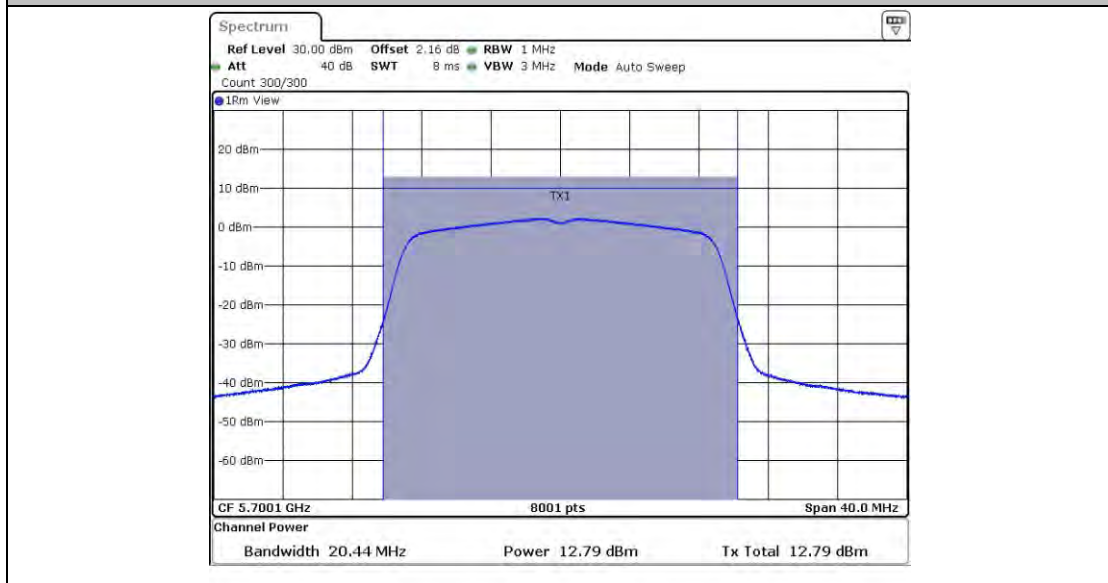
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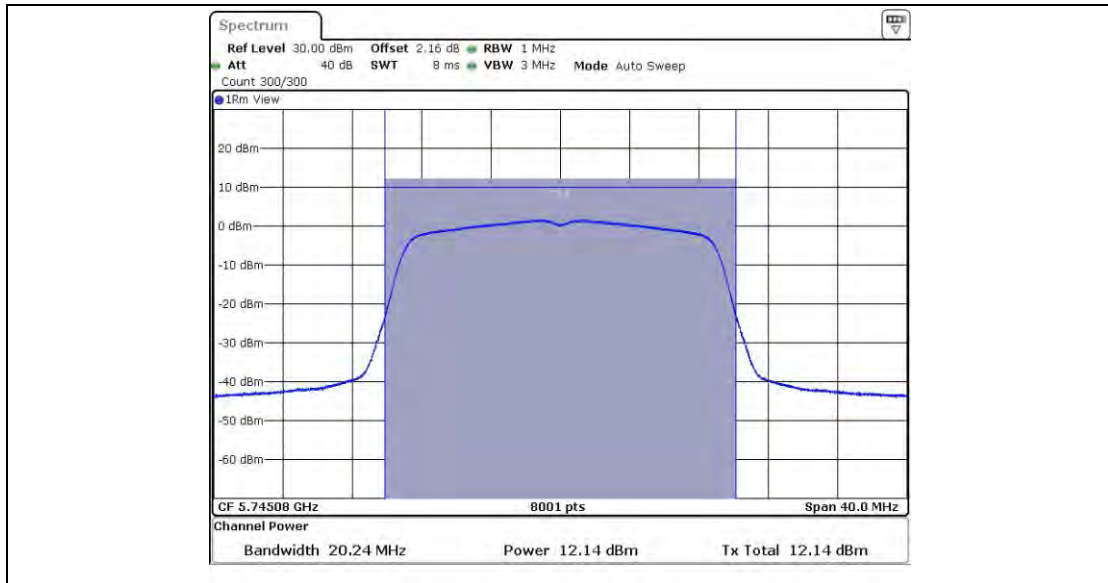
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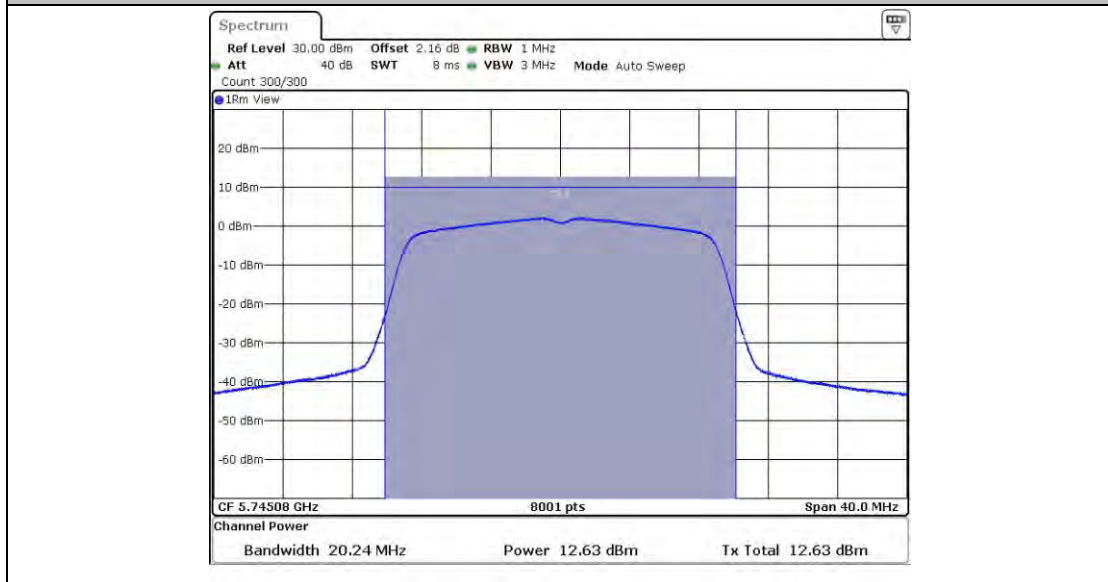
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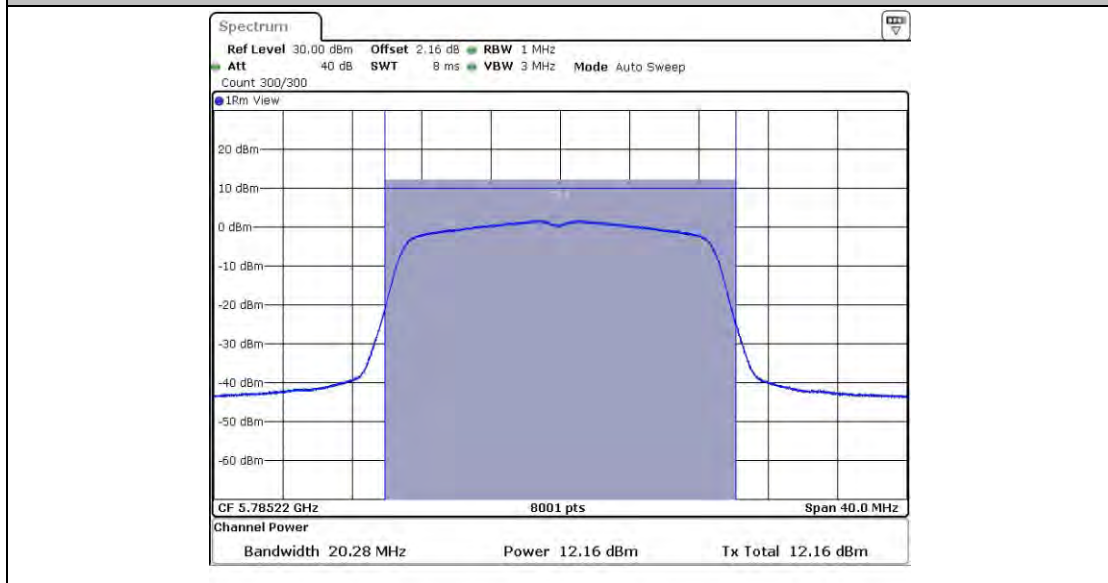
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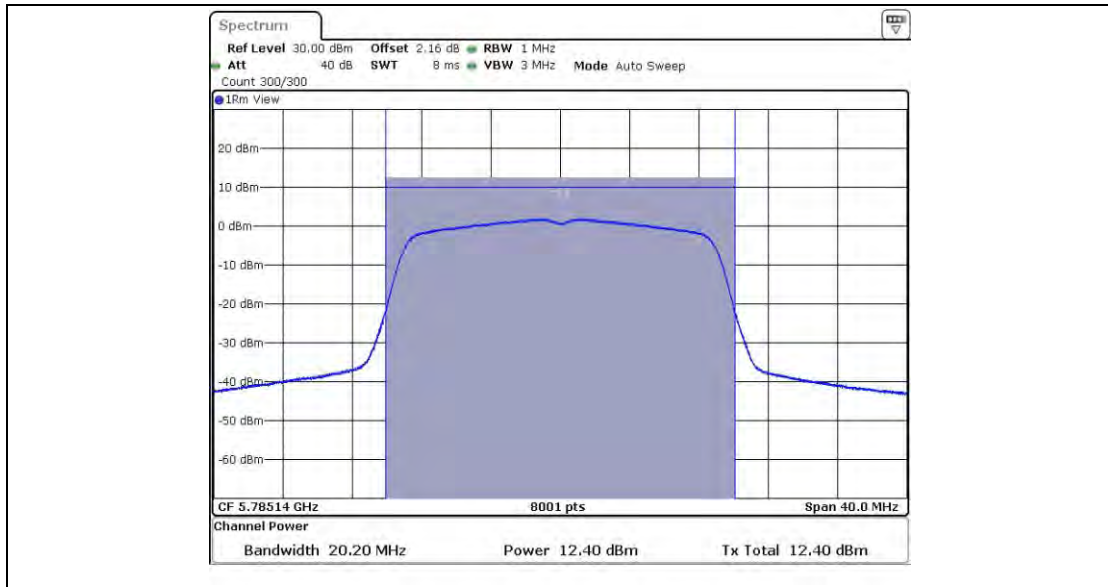
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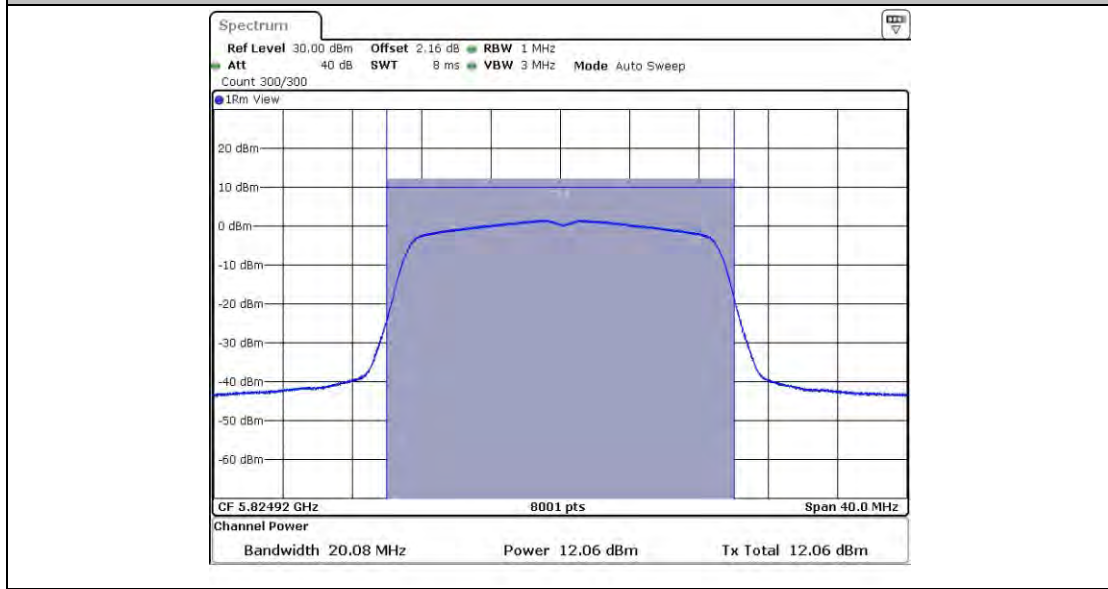
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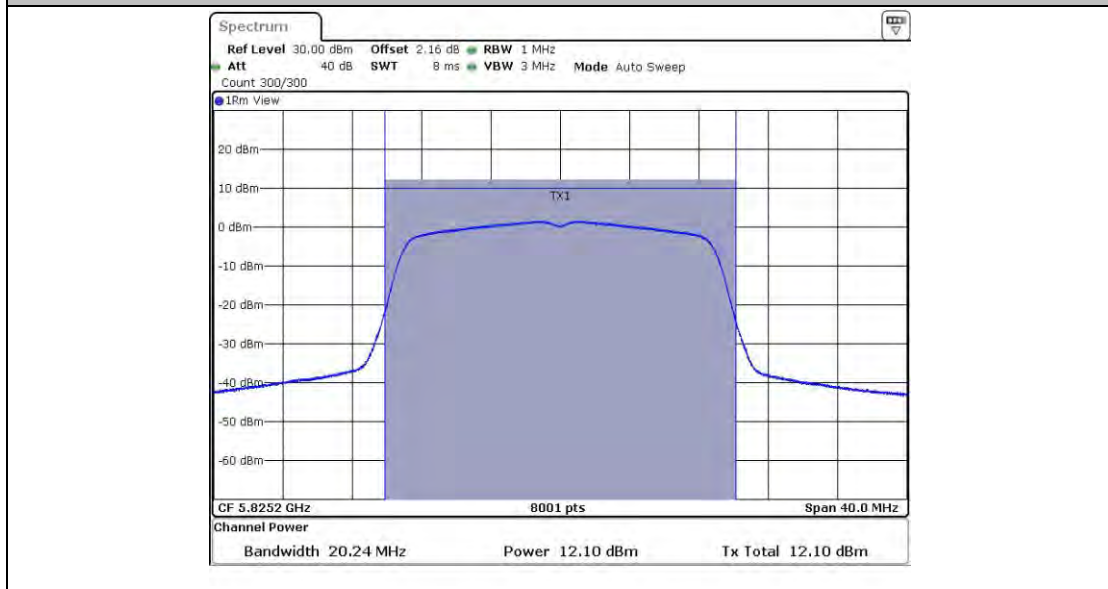
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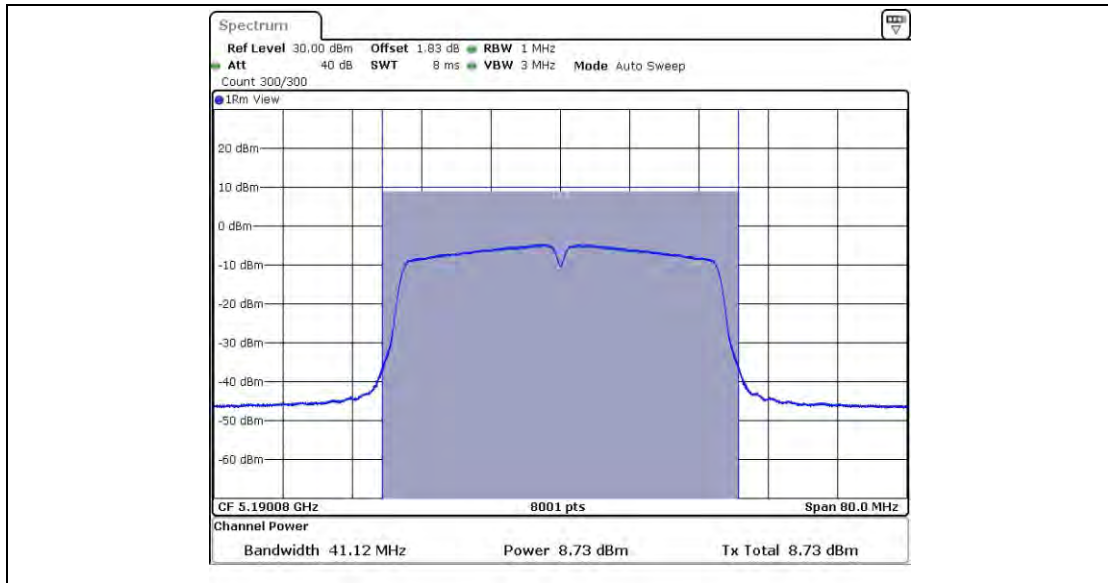
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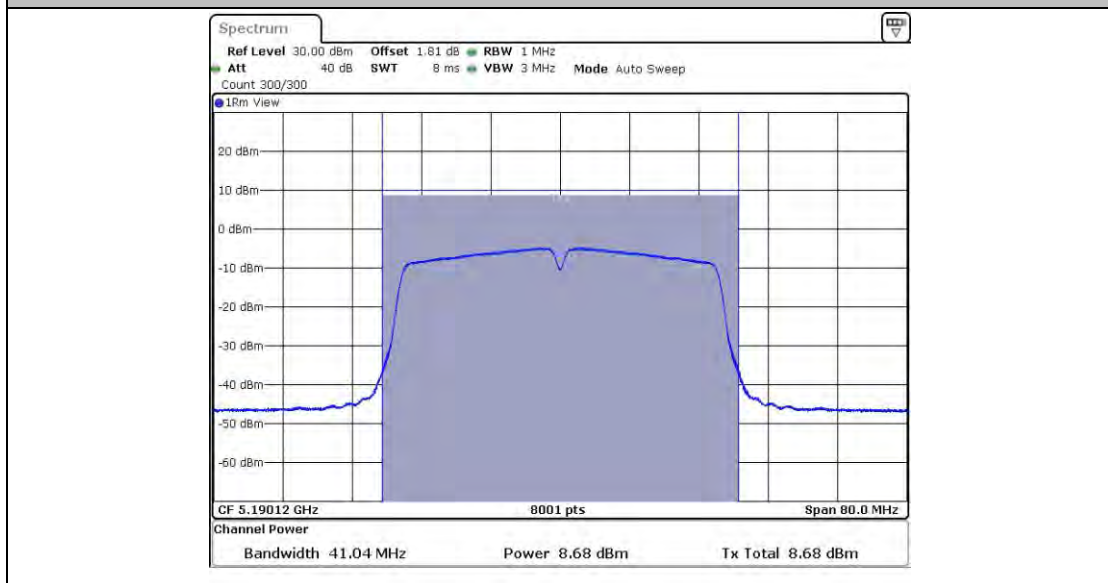
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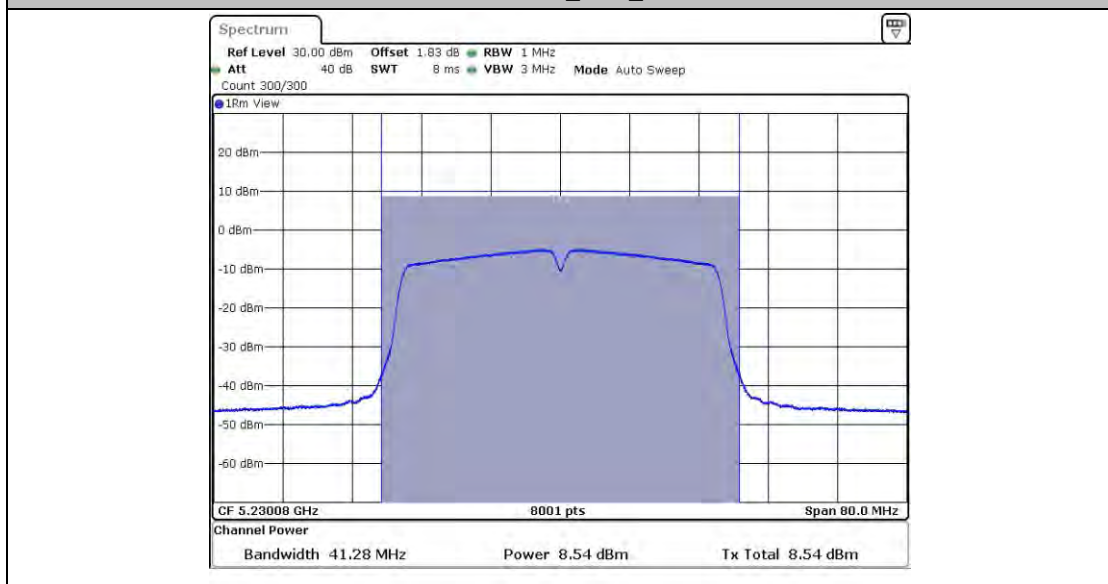
11AC20SISO\_Ant2\_5825



11AC40SISO\_Ant1\_5190

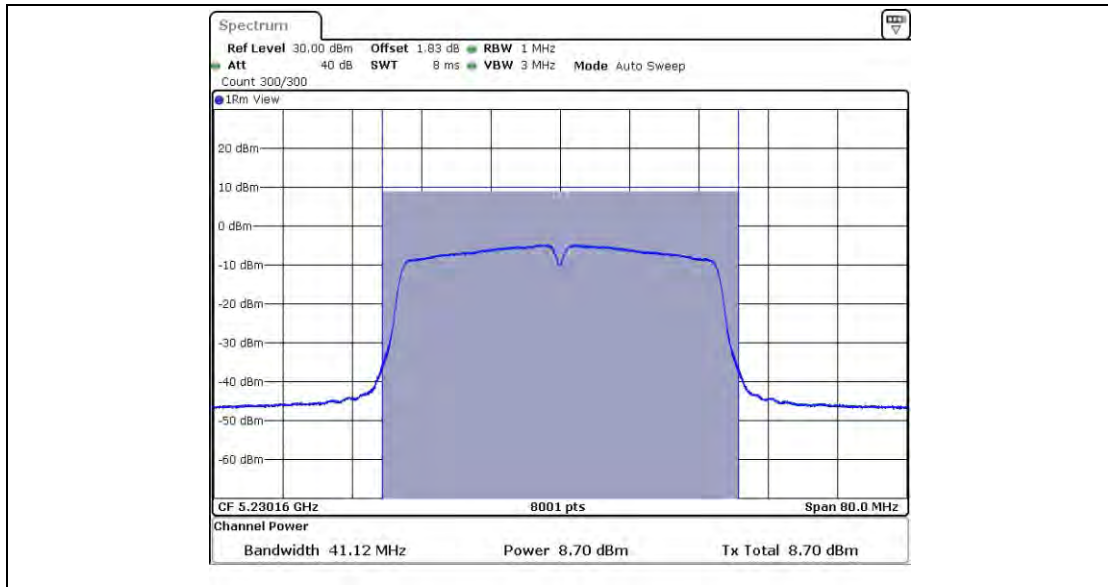


11AC40SISO\_Ant2\_5190

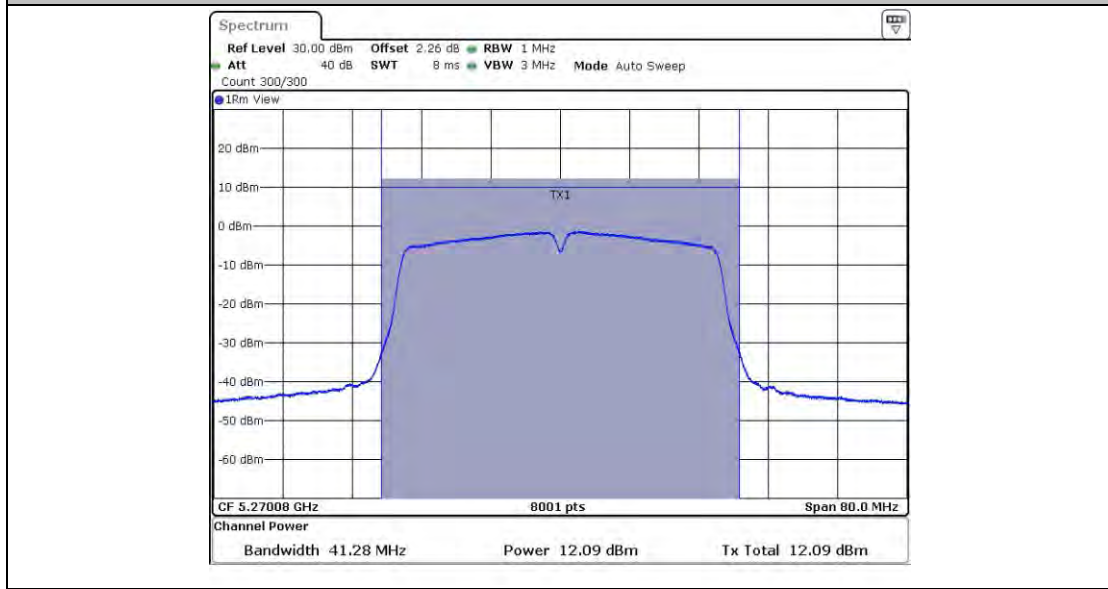


11AC40SISO\_Ant1\_5230

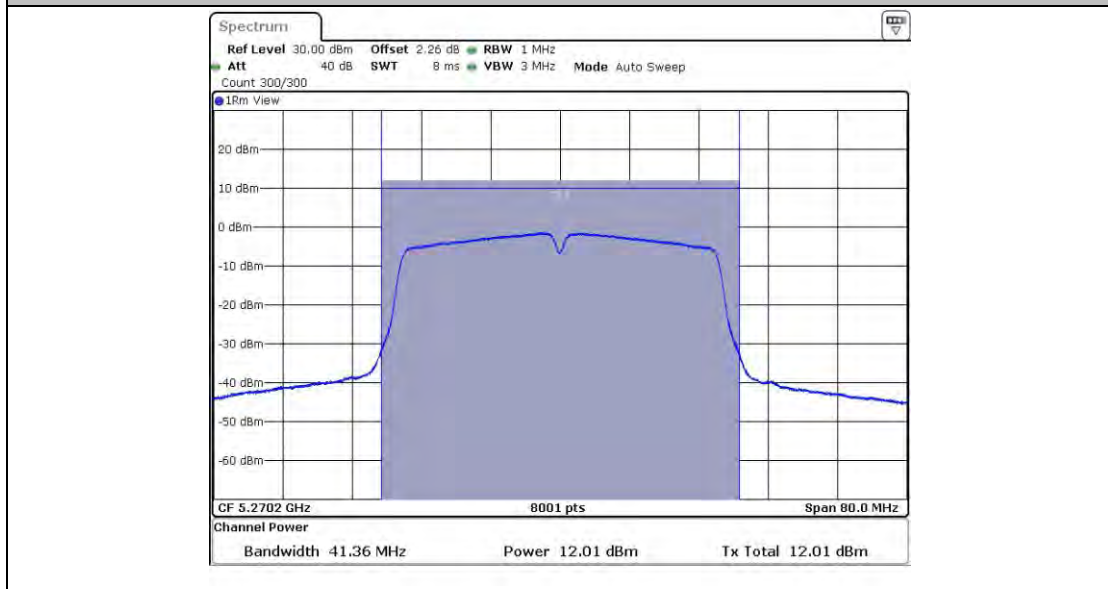




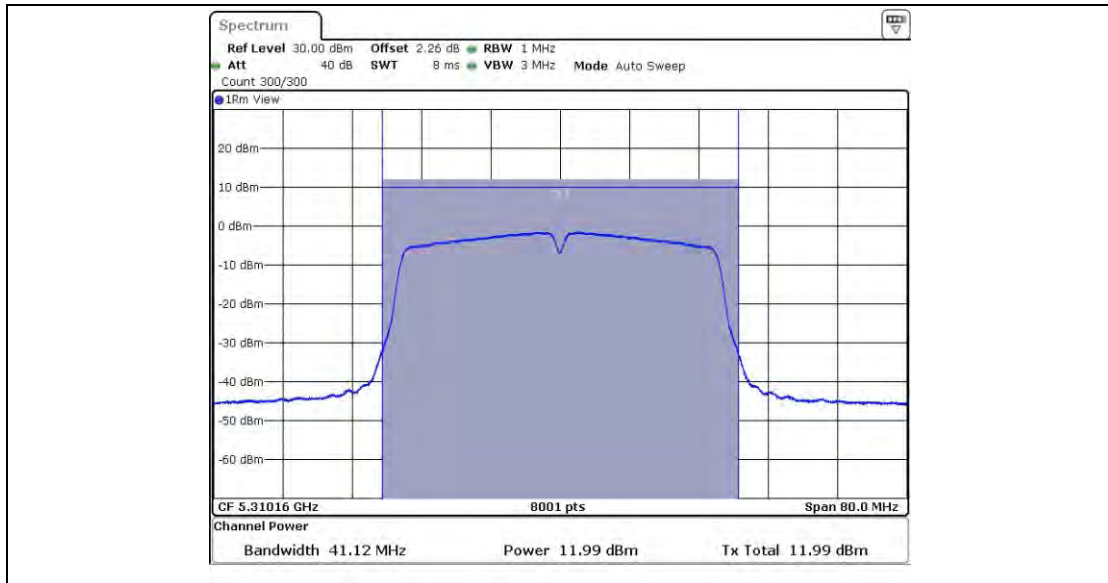
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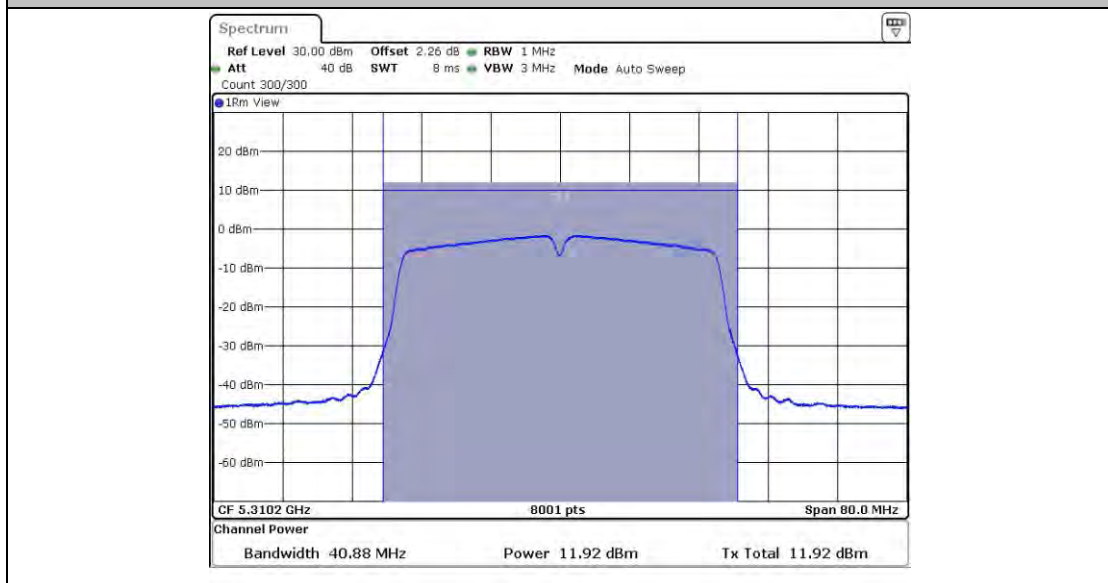
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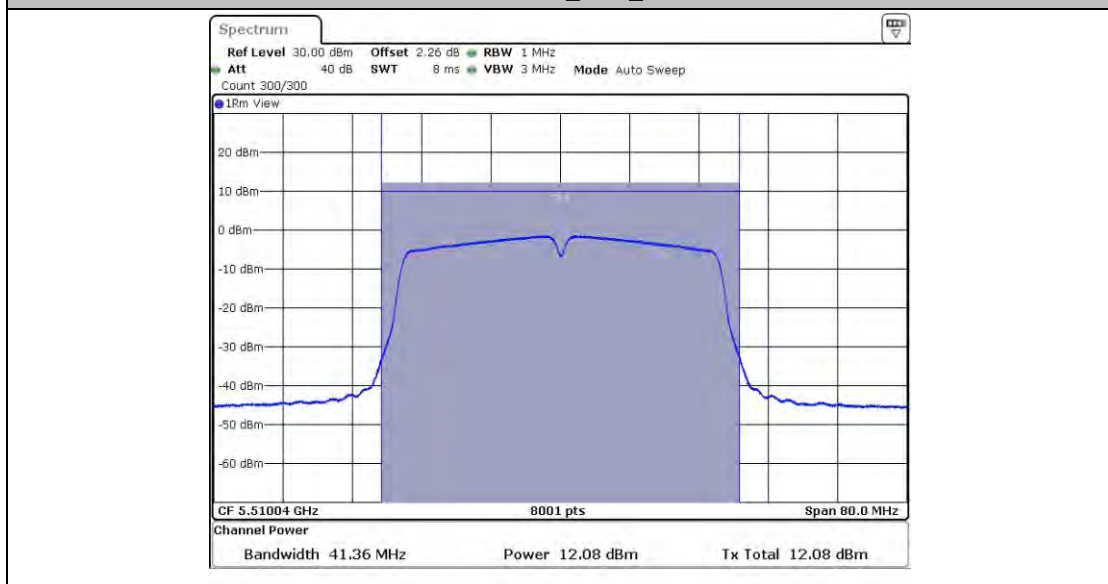
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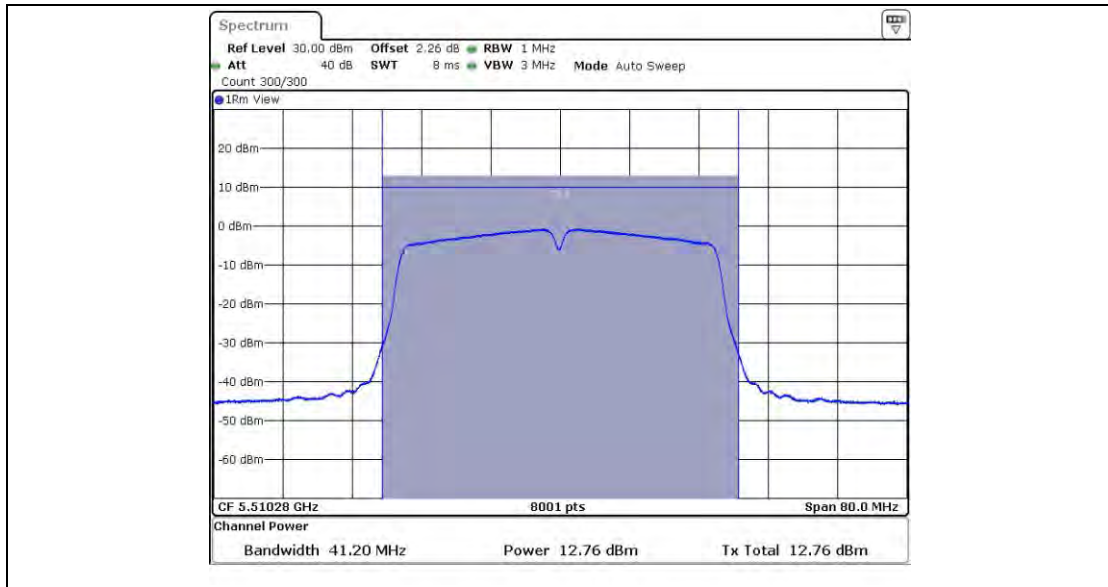
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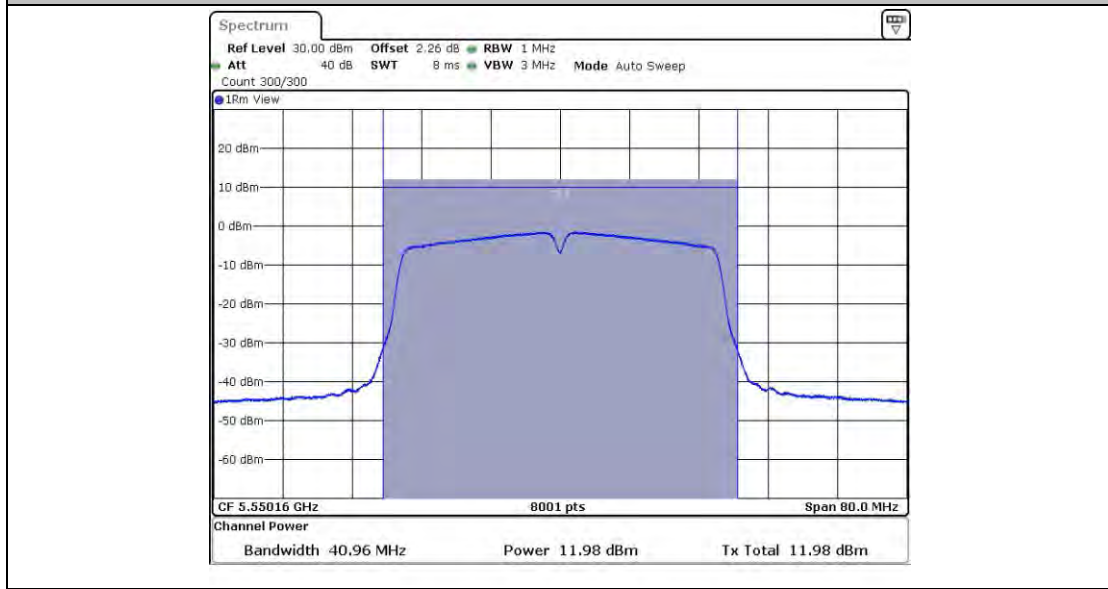
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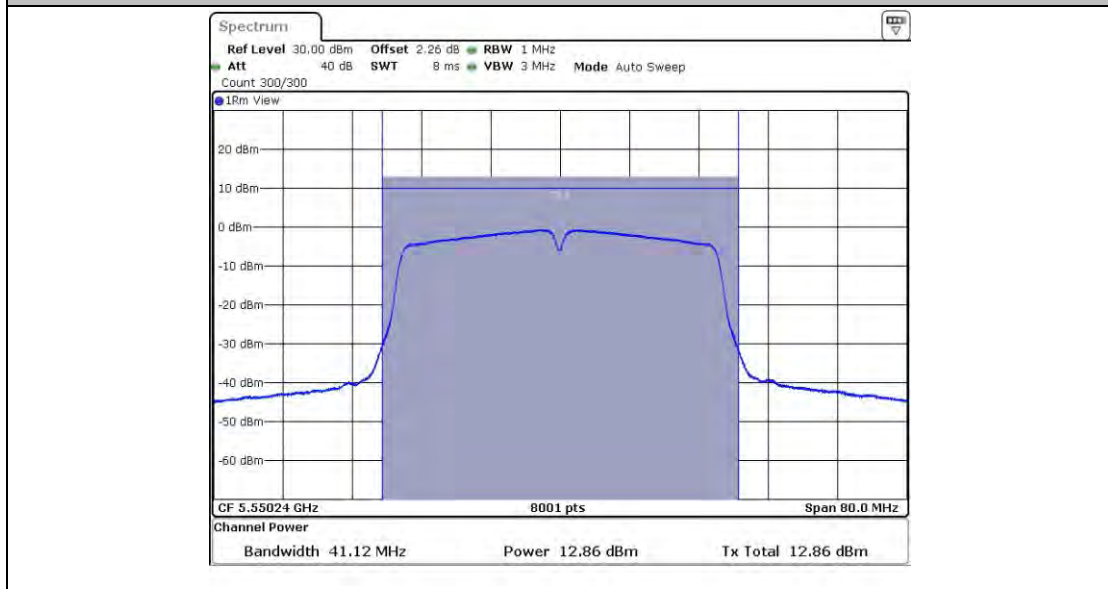
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11AC40SISO\_Ant2\_5510

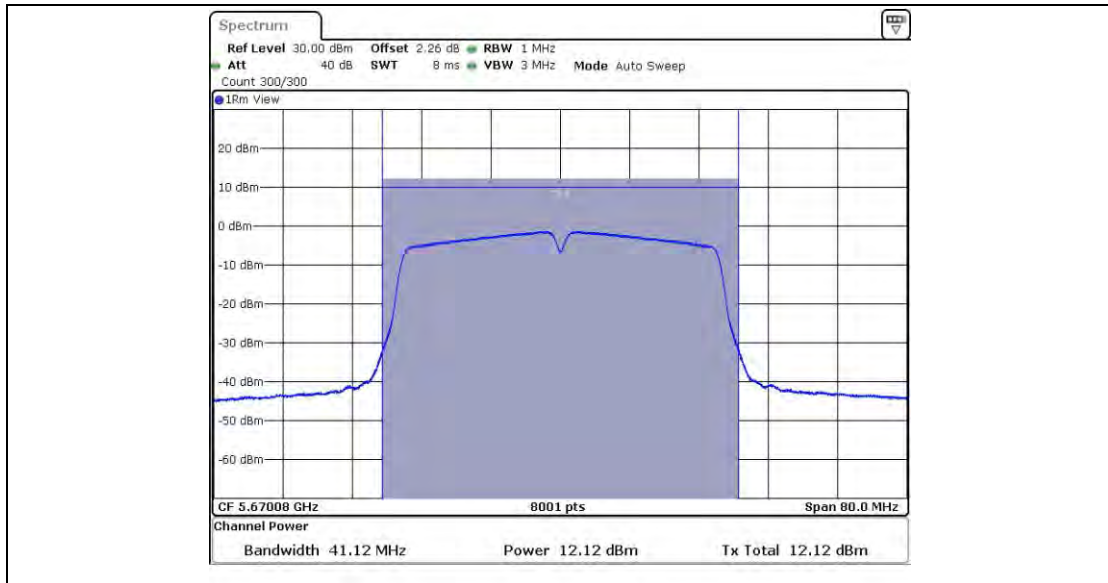


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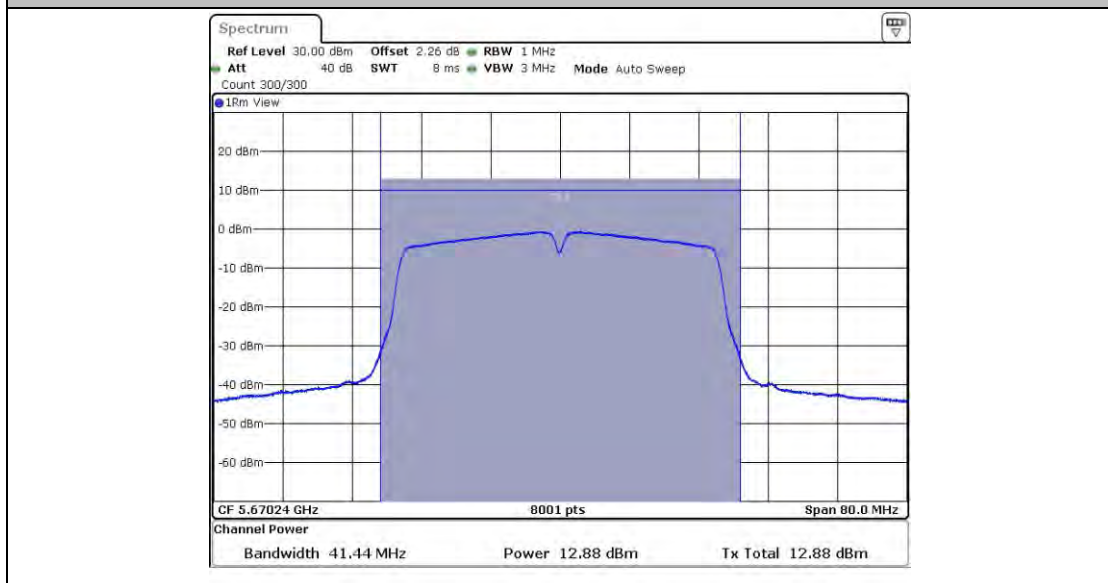


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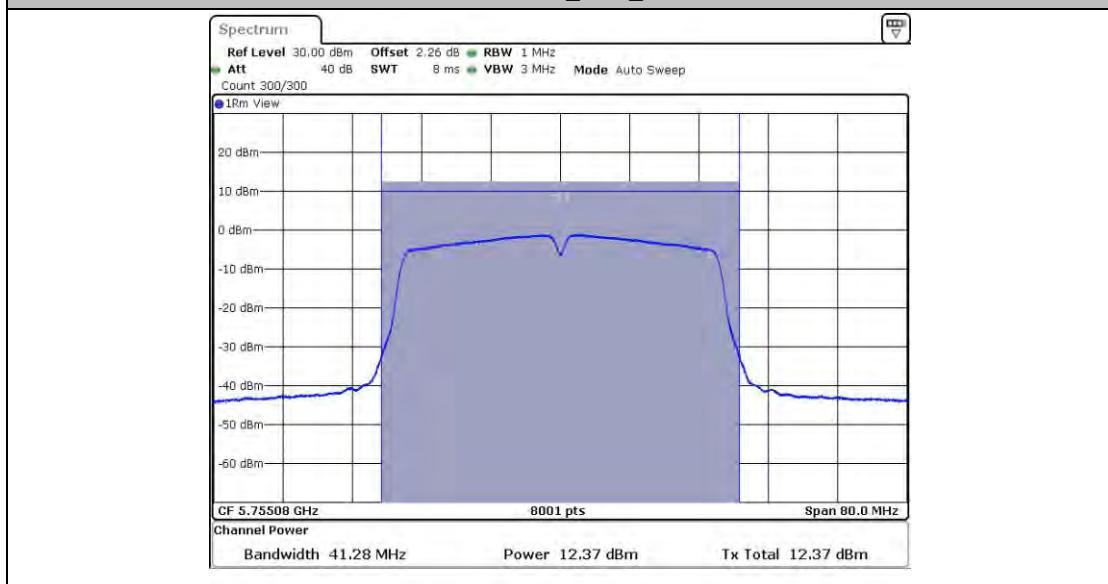




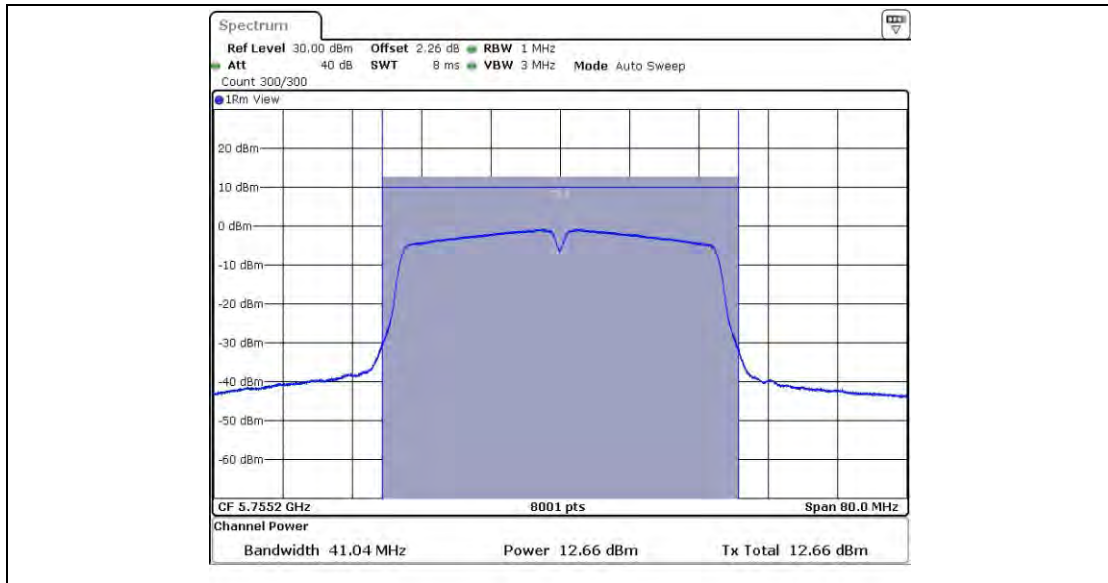
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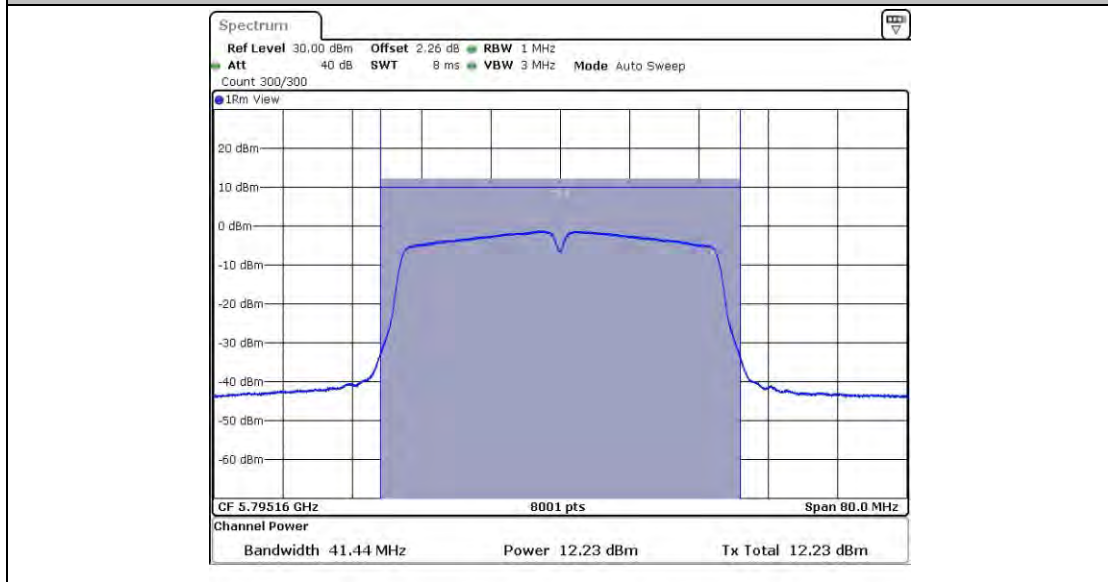
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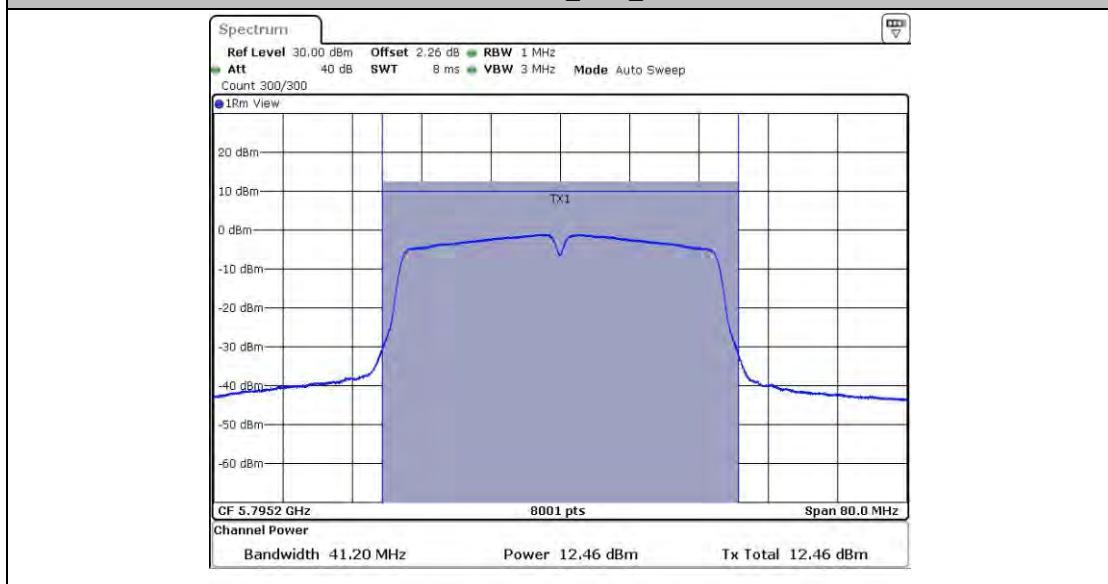
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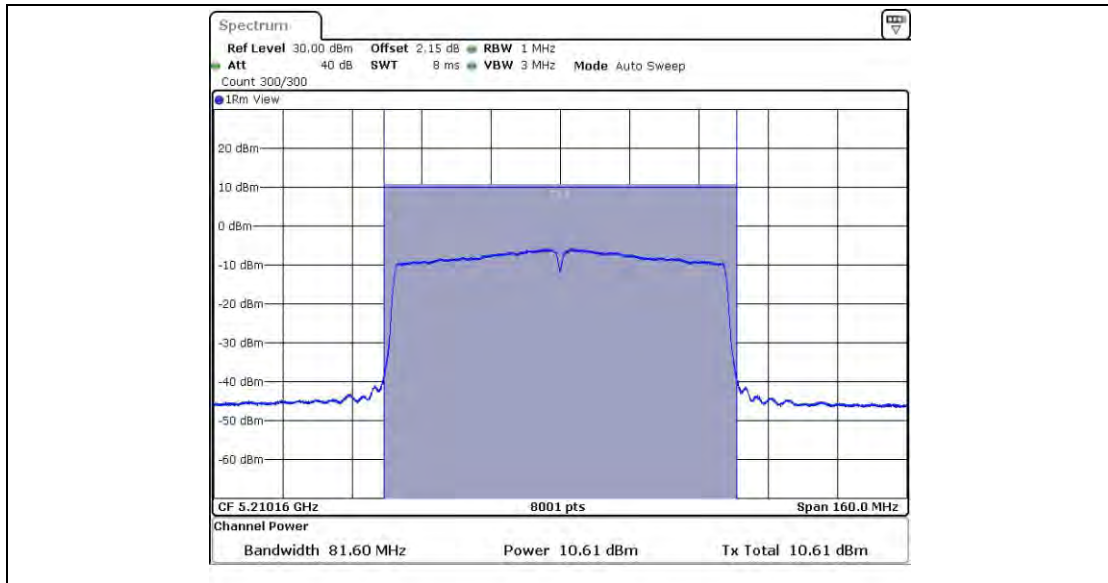
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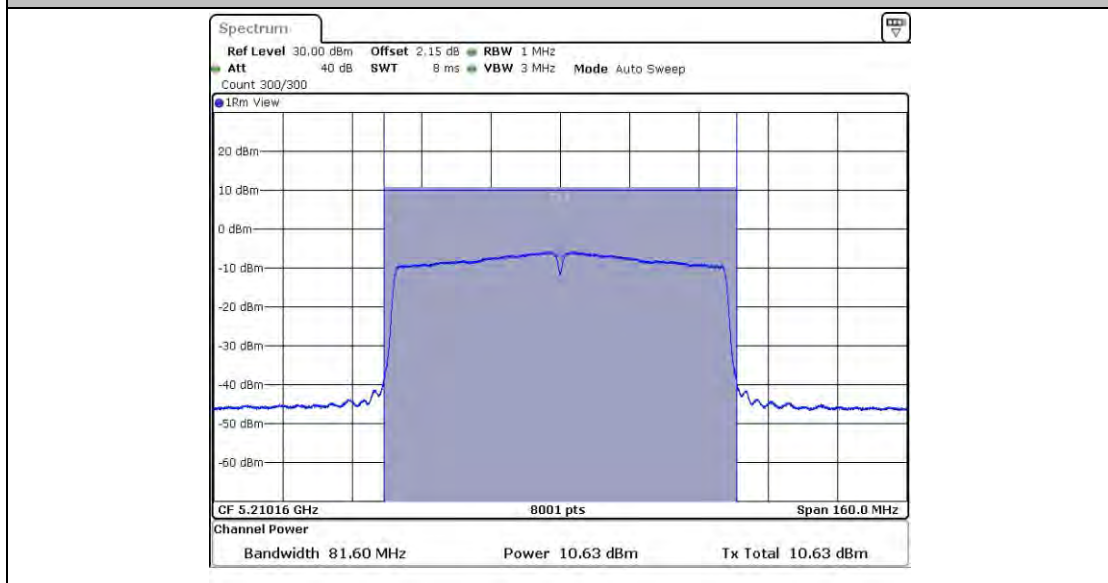
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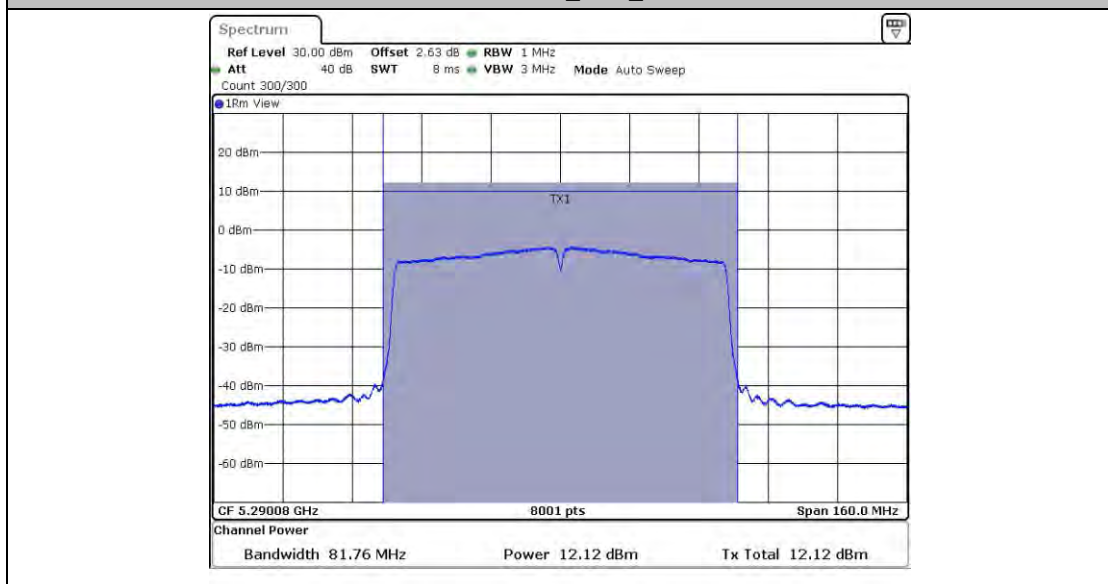
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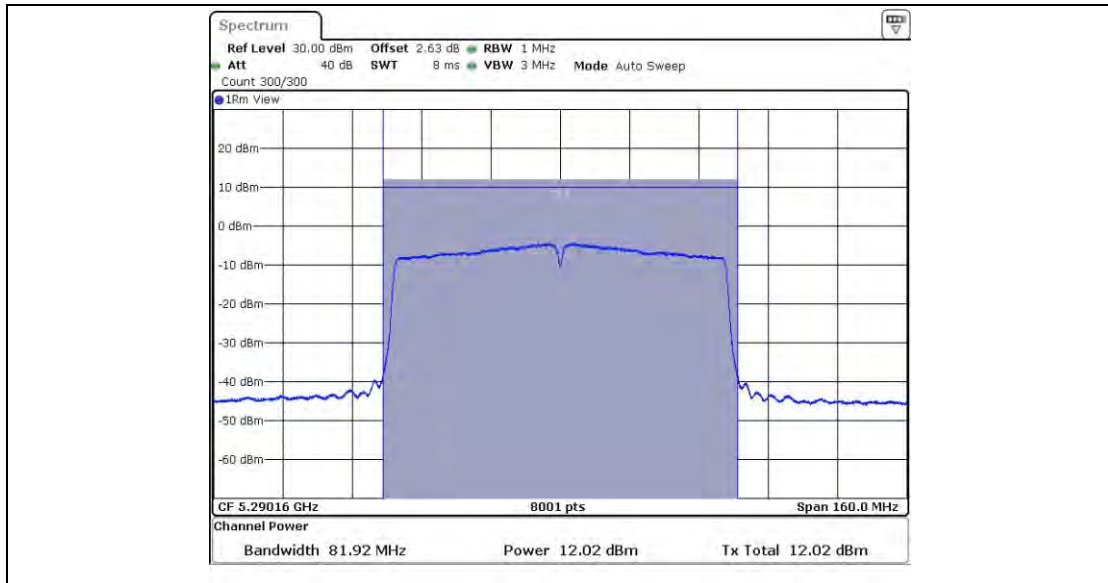
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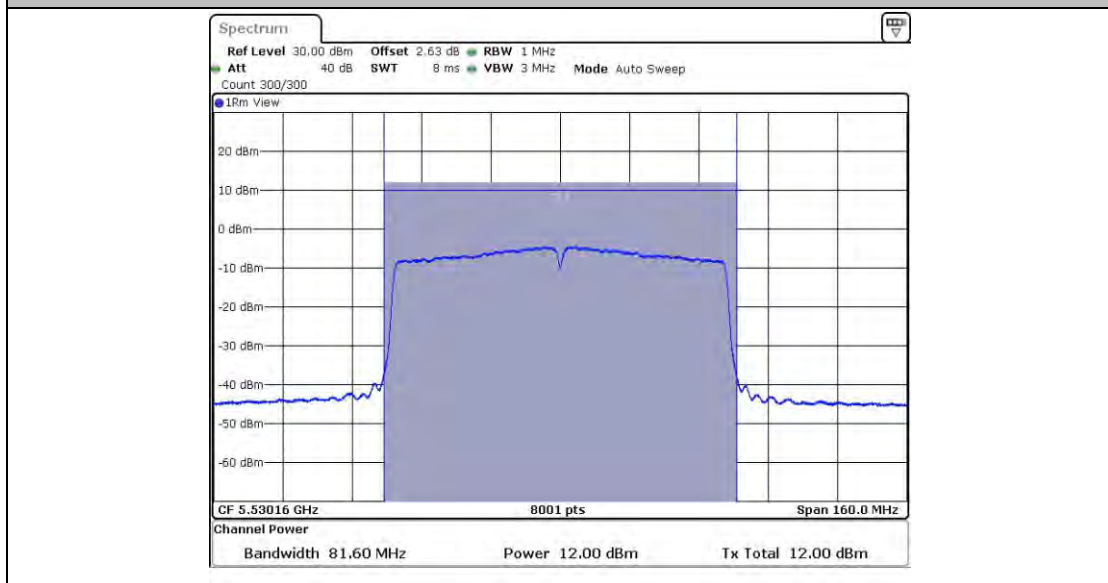
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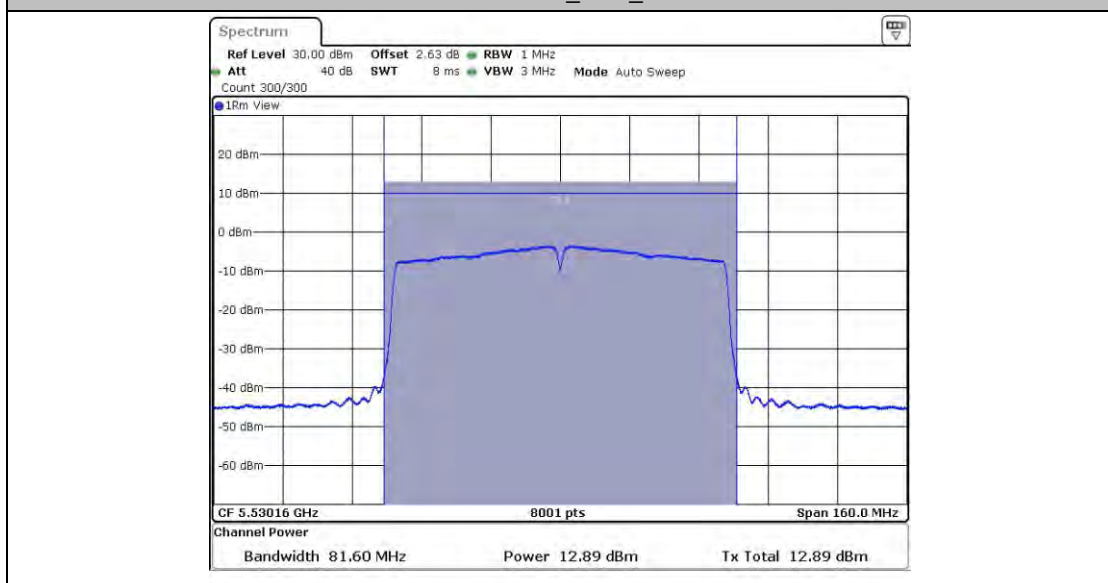
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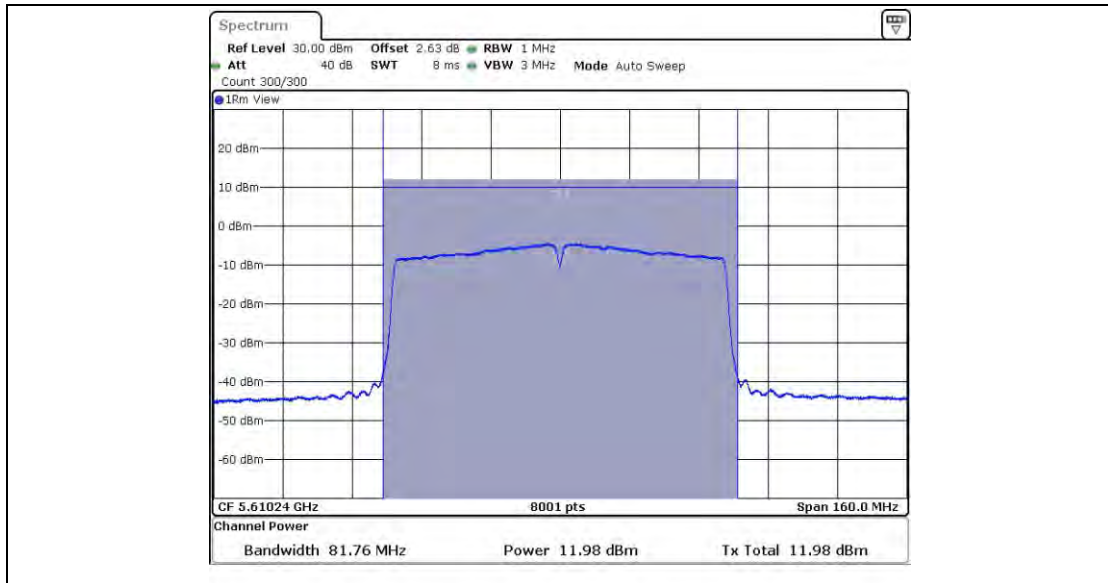
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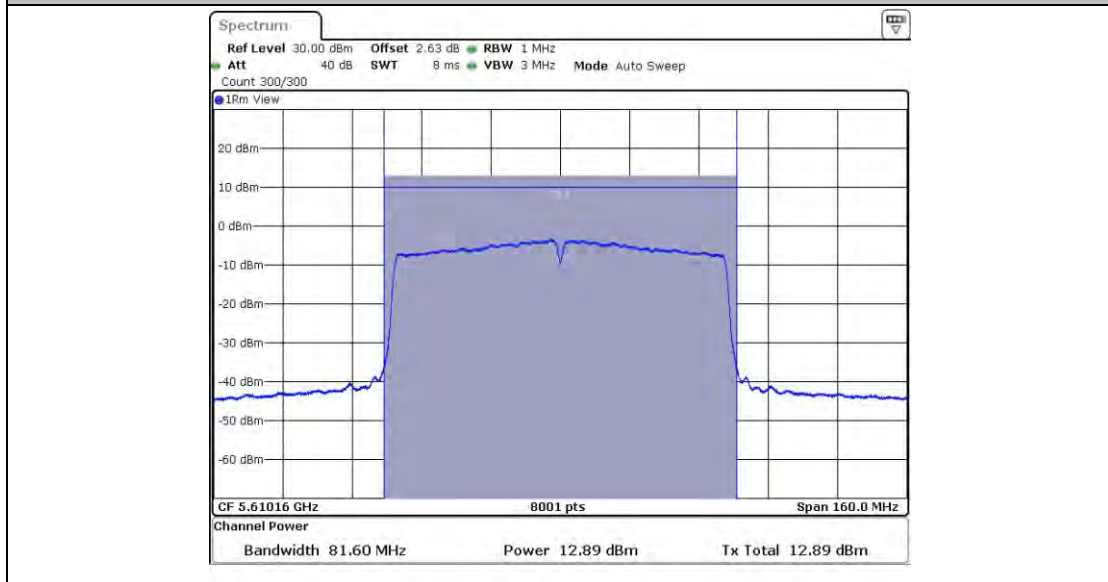
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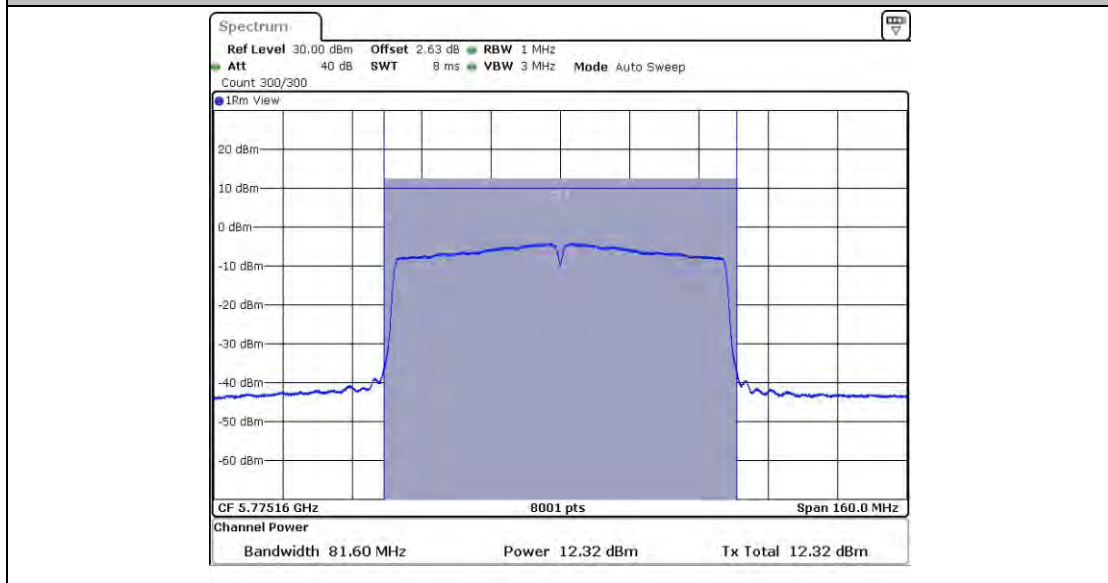
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11AC80SISO\_Ant1\_5610

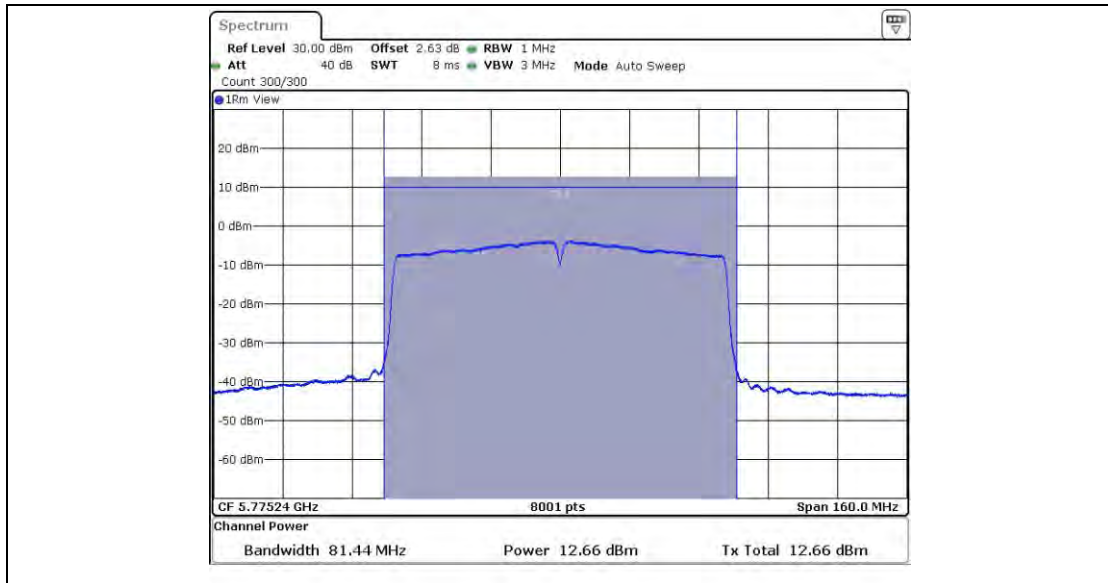


11AC80SISO\_Ant2\_5610



11AC80SISO\_Ant1\_5775

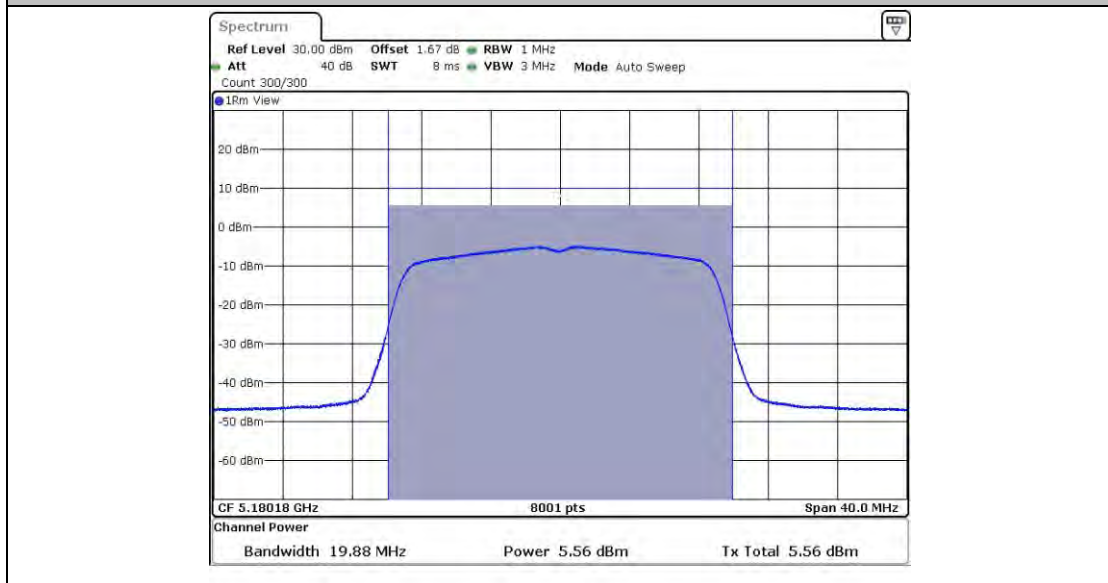




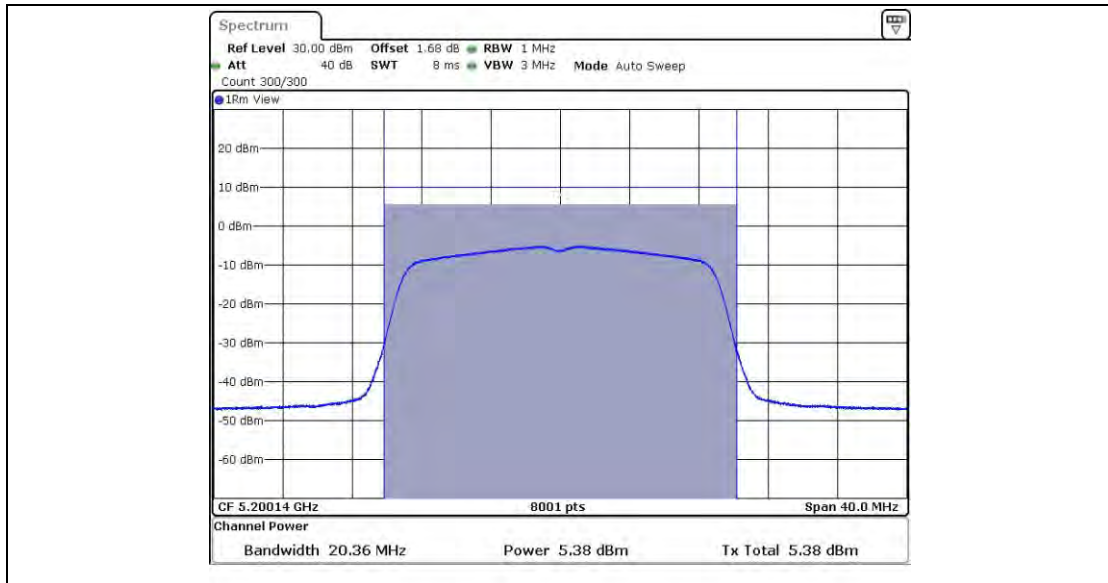
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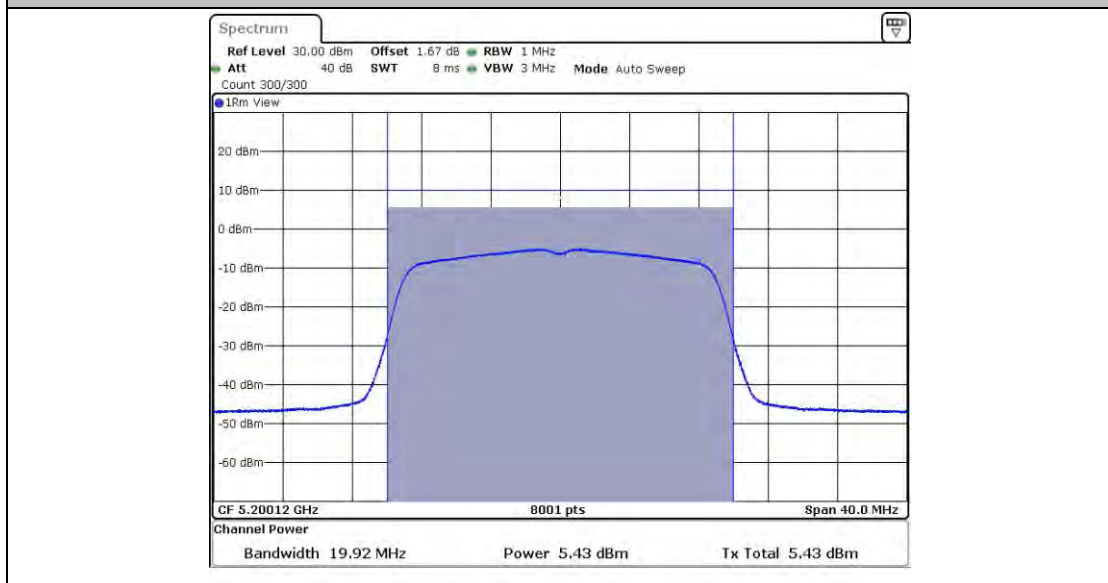
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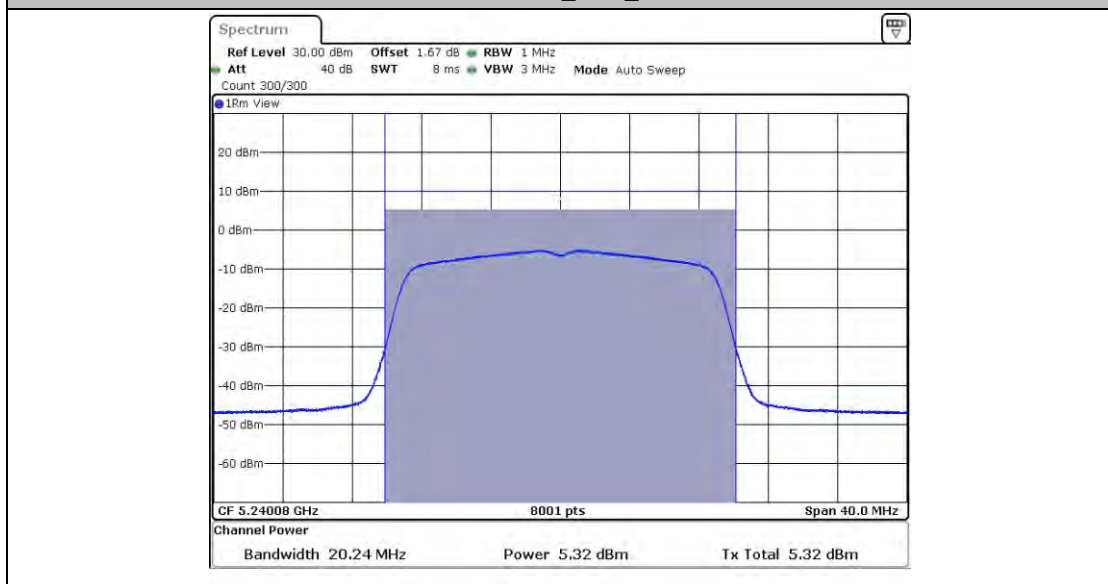
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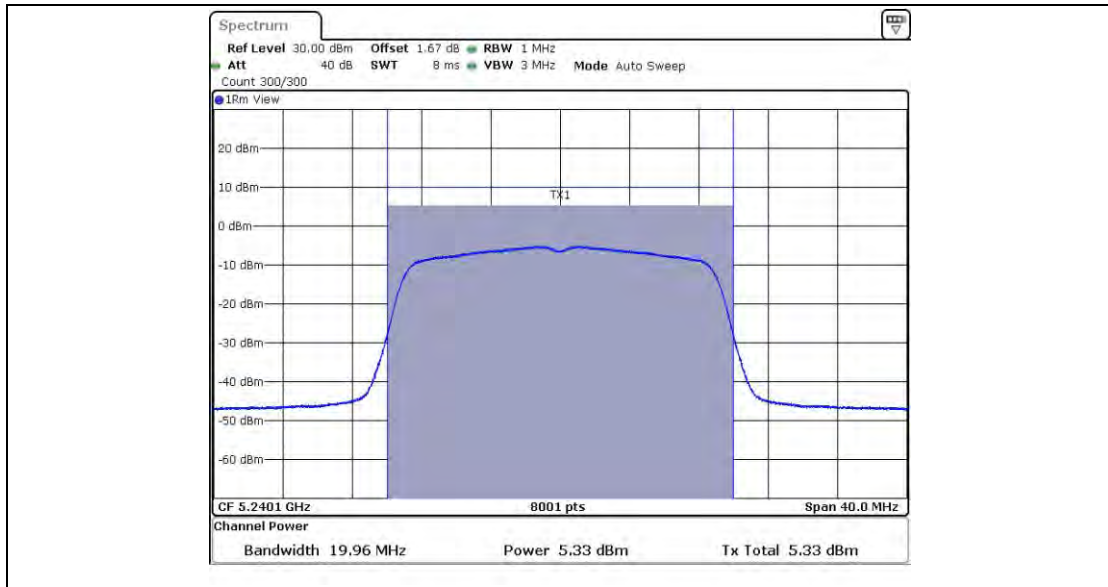
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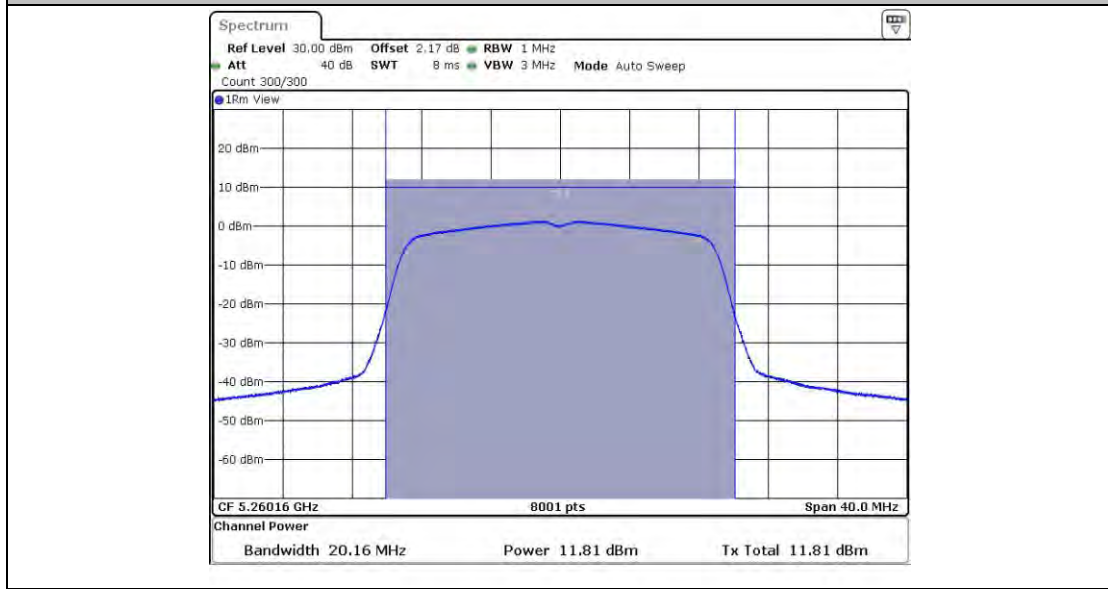
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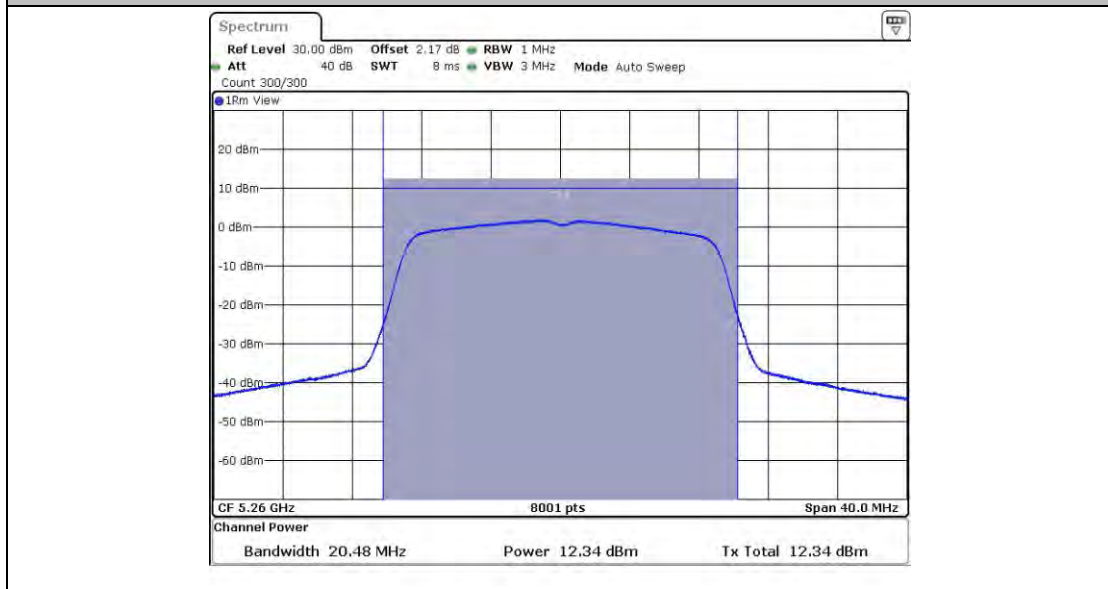
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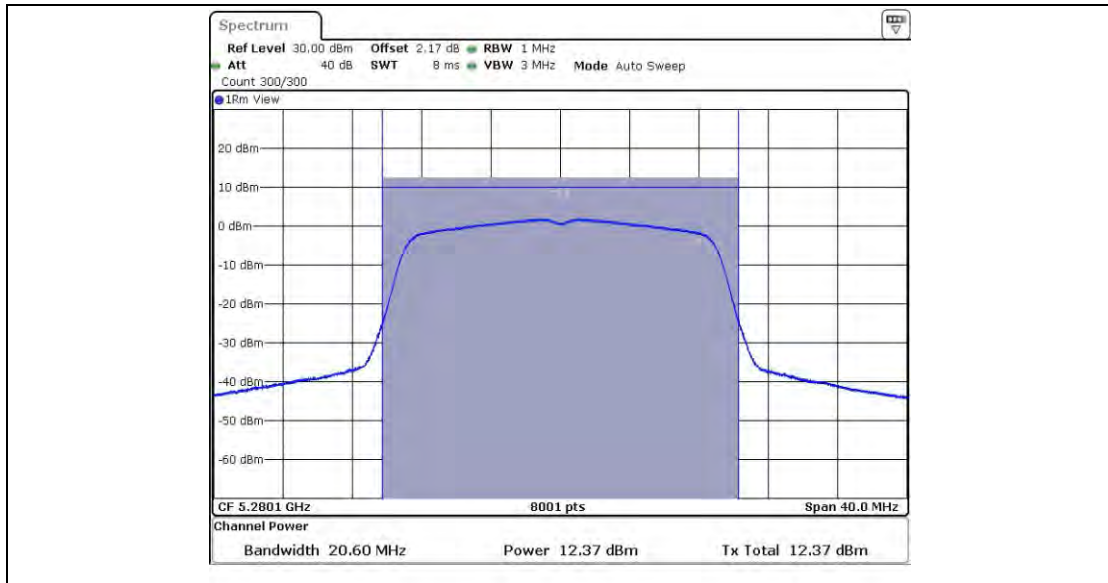
11N20MIMO\_Ant2\_5240



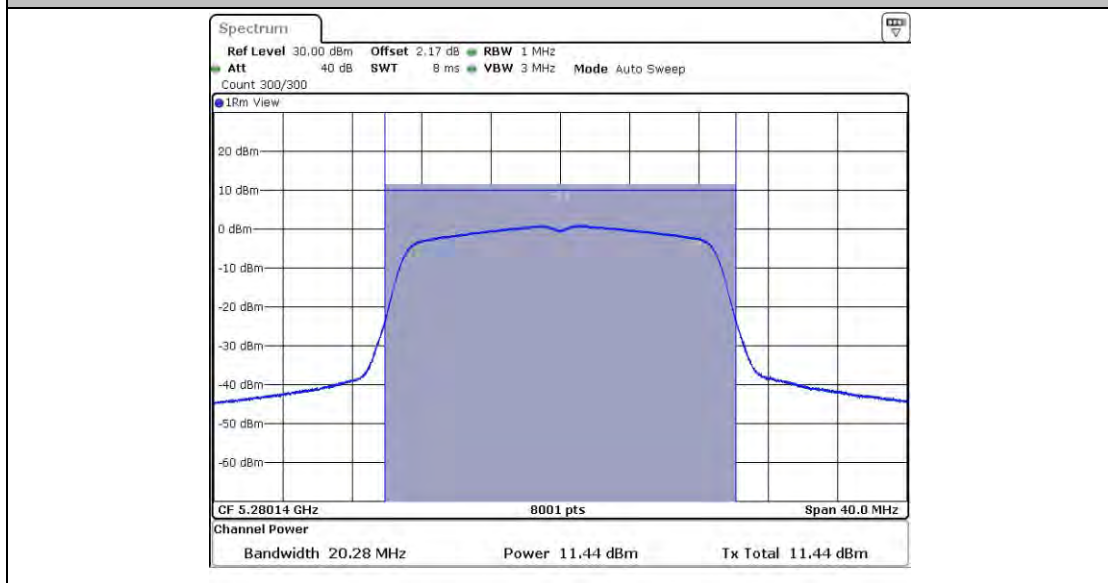
11N20MIMO\_Ant1\_5260



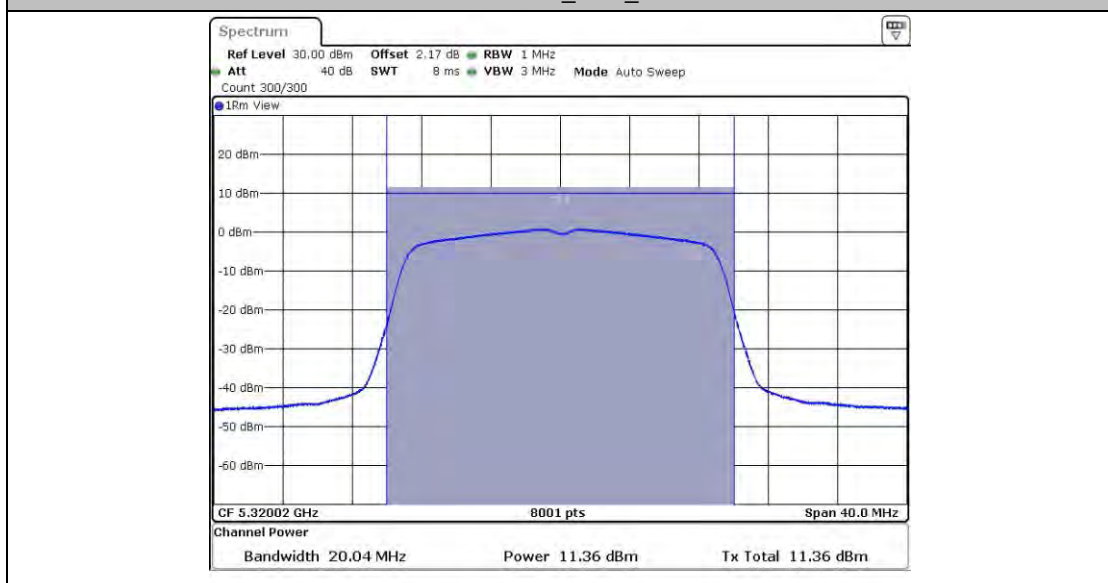
11N20MIMO\_Ant2\_5260



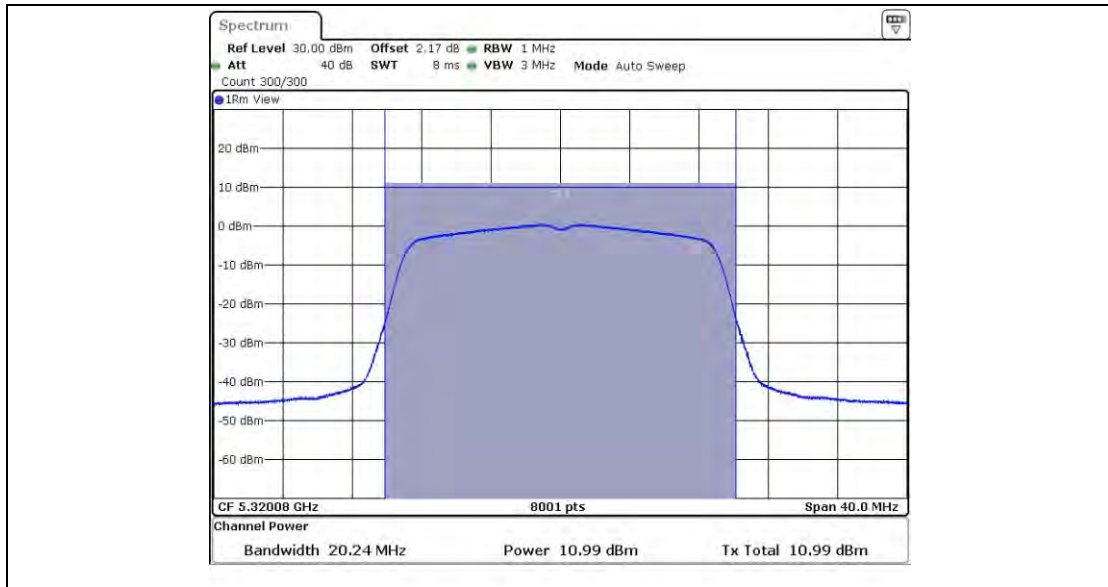
11N20MIMO\_Ant1\_5280



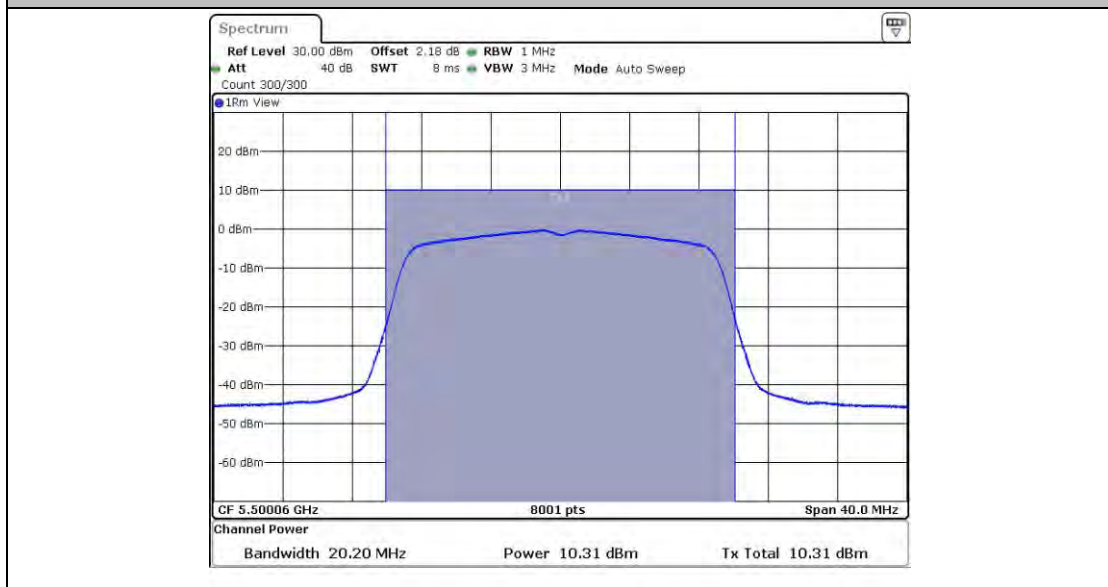
11N20MIMO\_Ant2\_5280



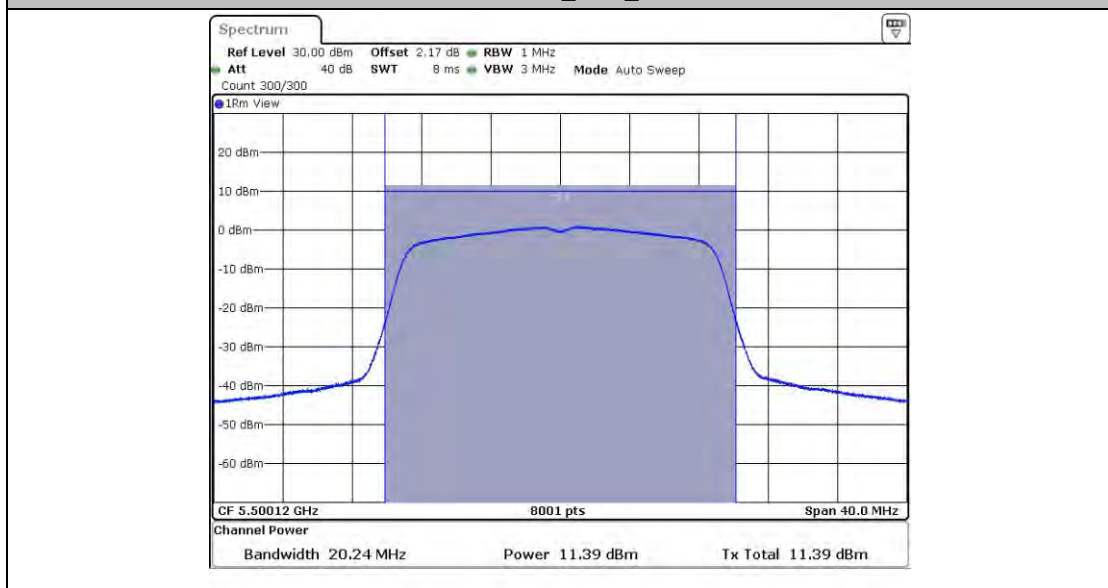
11N20MIMO\_Ant1\_5320



11N20MIMO\_Ant2\_5320

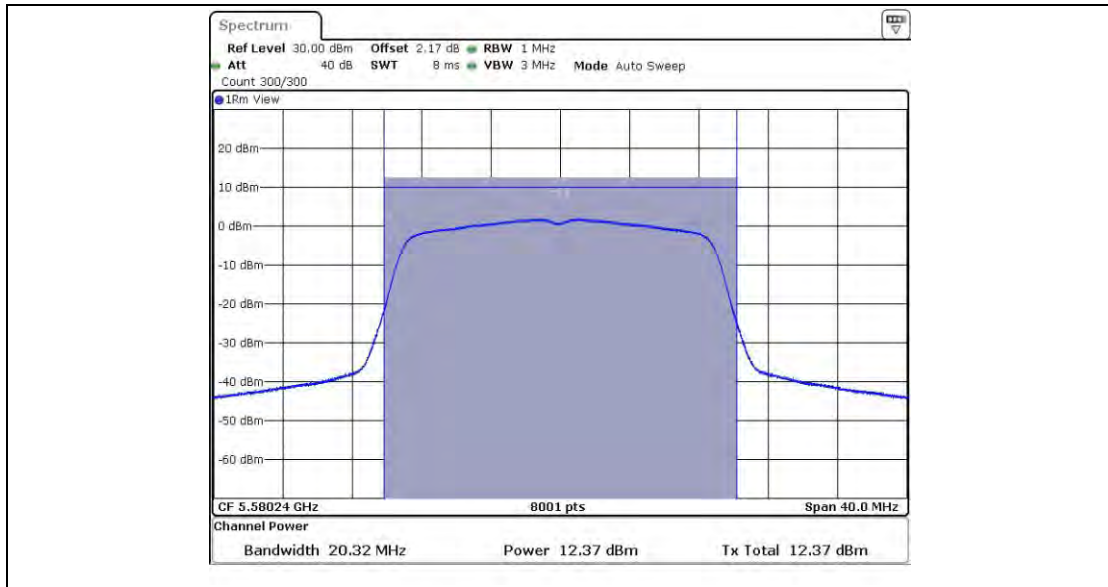


11N20MIMO\_Ant1\_5500

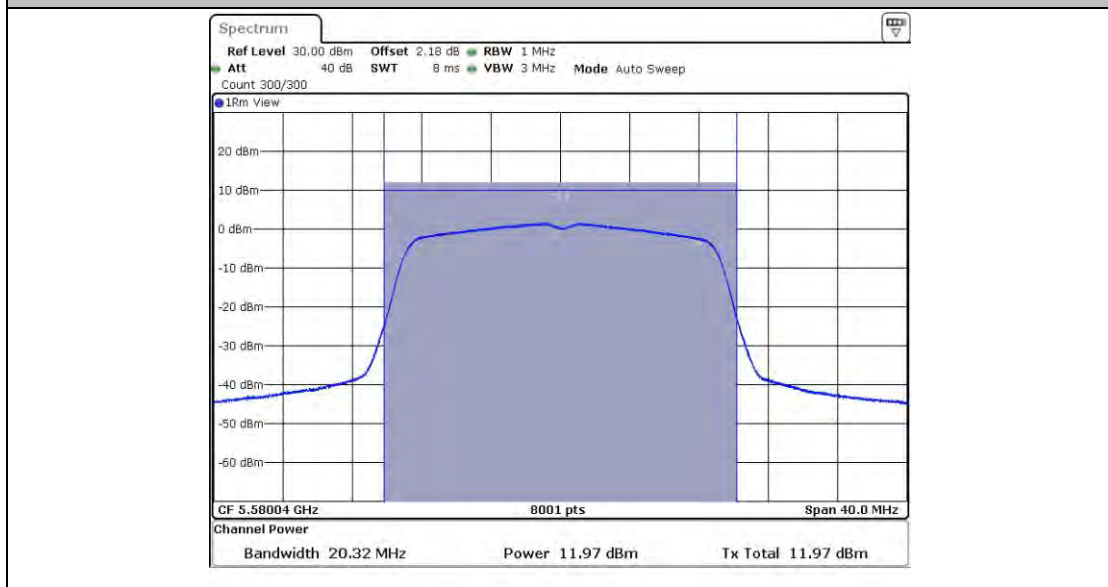


11N20MIMO\_Ant2\_5500

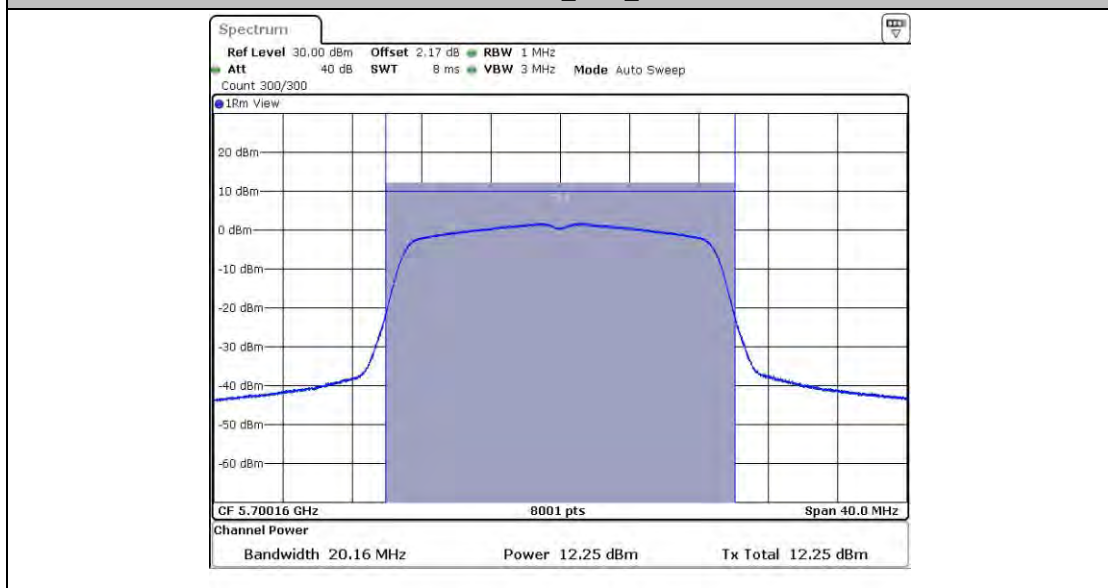




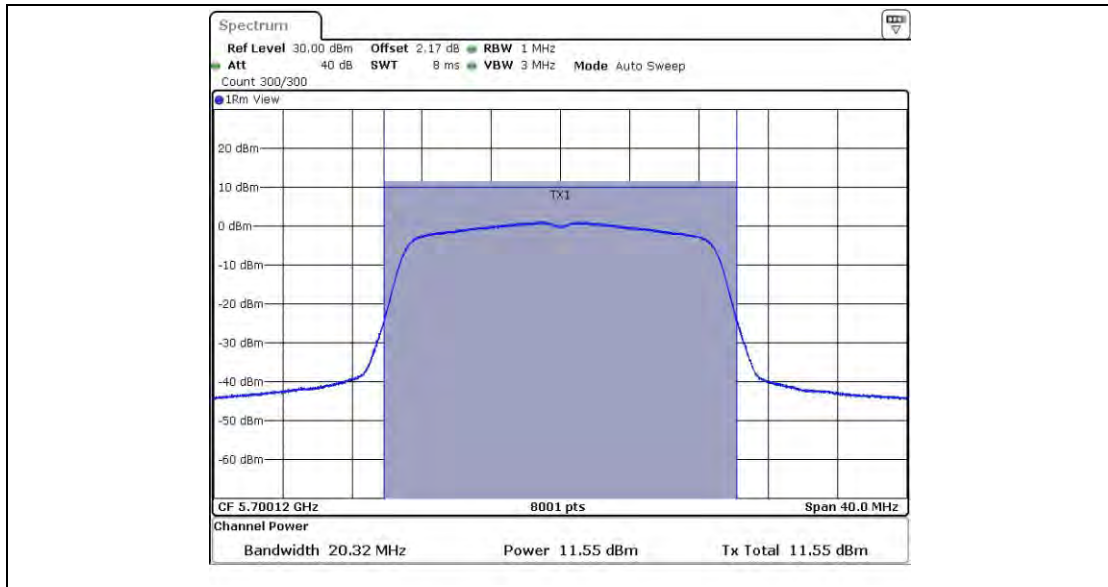
11N20MIMO\_Ant1\_5580



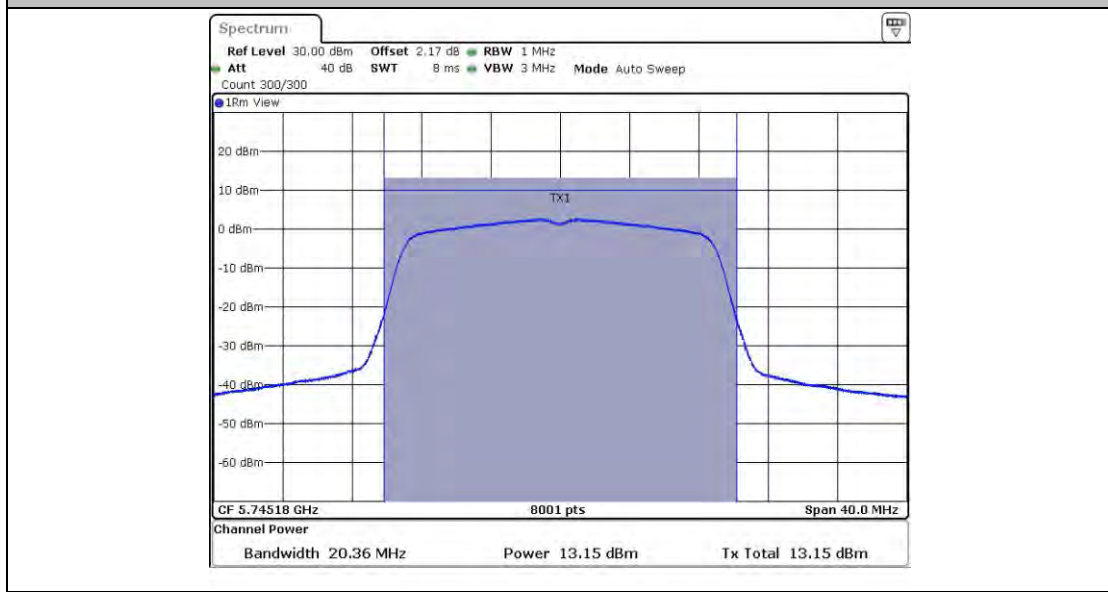
11N20MIMO\_Ant2\_5580



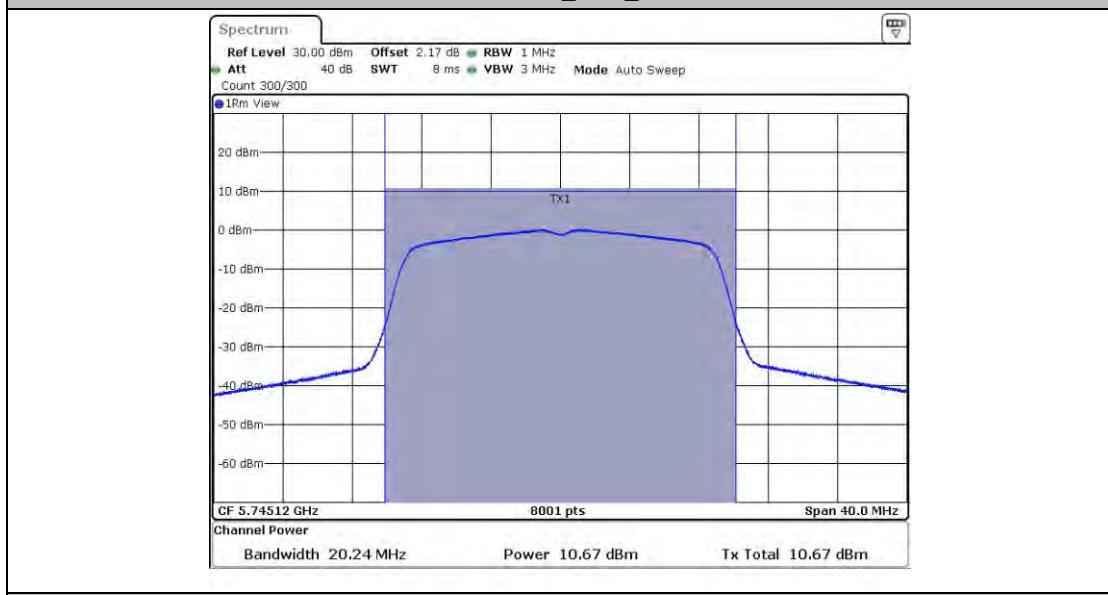
11N20MIMO\_Ant1\_5700



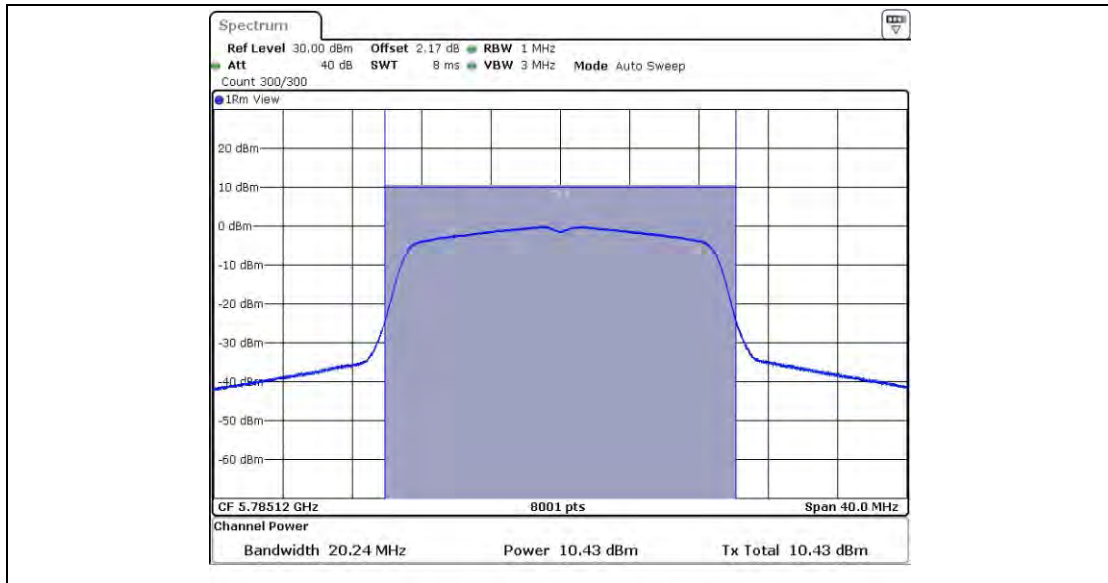
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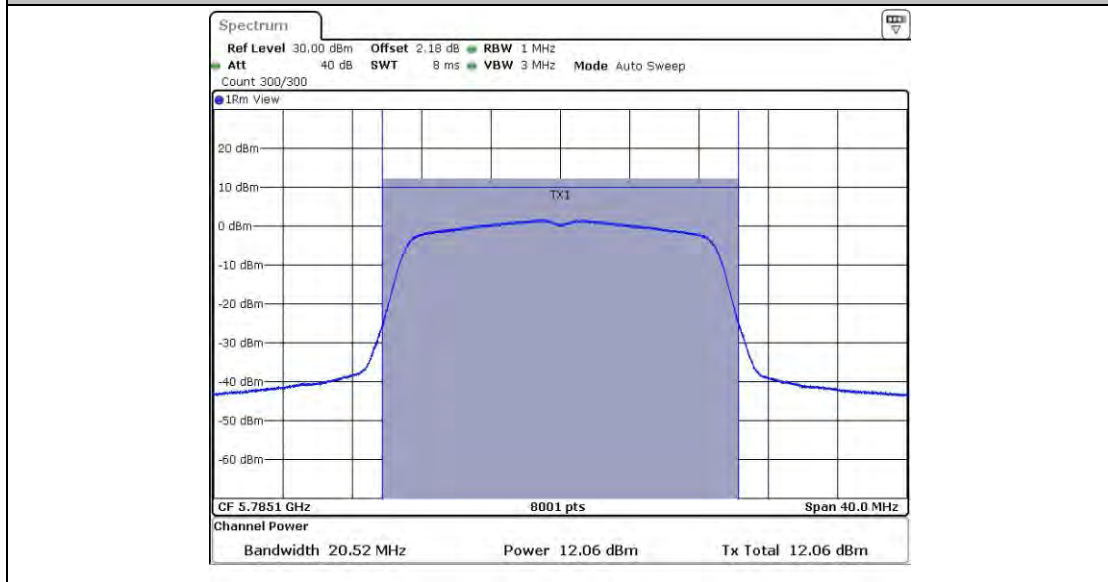
11N20MIMO\_Ant1\_5745



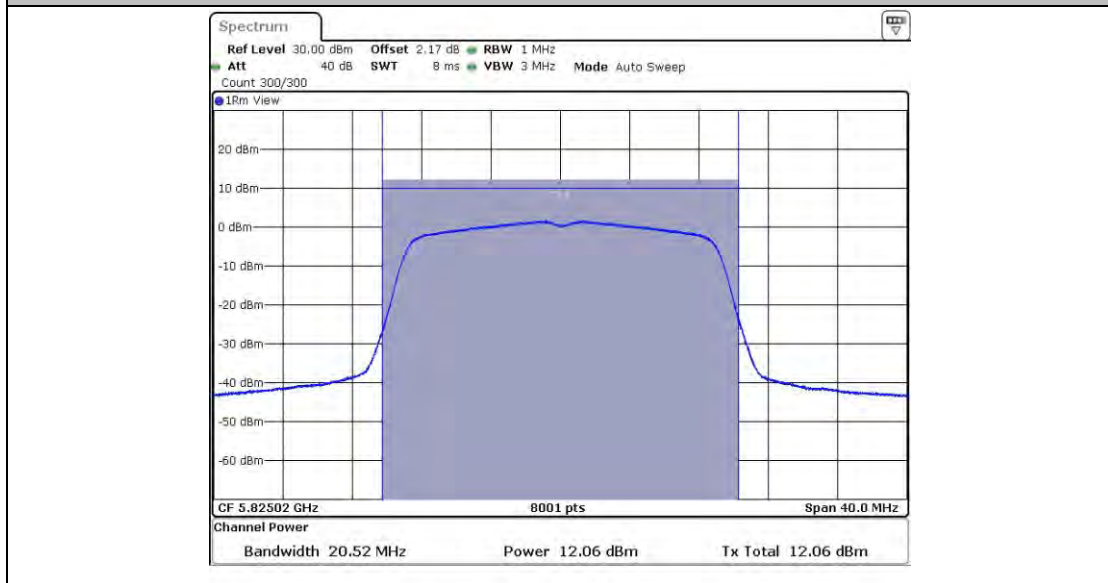
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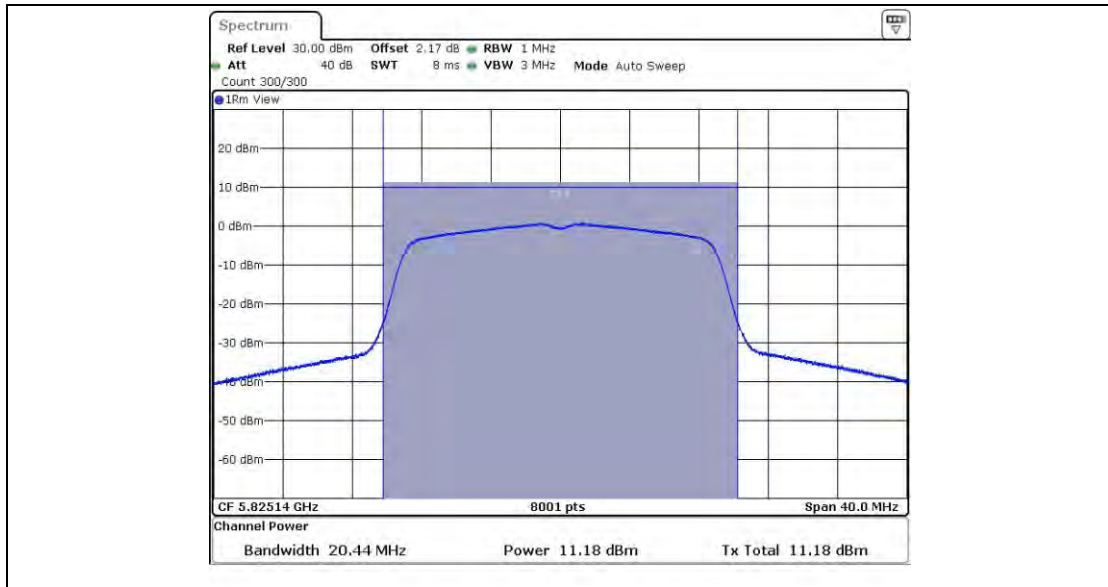
11N20MIMO\_Ant1\_5785



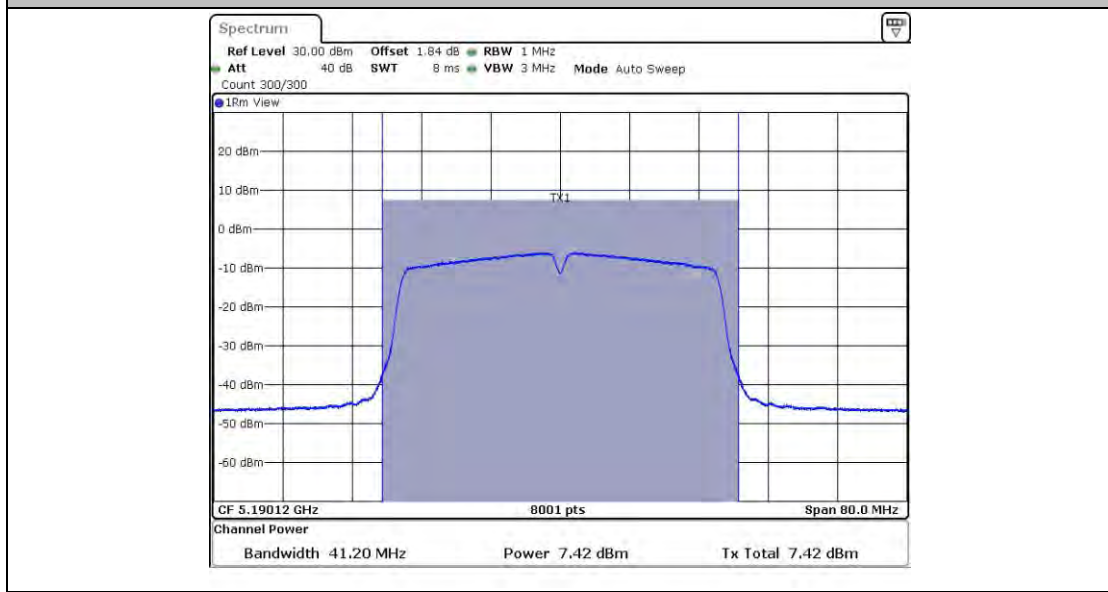
11N20MIMO\_Ant2\_5785



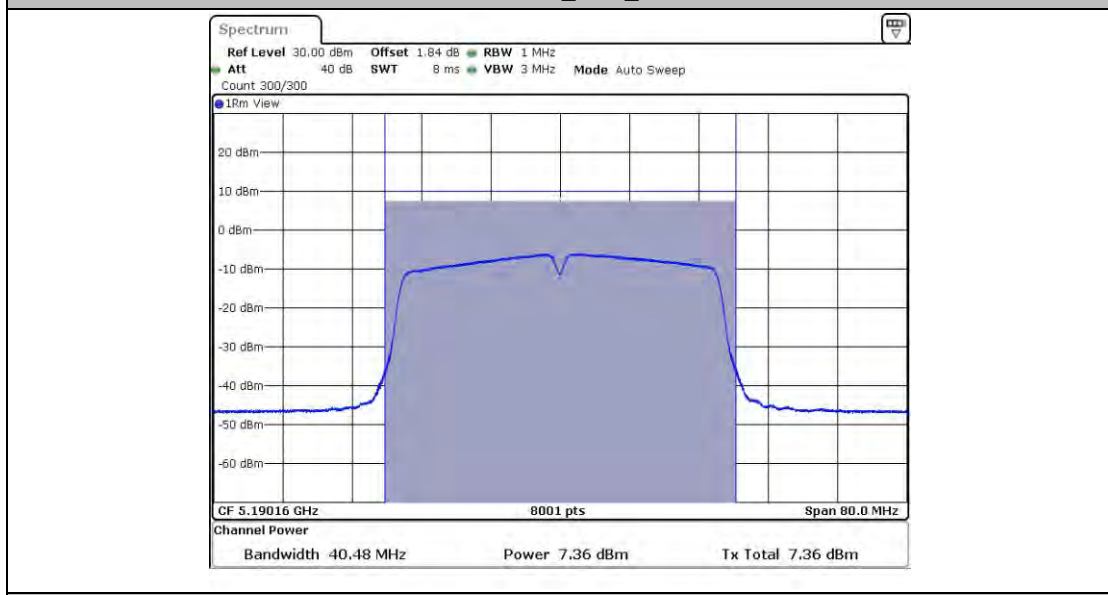
11N20MIMO\_Ant1\_5825



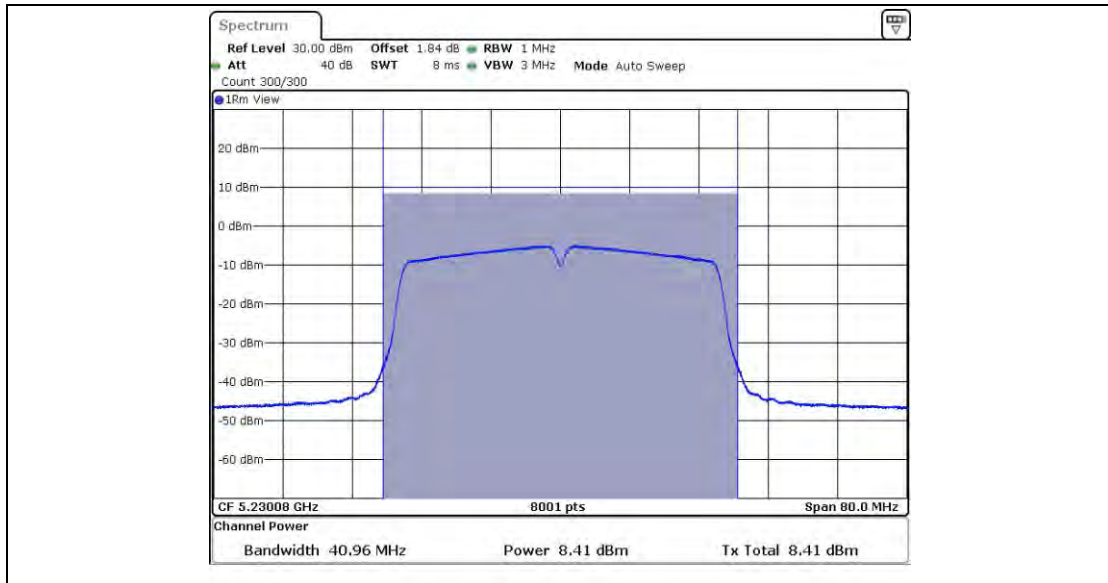
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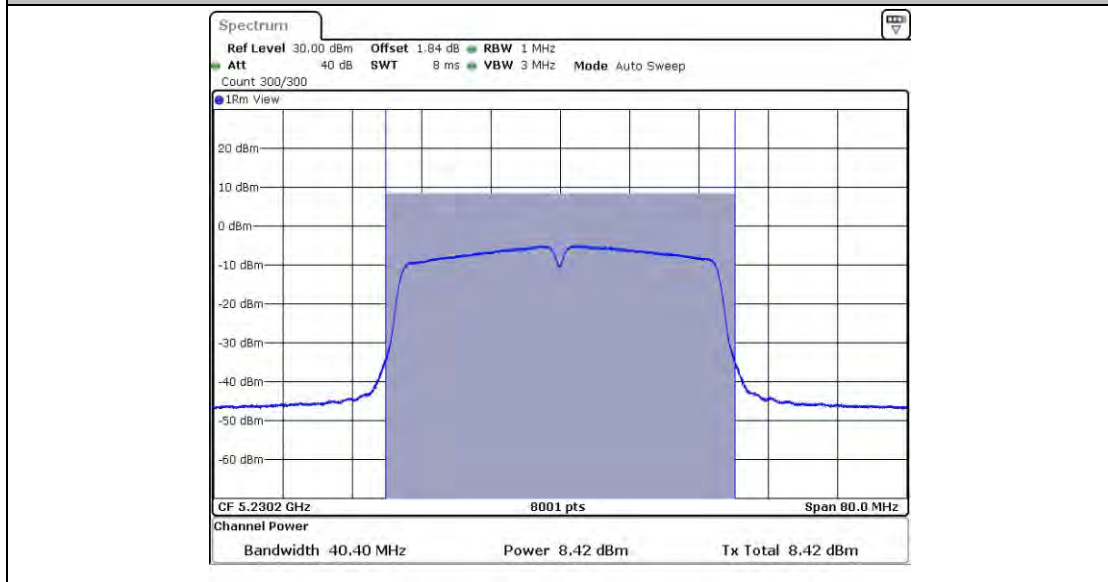
11N40MIMO\_Ant1\_5190



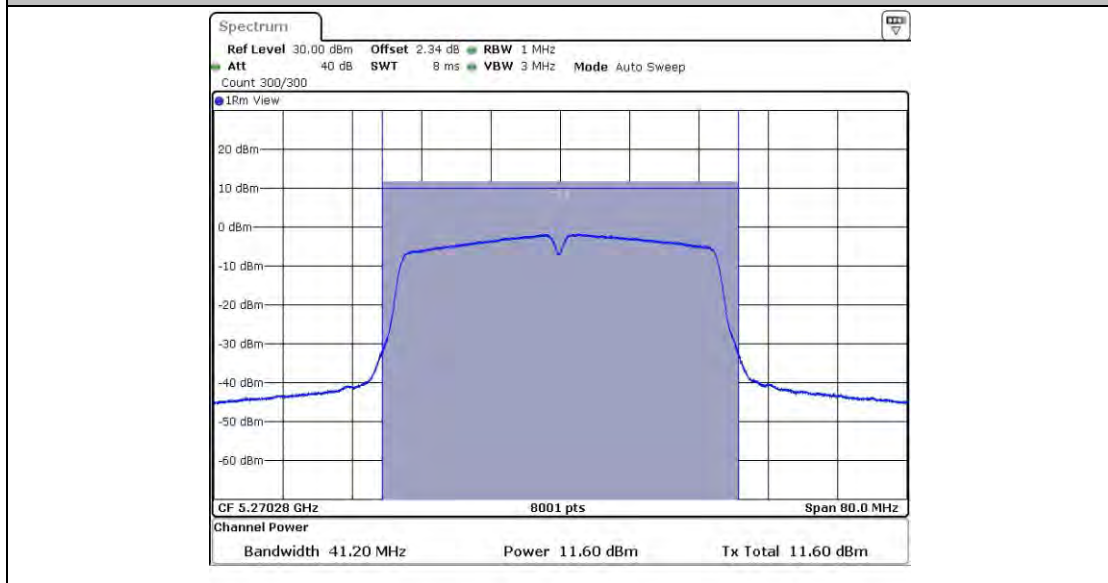
11N40MIMO\_Ant2\_5190



11N40MIMO\_Ant1\_5230

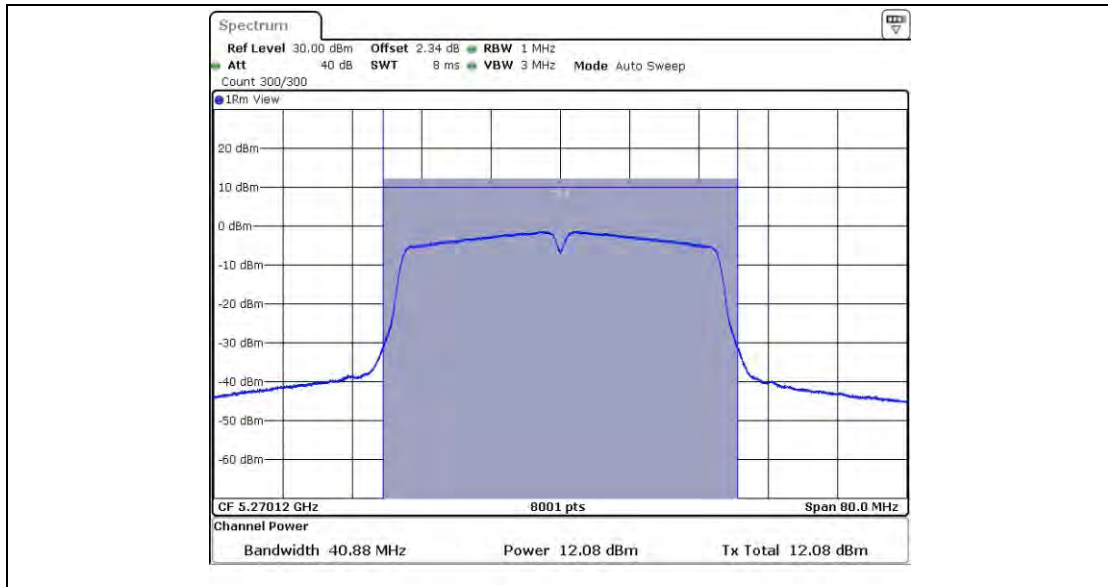


11N40MIMO\_Ant2\_5230

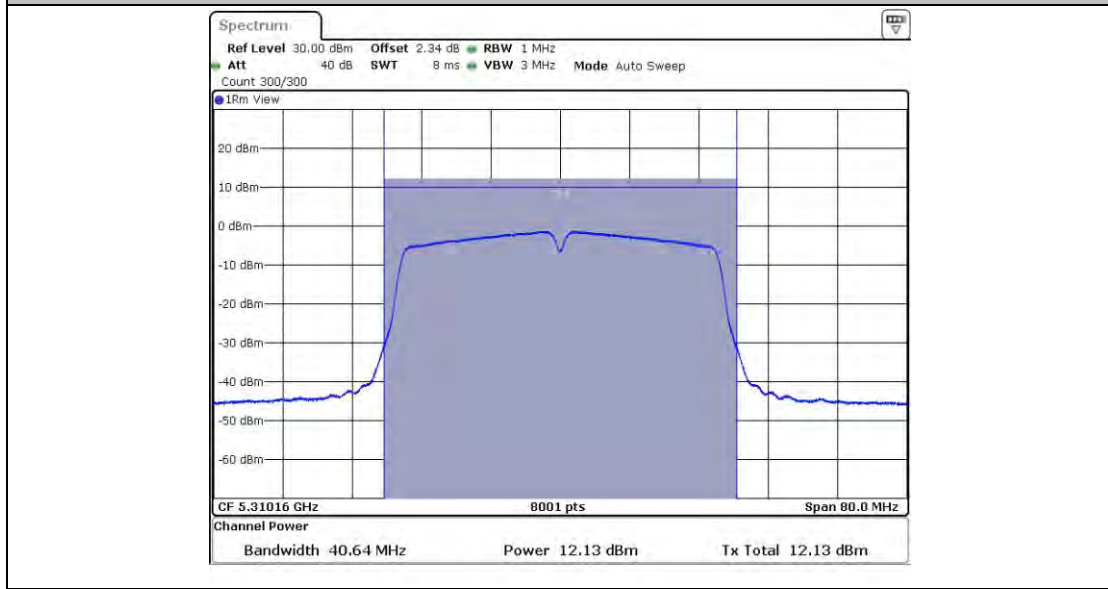


11N40MIMO\_Ant1\_5270

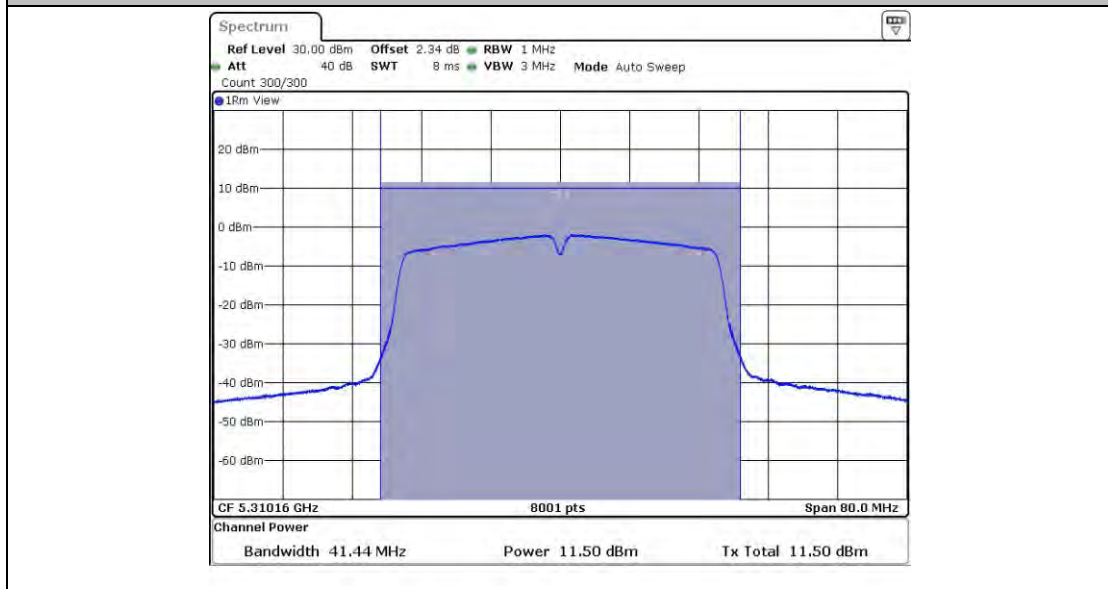




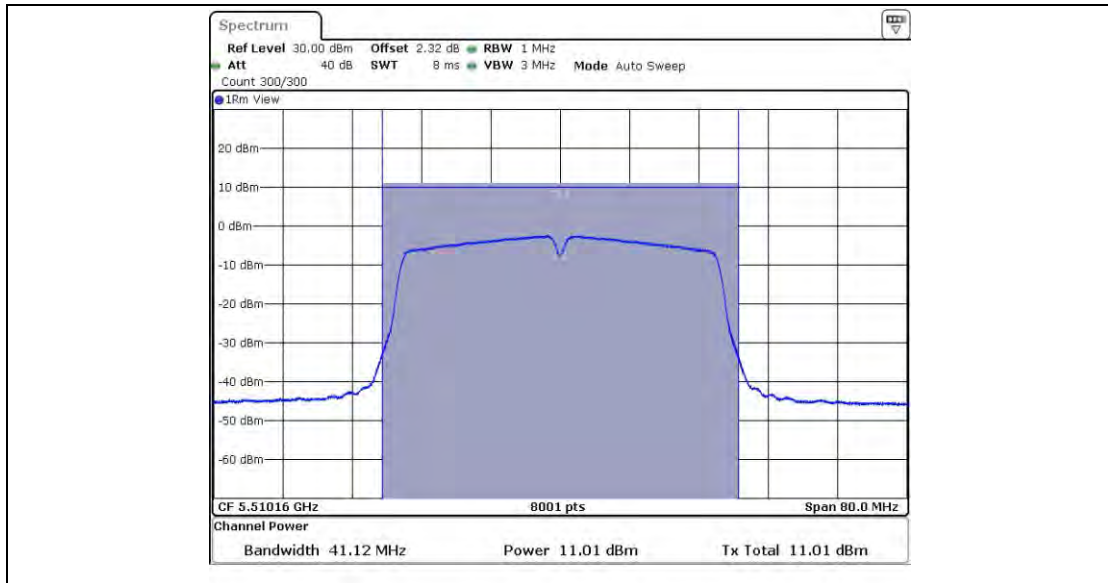
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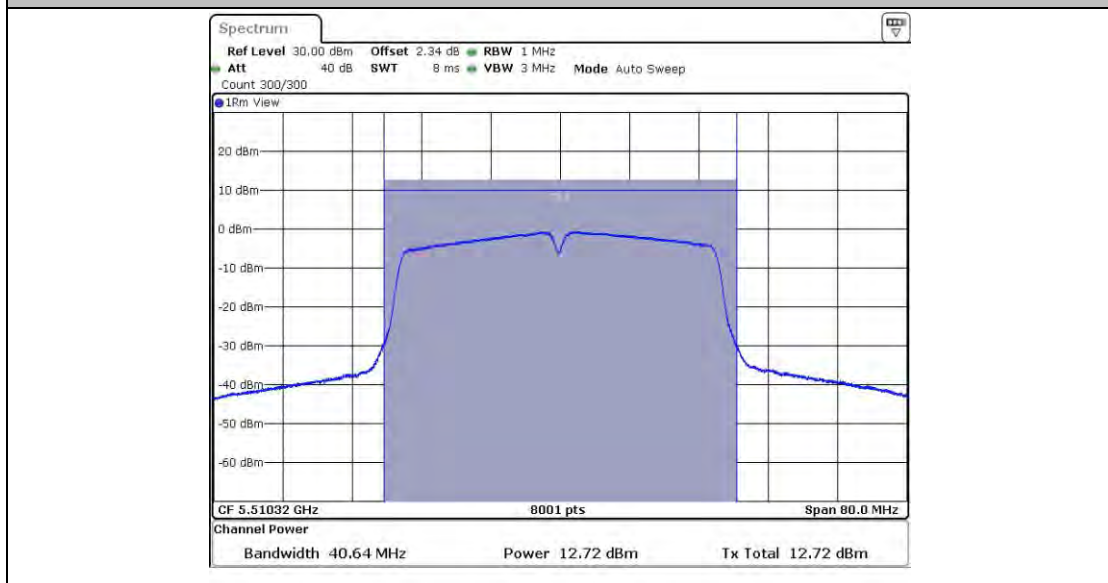
11N40MIMO\_Ant1\_5310



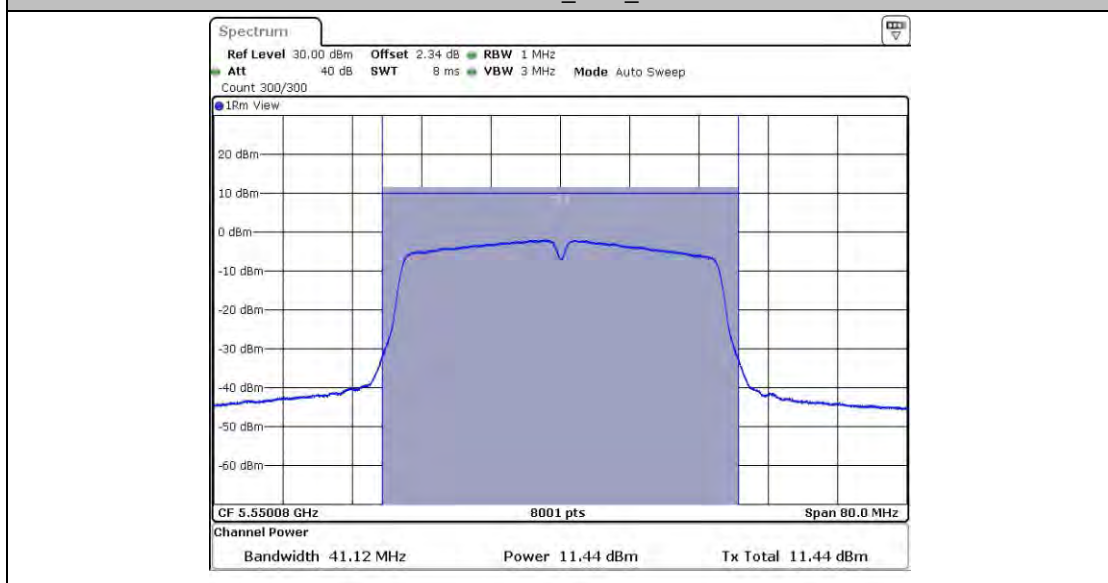
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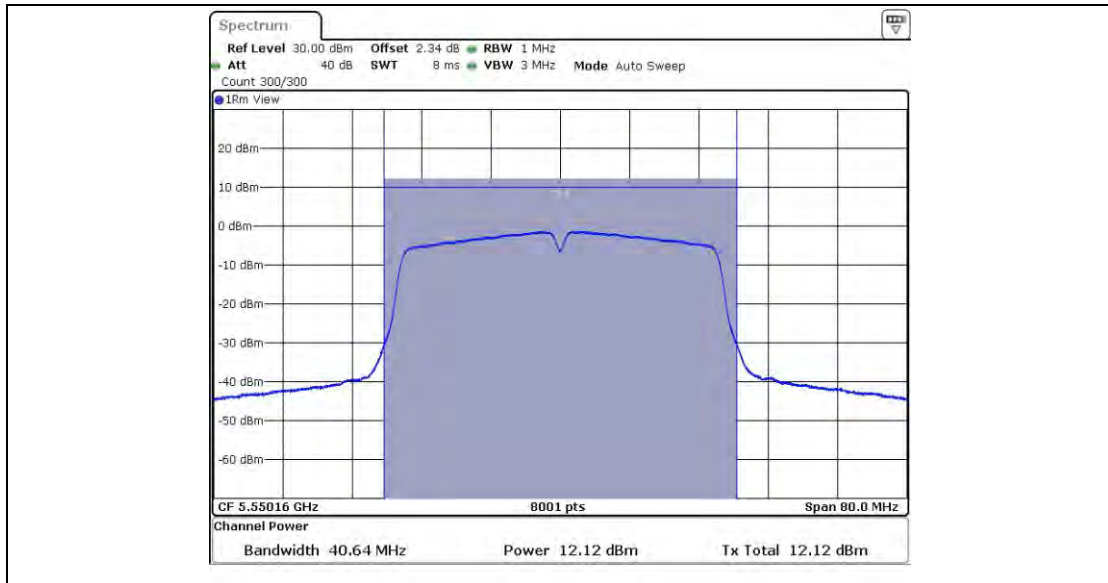
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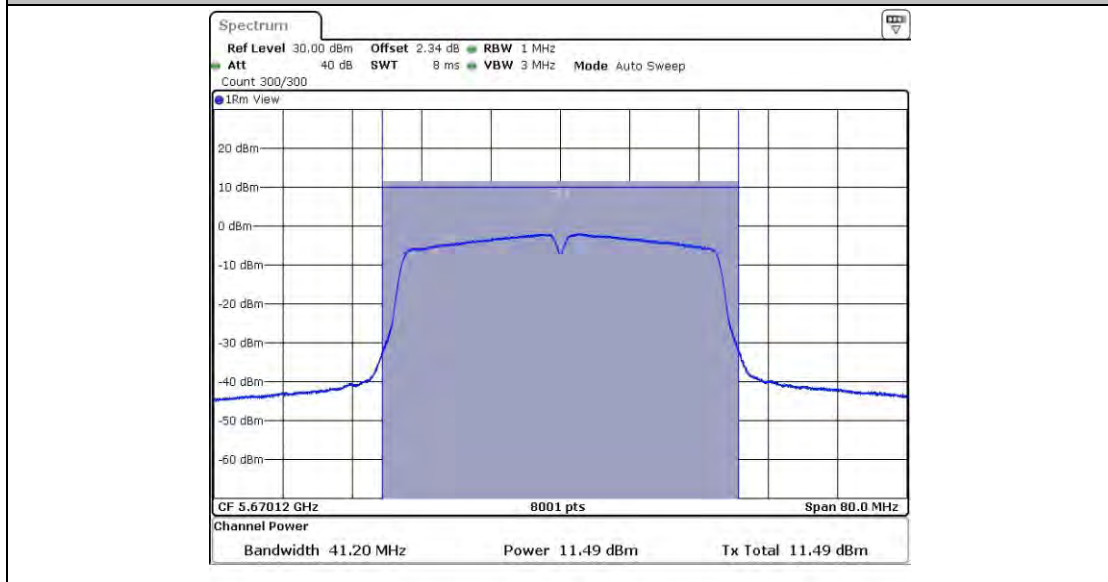
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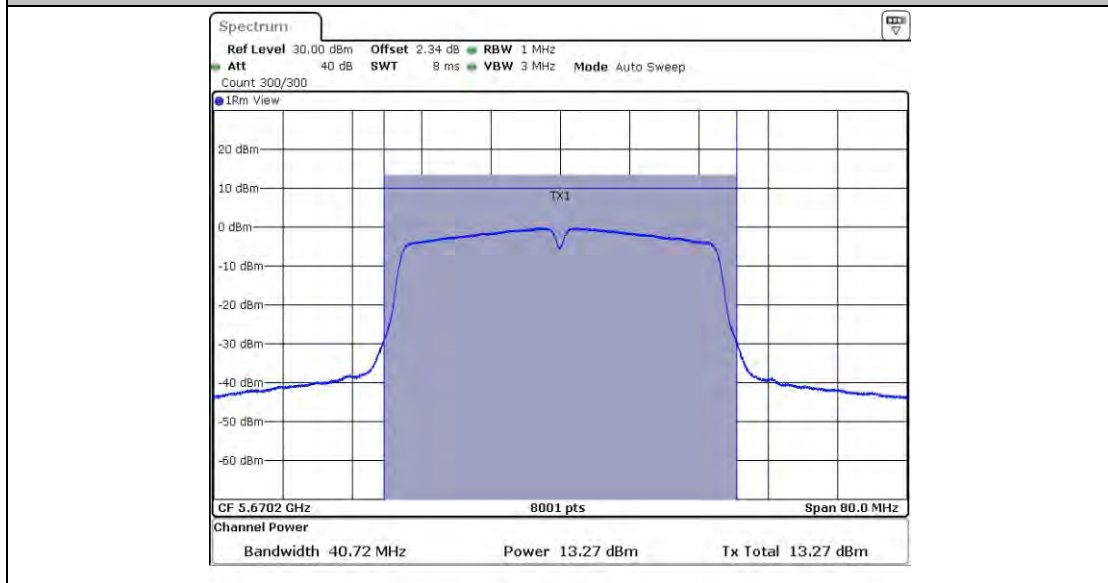
11N40MIMO\_Ant1\_5550



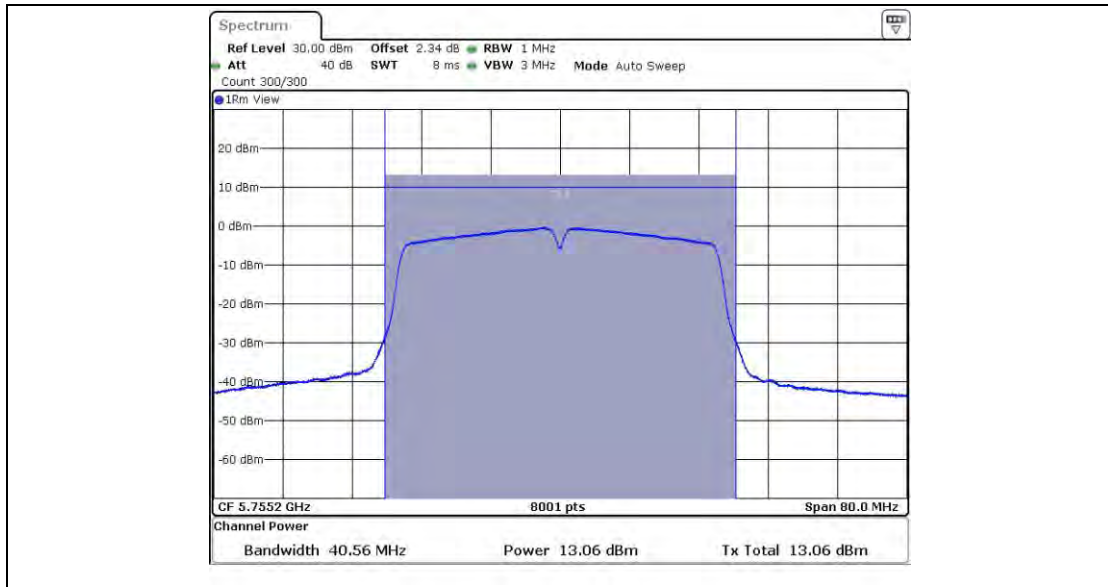
11N40MIMO\_Ant2\_5550



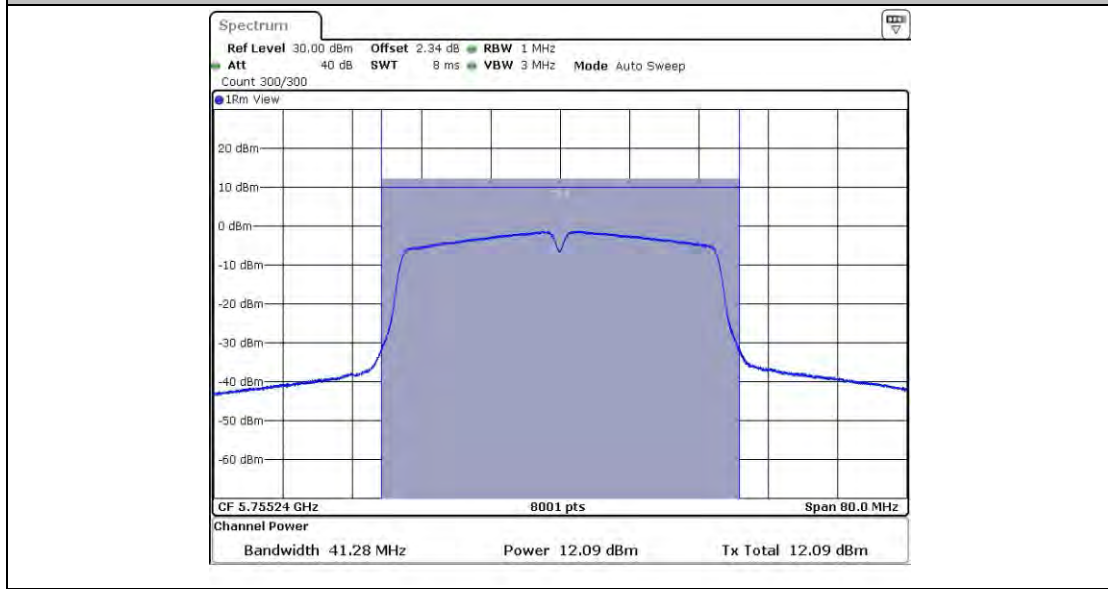
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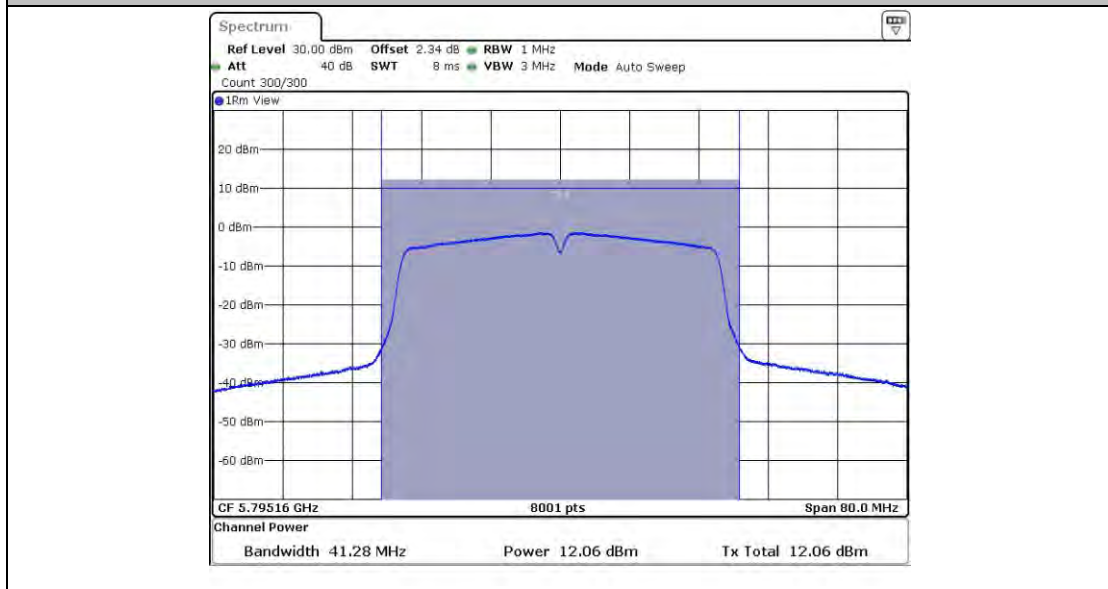
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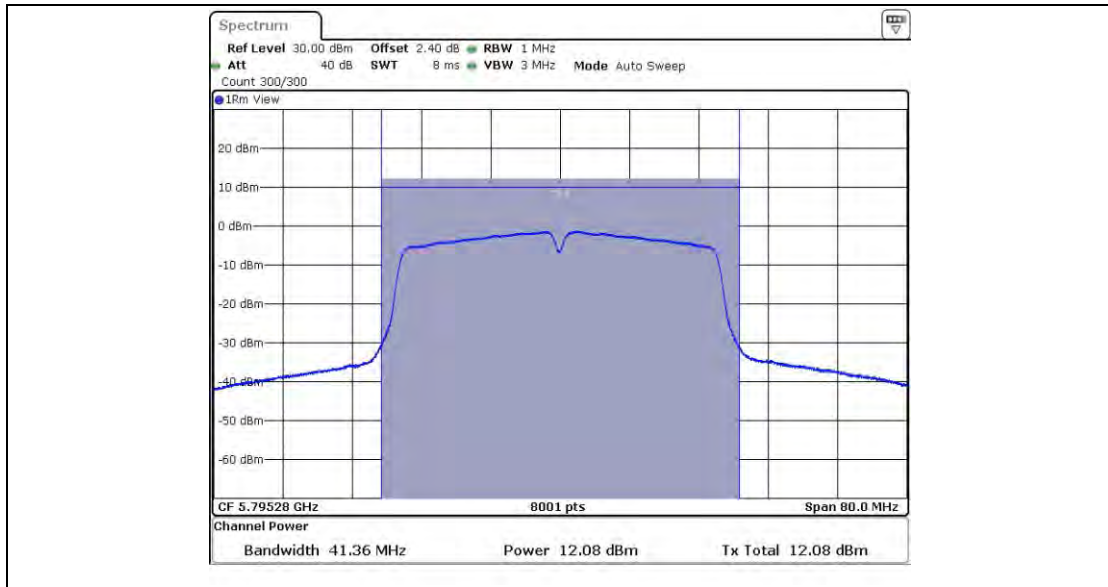
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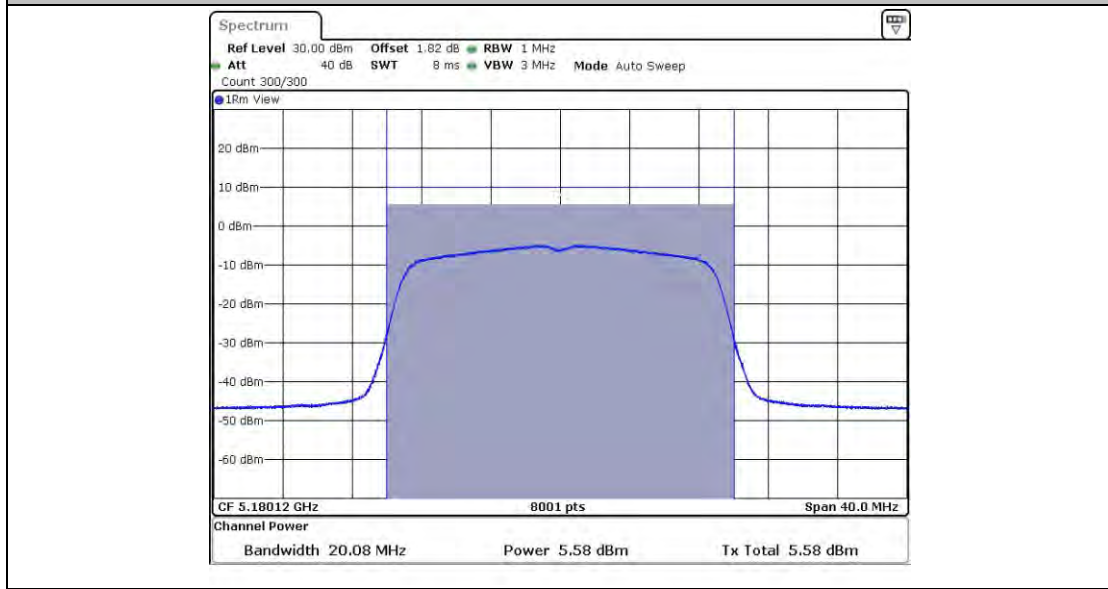
11N40MIMO\_Ant2\_5755



11N40MIMO\_Ant1\_5795



11N40MIMO\_Ant2\_5795

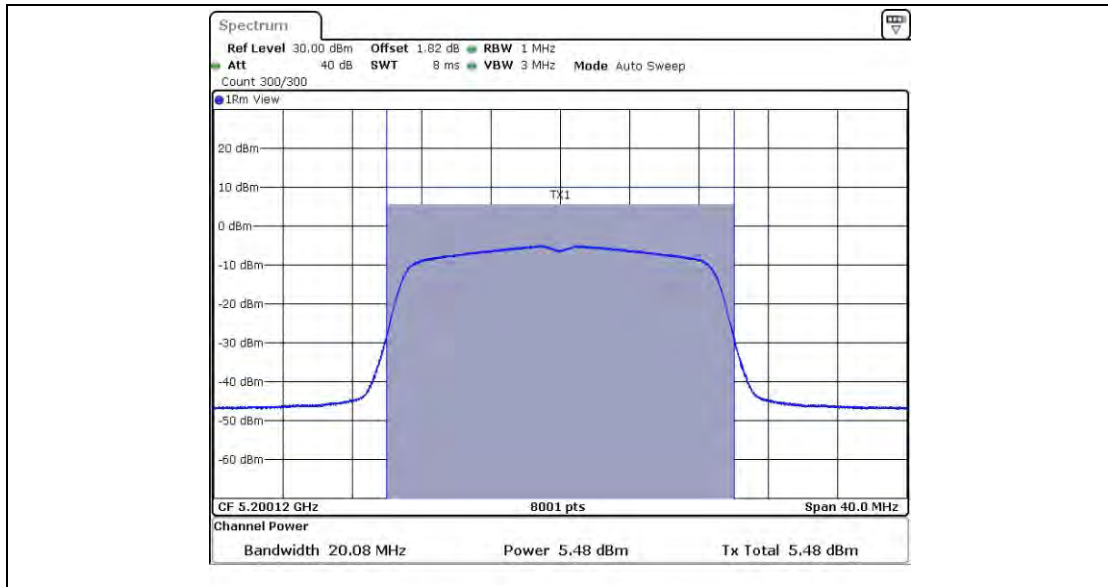


11AC20MIMO\_Ant1\_5180

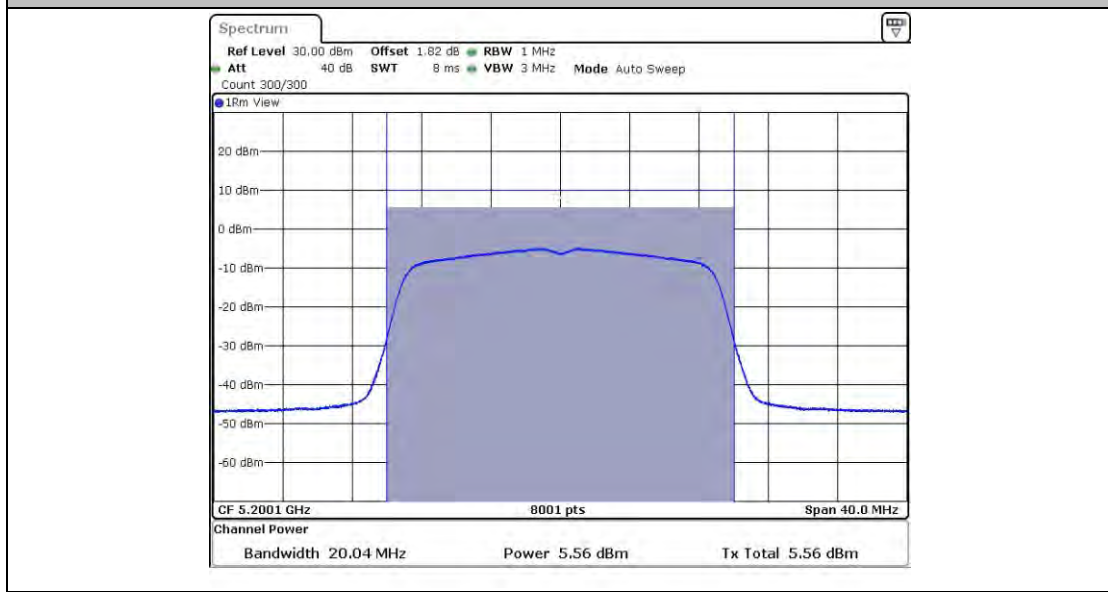


11AC20MIMO\_Ant2\_5180

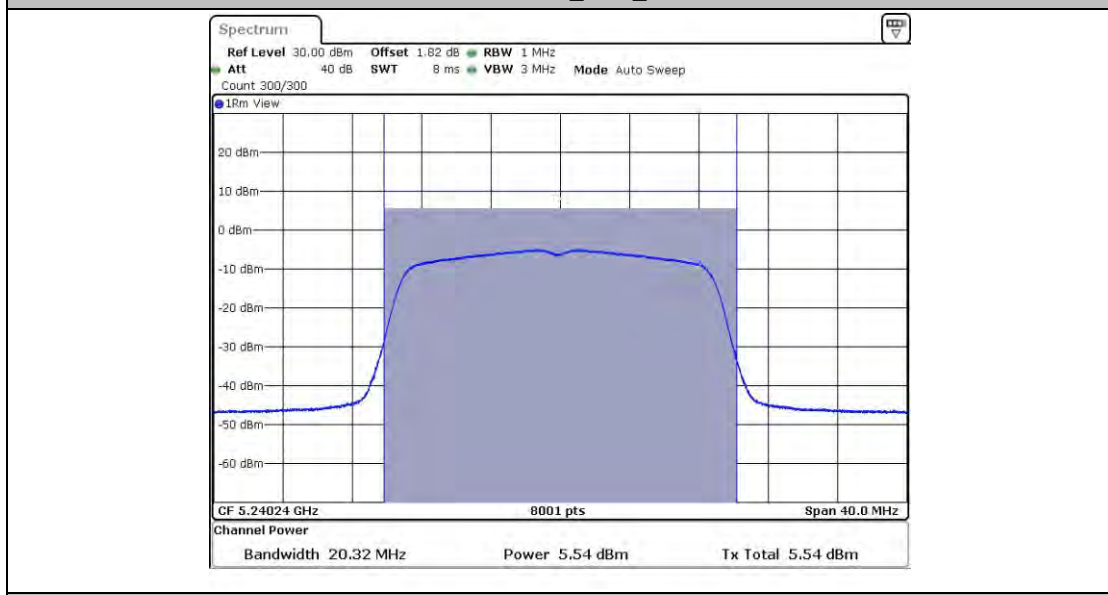




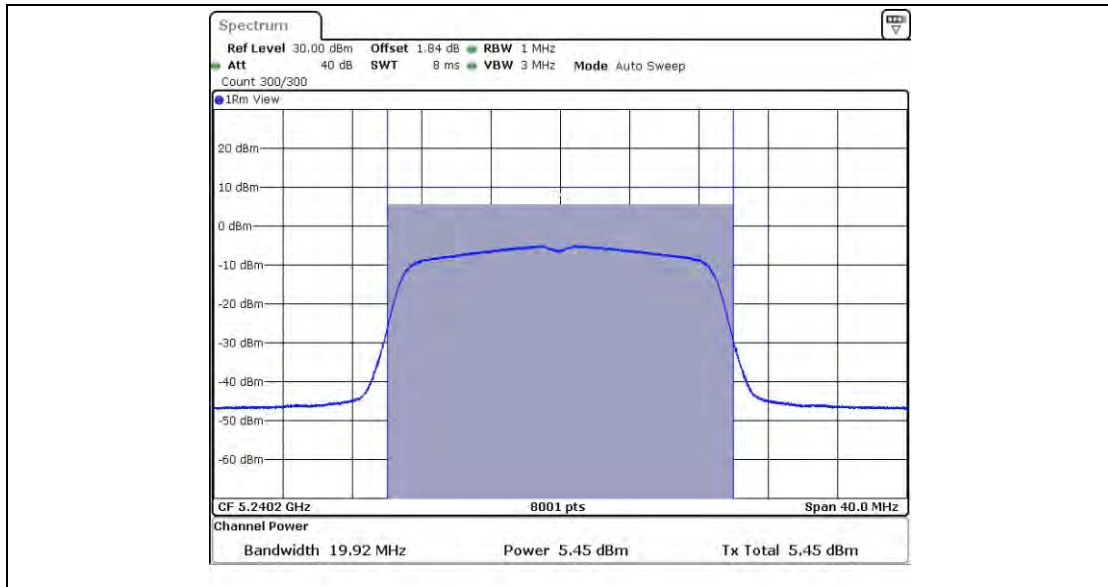
11AC20MIMO\_Ant1\_5200



11AC20MIMO\_Ant2\_5200



11AC20MIMO\_Ant1\_5240



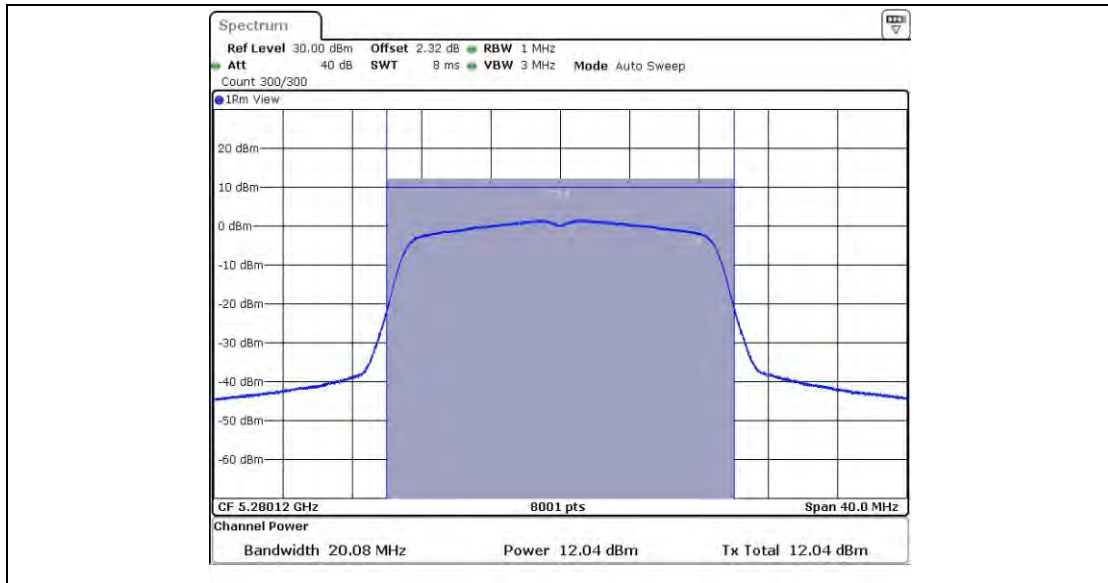
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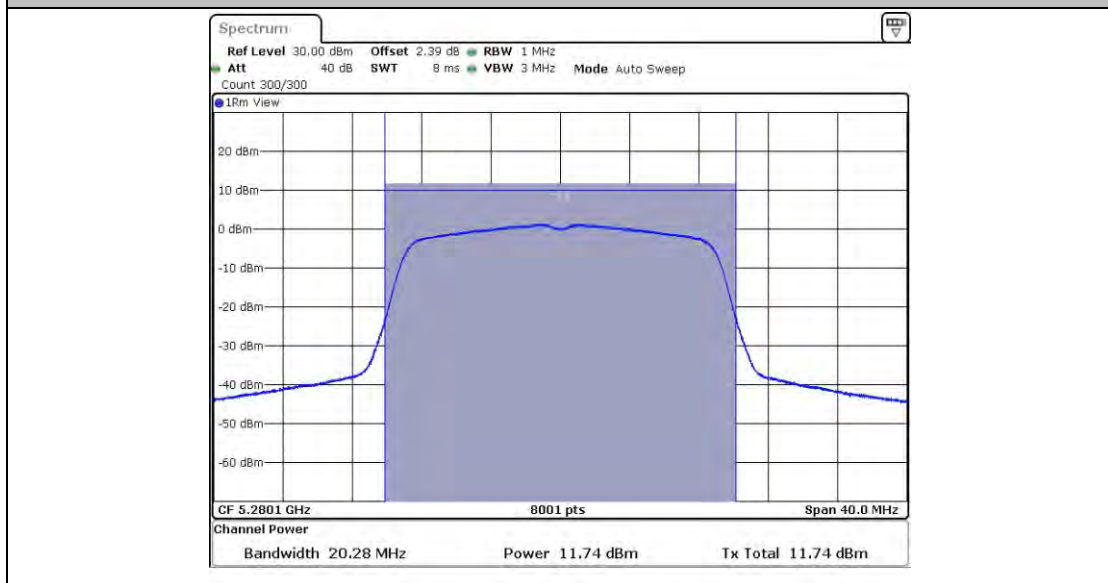
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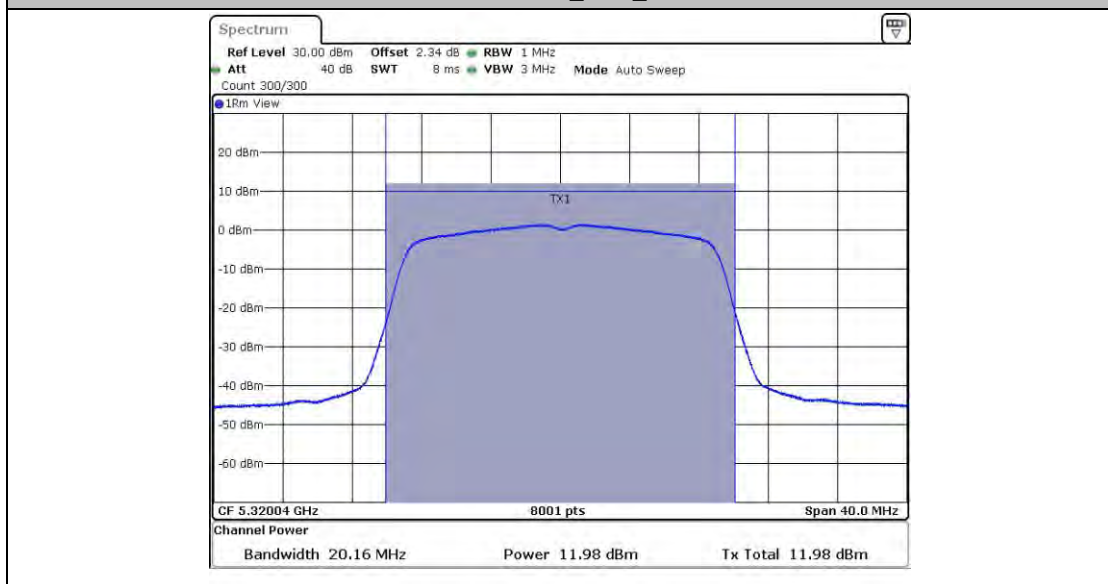
11AC20MIMO\_Ant2\_5260



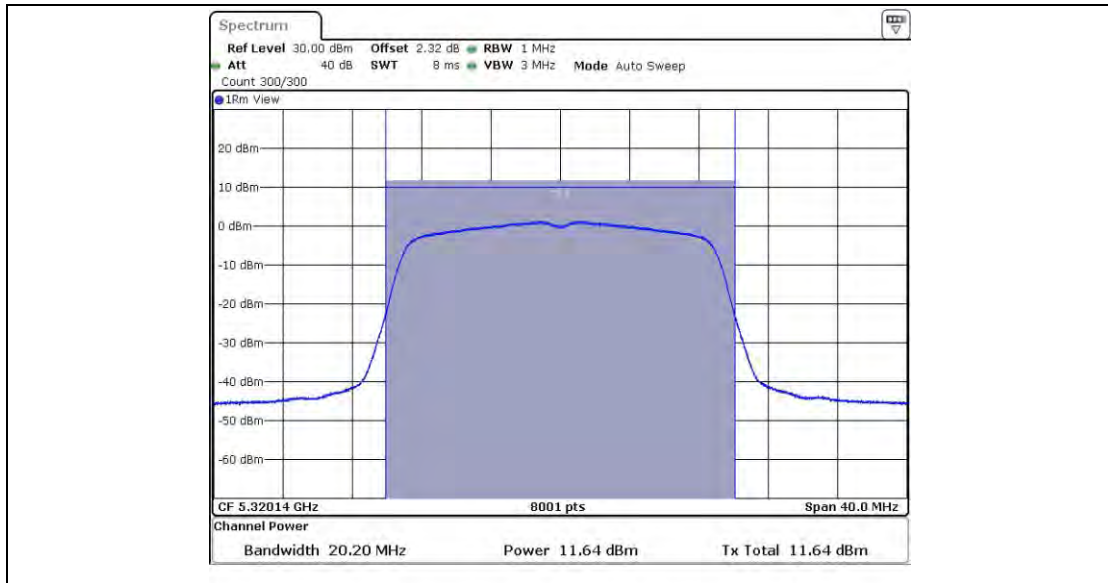
11AC20MIMO\_Ant1\_5280



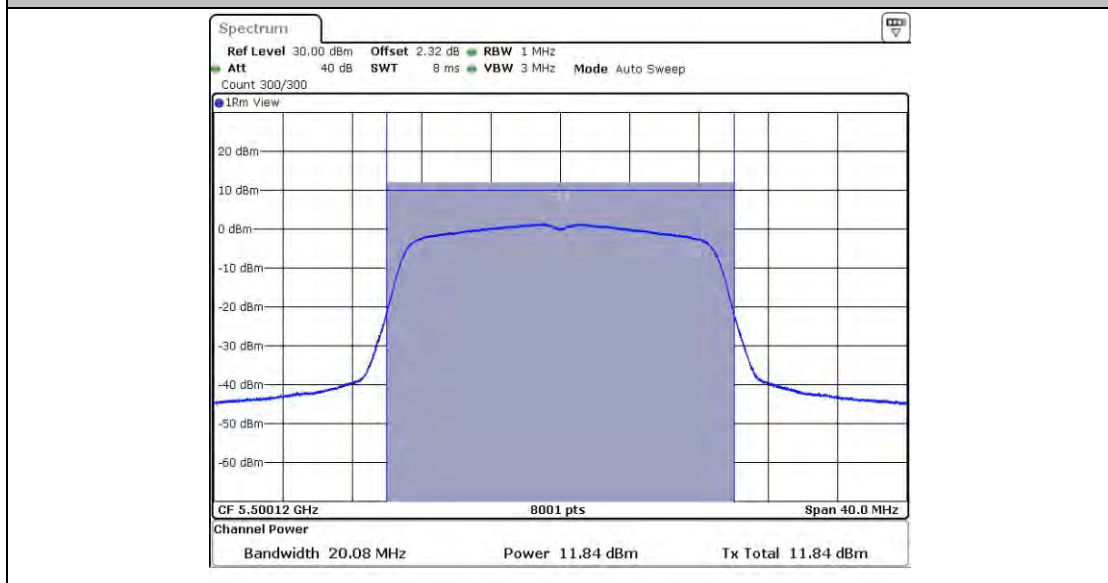
11AC20MIMO\_Ant2\_5280



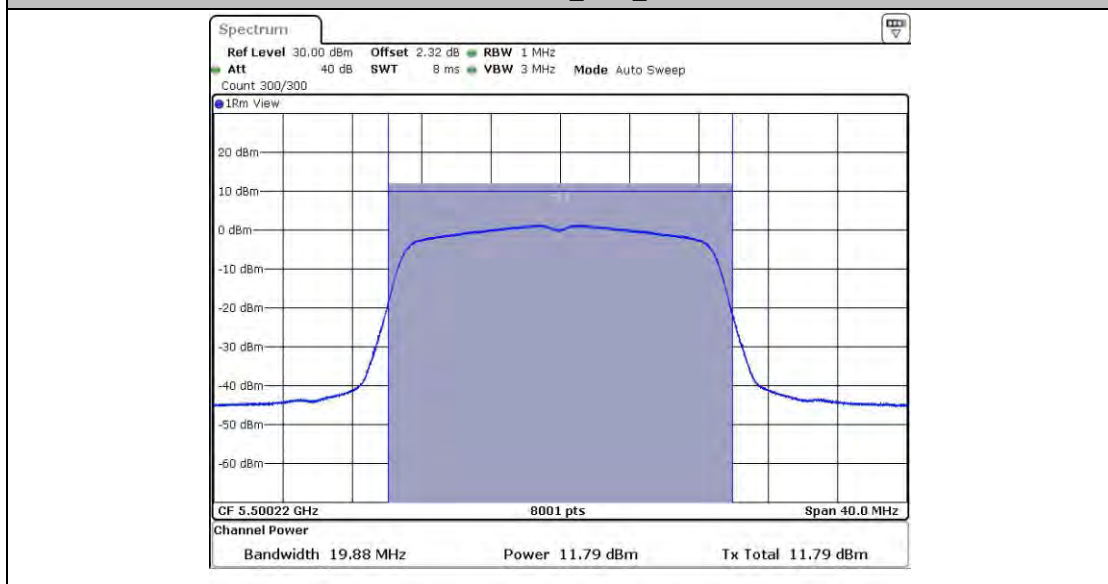
11AC20MIMO\_Ant1\_5320



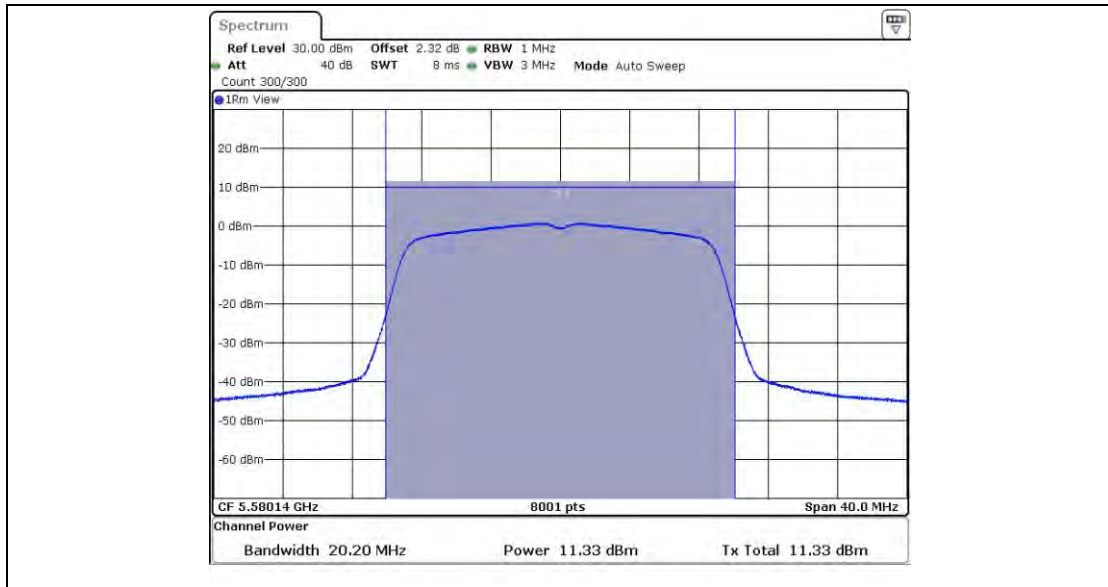
11AC20MIMO\_Ant2\_5320



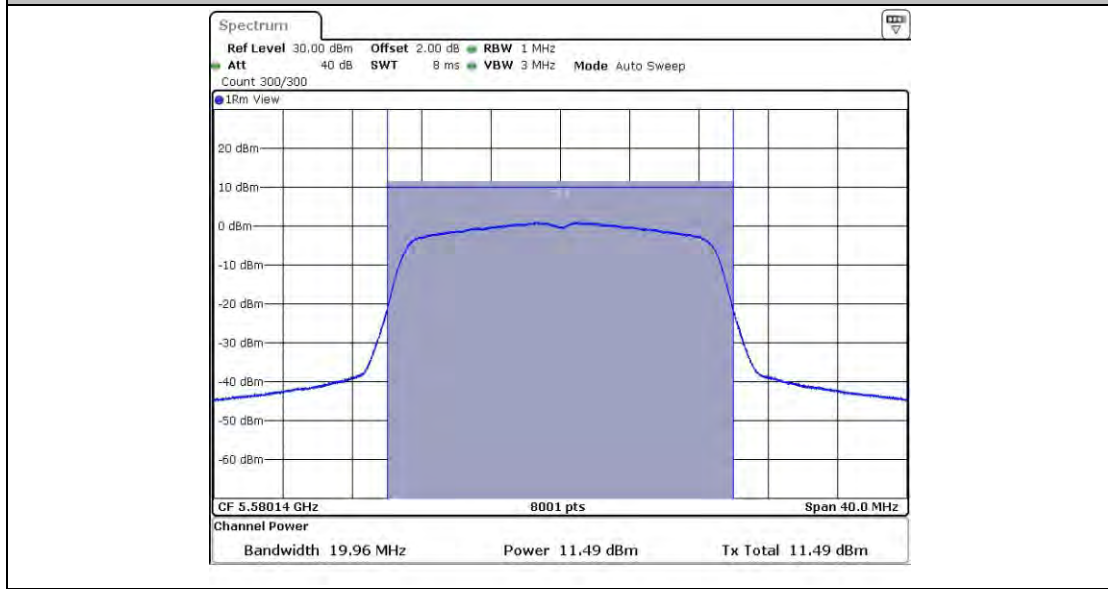
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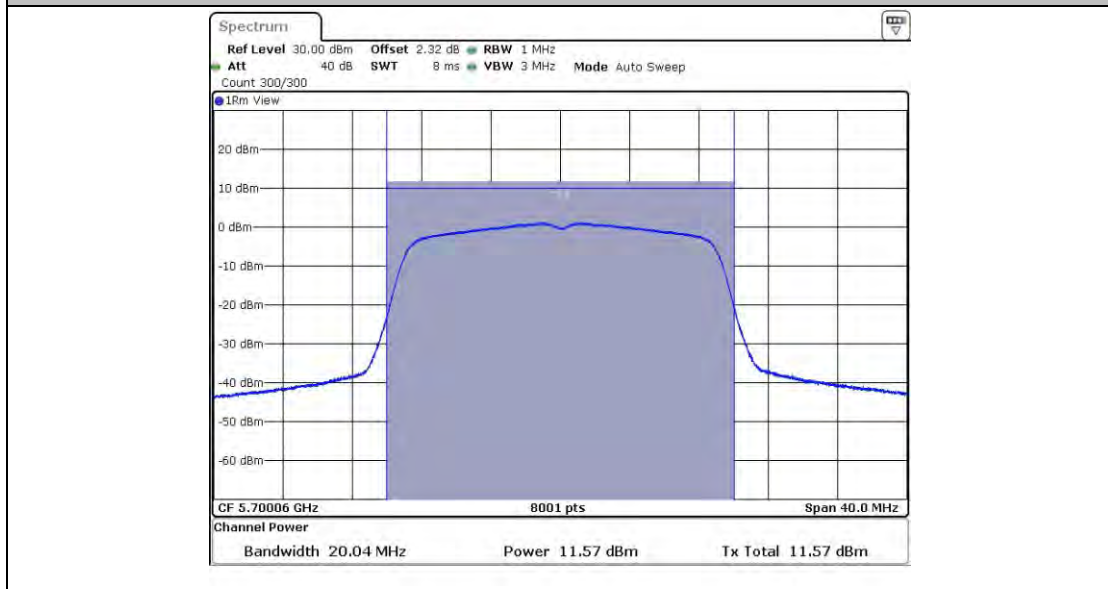
11AC20MIMO\_Ant2\_5500



11AC20MIMO\_Ant1\_5580

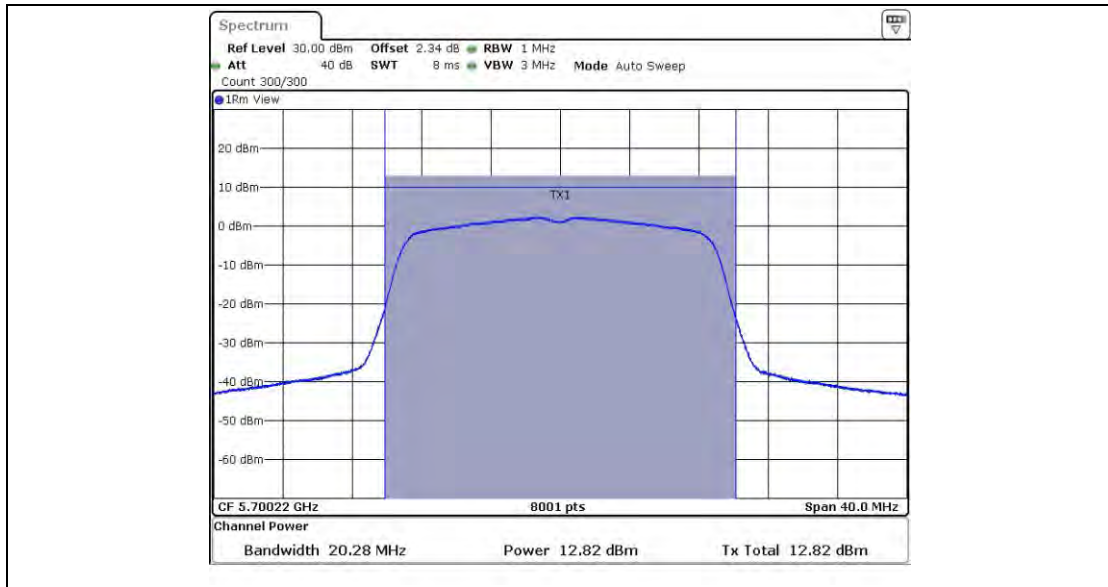


11AC20MIMO\_Ant2\_5580

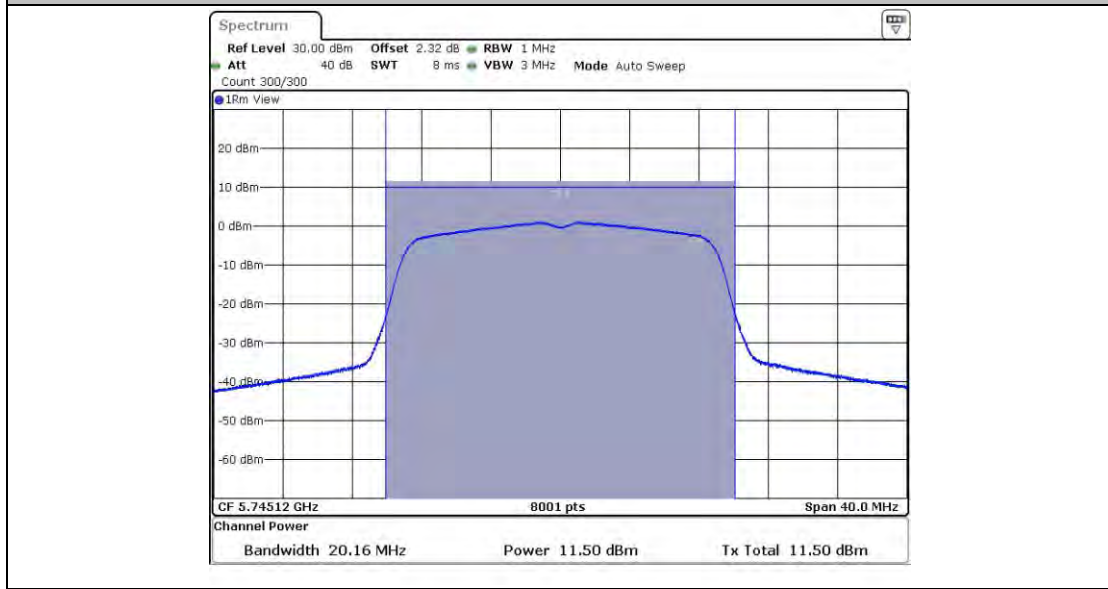


11AC20MIMO\_Ant1\_5700

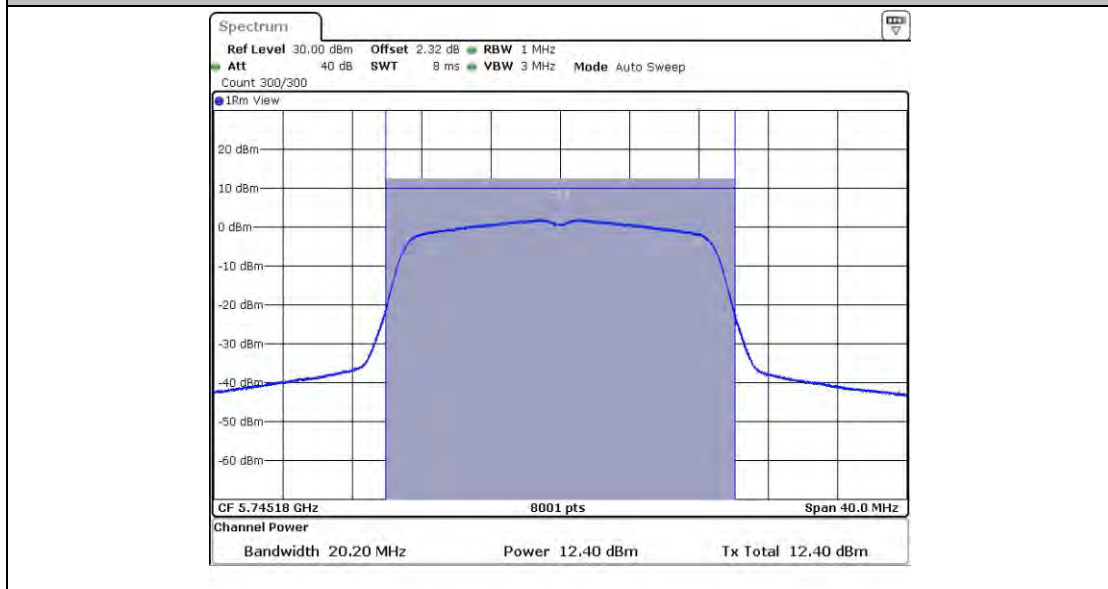




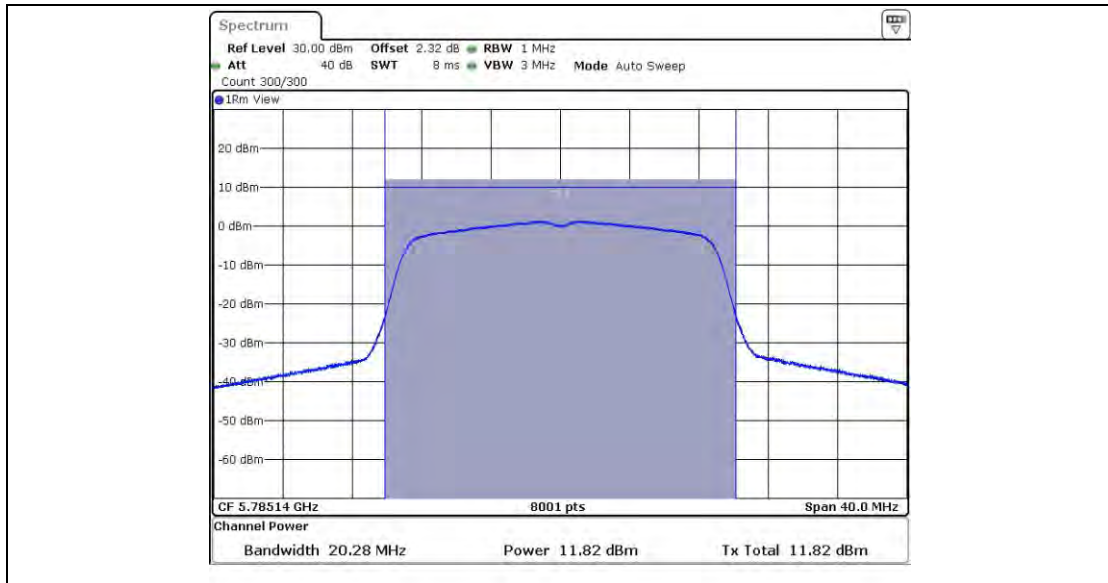
11AC20MIMO\_Ant2\_5700



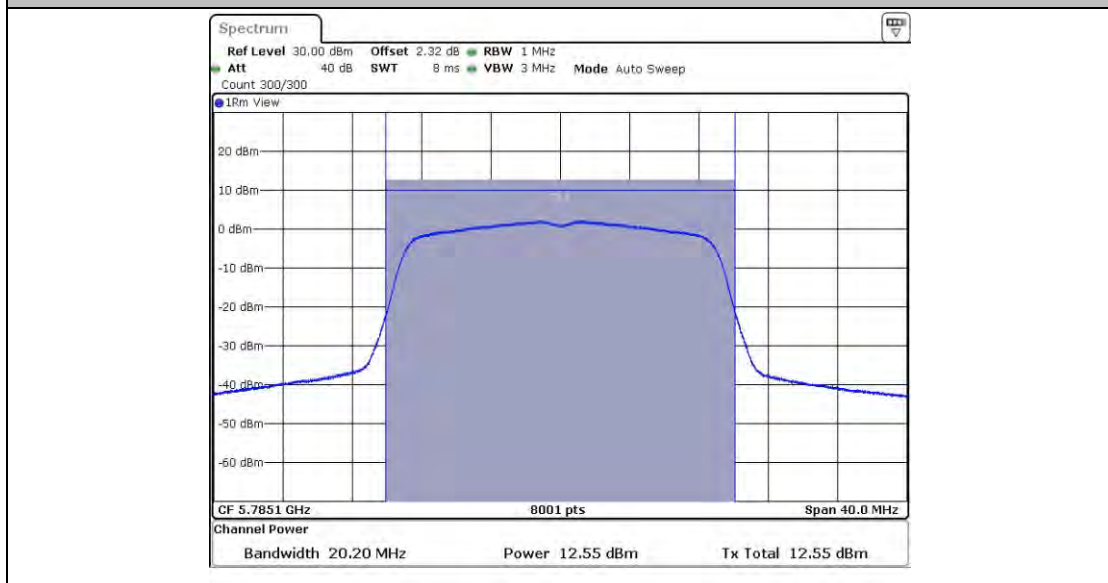
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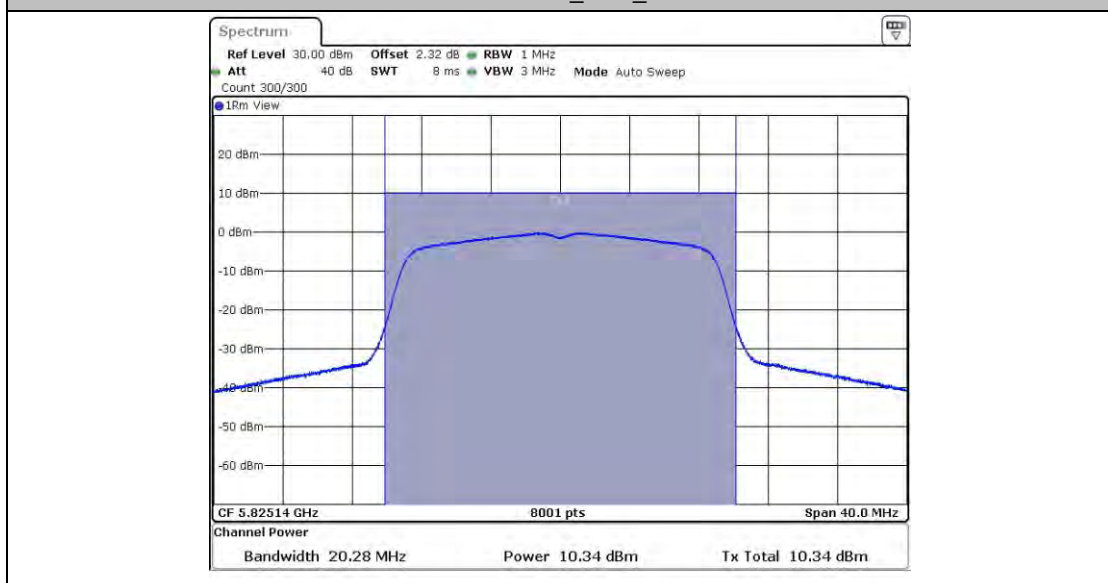
11AC20MIMO\_Ant2\_5745



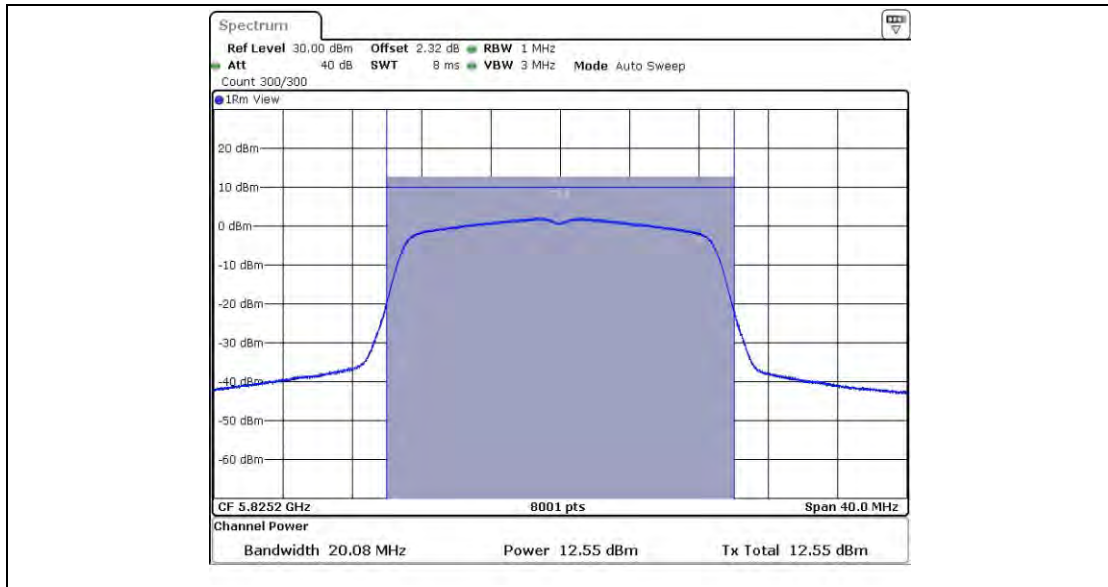
11AC20MIMO\_Ant1\_5785



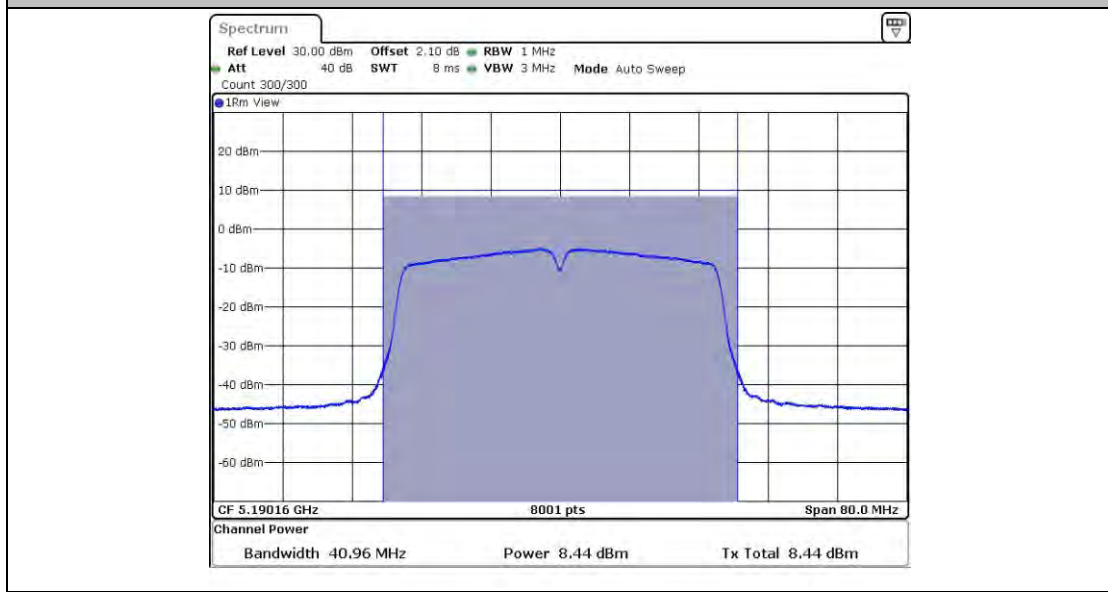
11AC20MIMO\_Ant2\_5785



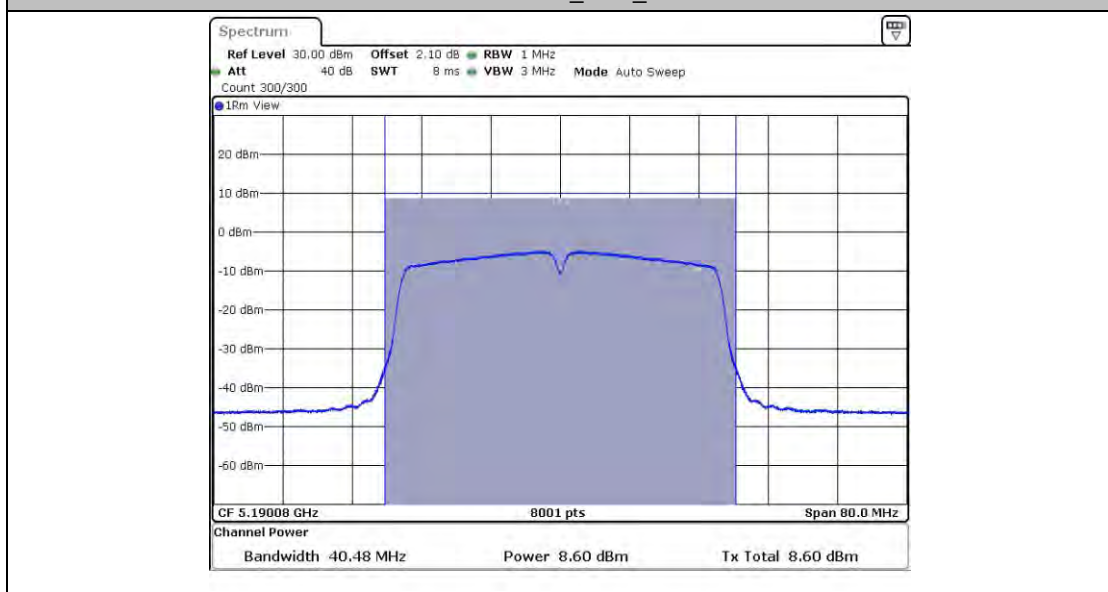
11AC20MIMO\_Ant1\_5825



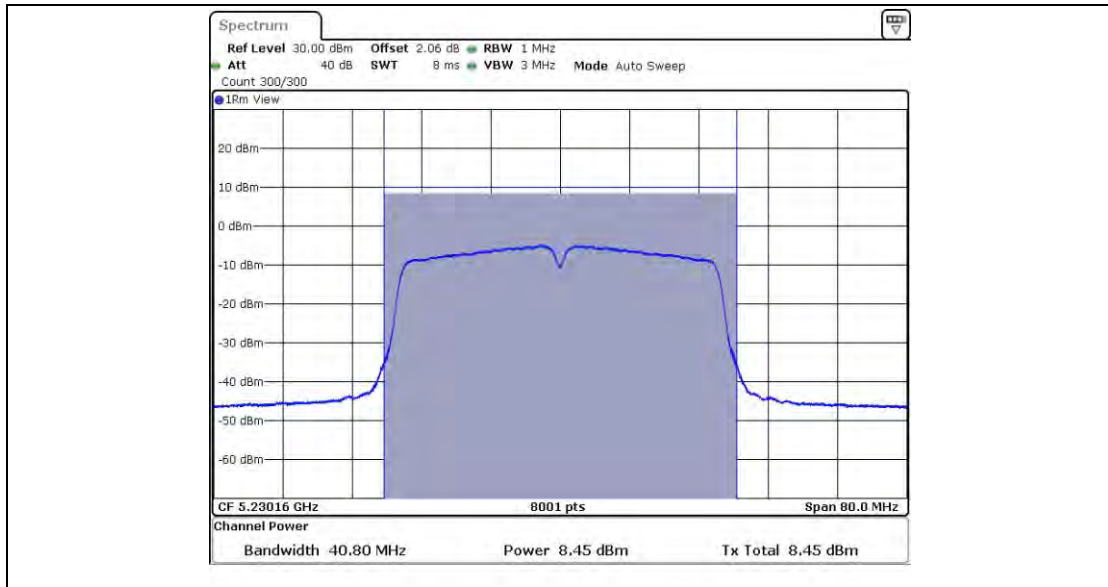
11AC20MIMO\_Ant2\_5825



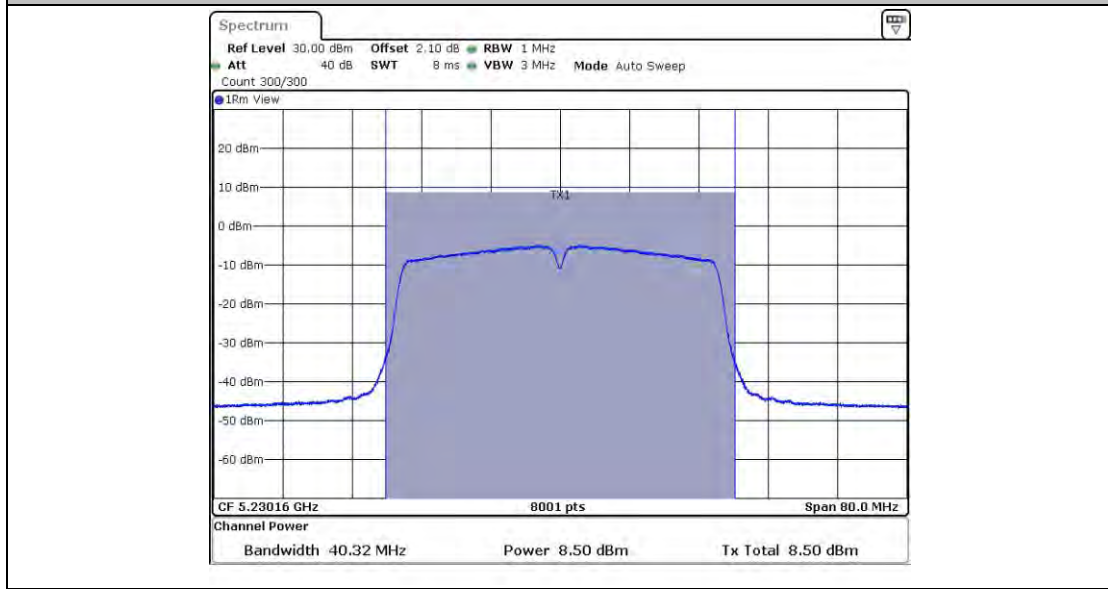
11AC40MIMO\_Ant1\_5190



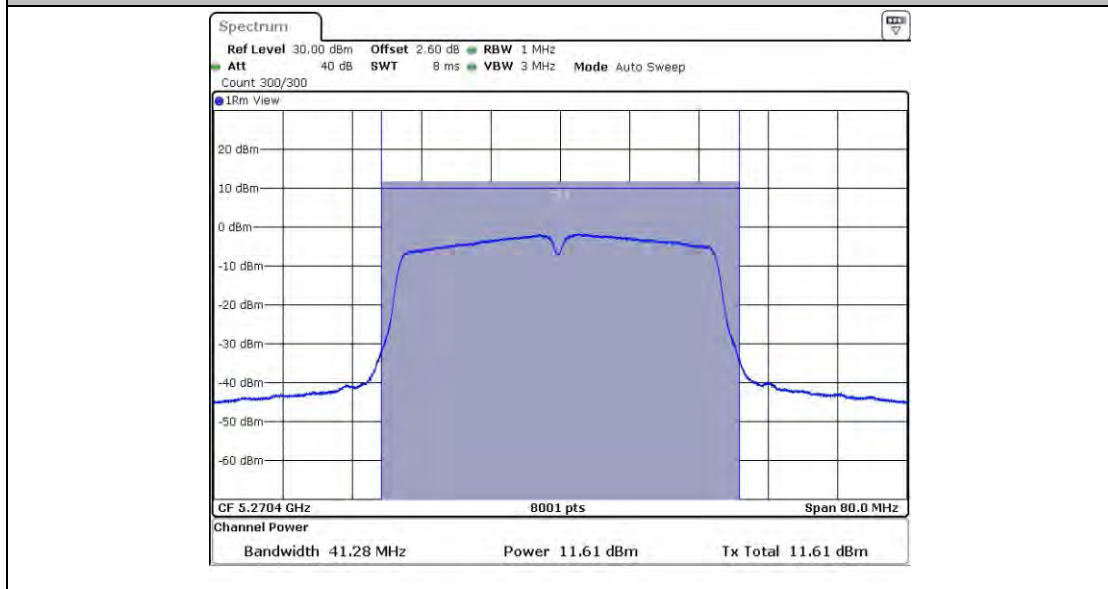
11AC40MIMO\_Ant2\_5190



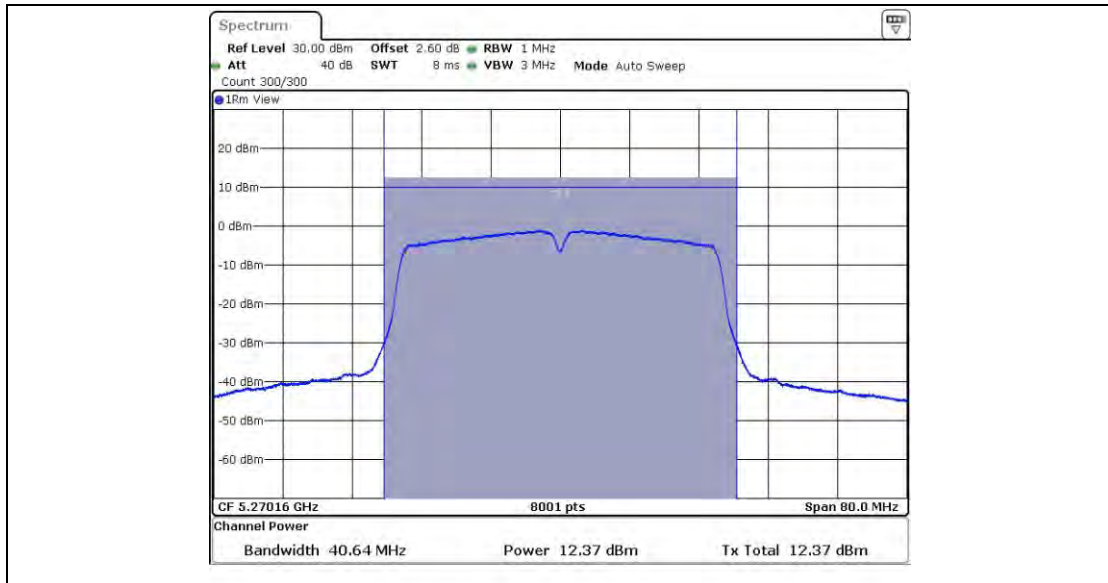
11AC40MIMO\_Ant1\_5230



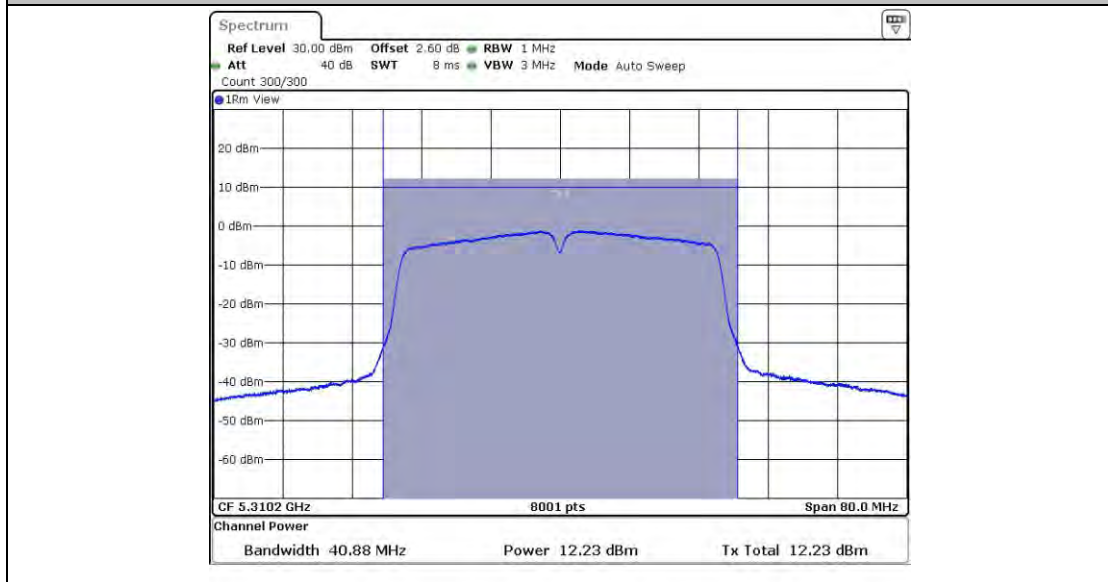
11AC40MIMO\_Ant2\_5230



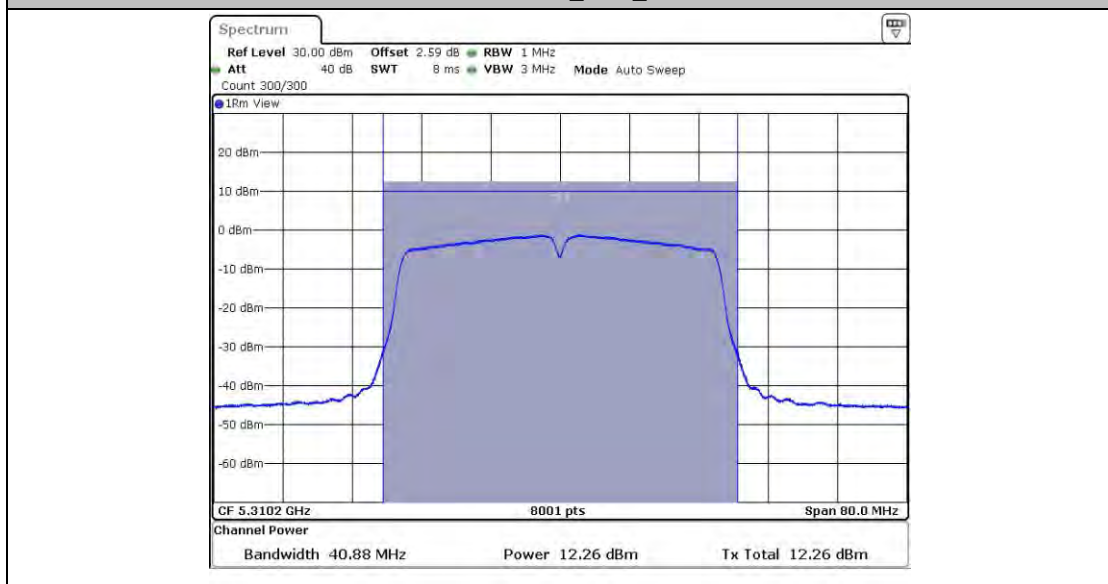
11AC40MIMO\_Ant1\_5270



11AC40MIMO\_Ant2\_5270

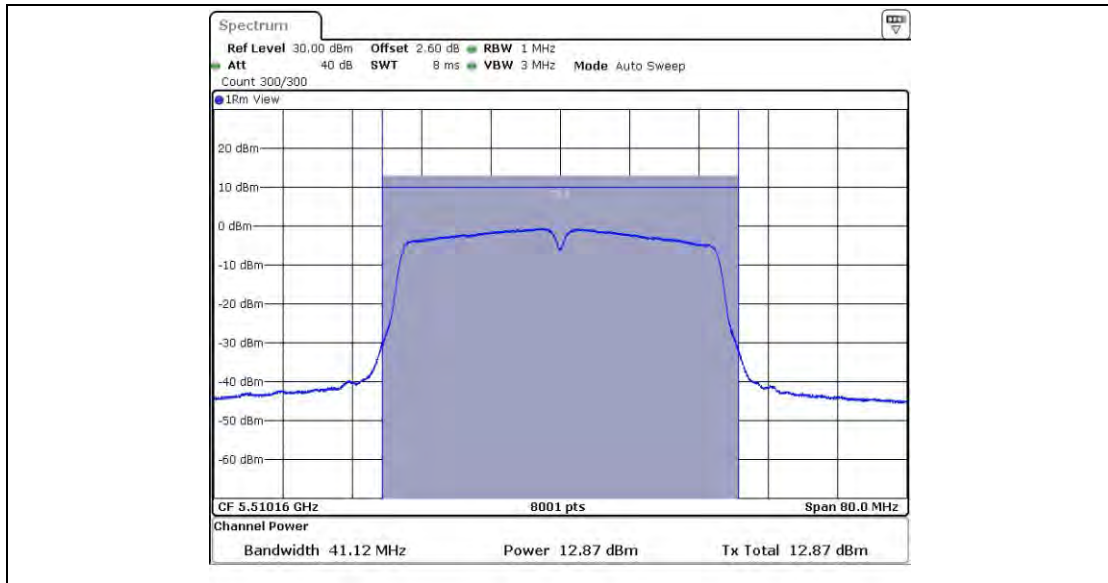


11AC40MIMO\_Ant1\_5310

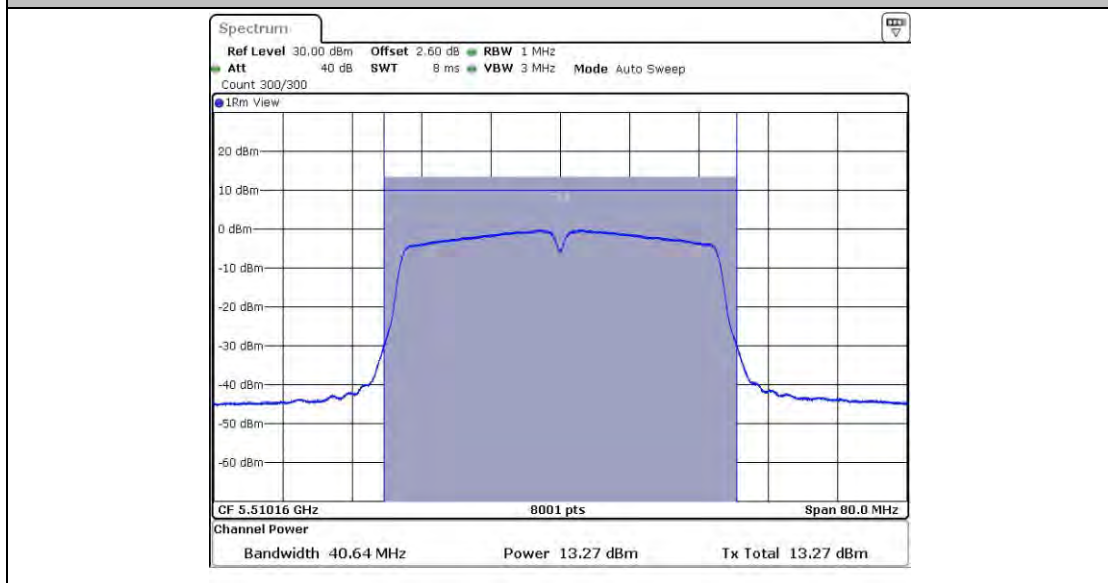


11AC40MIMO\_Ant2\_5310

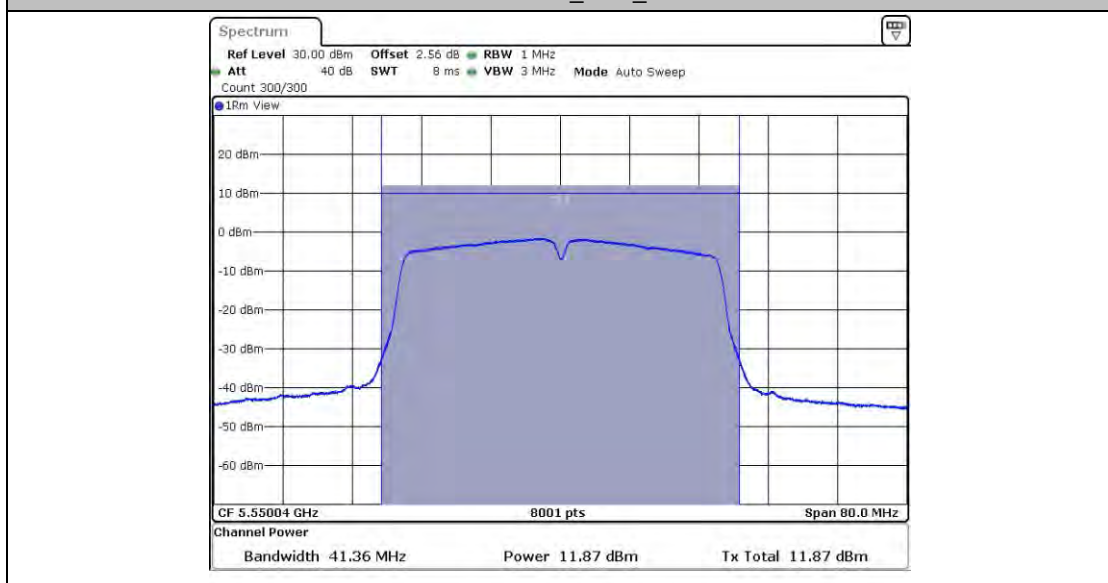




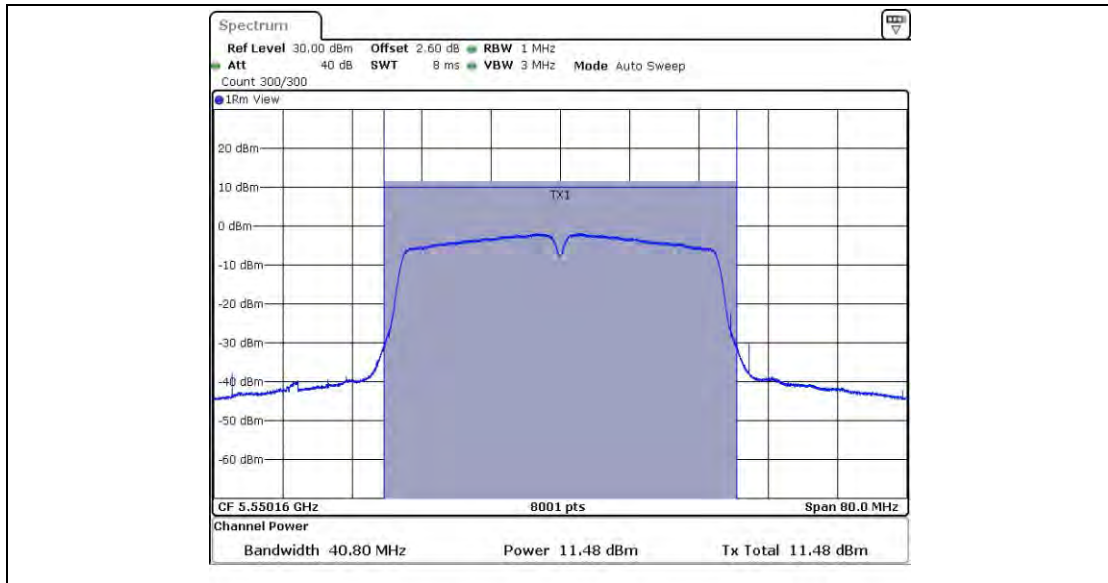
11AC40MIMO\_Ant1\_5510



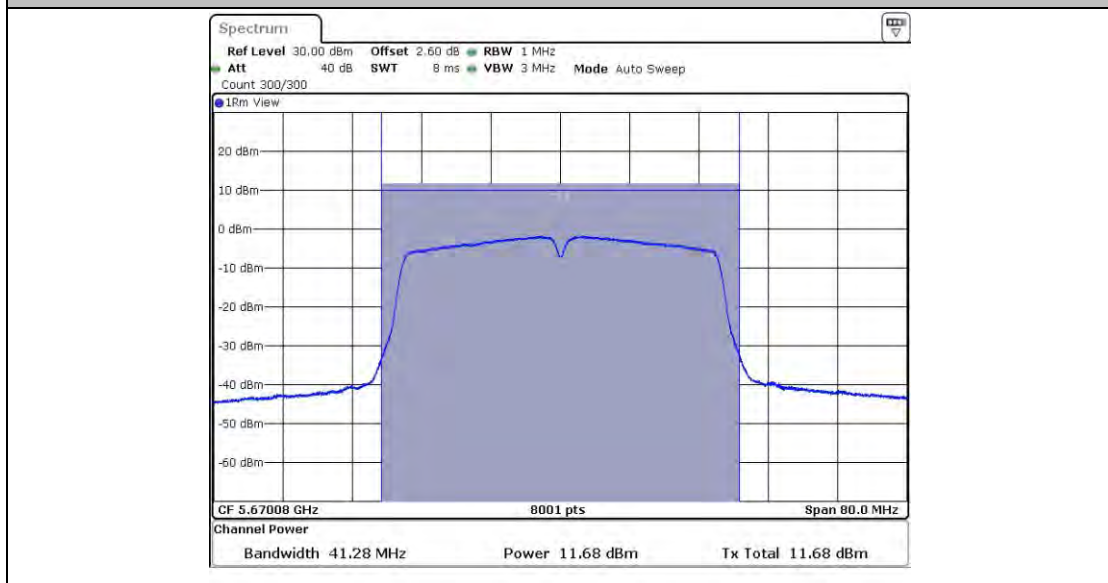
11AC40MIMO\_Ant2\_5510



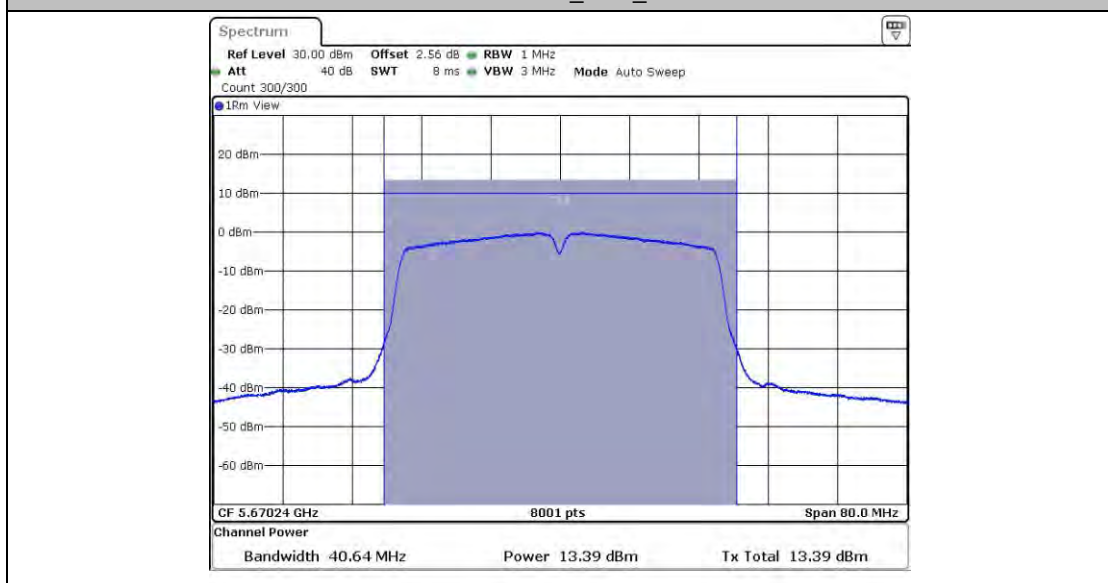
11AC40MIMO\_Ant1\_5550



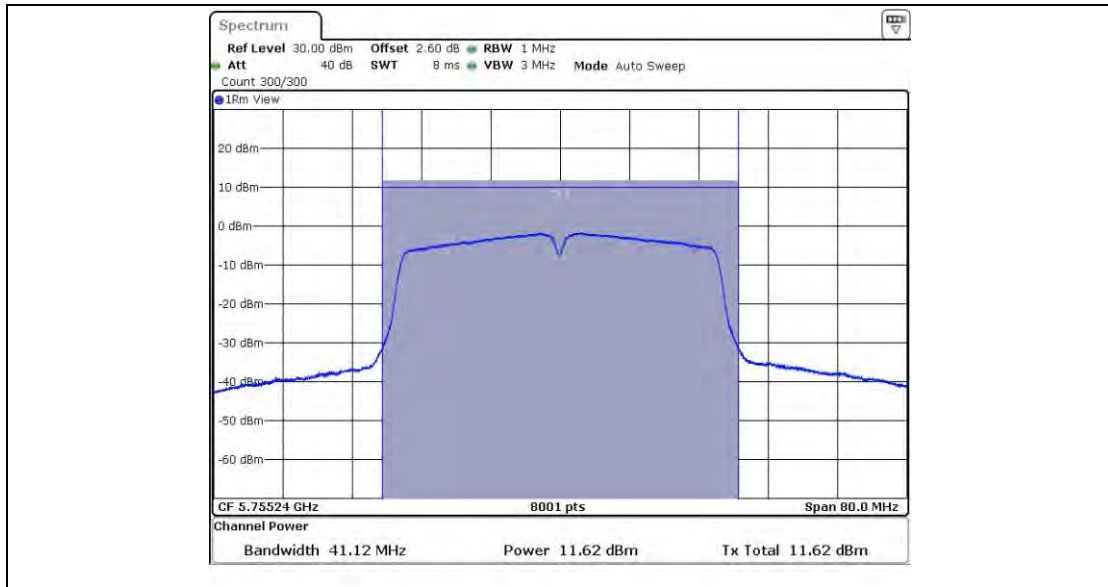
11AC40MIMO\_Ant2\_5550



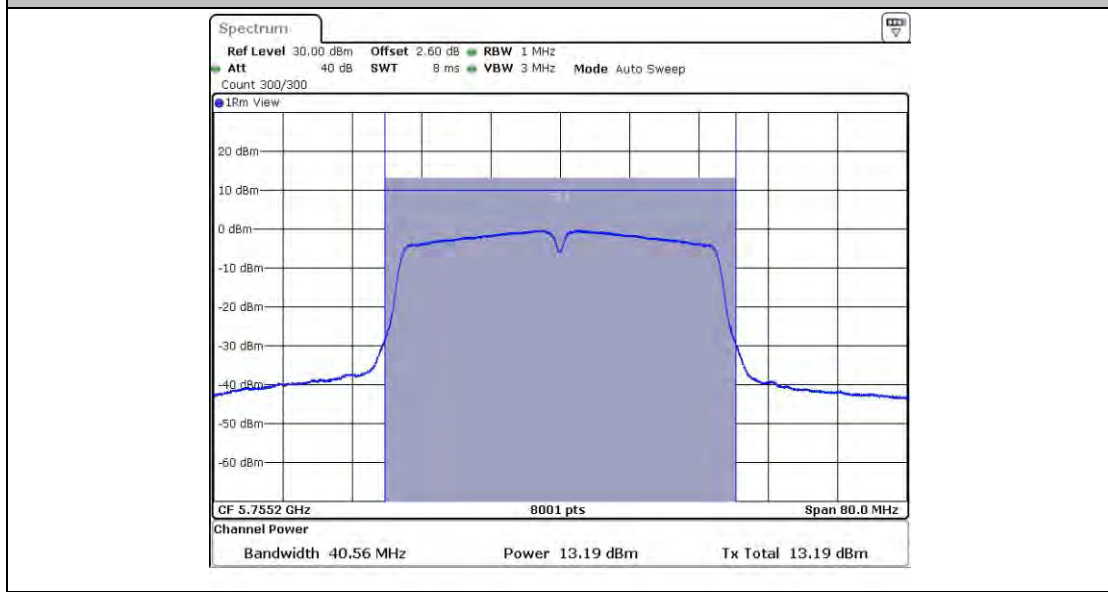
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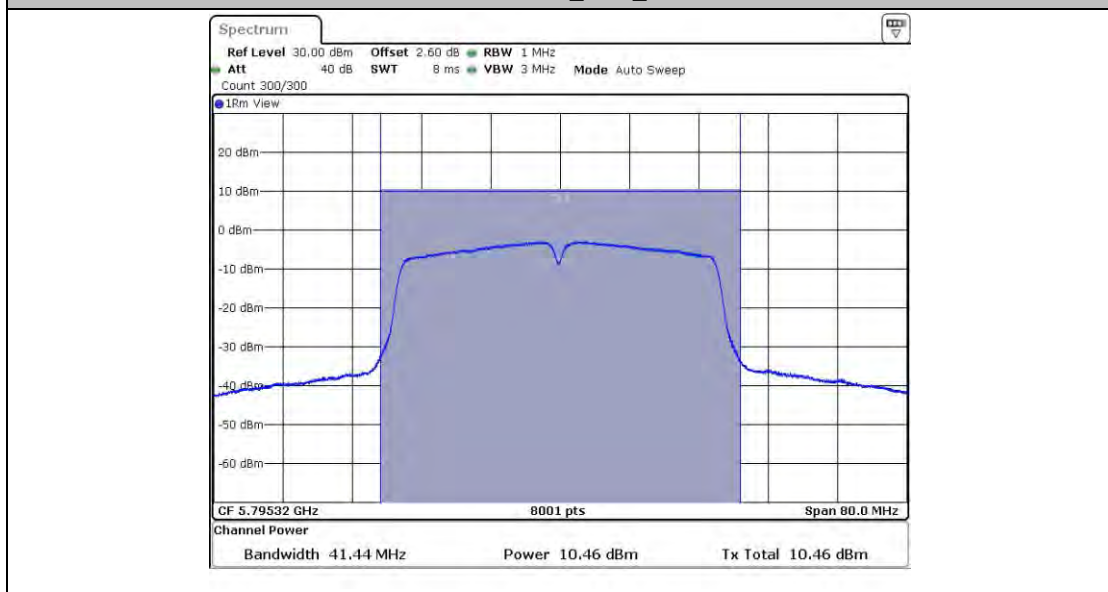
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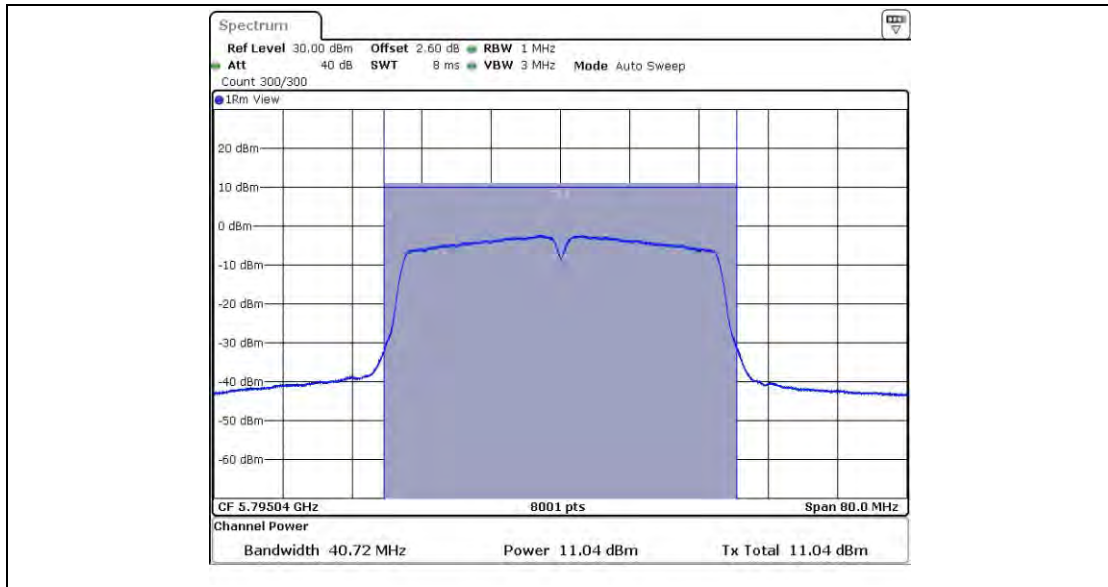
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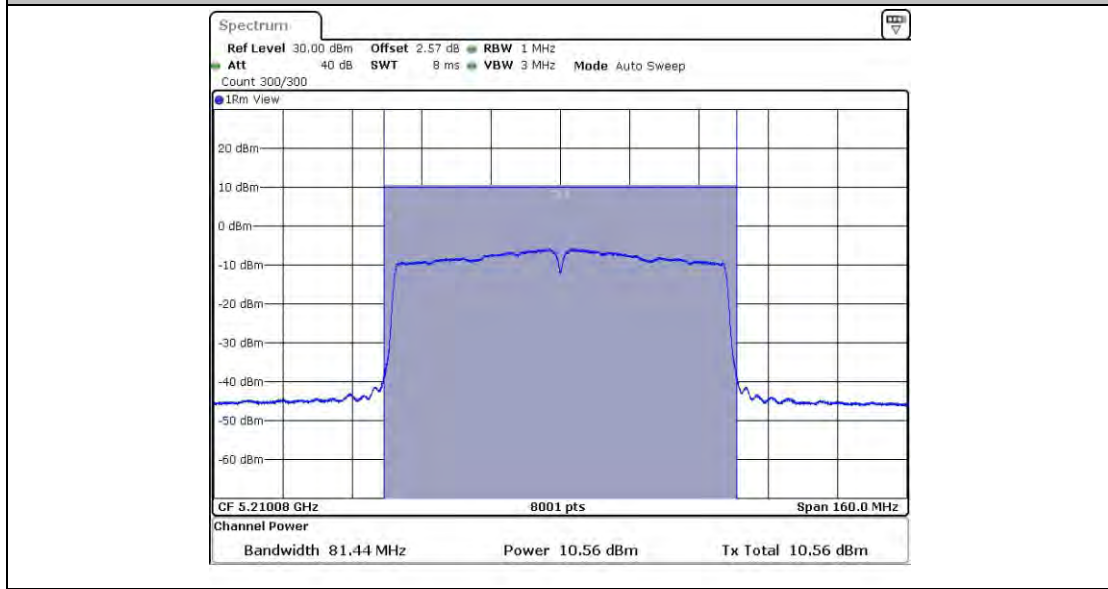
11AC40MIMO\_Ant2\_5755



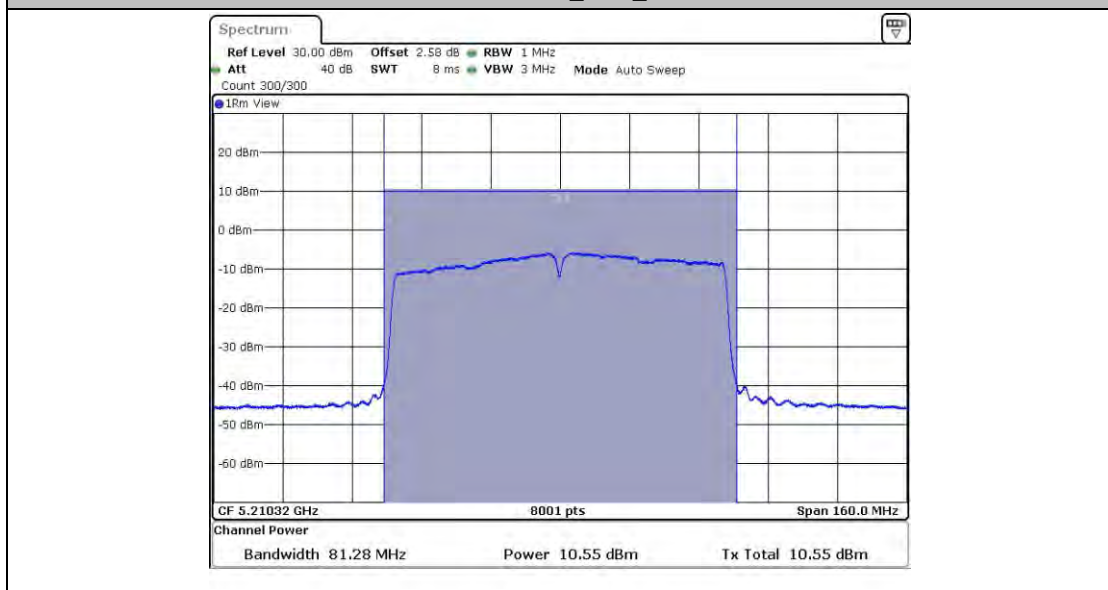
11AC40MIMO\_Ant1\_5795



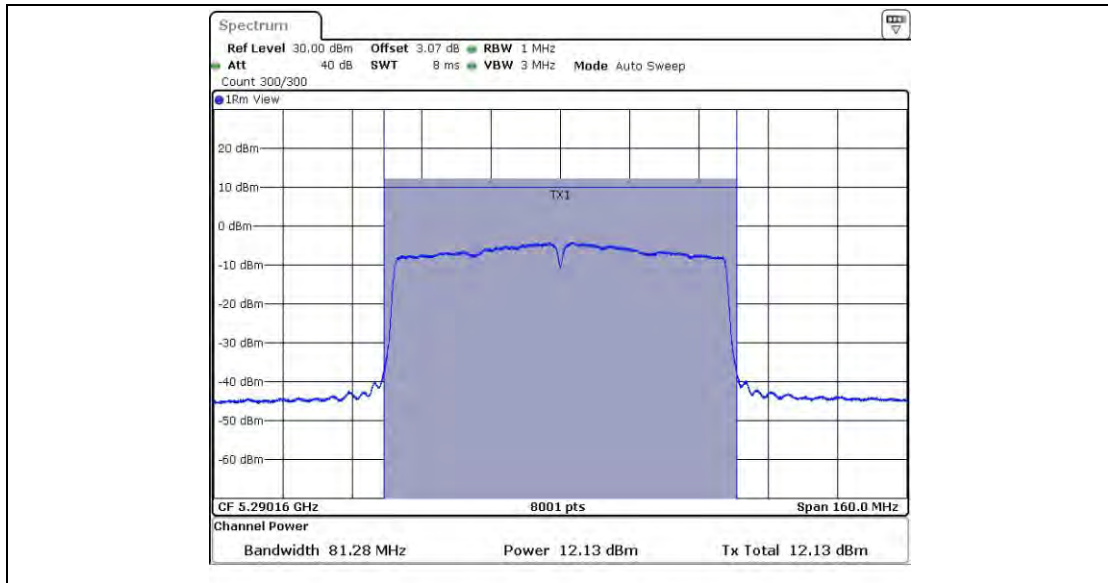
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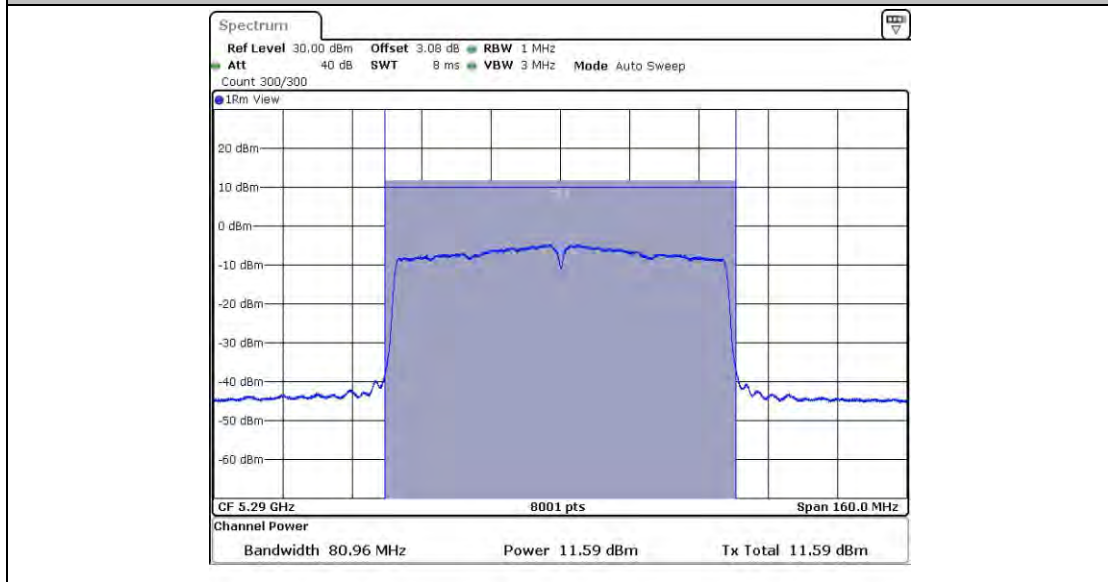
11AC80MIMO\_Ant1\_5210



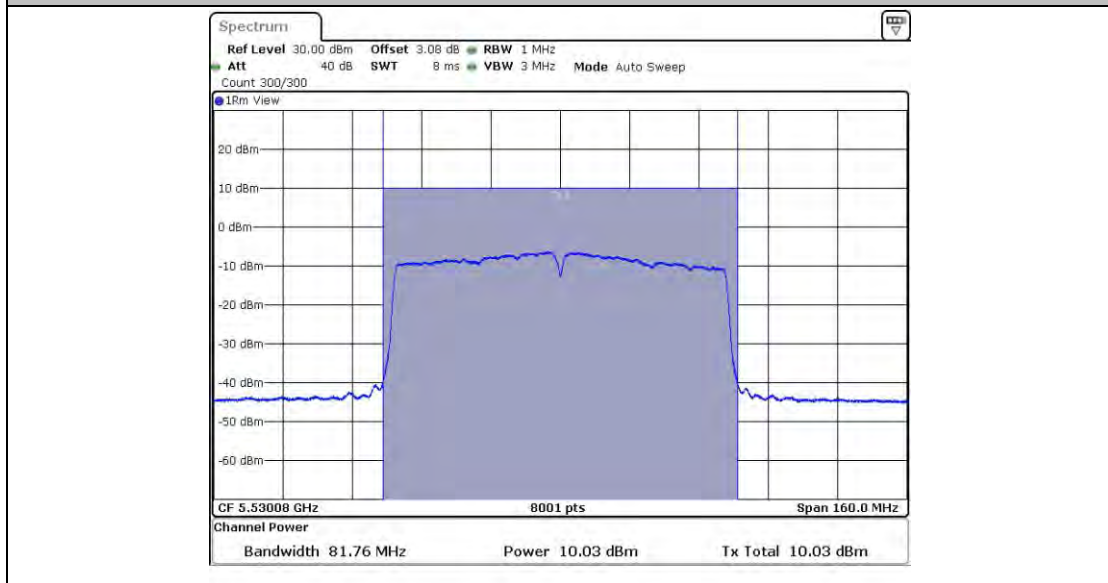
11AC80MIMO\_Ant2\_5210



11AC80MIMO\_Ant1\_5290

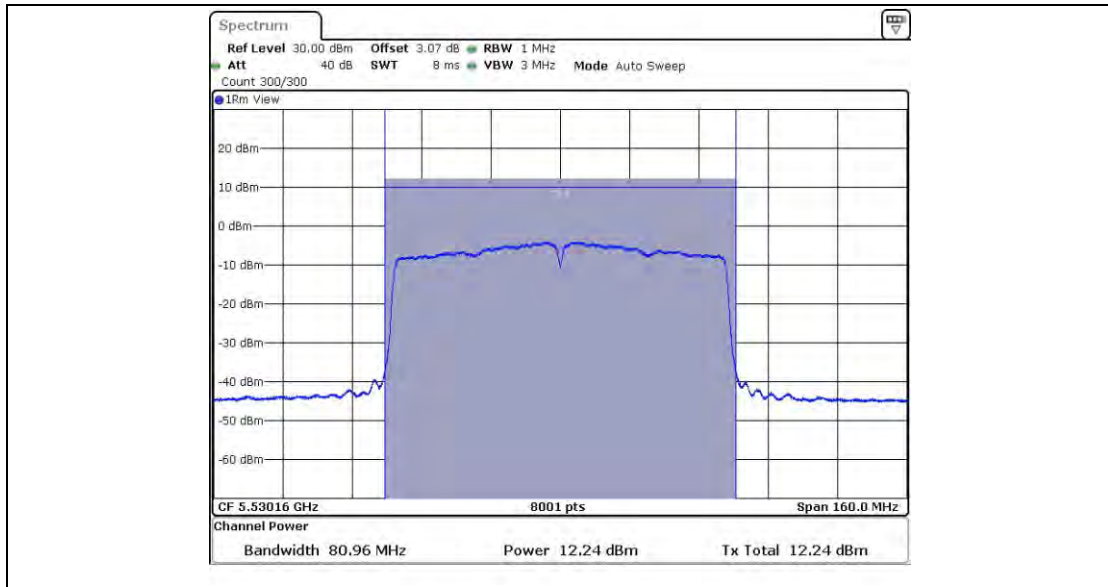


11AC80MIMO\_Ant2\_5290

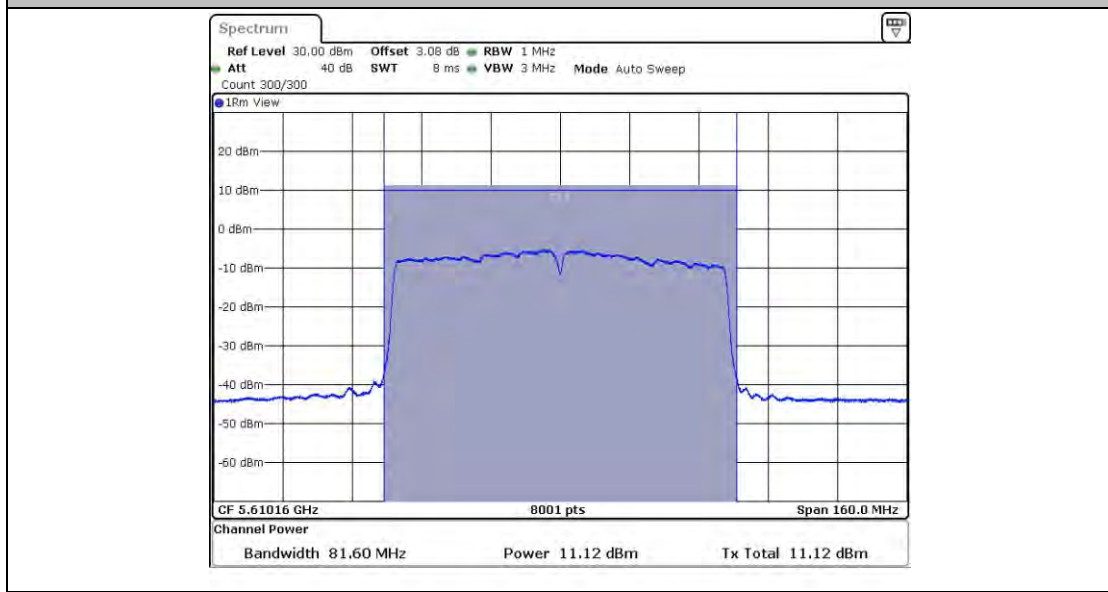


11AC80MIMO\_Ant1\_5530

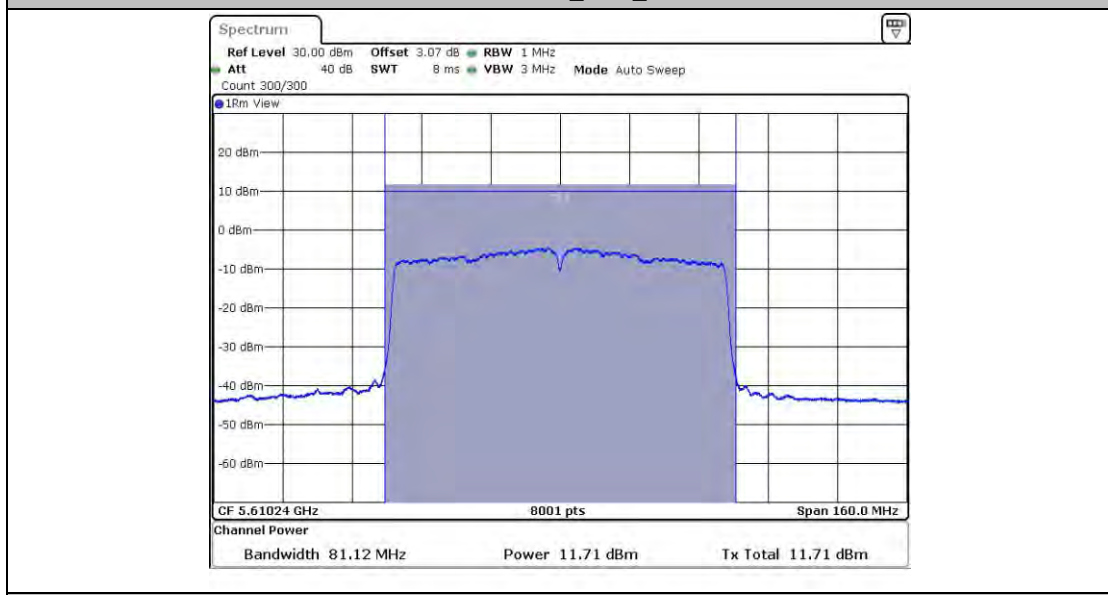




11AC80MIMO\_Ant2\_5530



11AC80MIMO\_Ant1\_5610



11AC80MIMO\_Ant2\_5610