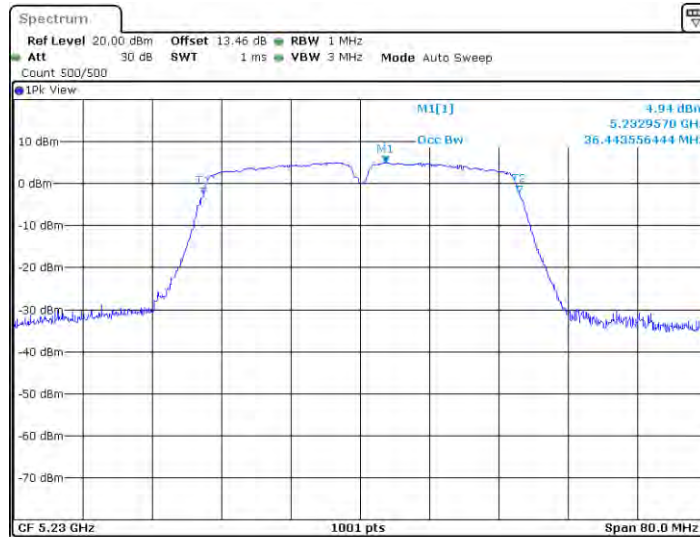


11N40SISO_Ant2_5190



Date: 26.OCT.2020 17:53:47

11N40SISO_Ant1_5230



Date: 26.OCT.2020 10:47:35

11N40SISO_Ant2_5230



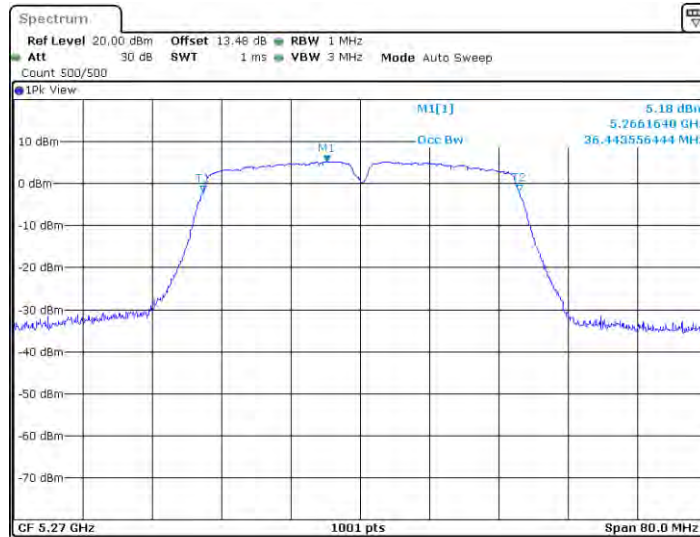
Date: 26.OCT.2020 17:59:53

11N40SISO_Ant1_5270



Date: 26.OCT.2020 10:50:46

11N40SISO_Ant2_5270



Date: 26.OCT.2020 18:05:58

11N40SISO_Ant1_5310



Date: 26.OCT.2020 10:53:24

11N40SISO_Ant2_5310



Date: 26.OCT.2020 18.12.09

11N40SISO_Ant1_5510



Date: 26.OCT.2020 10.58.40

11N40SISO_Ant2_5510



Date: 26.OCT.2020 18.18.25

11N40SISO_Ant1_5550



Date: 26.OCT.2020 11:07:40

11N40SISO_Ant2_5550



Date: 26.OCT.2020 18:28:57

11N40SISO_Ant1_5670



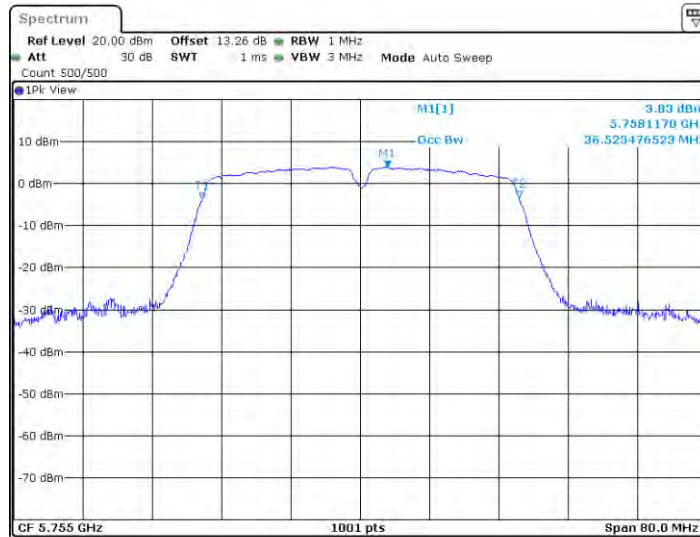
Date: 26.OCT.2020 11:14:56

11N40SISO_Ant2_5670



Date: 26.OCT.2020 18:42:53

11N40SISO_Ant1_5755



Date: 26.OCT.2020 11:18:24

11N40SISO_Ant2_5755



Date: 26.OCT.2020 18:52:09

11N40SISO_Ant1_5795



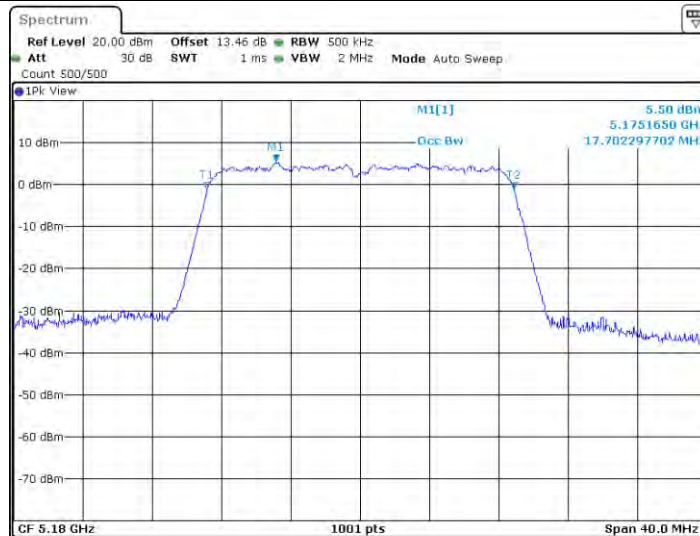
Date: 26.OCT.2020 11:21:25

11N40SISO_Ant2_5795

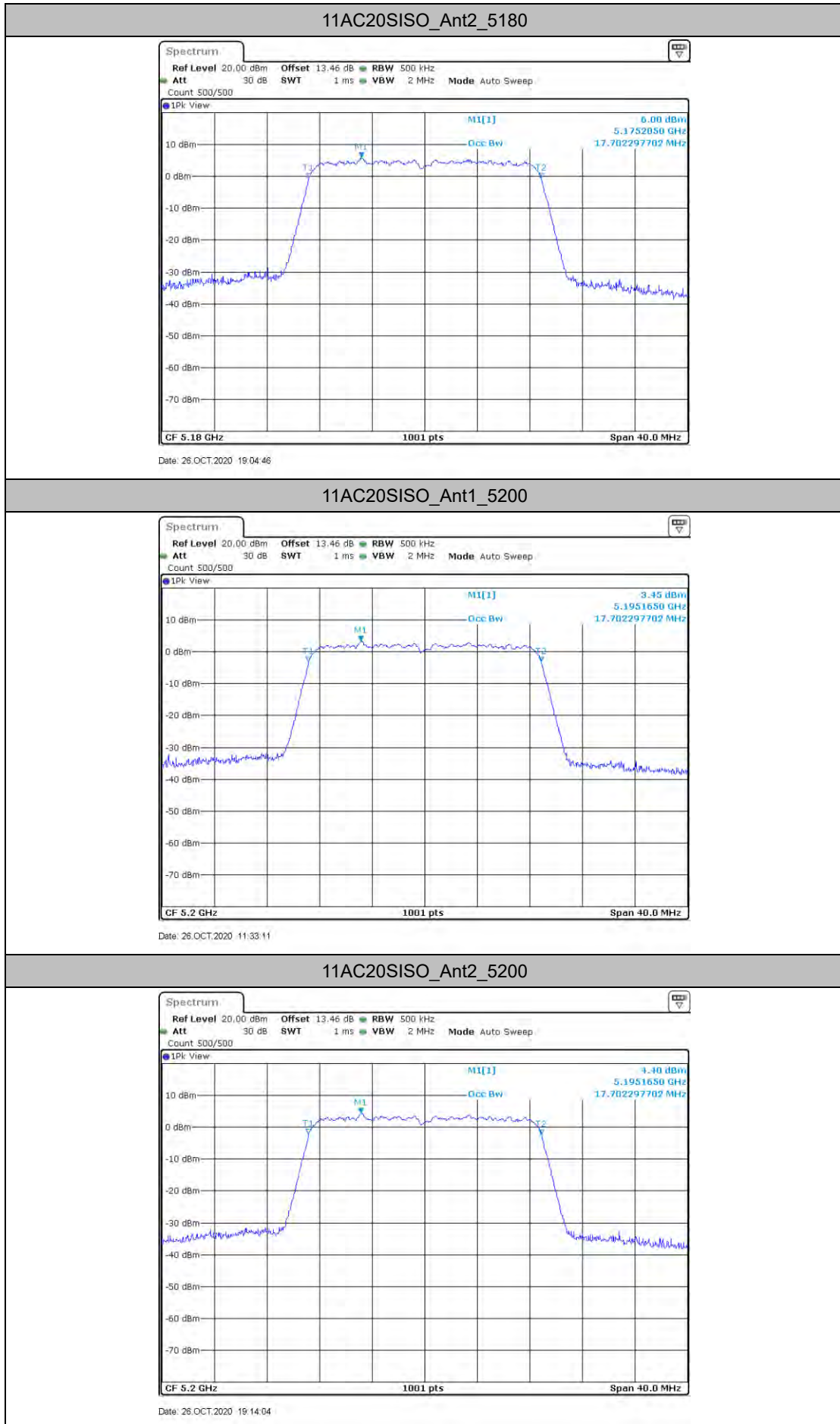


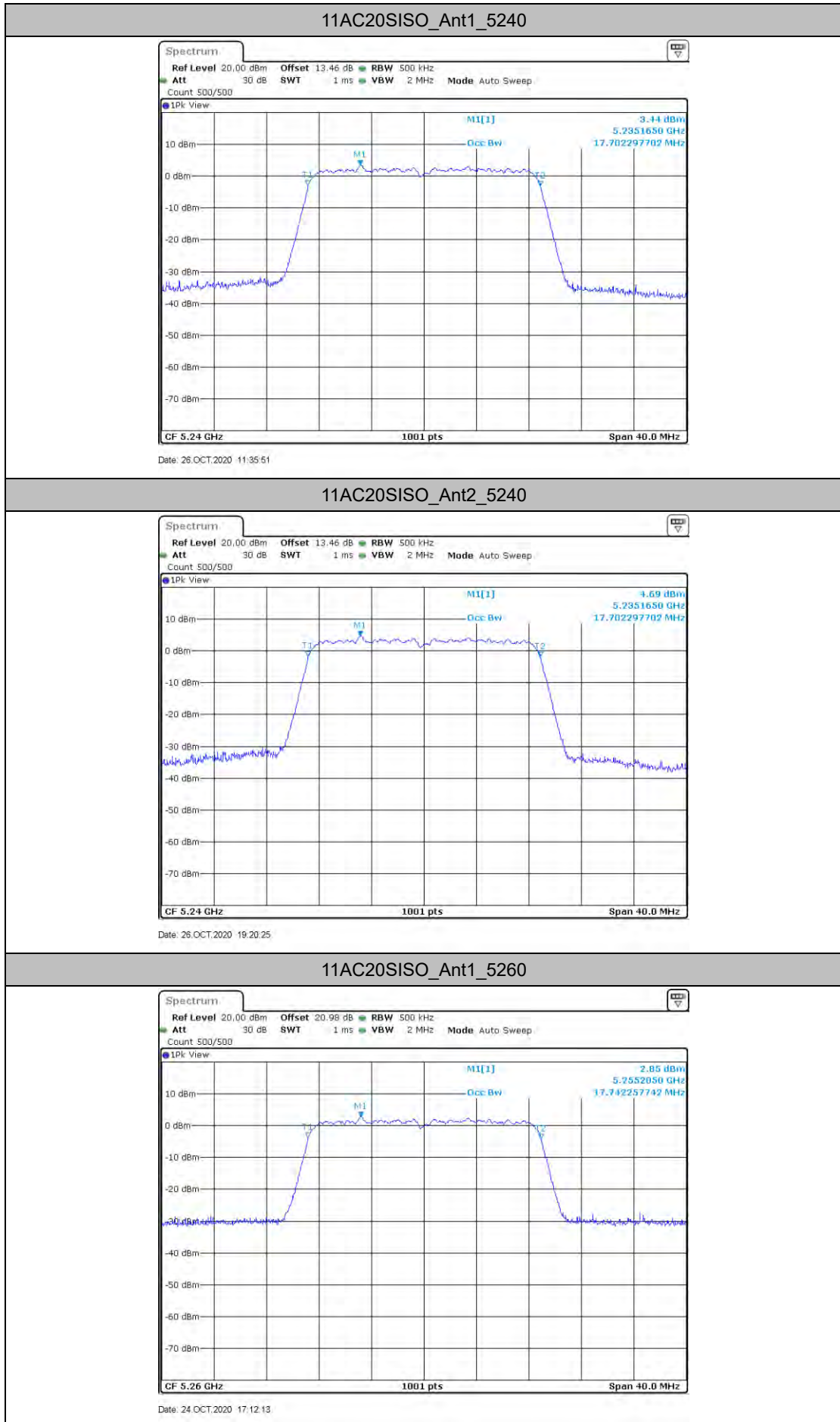
Date: 26.OCT.2020 18:58:32

11AC20SISO_Ant1_5180

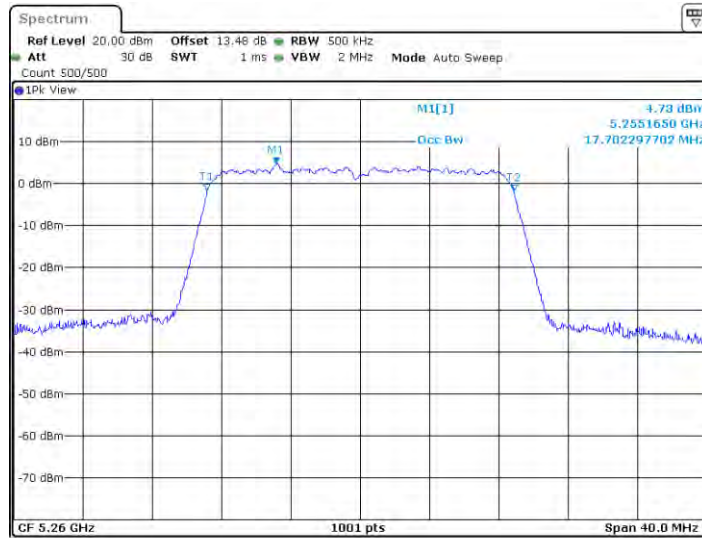


Date: 26.OCT.2020 11:29:25



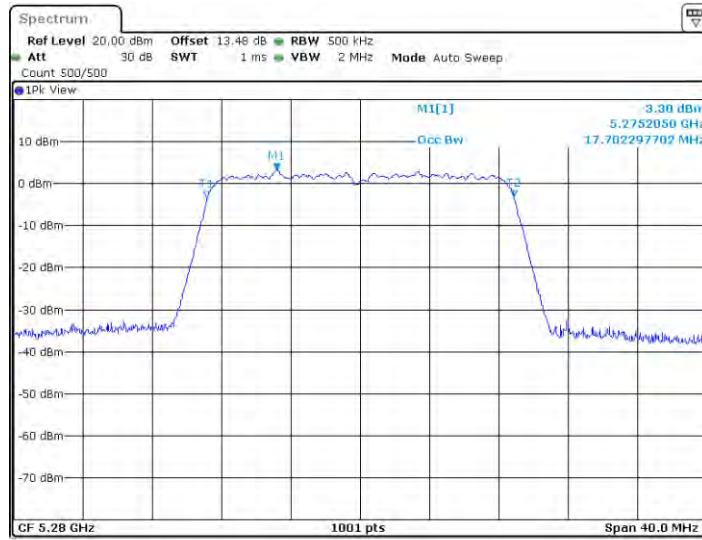


11AC20SISO_Ant2_5260



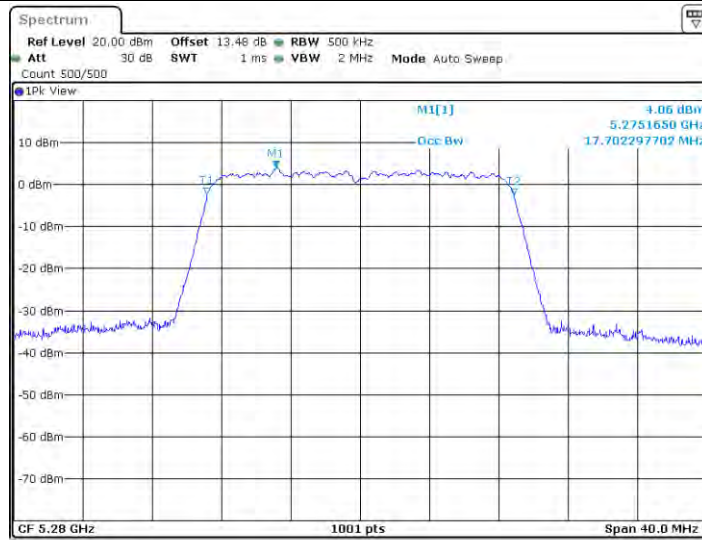
Date: 26.OCT.2020 19:26:21

11AC20SISO_Ant1_5280

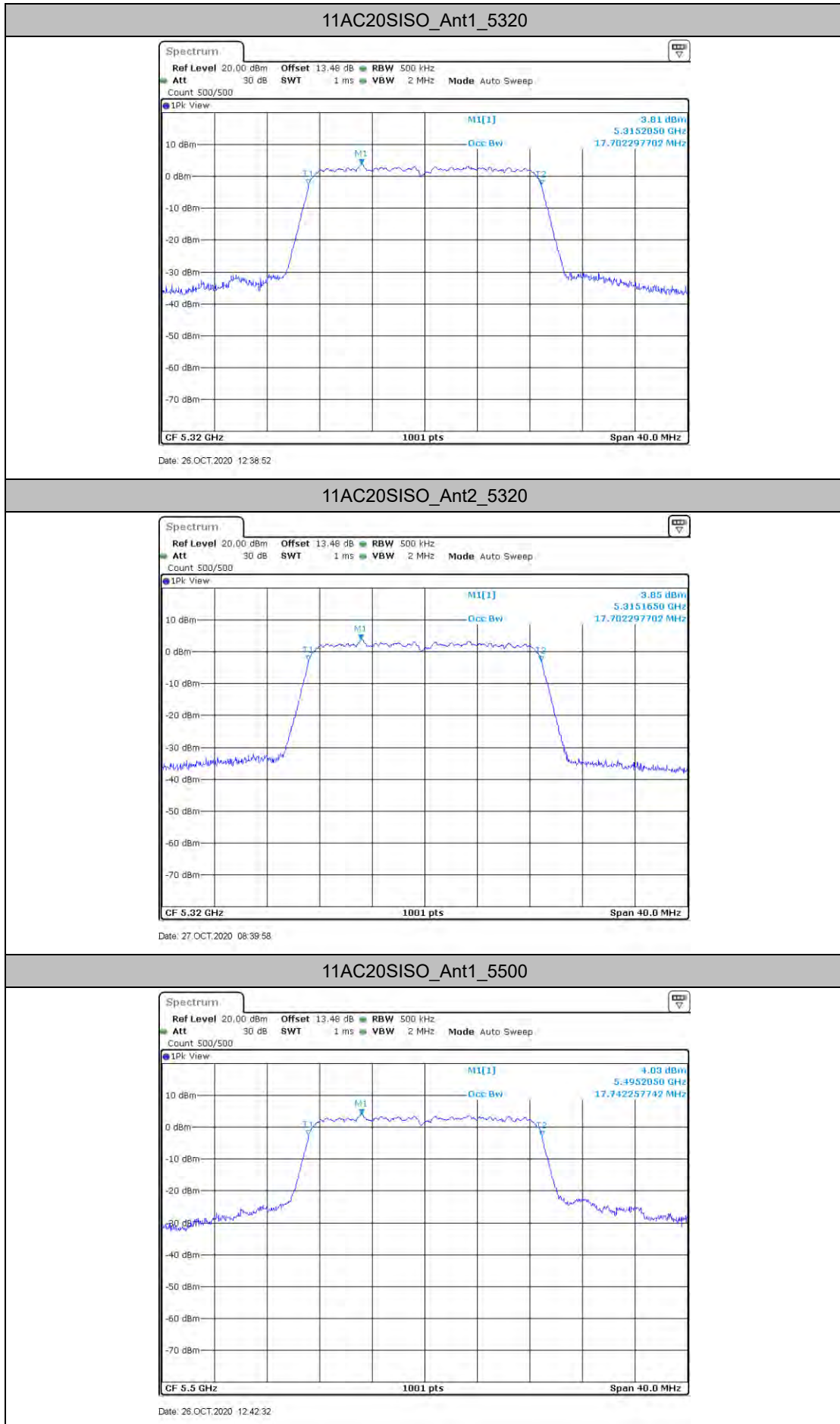


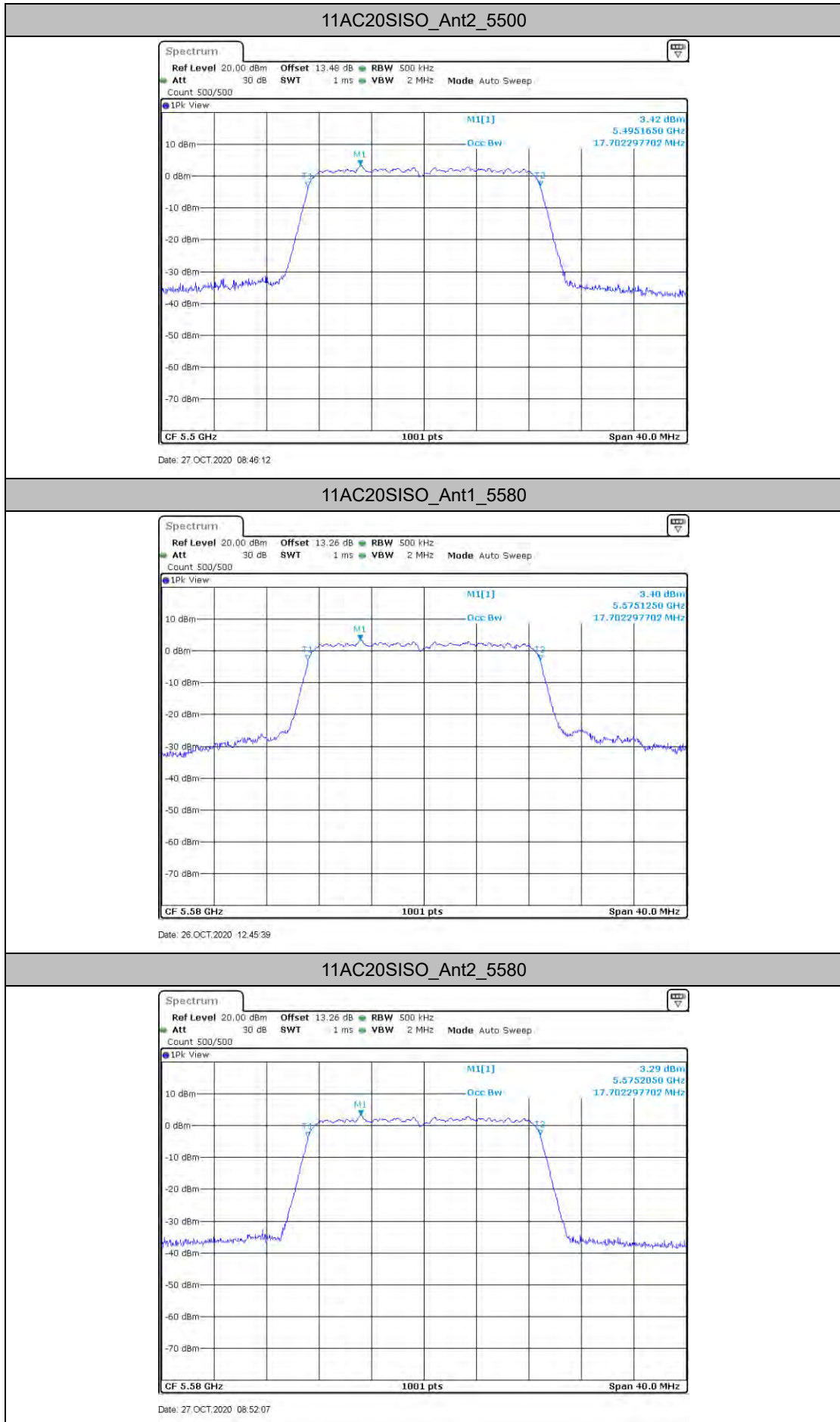
Date: 26.OCT.2020 12:00:55

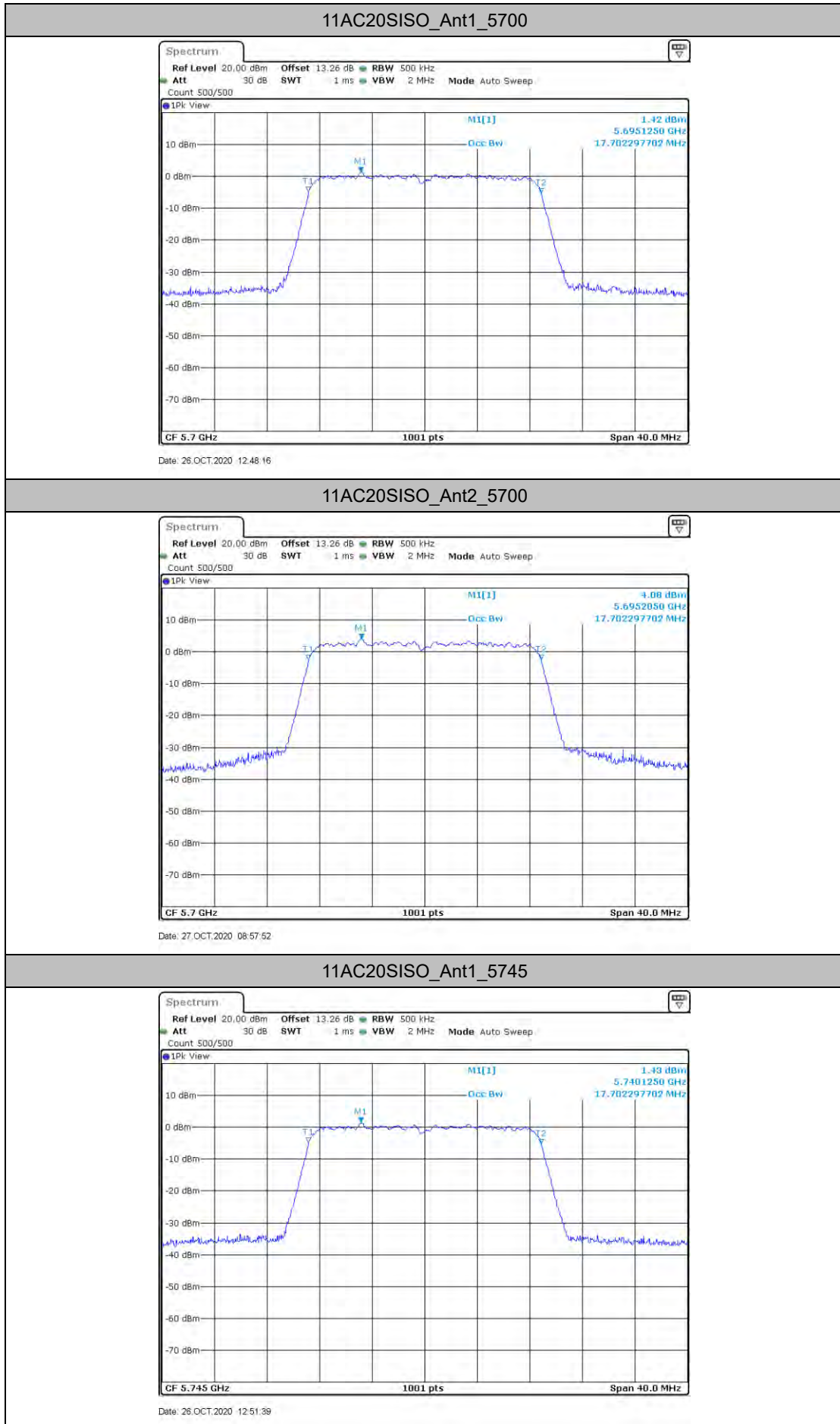
11AC20SISO_Ant2_5280

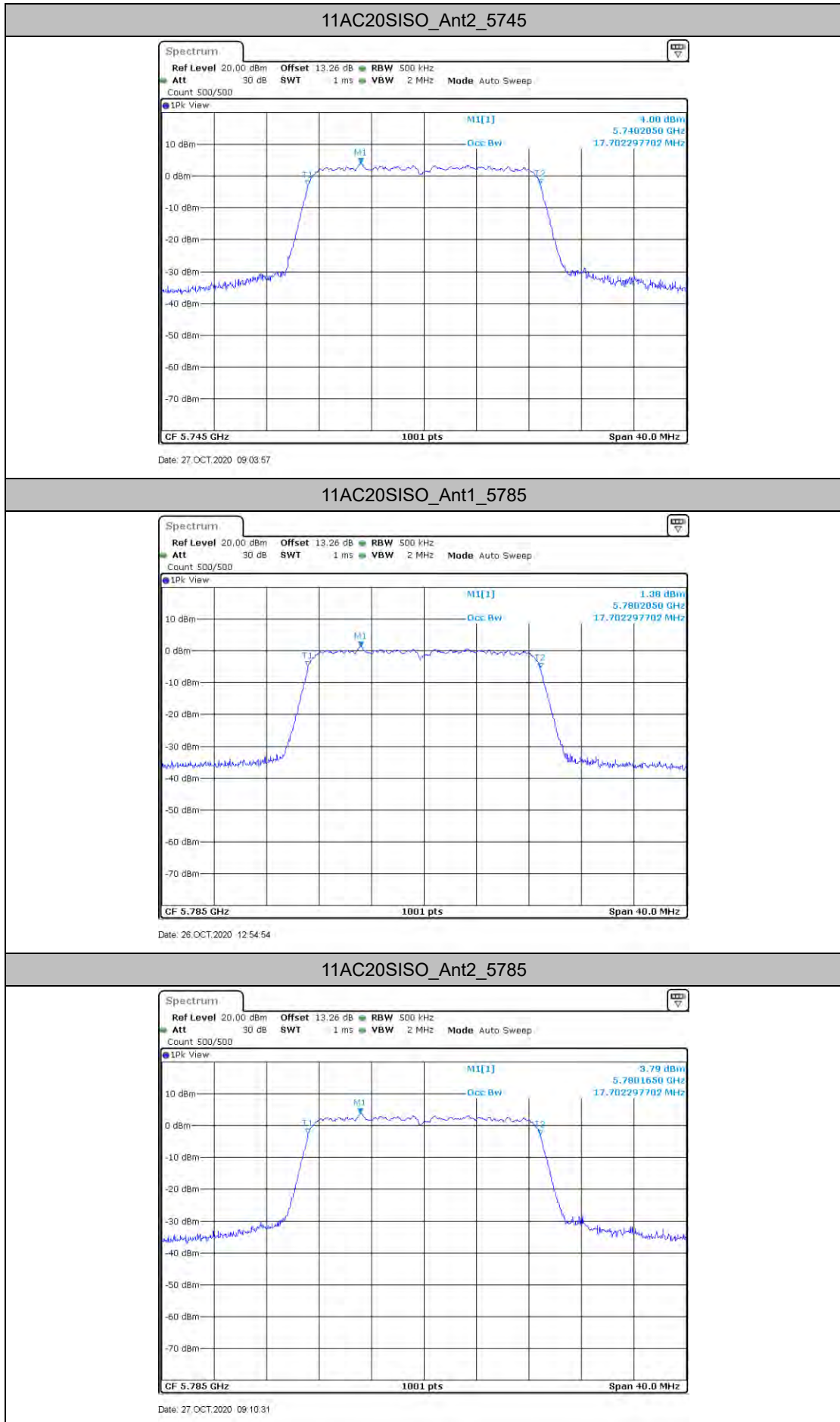


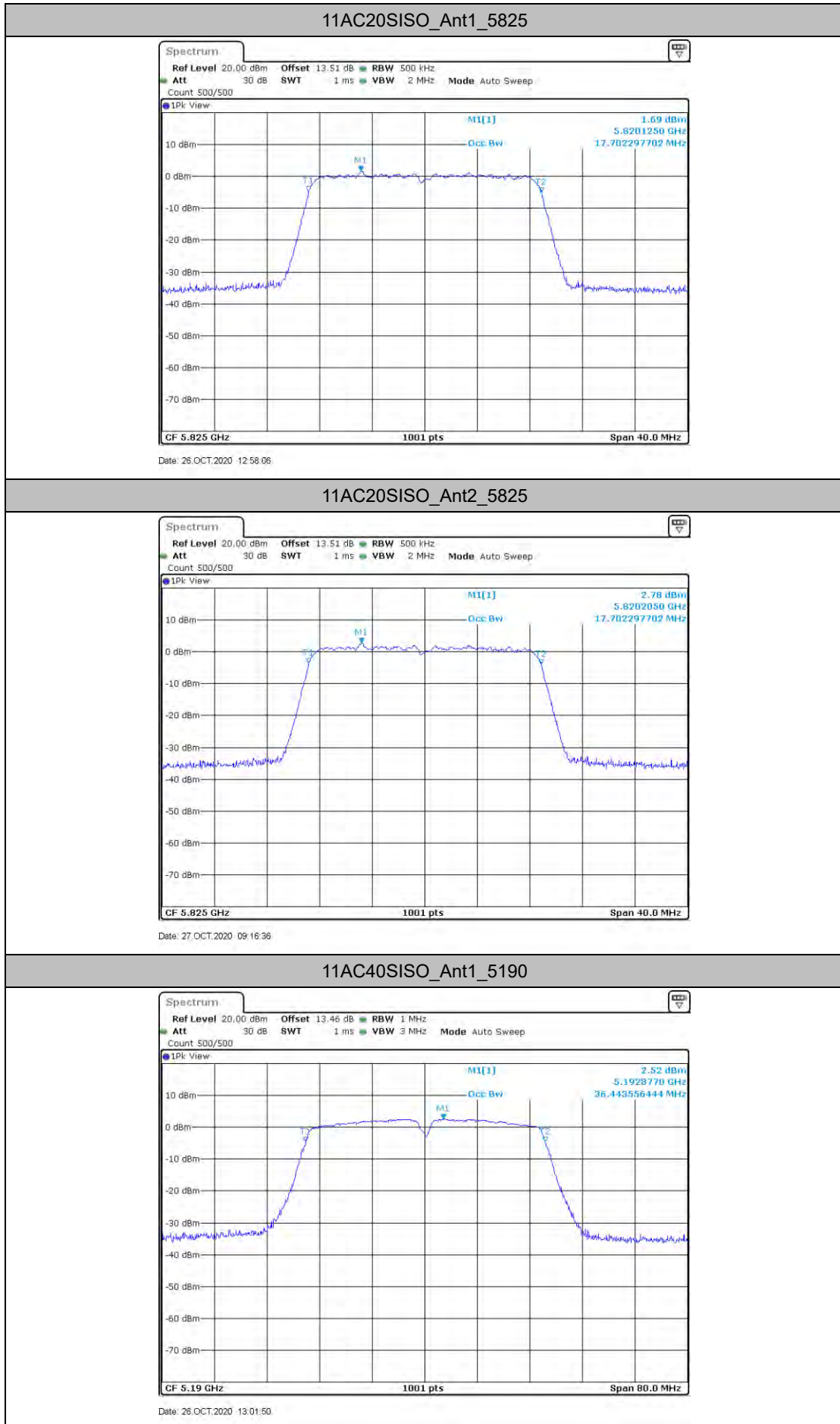
Date: 26.OCT.2020 19:32:26

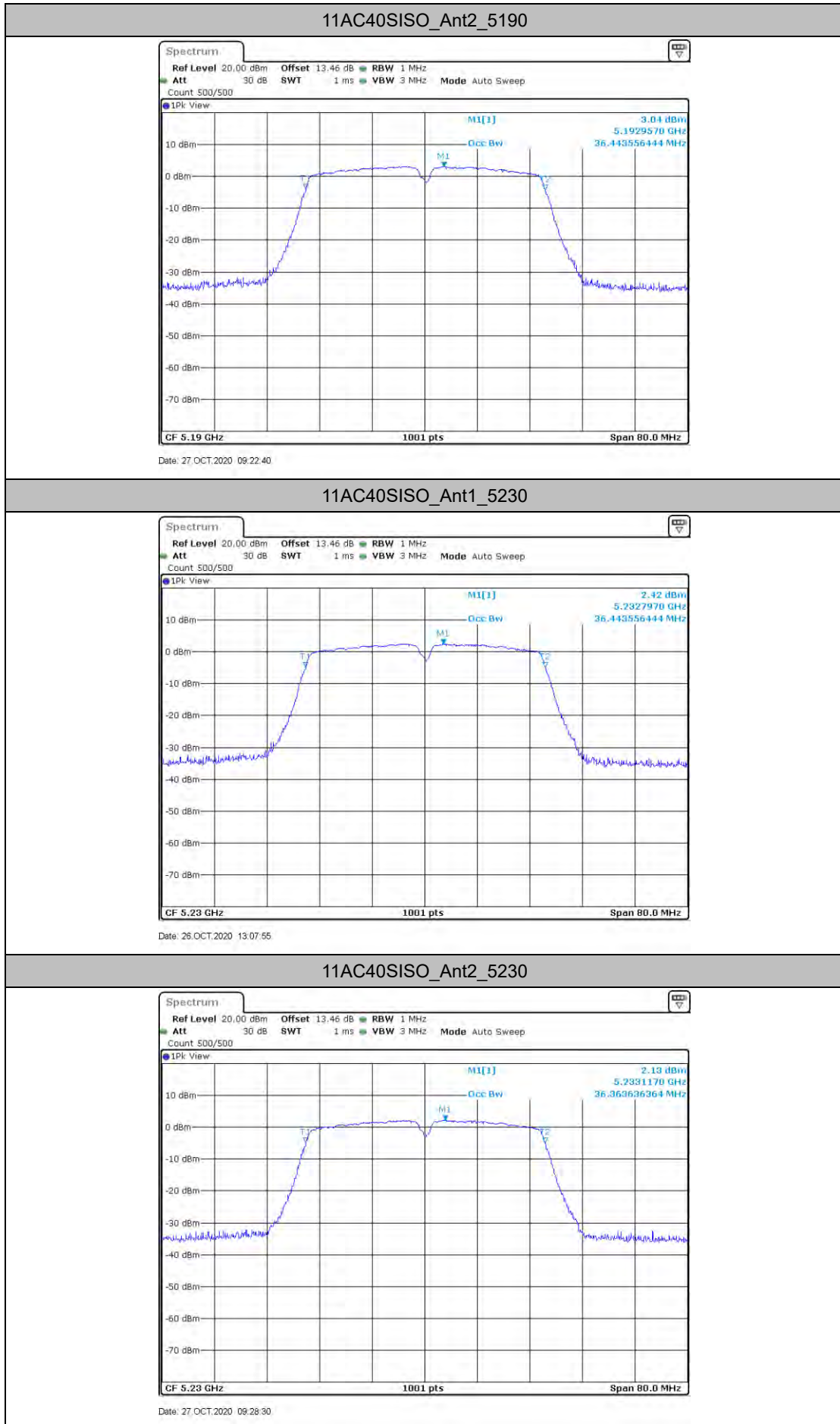


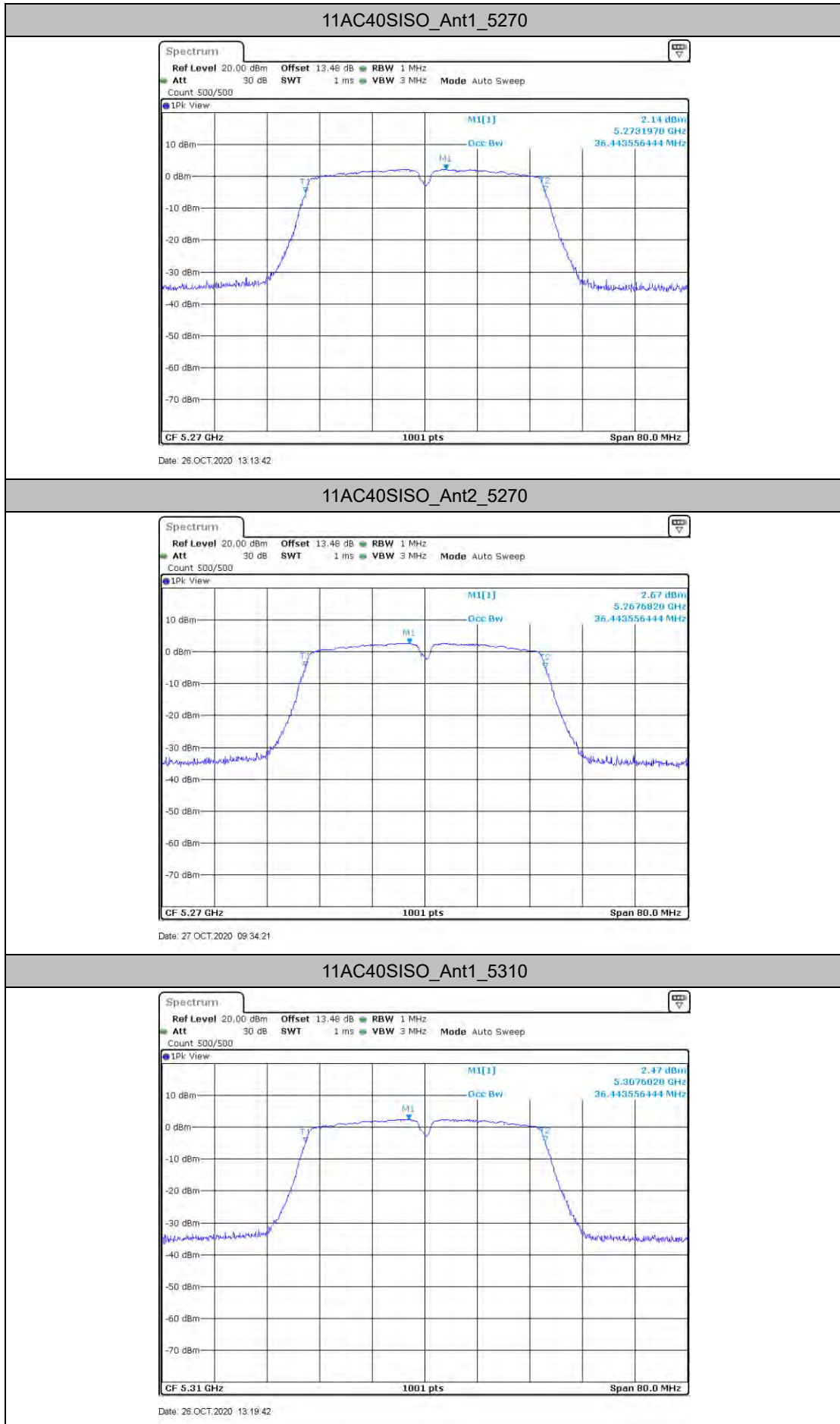


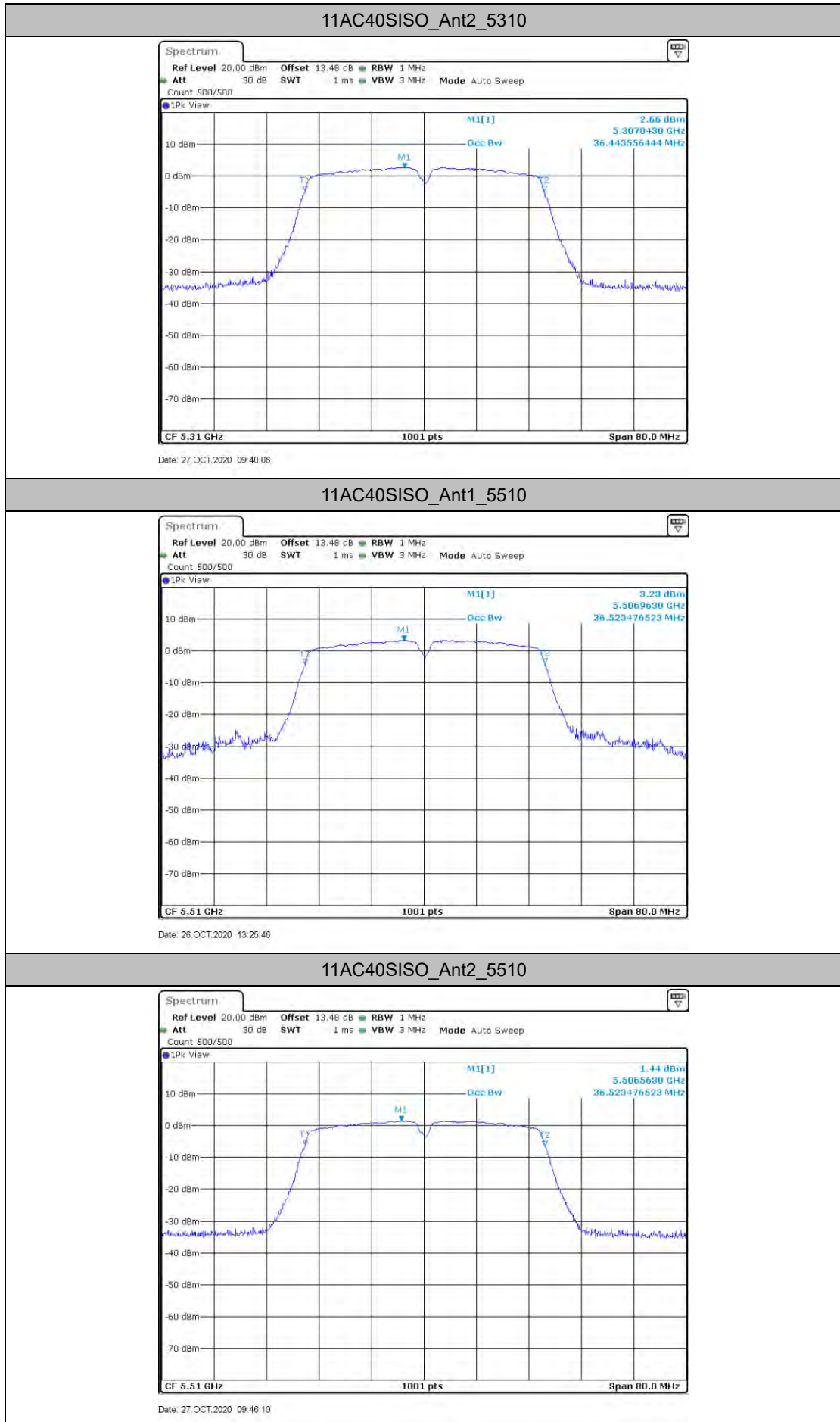


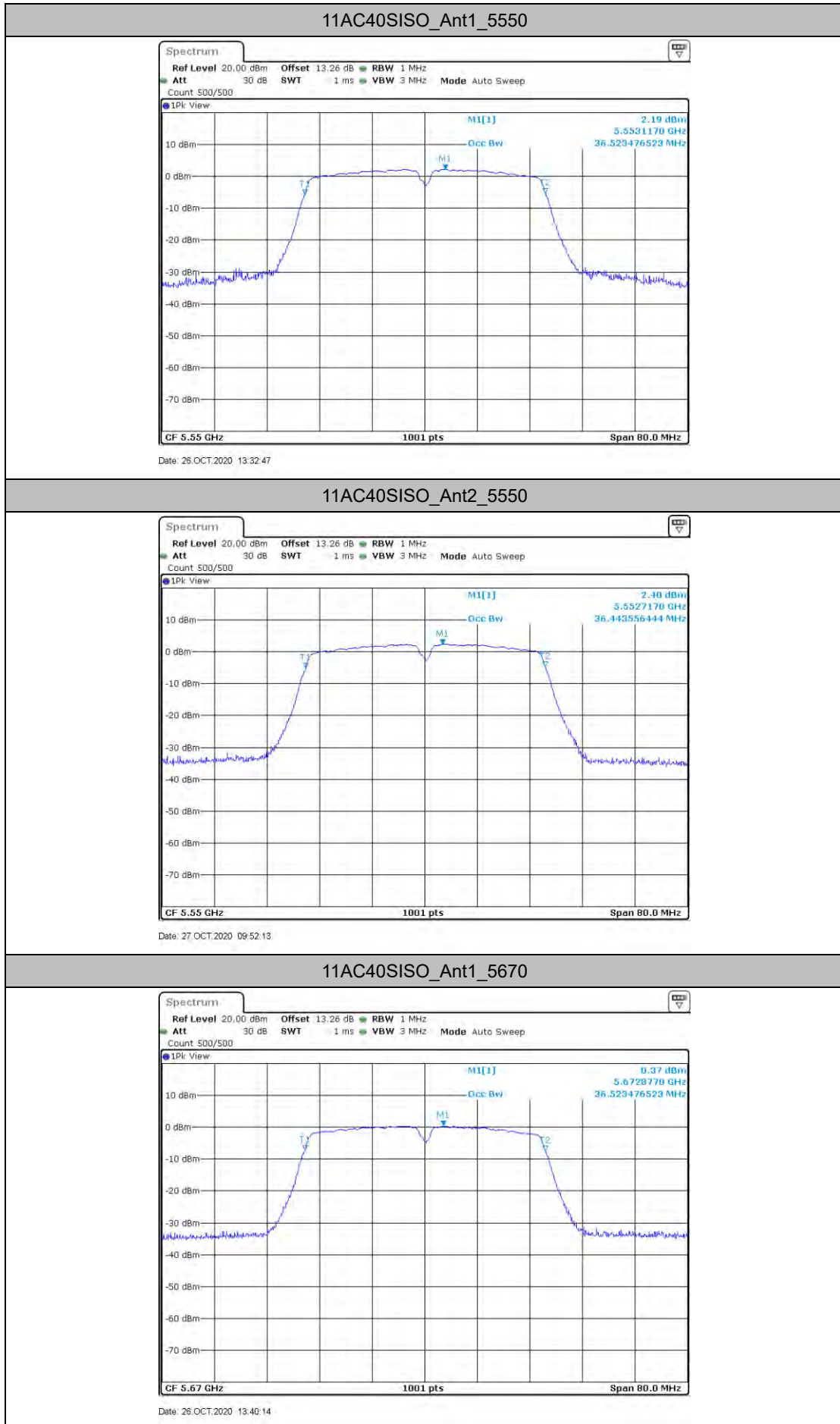


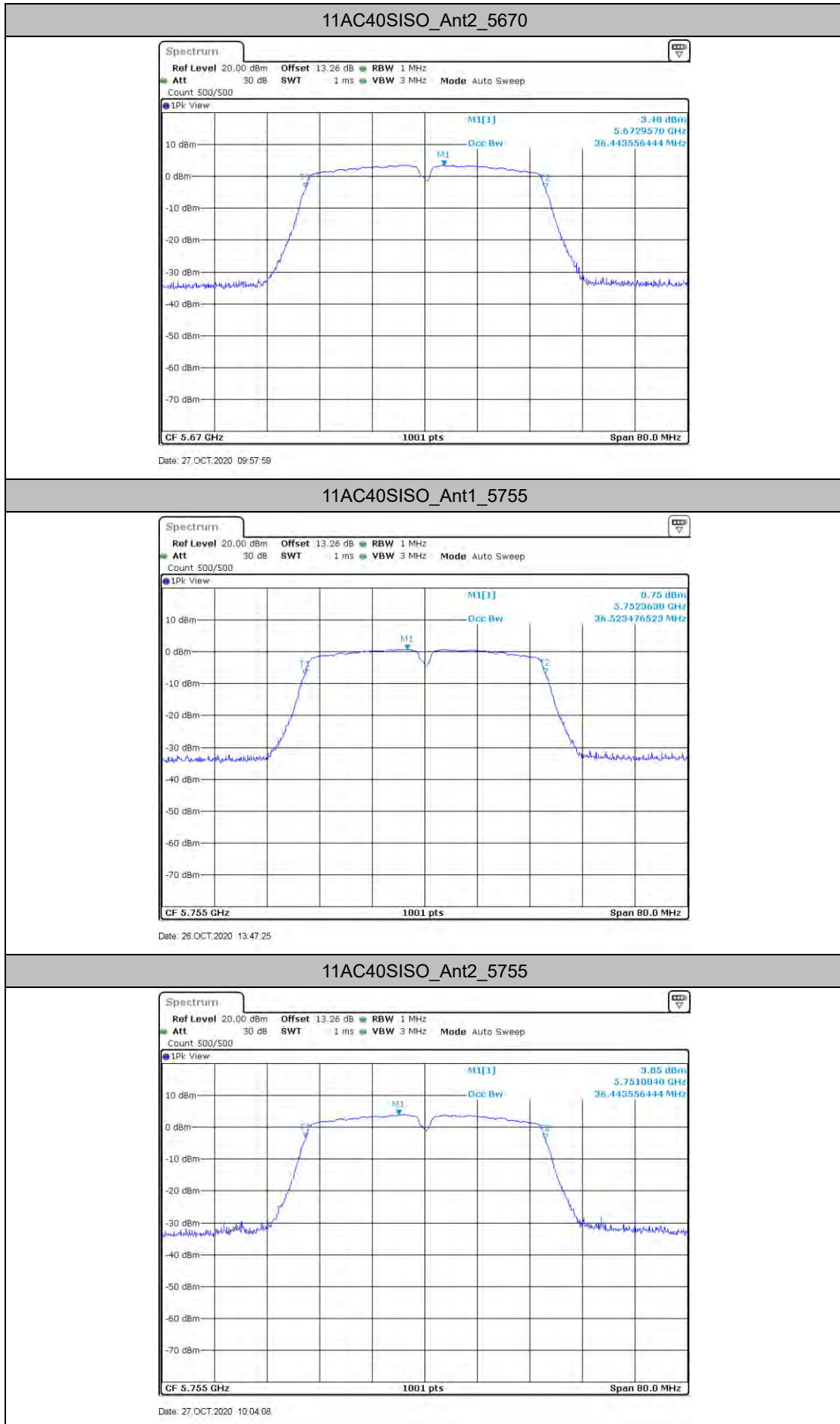










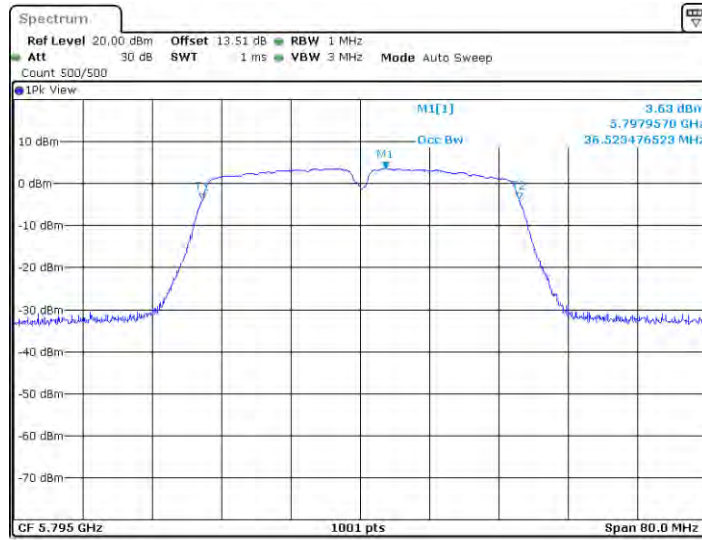


11AC40SISO_Ant1_5795



Date: 26.OCT.2020 13:57:11

11AC40SISO_Ant2_5795

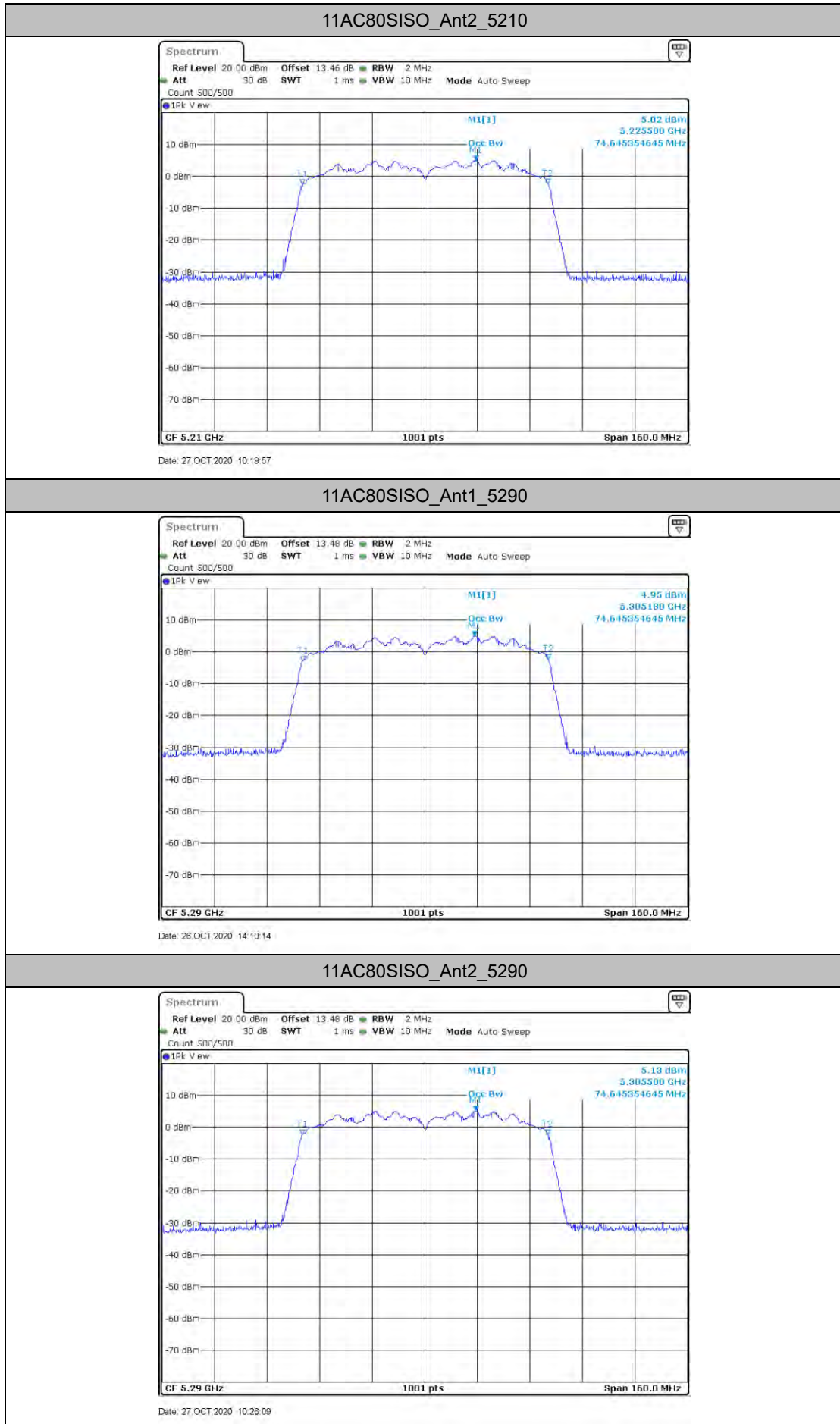


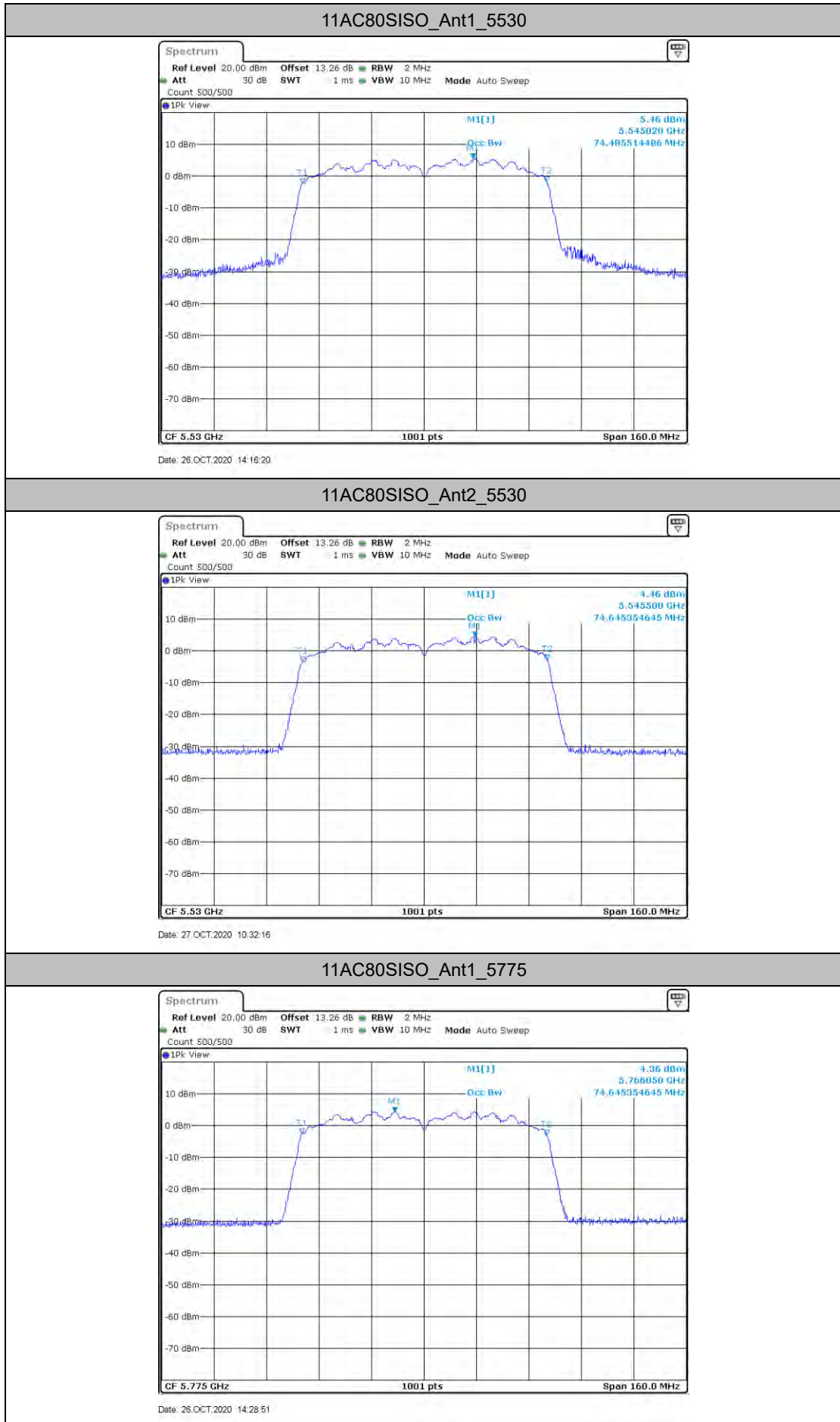
Date: 27.OCT.2020 10:10:36

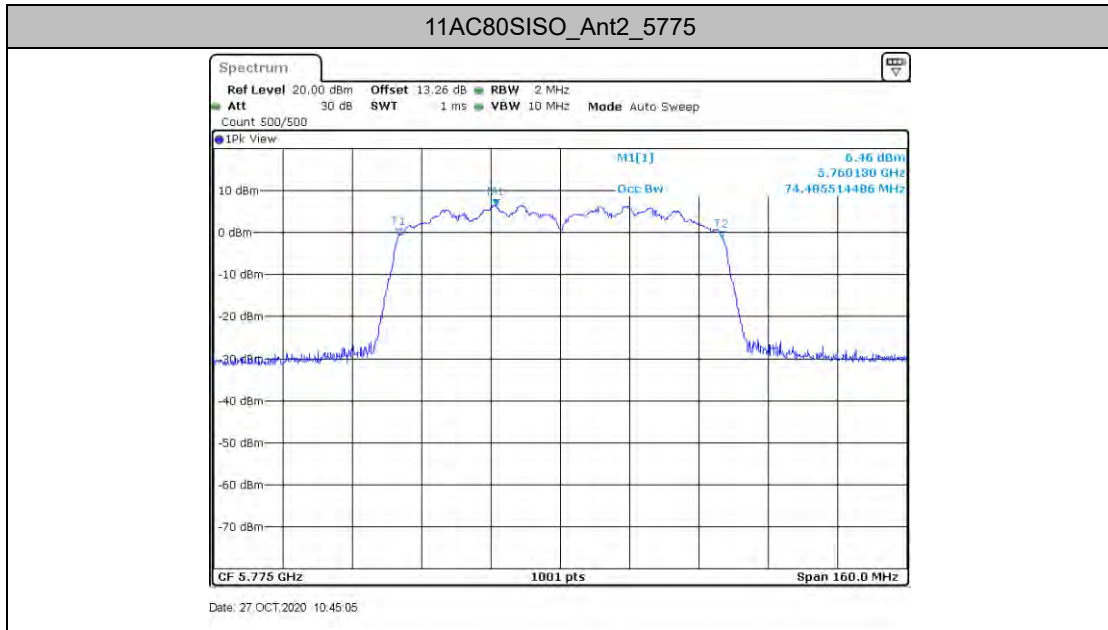
11AC80SISO_Ant1_5210



Date: 26.OCT.2020 14:03:22





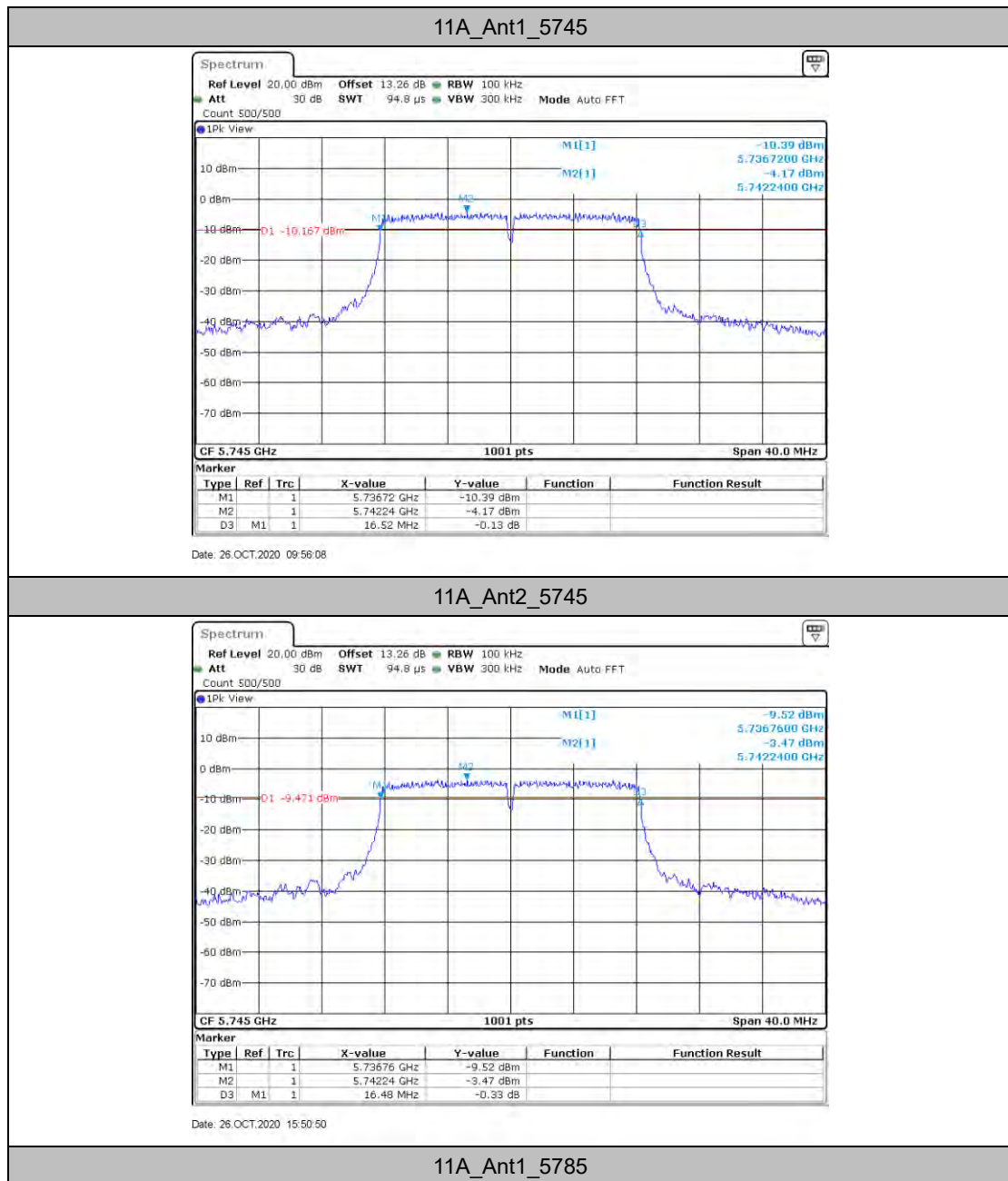


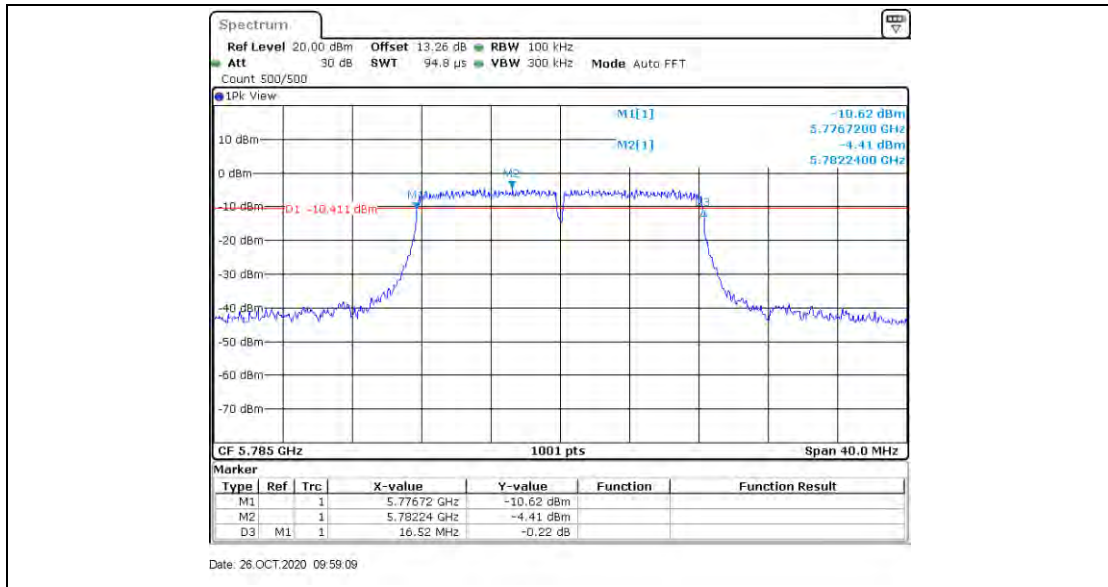
Appendix A3: Min emission bandwidth

Test Result

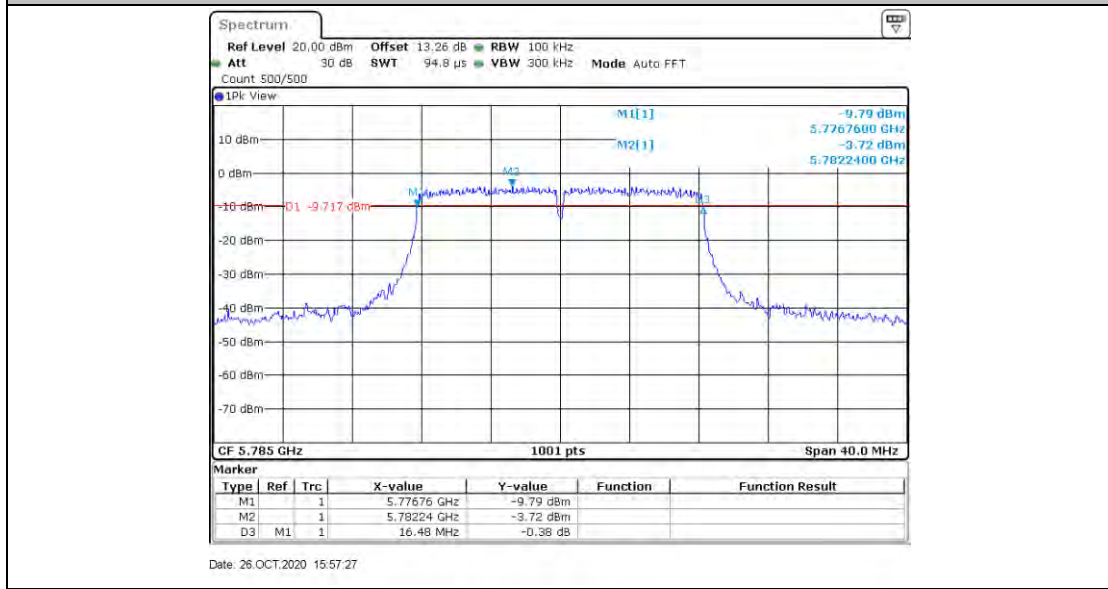
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.520	5736.720	5753.240	0.5	PASS
	Ant2	5745	16.480	5736.760	5753.240	0.5	PASS
	Ant1	5785	16.520	5776.720	5793.240	0.5	PASS
	Ant2	5785	16.480	5776.760	5793.240	0.5	PASS
	Ant1	5825	16.520	5816.720	5833.240	0.5	PASS
	Ant2	5825	16.520	5816.720	5833.240	0.5	PASS
11N20SISO	Ant1	5745	17.720	5736.120	5753.840	0.5	PASS
	Ant2	5745	17.680	5736.160	5753.840	0.5	PASS
	Ant1	5785	17.720	5776.120	5793.840	0.5	PASS
	Ant2	5785	17.680	5776.160	5793.840	0.5	PASS
	Ant1	5825	17.720	5816.120	5833.840	0.5	PASS
	Ant2	5825	17.680	5816.160	5833.840	0.5	PASS
11N40SISO	Ant1	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant2	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant1	5795	36.480	5776.760	5813.240	0.5	PASS
	Ant2	5795	36.480	5776.760	5813.240	0.5	PASS
11AC20SISO	Ant1	5745	17.720	5736.120	5753.840	0.5	PASS
	Ant2	5745	17.680	5736.160	5753.840	0.5	PASS
	Ant1	5785	17.680	5776.160	5793.840	0.5	PASS
	Ant2	5785	17.680	5776.160	5793.840	0.5	PASS
	Ant1	5825	17.680	5816.160	5833.840	0.5	PASS
	Ant2	5825	17.680	5816.160	5833.840	0.5	PASS
11AC40SISO	Ant1	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant2	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant1	5795	36.480	5776.760	5813.240	0.5	PASS
	Ant2	5795	36.480	5776.760	5813.240	0.5	PASS
11AC80SISO	Ant1	5775	75.840	5736.600	5812.440	0.5	PASS
	Ant2	5775	75.360	5737.080	5812.440	0.5	PASS

Test Graphs

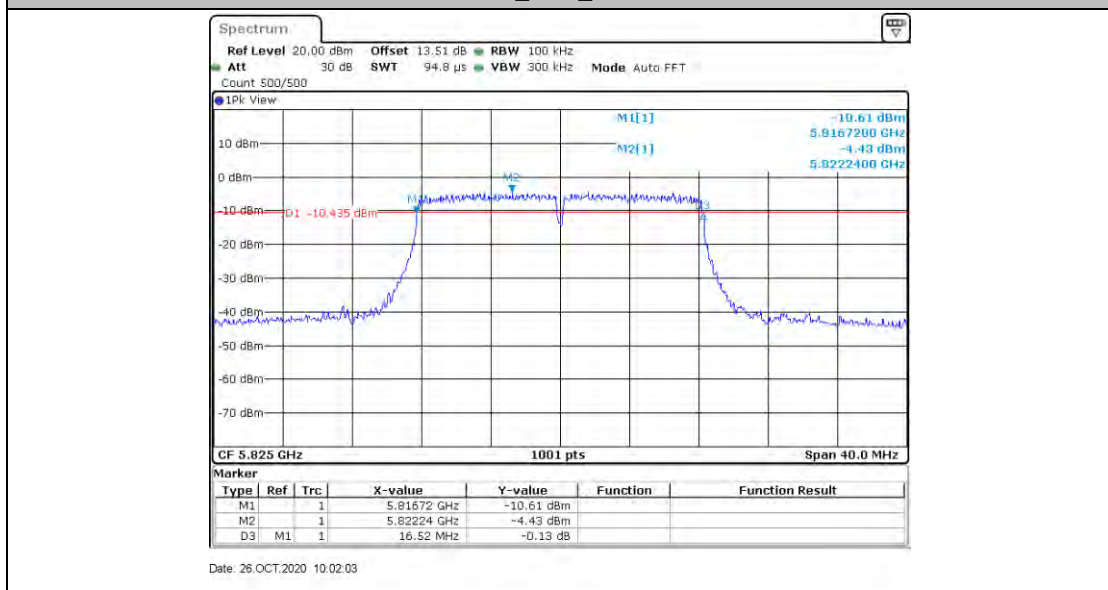




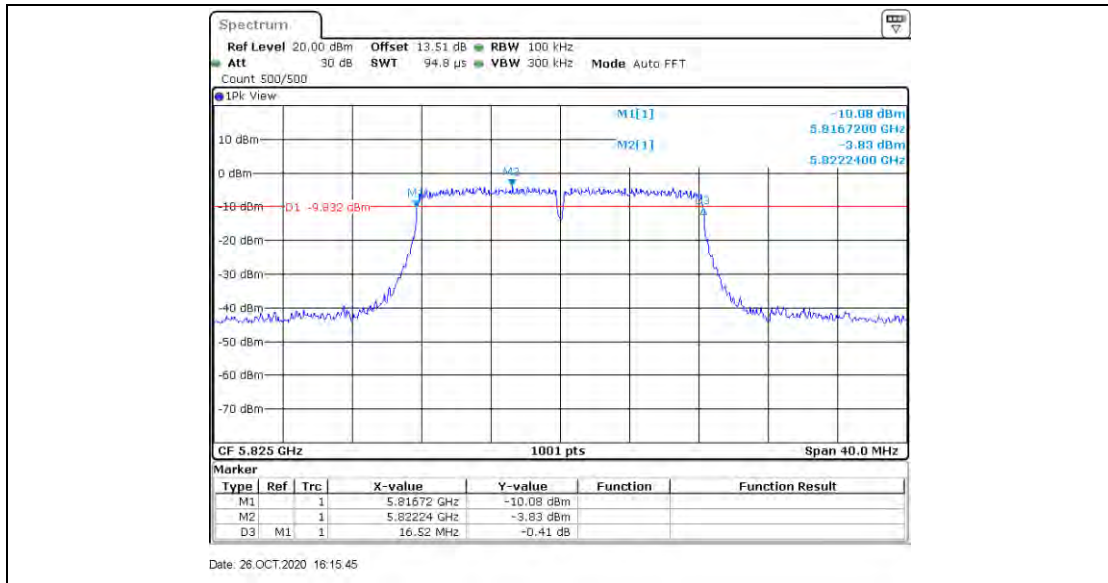
11A_Ant2_5785



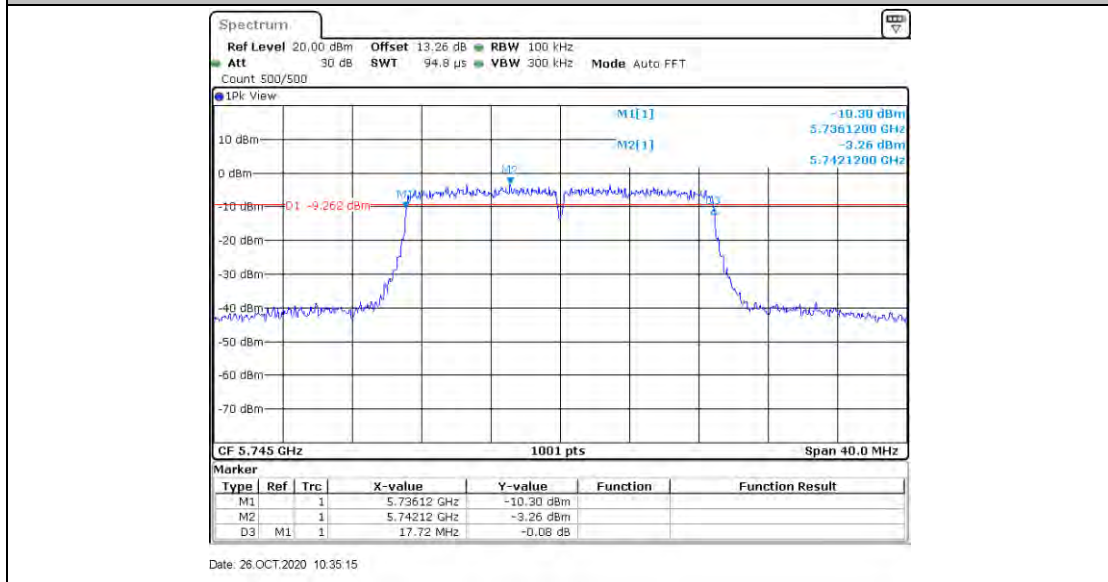
11A_Ant1_5825



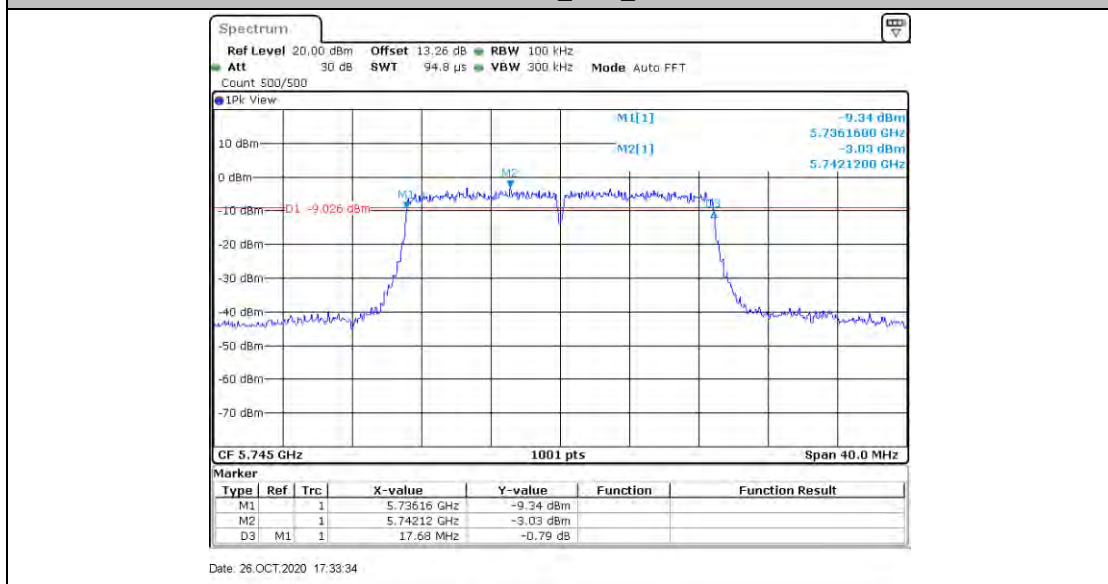
11A_Ant2_5825



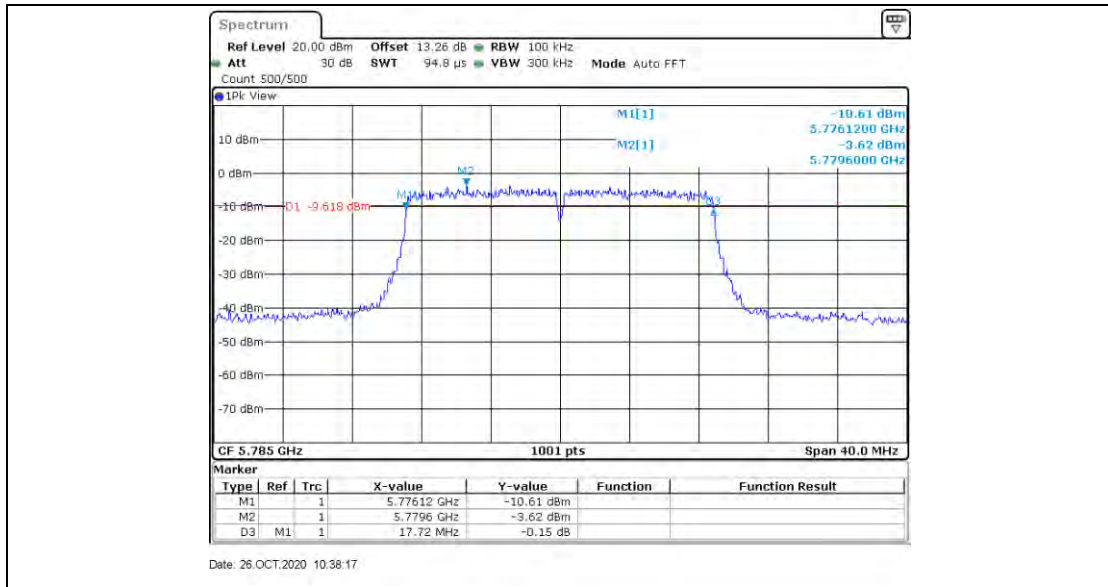
11N20SISO_Ant1_5745



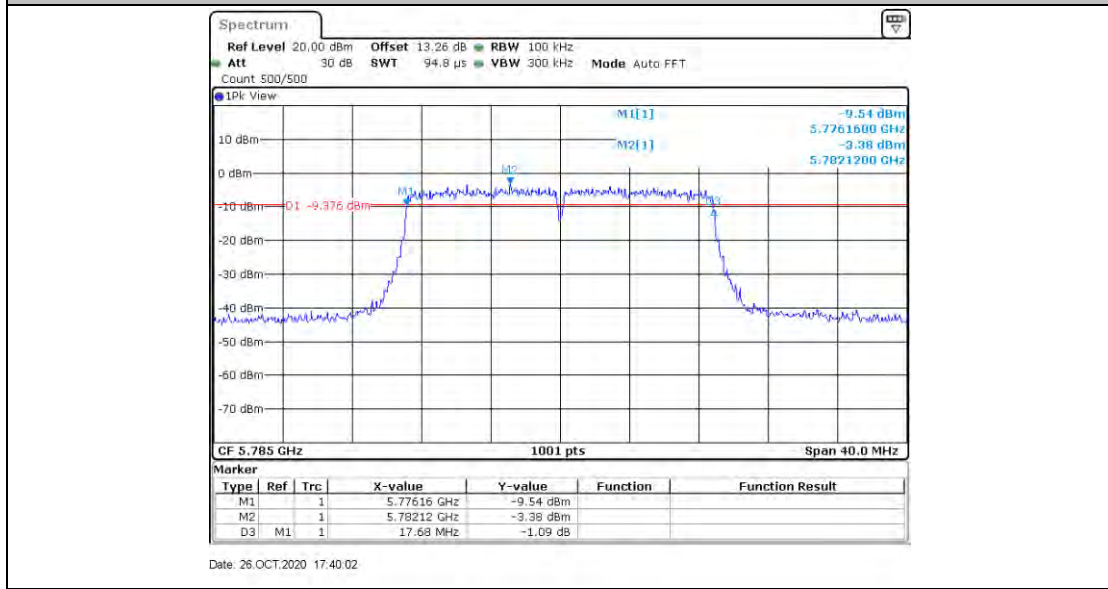
11N20SISO_Ant2_5745



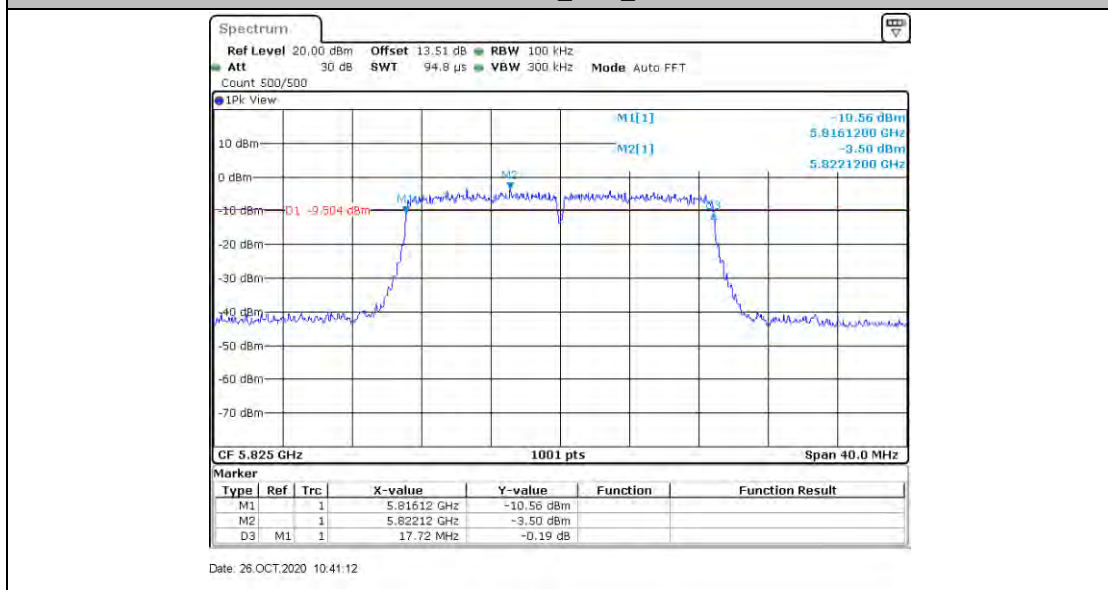
11N20SISO_Ant1_5785



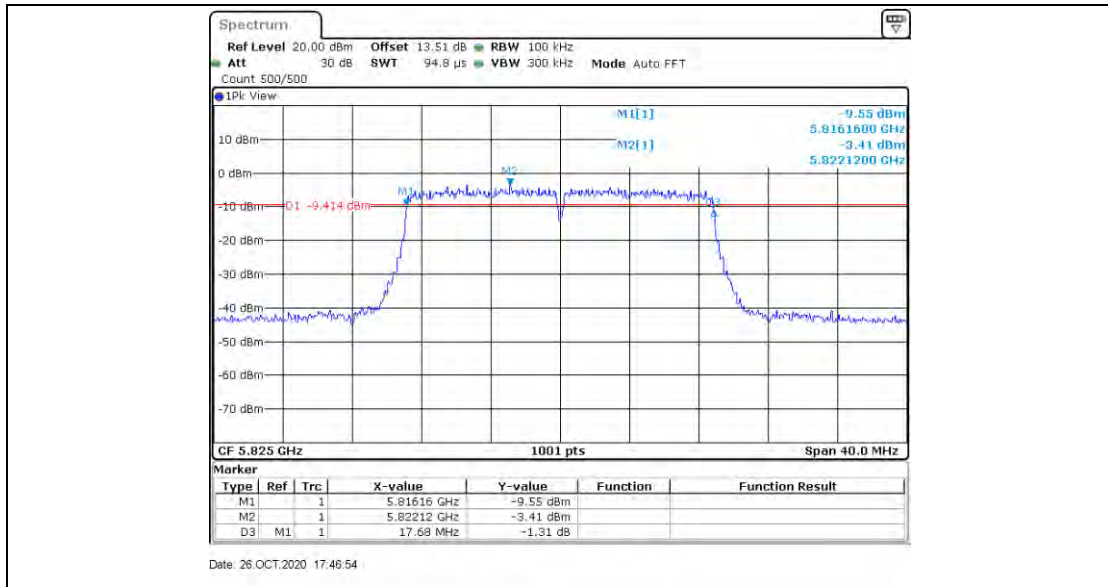
11N20SISO_Ant2_5785



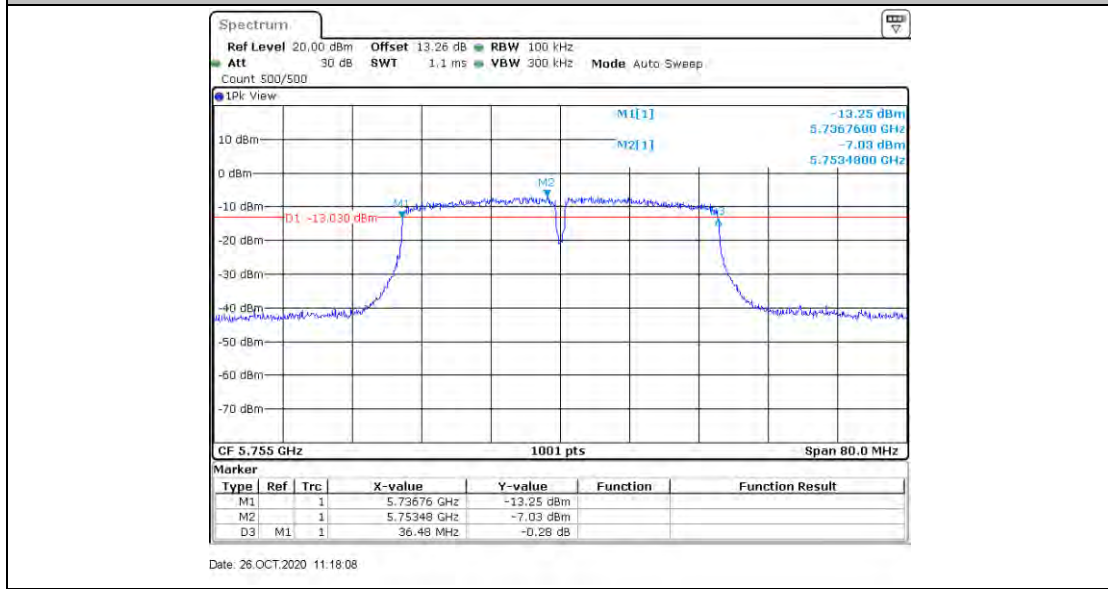
11N20SISO_Ant1_5825



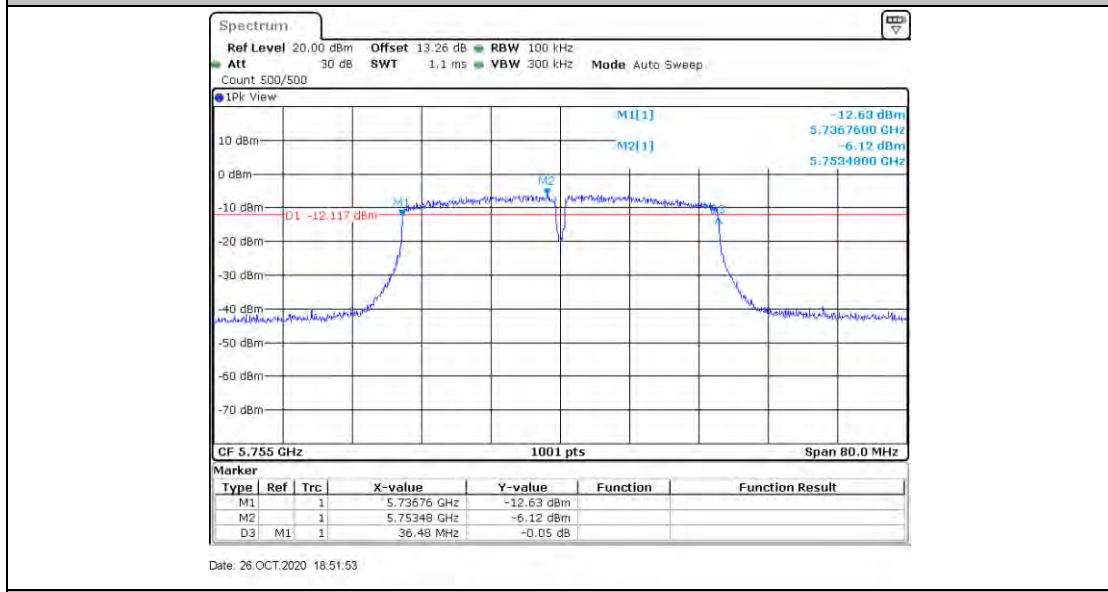
11N20SISO_Ant2_5825



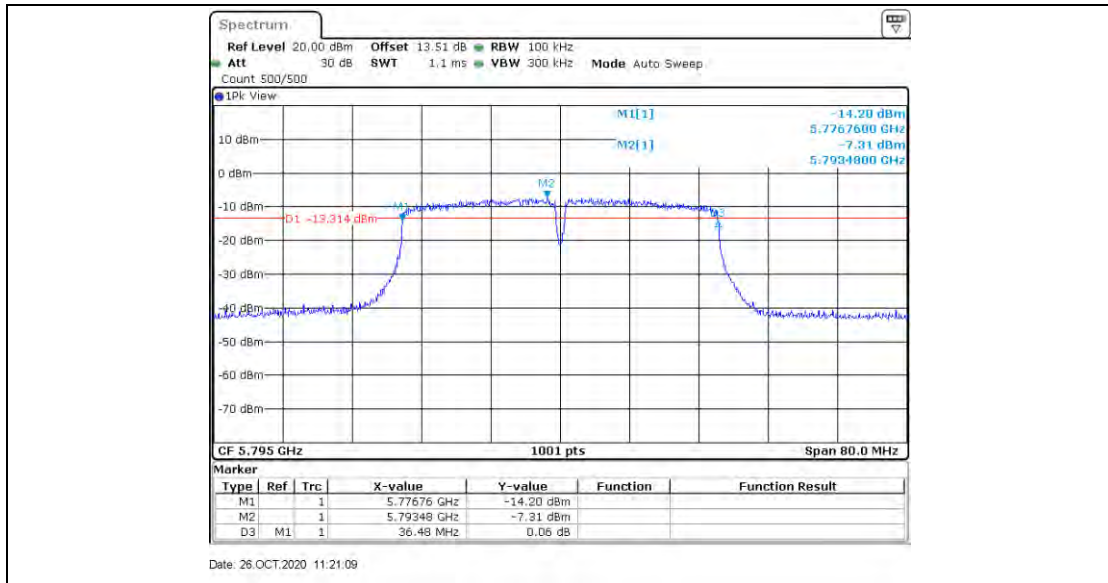
11N40SISO_Ant1_5755



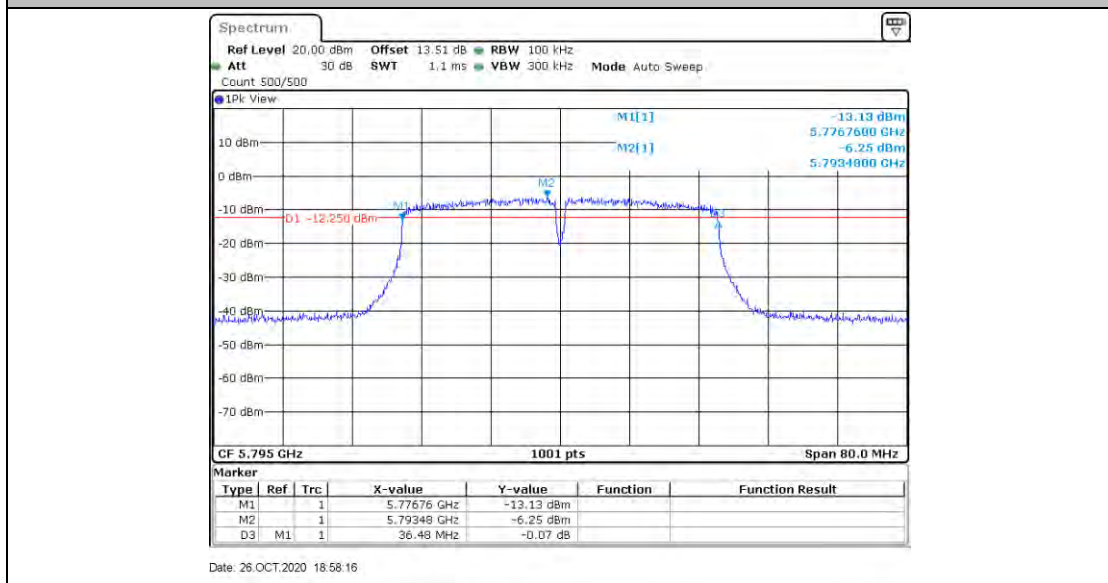
11N40SISO_Ant2_5755



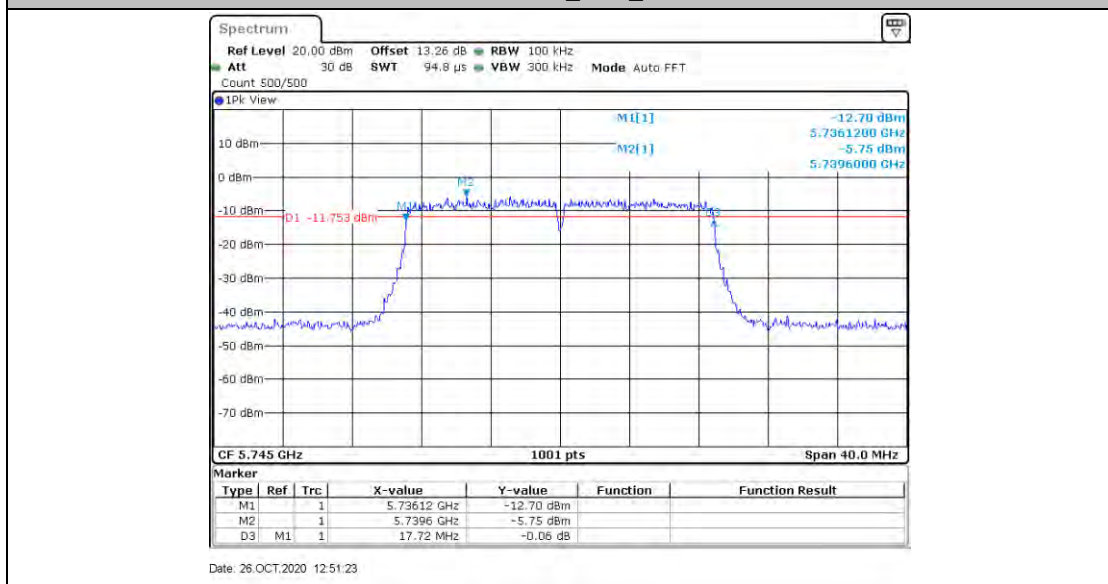
11N40SISO_Ant1_5795



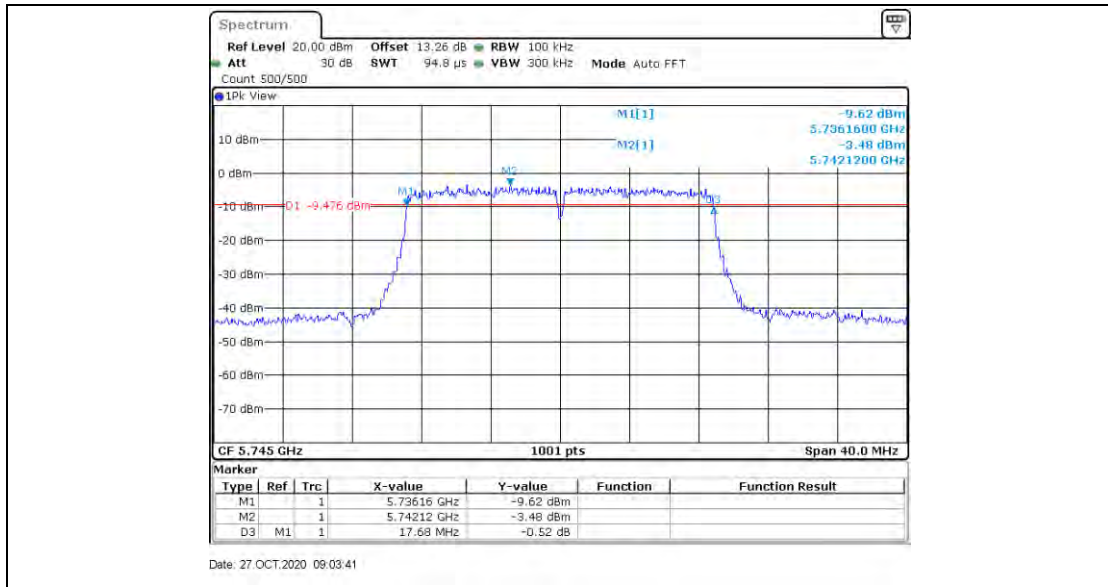
11N40SISO_Ant2_5795



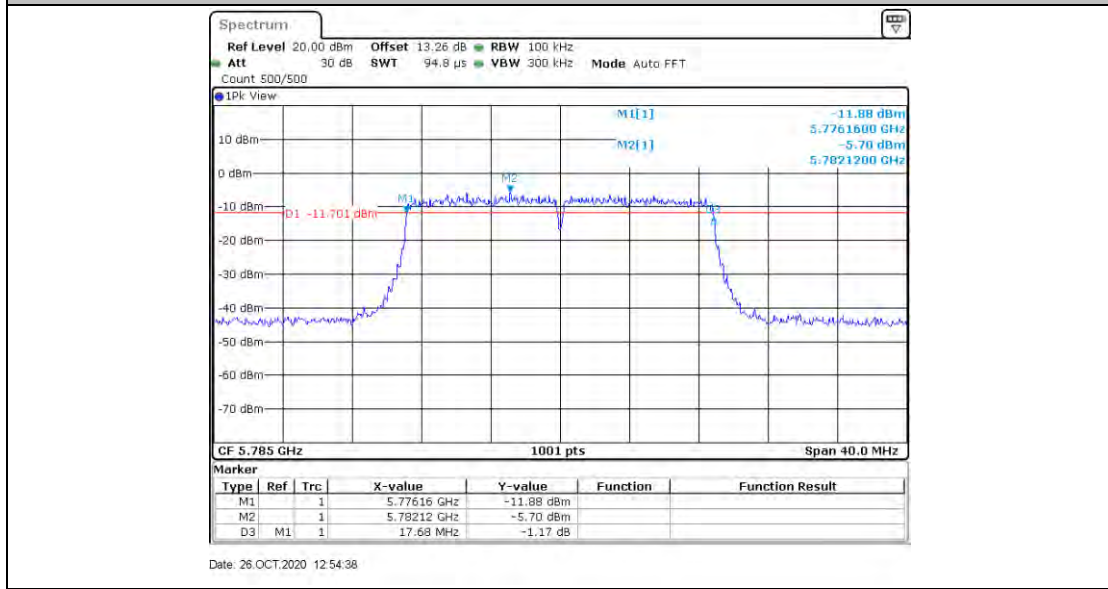
11AC20SISO_Ant1_5745



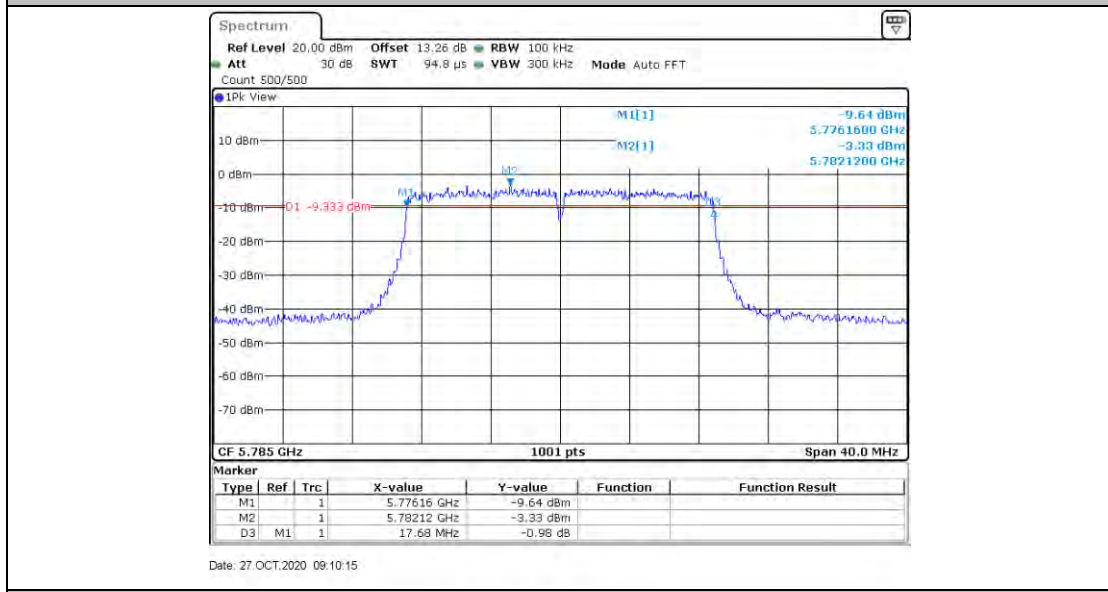
11AC20SISO_Ant2_5745



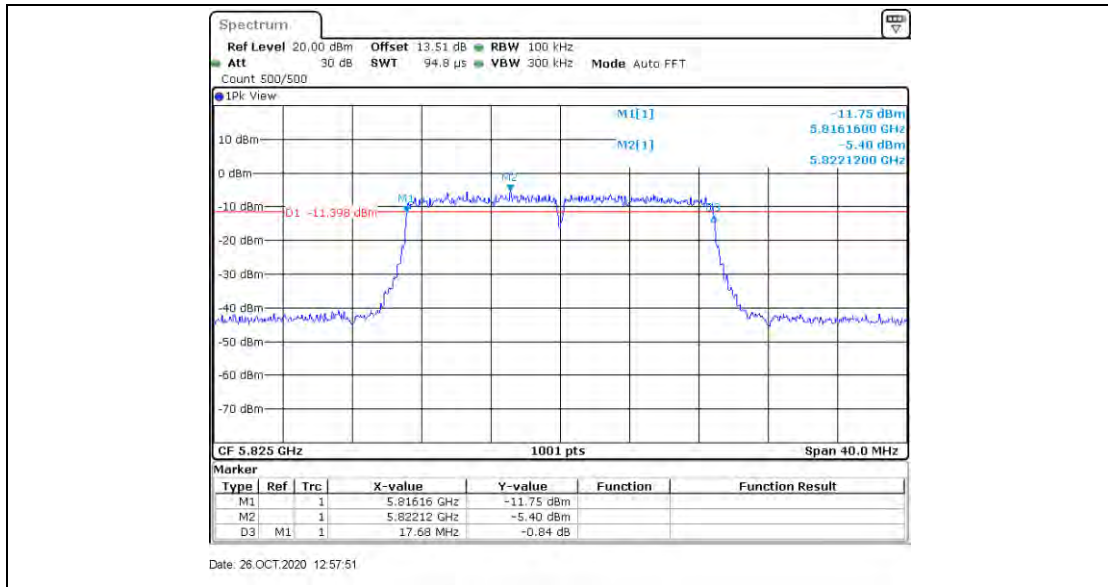
11AC20SISO_Ant1_5785



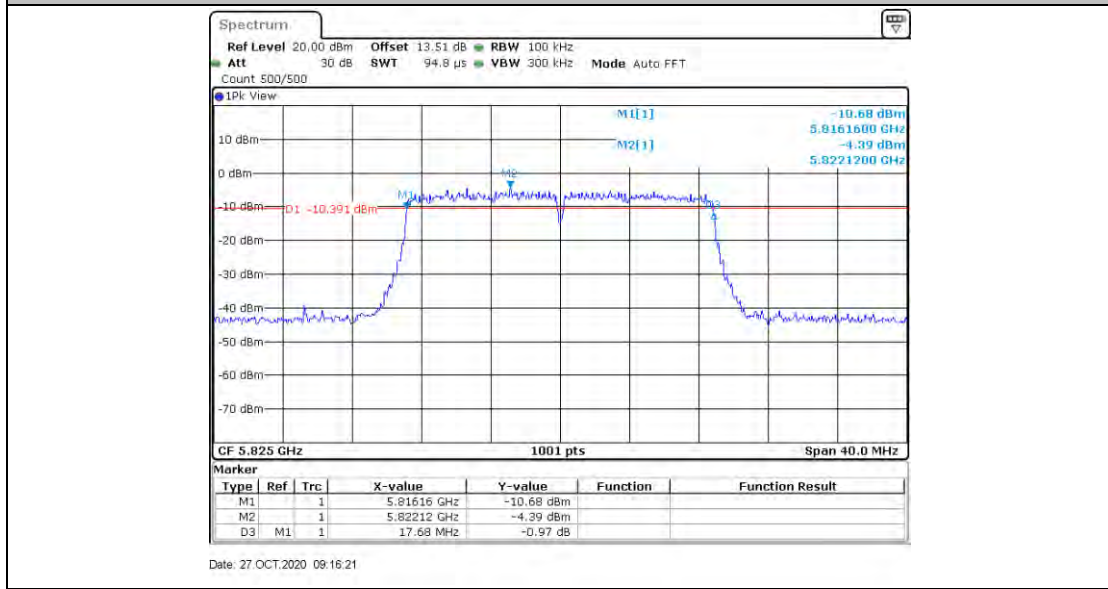
11AC20SISO_Ant2_5785



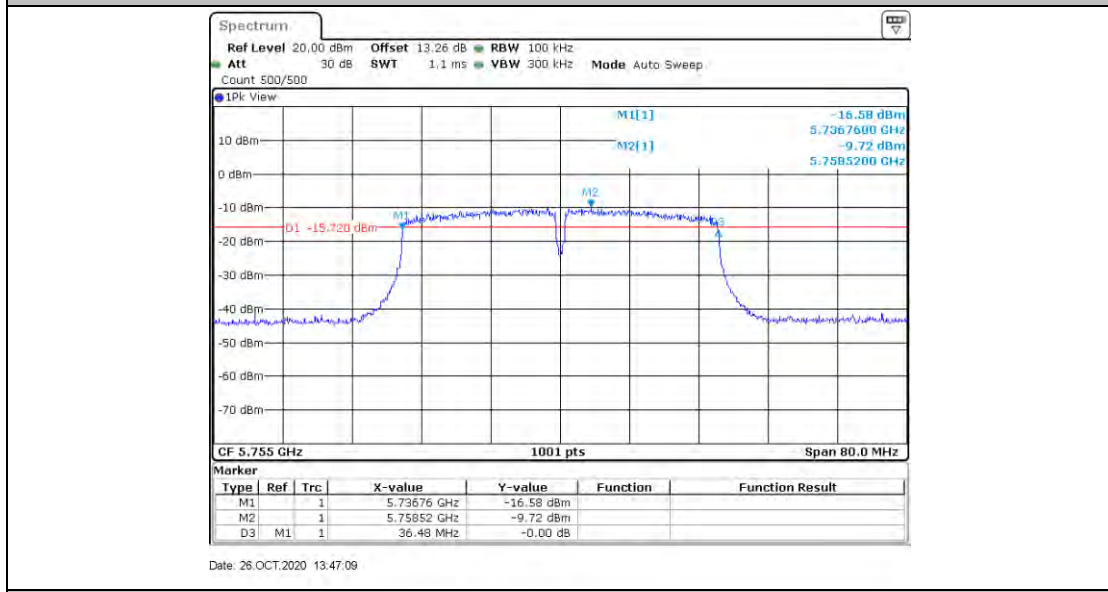
11AC20SISO_Ant1_5825



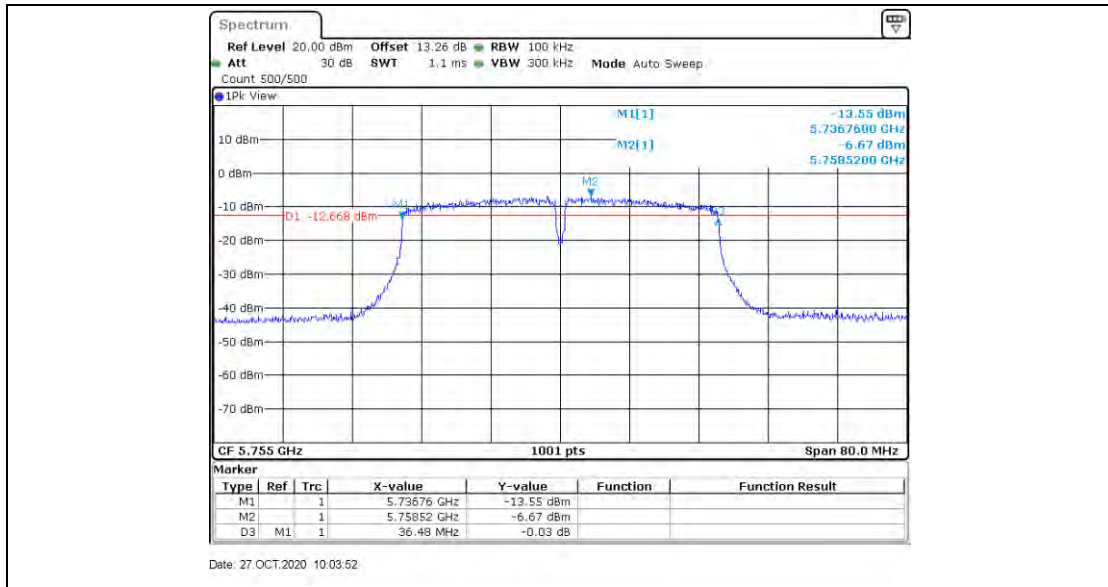
11AC20SISO_Ant2_5825



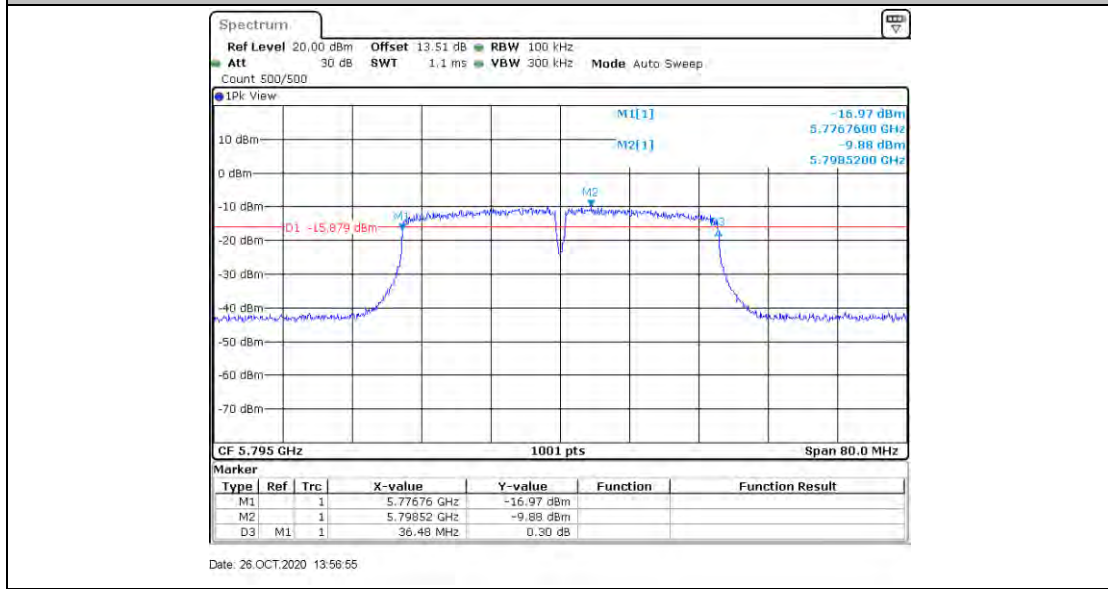
11AC40SISO_Ant1_5755



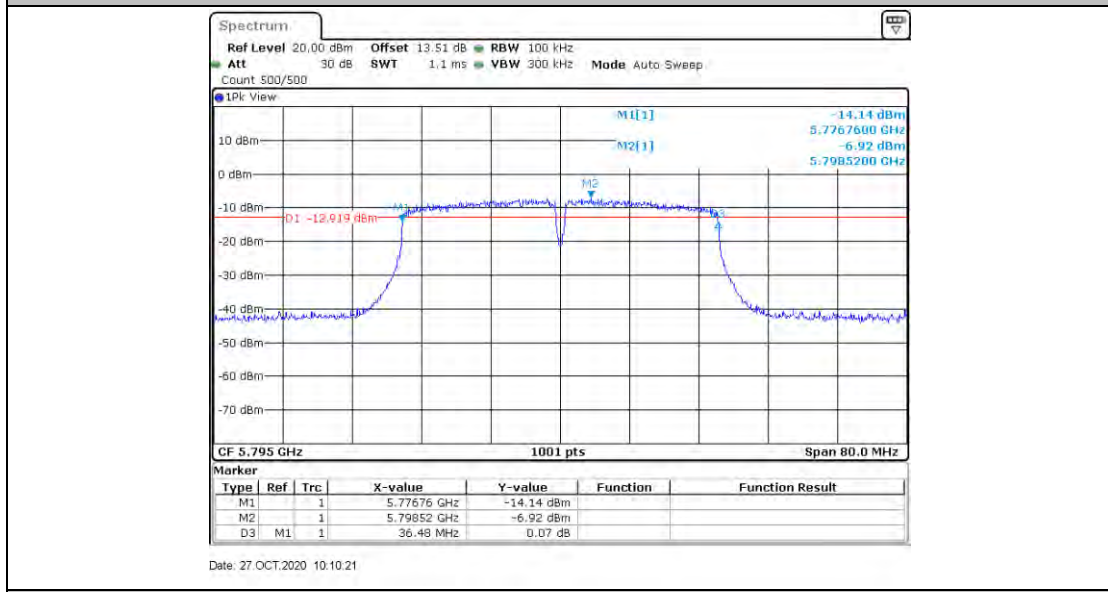
11AC40SISO_Ant2_5755



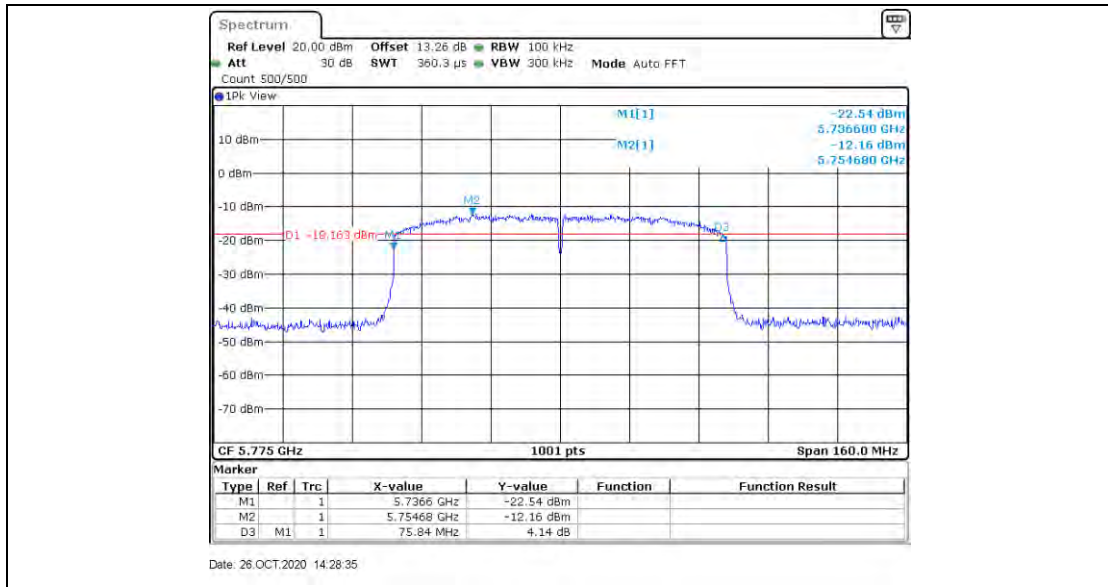
11AC40SISO_Ant1_5795



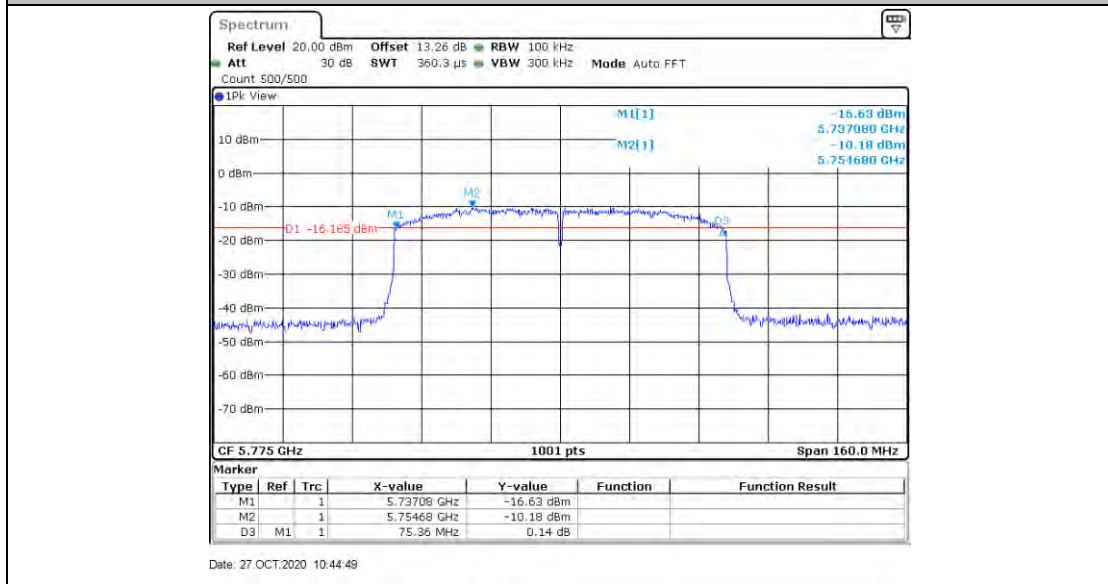
11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775



Appendix B1: Max Conducted TX Power

Test Result

Test Mode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	14.95	<=24	PASS
	Ant2	5180	15.28	<=24	PASS
	Ant1	5200	15.15	<=24	PASS
	Ant2	5200	15.48	<=24	PASS
	Ant1	5240	15.60	<=24	PASS
	Ant2	5240	15.91	<=24	PASS
	Ant1	5260	14.93	<=23.69	PASS
	Ant2	5260	15.95	<=23.70	PASS
	Ant1	5280	14.99	<=23.70	PASS
	Ant2	5280	16.25	<=23.69	PASS
	Ant1	5320	15.62	<=23.98	PASS
	Ant2	5320	15.53	<=23.70	PASS
	Ant1	5500	15.72	<=23.98	PASS
	Ant2	5500	15.18	<=23.70	PASS
	Ant1	5580	15.94	<=23.98	PASS
	Ant2	5580	15.85	<=23.70	PASS
	Ant1	5700	13.92	<=23.70	PASS
	Ant2	5700	16.57	<=23.91	PASS
	Ant1	5745	14.54	<=30	PASS
	Ant2	5745	15.43	<=30	PASS
	Ant1	5785	14.32	<=30	PASS
	Ant2	5785	15.10	<=30	PASS
	Ant1	5825	14.44	<=30	PASS
	Ant2	5825	14.87	<=30	PASS
11N20SISO	Ant1	5180	14.71	<=24	PASS
	Ant2	5180	16.09	<=24	PASS
	Ant1	5200	14.76	<=24	PASS
	Ant2	5200	16.29	<=24	PASS
	Ant1	5240	15.16	<=24	PASS
	Ant2	5240	15.56	<=24	PASS
	Ant1	5260	14.86	<=23.90	PASS
	Ant2	5260	16.40	<=23.89	PASS
	Ant1	5280	15.39	<=23.90	PASS
	Ant2	5280	16.46	<=23.90	PASS
	Ant1	5320	15.68	<=23.95	PASS
	Ant2	5320	15.61	<=23.90	PASS
	Ant1	5500	15.85	<=23.98	PASS
	Ant2	5500	15.04	<=23.90	PASS
	Ant1	5580	15.80	<=23.98	PASS

Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5580	15.56	<=23.90	PASS
	Ant1	5700	14.46	<=23.91	PASS
	Ant2	5700	15.24	<=23.90	PASS
	Ant1	5745	15.03	<=30	PASS
	Ant2	5745	15.23	<=30	PASS
	Ant1	5785	14.77	<=30	PASS
	Ant2	5785	15.04	<=30	PASS
	Ant1	5825	14.81	<=30	PASS
	Ant2	5825	14.93	<=30	PASS
11N40SISO	Ant1	5190	15.16	<=24	PASS
	Ant2	5190	15.88	<=24	PASS
	Ant1	5230	15.44	<=24	PASS
	Ant2	5230	15.36	<=24	PASS
	Ant1	5270	15.09	<=23.98	PASS
	Ant2	5270	15.36	<=23.98	PASS
	Ant1	5310	15.63	<=23.98	PASS
	Ant2	5310	14.84	<=23.98	PASS
	Ant1	5510	16.14	<=23.98	PASS
	Ant2	5510	15.94	<=23.98	PASS
	Ant1	5550	15.55	<=23.98	PASS
	Ant2	5550	15.26	<=23.98	PASS
	Ant1	5670	14.41	<=23.98	PASS
	Ant2	5670	16.68	<=23.98	PASS
	Ant1	5755	14.57	<=30	PASS
	Ant2	5755	15.53	<=30	PASS
	Ant1	5795	14.31	<=30	PASS
	Ant2	5795	15.43	<=30	PASS
11AC20SISO	Ant1	5180	14.39	<=24	PASS
	Ant2	5180	15.07	<=24	PASS
	Ant1	5200	14.36	<=24	PASS
	Ant2	5200	15.33	<=24	PASS
	Ant1	5240	14.41	<=24	PASS
	Ant2	5240	15.58	<=24	PASS
	Ant1	5260	14.81	<=23.92	PASS
	Ant2	5260	15.40	<=23.90	PASS
	Ant1	5280	14.33	<=23.90	PASS
	Ant2	5280	15.02	<=23.90	PASS
	Ant1	5320	14.55	<=23.90	PASS
	Ant2	5320	14.66	<=23.90	PASS
	Ant1	5500	14.48	<=23.94	PASS
	Ant2	5500	14.48	<=23.91	PASS
	Ant1	5580	14.57	<=23.95	PASS

Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5580	14.42	<=23.90	PASS
	Ant1	5700	12.45	<=23.90	PASS
	Ant2	5700	15.20	<=23.90	PASS
	Ant1	5745	12.60	<=30	PASS
	Ant2	5745	14.04	<=30	PASS
	Ant1	5785	12.63	<=30	PASS
	Ant2	5785	14.34	<=30	PASS
	Ant1	5825	12.91	<=30	PASS
	Ant2	5825	13.98	<=30	PASS
11AC40SISO	Ant1	5190	12.89	<=24	PASS
	Ant2	5190	13.58	<=24	PASS
	Ant1	5230	12.95	<=24	PASS
	Ant2	5230	12.64	<=24	PASS
	Ant1	5270	12.79	<=23.98	PASS
	Ant2	5270	13.25	<=23.98	PASS
	Ant1	5310	13.11	<=23.98	PASS
	Ant2	5310	13.25	<=23.98	PASS
	Ant1	5510	12.97	<=23.98	PASS
	Ant2	5510	12.14	<=23.98	PASS
	Ant1	5550	12.93	<=23.98	PASS
	Ant2	5550	13.03	<=23.98	PASS
	Ant1	5670	11.20	<=23.98	PASS
	Ant2	5670	13.52	<=23.98	PASS
	Ant1	5755	11.45	<=30	PASS
	Ant2	5755	13.97	<=30	PASS
	Ant1	5795	11.23	<=30	PASS
	Ant2	5795	12.82	<=30	PASS
11AC80SISO	Ant1	5210	13.00	<=24	PASS
	Ant2	5210	13.05	<=24	PASS
	Ant1	5290	12.95	<=23.98	PASS
	Ant2	5290	13.20	<=23.98	PASS
	Ant1	5530	13.59	<=23.98	PASS
	Ant2	5530	12.51	<=23.98	PASS
	Ant1	5775	12.70	<=30	PASS
	Ant2	5775	13.11	<=30	PASS
11N20MIMO	Ant1	5180	15.94	<=24	PASS
	Ant2	5180	16.34	<=24	PASS
	total	5180	19.2	<=24	PASS
	Ant1	5200	13.97	<=24	PASS
	Ant2	5200	15.81	<=24	PASS
	total	5200	18.0	<=24	PASS
	Ant1	5240	14.25	<=24	PASS

Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5240	16.09	<=24	PASS
	total	5240	18.3	<=24	PASS
	Ant1	5260	14.39	<=23.90	PASS
	Ant2	5260	15.83	<=23.88	PASS
	total	5260	18.2	<=23.88	PASS
	Ant1	5280	14.65	<=23.92	PASS
	Ant2	5280	16.11	<=23.87	PASS
	total	5280	18.5	<=23.87	PASS
	Ant1	5320	15.11	<=23.90	PASS
	Ant2	5320	15.66	<=23.89	PASS
	total	5320	18.4	<=23.89	PASS
	Ant1	5500	15.27	<=23.98	PASS
	Ant2	5500	14.85	<=23.87	PASS
	total	5500	18.1	<=23.87	PASS
	Ant1	5580	15.12	<=23.98	PASS
	Ant2	5580	15.79	<=23.89	PASS
	total	5580	18.5	<=23.89	PASS
	Ant1	5700	13.30	<=23.90	PASS
	Ant2	5700	16.96	<=23.87	PASS
	total	5700	18.5	<=23.87	PASS
	Ant1	5745	12.89	<=30	PASS
	Ant2	5745	15.90	<=30	PASS
	total	5745	17.7	<=30	PASS
	Ant1	5785	12.65	<=30	PASS
	Ant2	5785	14.64	<=30	PASS
	total	5785	16.8	<=30	PASS
	Ant1	5825	12.83	<=30	PASS
	Ant2	5825	15.17	<=30	PASS
	total	5825	17.2	<=30	PASS
	11N40MIMO	Ant1	5190	14.84	<=24
Ant2		5190	15.95	<=24	PASS
total		5190	18.4	<=24	PASS
Ant1		5230	16.16	<=24	PASS
Ant2		5230	15.04	<=24	PASS
total		5230	18.6	<=24	PASS
Ant1		5270	15.39	<=23.98	PASS
Ant2		5270	15.36	<=23.98	PASS
total		5270	18.4	<=23.98	PASS
Ant1		5310	16.13	<=23.98	PASS
Ant2		5310	14.98	<=23.98	PASS
total		5310	18.6	<=23.98	PASS
Ant1	5510	15.27	<=23.98	PASS	

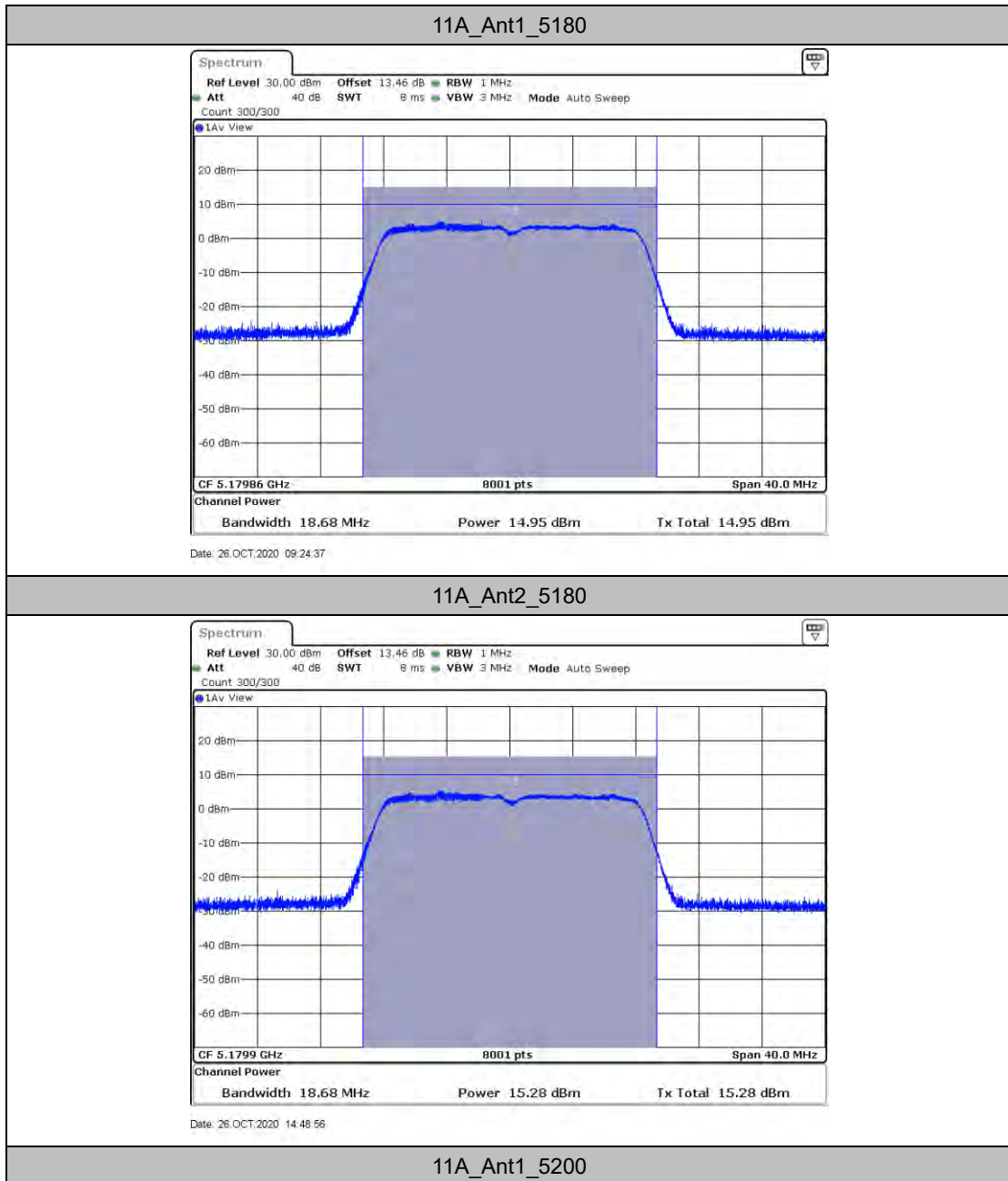
Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5510	14.10	<=23.98	PASS
	total	5510	17.7	<=23.98	PASS
	Ant1	5550	14.85	<=23.98	PASS
	Ant2	5550	15.76	<=23.98	PASS
	total	5550	18.3	<=23.98	PASS
	Ant1	5670	13.27	<=23.98	PASS
	Ant2	5670	15.38	<=23.98	PASS
	total	5670	17.5	<=23.98	PASS
	Ant1	5755	13.46	<=30	PASS
	Ant2	5755	16.39	<=30	PASS
	total	5755	18.2	<=30	PASS
	Ant1	5795	13.62	<=30	PASS
	Ant2	5795	15.16	<=30	PASS
	total	5795	17.5	<=30	PASS
11AC20MIMO	Ant1	5180	13.52	<=24	PASS
	Ant2	5180	13.26	<=24	PASS
	total	5180	16.4	<=24	PASS
	Ant1	5200	13.83	<=24	PASS
	Ant2	5200	13.27	<=24	PASS
	total	5200	16.6	<=24	PASS
	Ant1	5240	12.56	<=24	PASS
	Ant2	5240	14.00	<=24	PASS
	total	5240	16.3	<=24	PASS
	Ant1	5260	11.89	<=23.91	PASS
	Ant2	5260	13.00	<=23.89	PASS
	total	5260	15.5	<=23.89	PASS
	Ant1	5280	12.97	<=23.90	PASS
	Ant2	5280	13.35	<=23.89	PASS
	total	5280	16.2	<=23.89	PASS
	Ant1	5320	12.95	<=23.90	PASS
	Ant2	5320	13.52	<=23.90	PASS
	total	5320	16.3	<=23.90	PASS
	Ant1	5500	12.82	<=23.92	PASS
	Ant2	5500	13.58	<=23.90	PASS
	total	5500	16.2	<=23.90	PASS
	Ant1	5580	13.39	<=23.92	PASS
	Ant2	5580	14.51	<=23.88	PASS
	total	5580	17.0	<=23.88	PASS
	Ant1	5700	11.57	<=23.92	PASS
	Ant2	5700	14.50	<=23.88	PASS
	total	5700	16.3	<=23.88	PASS
	Ant1	5745	13.88	<=30	PASS

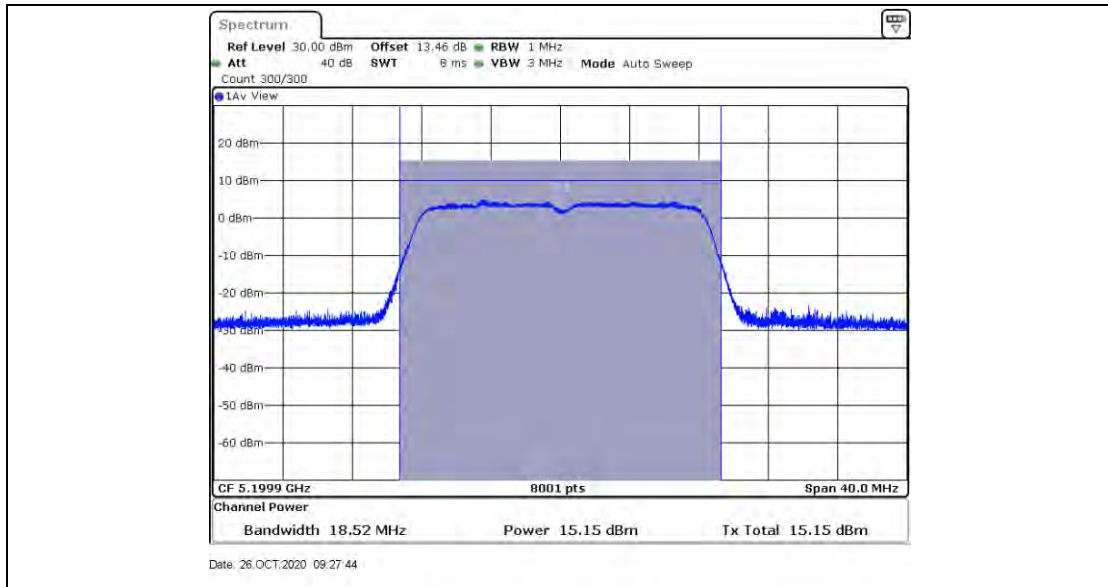
Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5745	14.85	<=30	PASS
	total	5745	17.4	<=30	PASS
	Ant1	5785	12.70	<=30	PASS
	Ant2	5785	14.37	<=30	PASS
	total	5785	16.6	<=30	PASS
	Ant1	5825	12.78	<=30	PASS
	Ant2	5825	13.83	<=30	PASS
	total	5825	16.3	<=30	PASS
11AC40MIMO	Ant1	5190	11.57	<=24	PASS
	Ant2	5190	12.97	<=24	PASS
	total	5190	15.3	<=24	PASS
	Ant1	5230	11.64	<=24	PASS
	Ant2	5230	13.15	<=24	PASS
	total	5230	15.5	<=24	PASS
	Ant1	5270	11.65	<=23.98	PASS
	Ant2	5270	13.07	<=23.98	PASS
	total	5270	15.4	<=23.98	PASS
	Ant1	5310	11.87	<=23.98	PASS
	Ant2	5310	13.23	<=23.98	PASS
	total	5310	15.6	<=23.98	PASS
	Ant1	5510	13.36	<=23.98	PASS
	Ant2	5510	12.70	<=23.98	PASS
	total	5510	16.1	<=23.98	PASS
	Ant1	5550	13.30	<=23.98	PASS
	Ant2	5550	13.10	<=23.98	PASS
	total	5550	16.2	<=23.98	PASS
	Ant1	5670	12.67	<=23.98	PASS
	Ant2	5670	15.07	<=23.98	PASS
	total	5670	17.0	<=23.98	PASS
	Ant1	5755	11.47	<=30	PASS
	Ant2	5755	13.17	<=30	PASS
	total	5755	15.4	<=30	PASS
Ant1	5795	12.37	<=30	PASS	
Ant2	5795	13.64	<=30	PASS	
total	5795	16.1	<=30	PASS	
11AC80MIMO	Ant1	5210	12.13	<=24	PASS
	Ant2	5210	12.47	<=24	PASS
	total	5210	15.3	<=24	PASS
	Ant1	5290	12.08	<=23.98	PASS
	Ant2	5290	12.86	<=23.98	PASS
	total	5290	15.5	<=23.98	PASS
	Ant1	5530	14.79	<=23.98	PASS

Test Mode	Antenna	Channel	Result	Limit	Verdict
	Ant2	5530	12.95	≤ 23.98	PASS
	total	5530	17.0	≤ 23.98	PASS
	Ant1	5775	11.33	≤ 30	PASS
	Ant2	5775	12.63	≤ 30	PASS
	total	5775	15.0	≤ 30	PASS

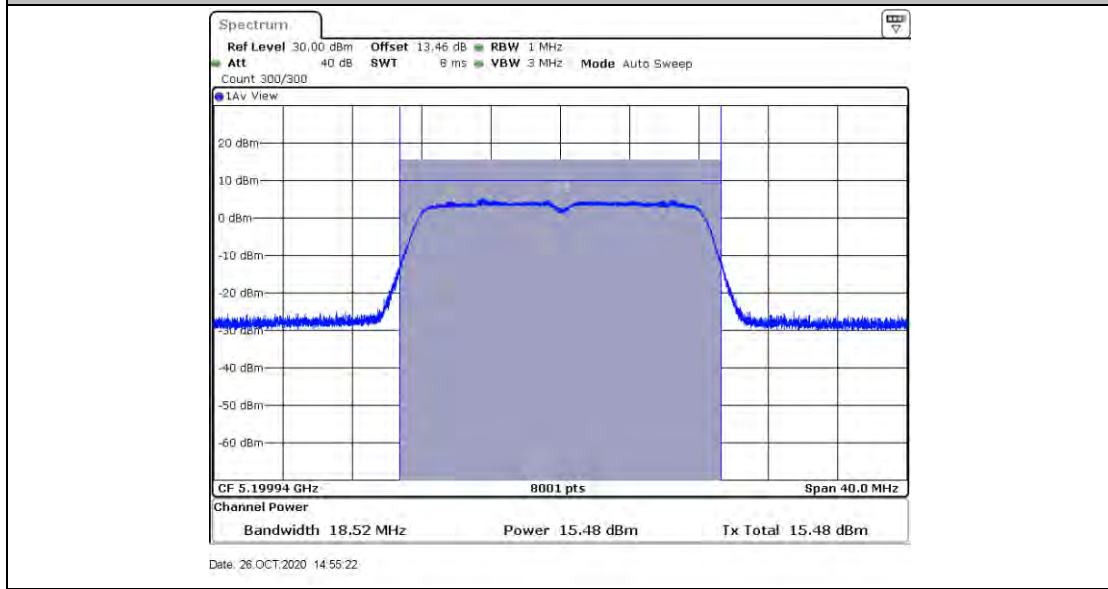
Note: The Duty Cycle Factor is compensated in the graph.

Test Graphs

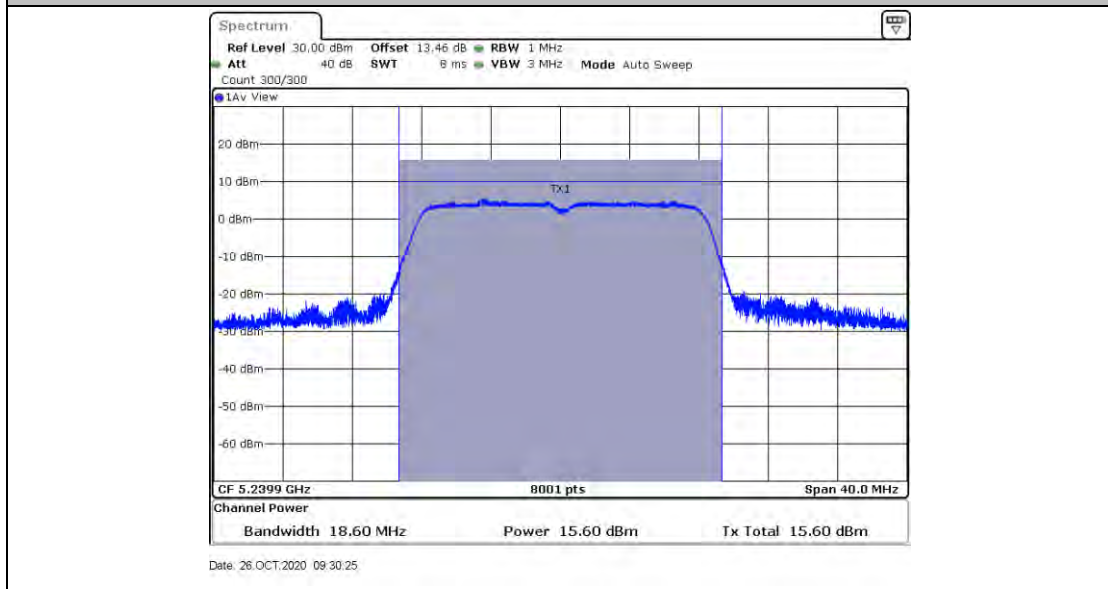




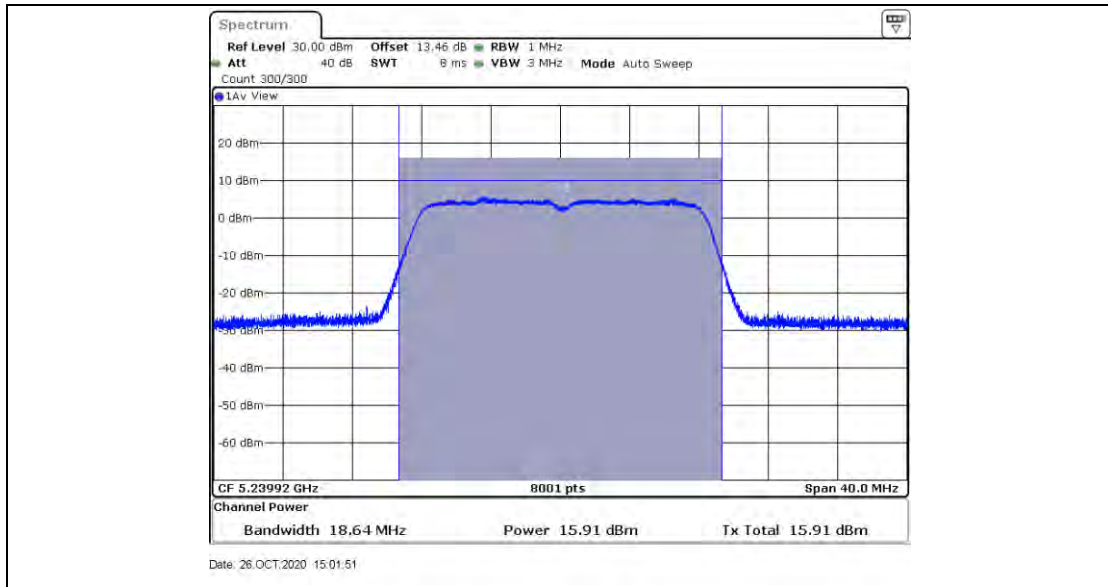
11A_Ant2_5200



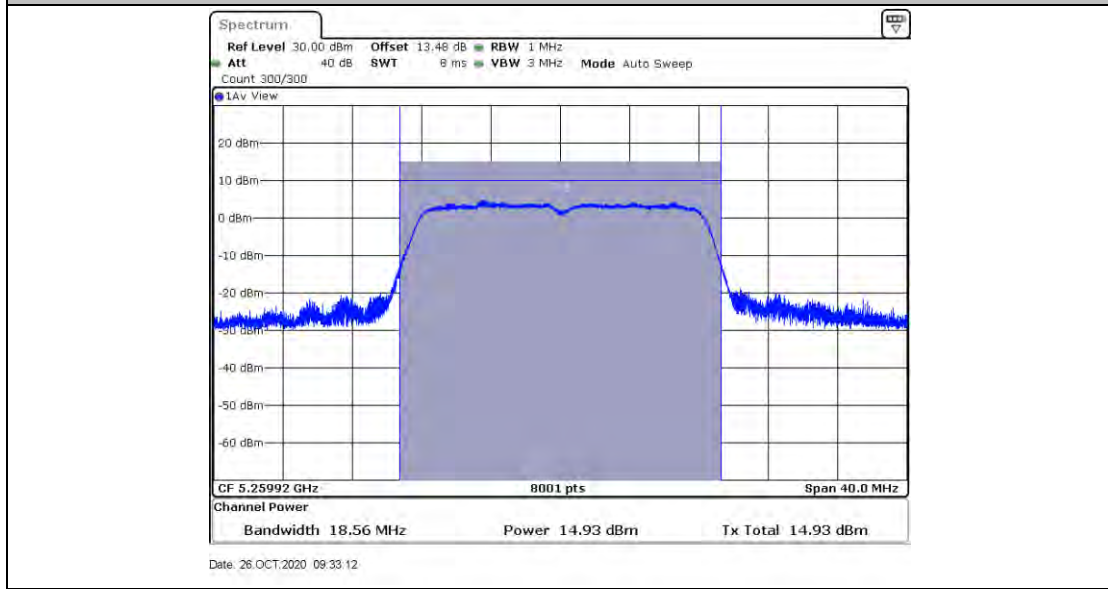
11A_Ant1_5240



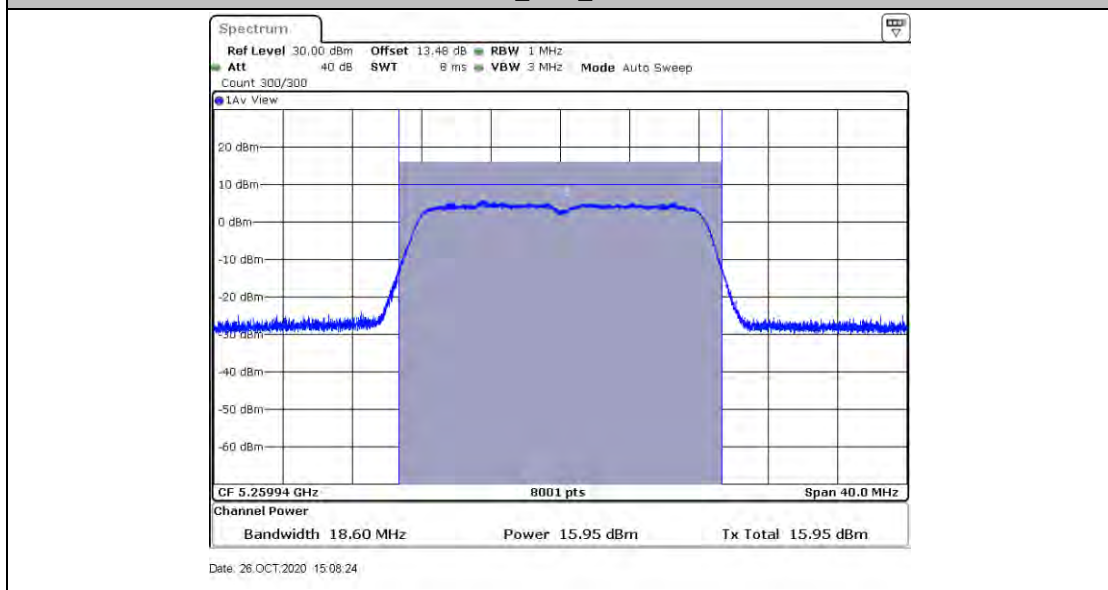
11A_Ant2_5240



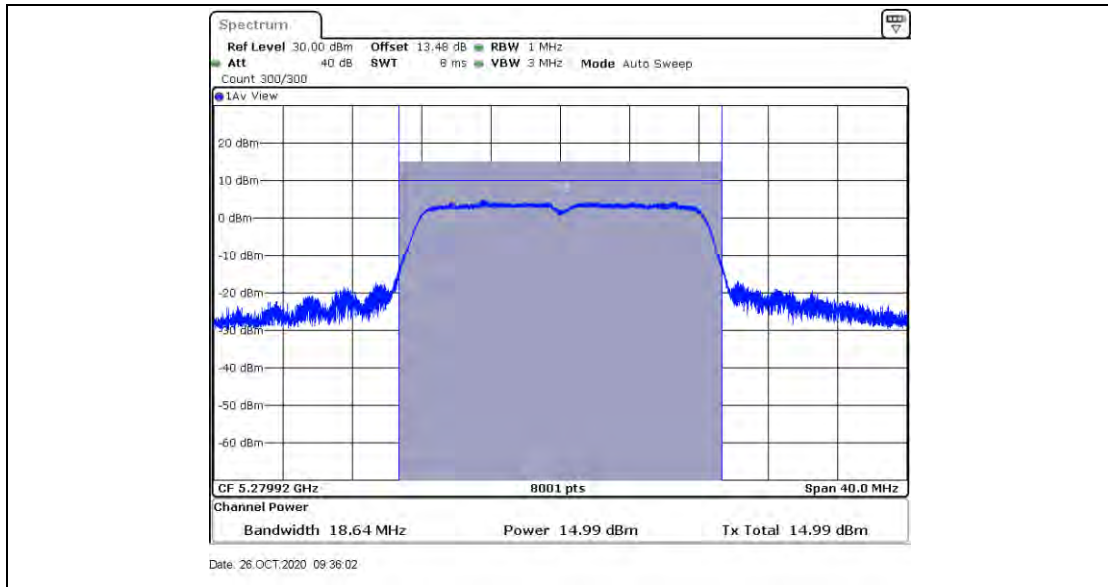
11A_Ant1_5260



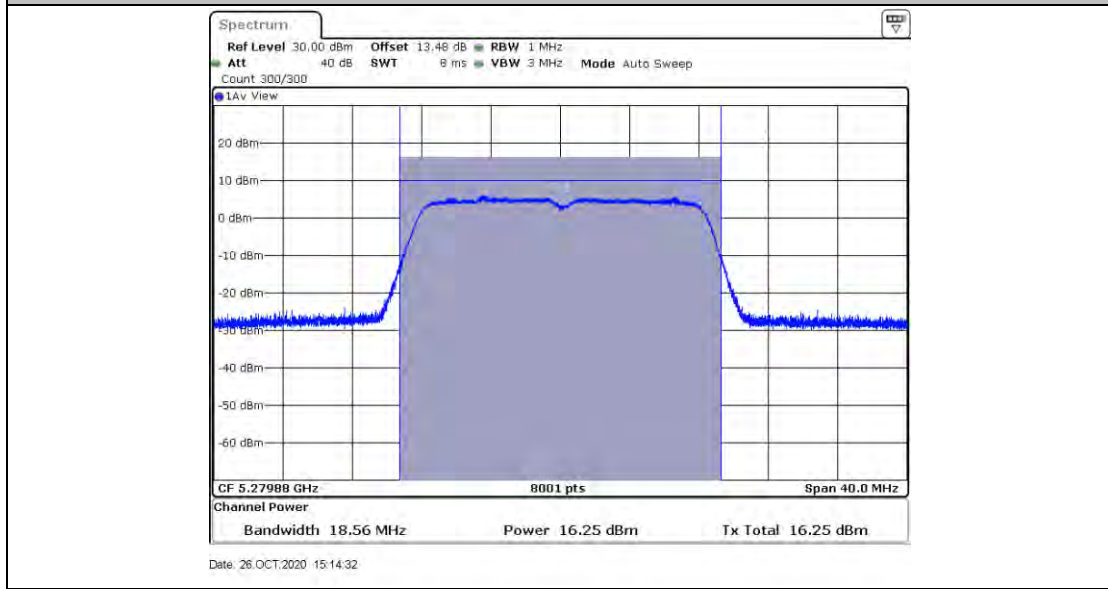
11A_Ant2_5260



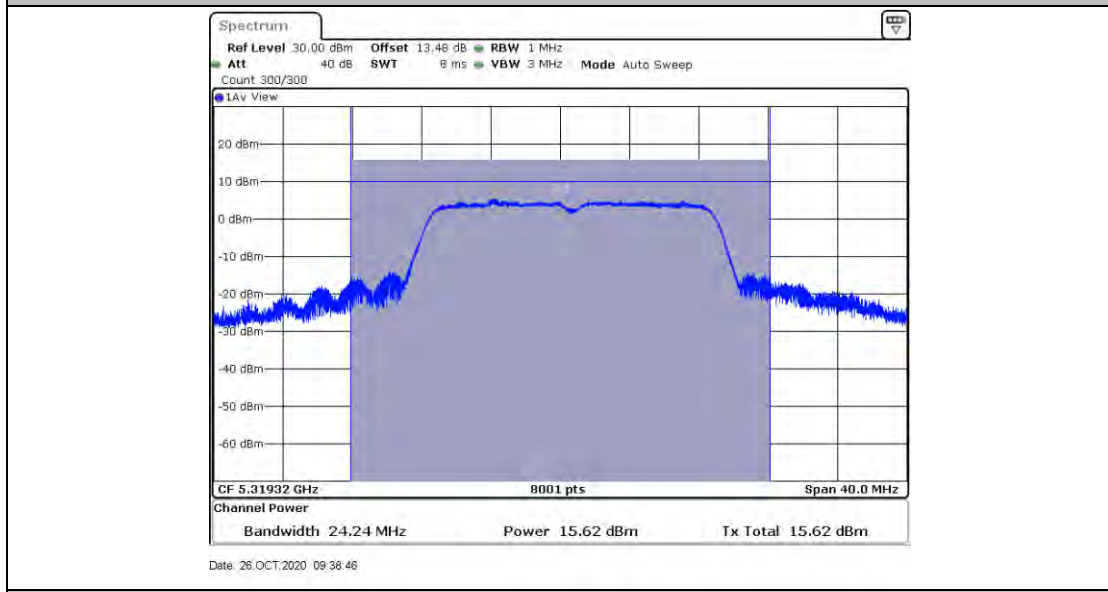
11A_Ant1_5280



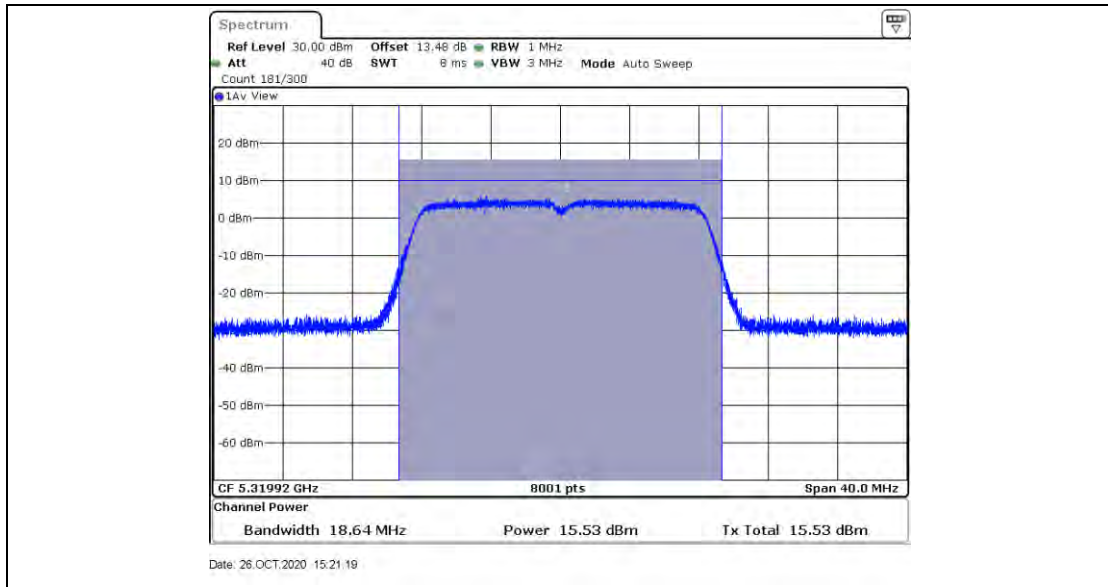
11A_Ant2_5280



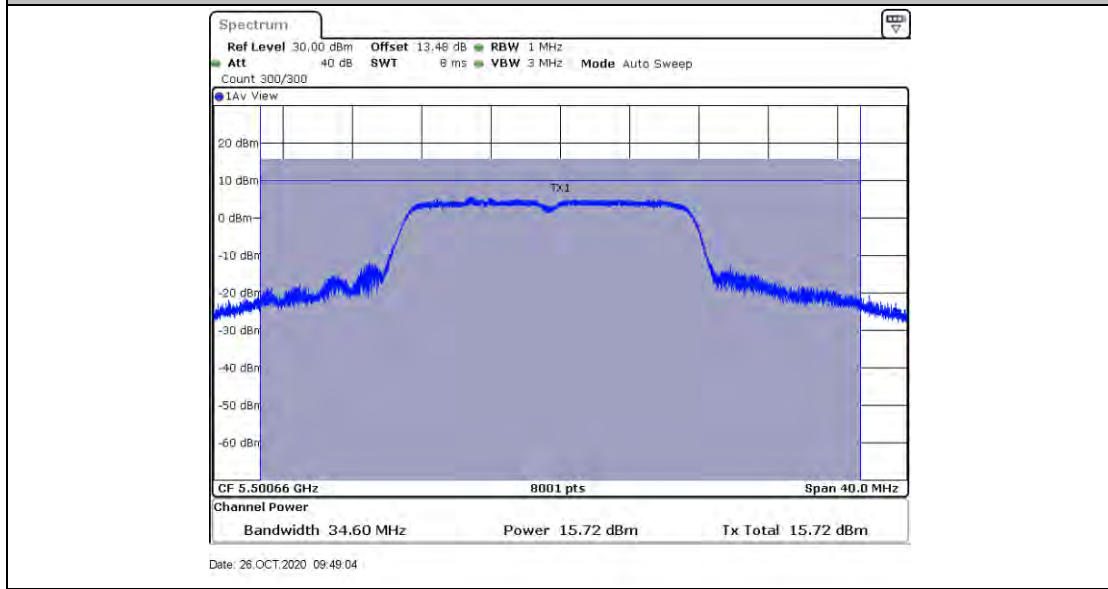
11A_Ant1_5320



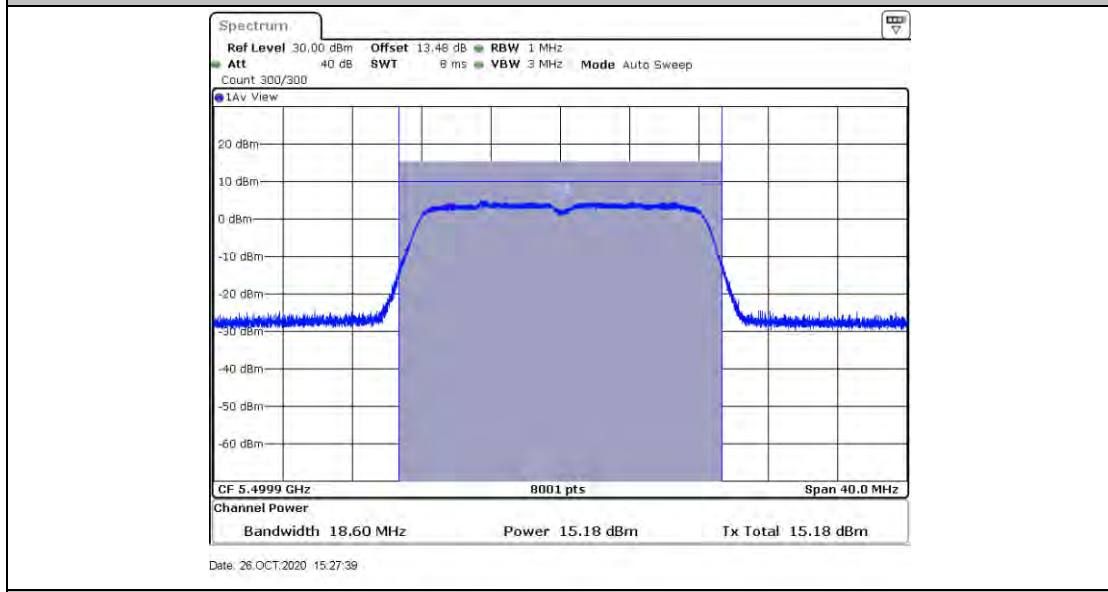
11A_Ant2_5320



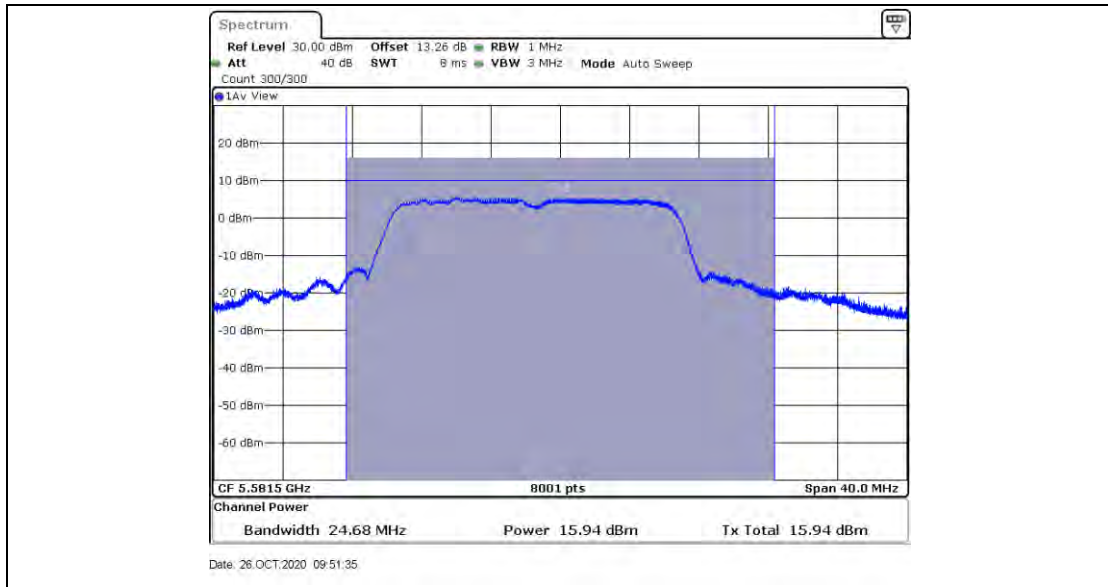
11A_Ant1_5500



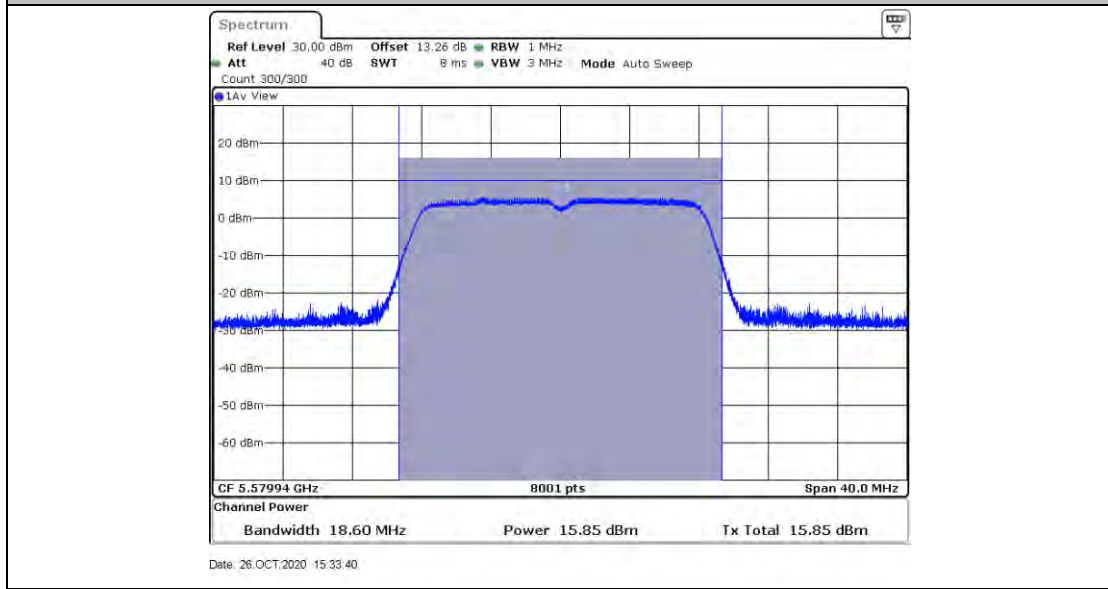
11A_Ant2_5500



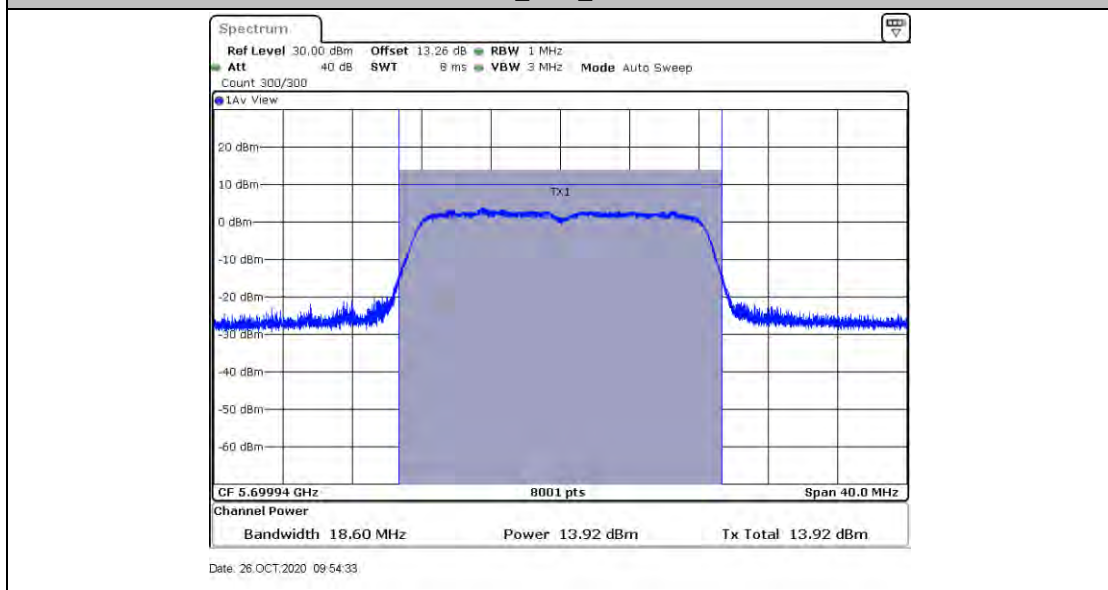
11A_Ant1_5580



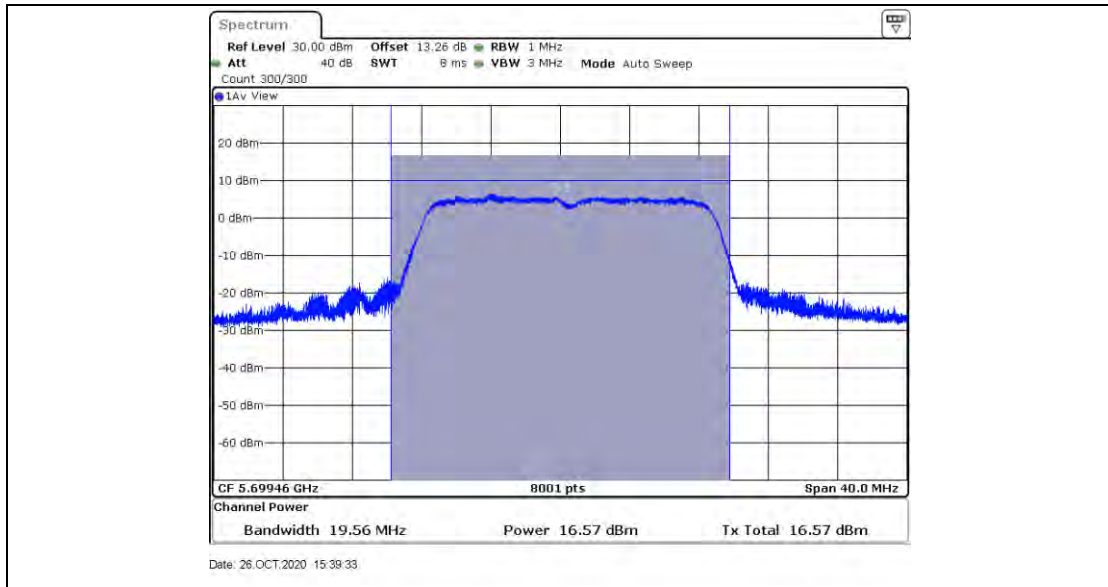
11A_Ant2_5580



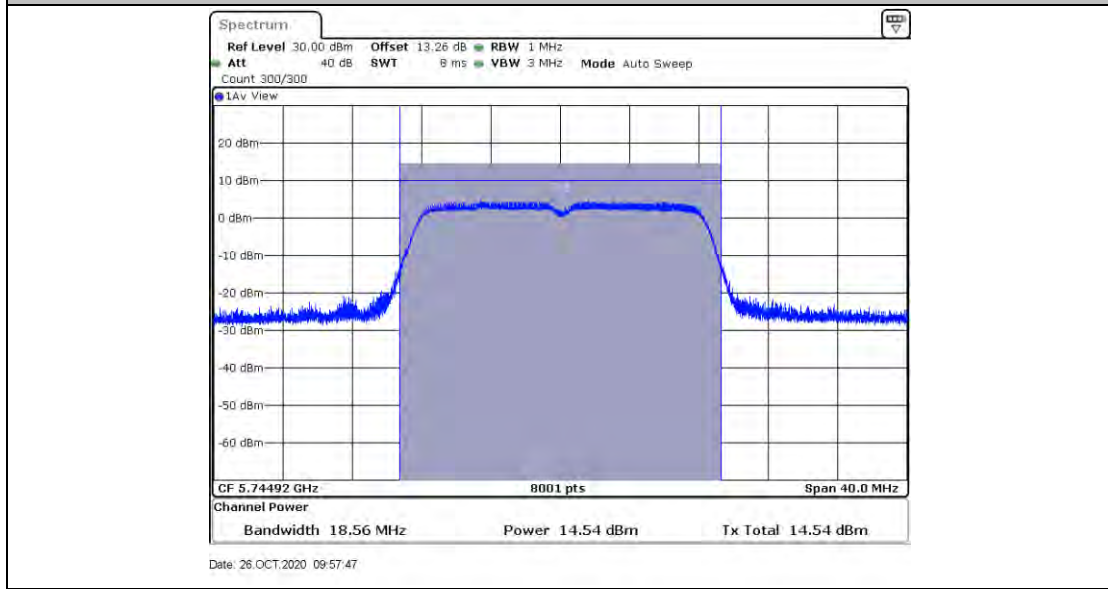
11A_Ant1_5700



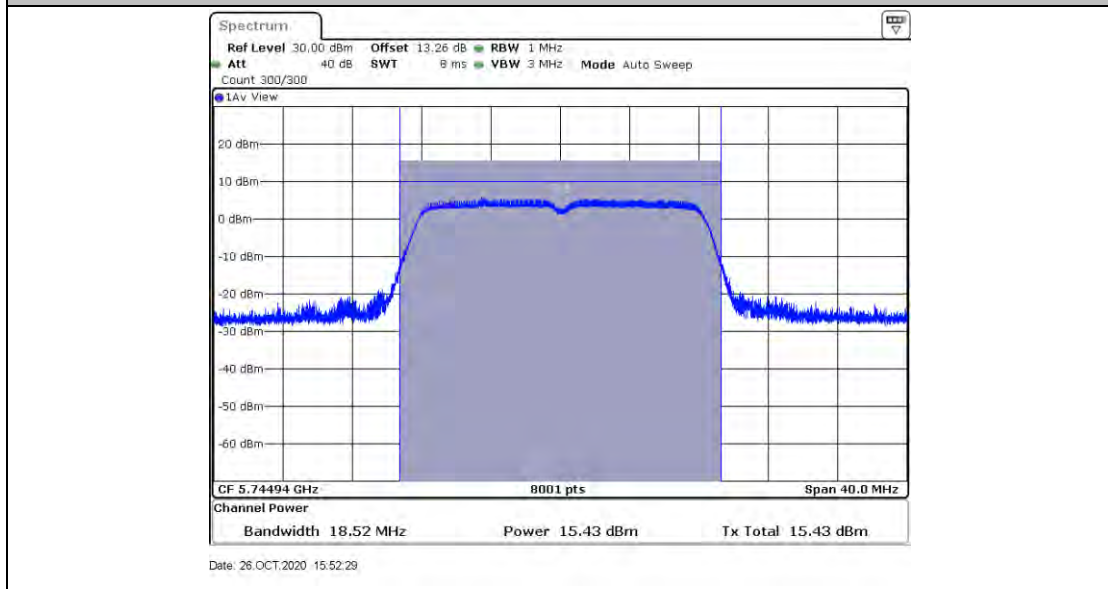
11A_Ant2_5700



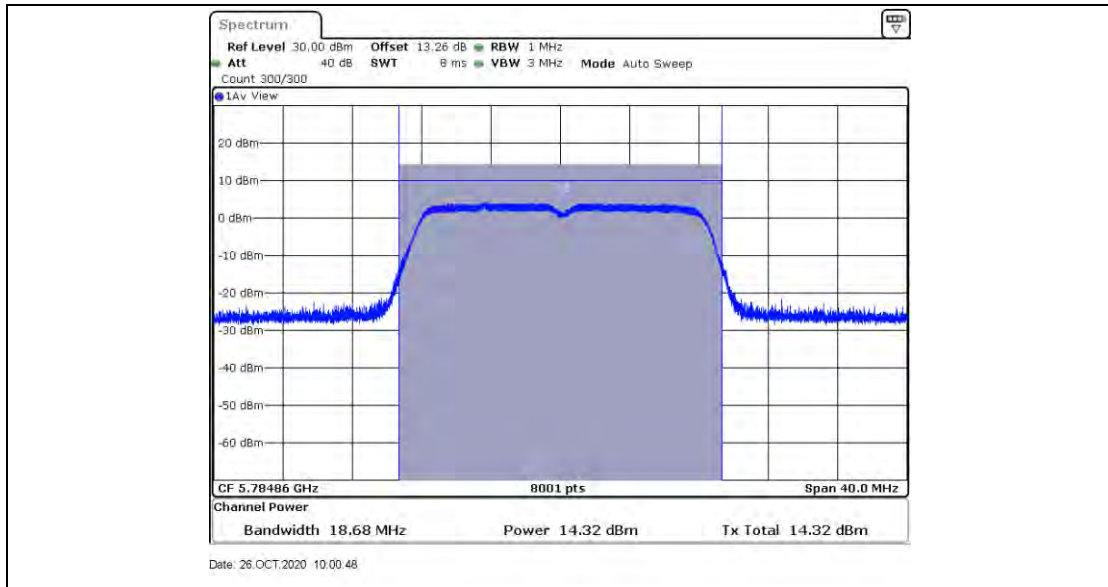
11A_Ant1_5745



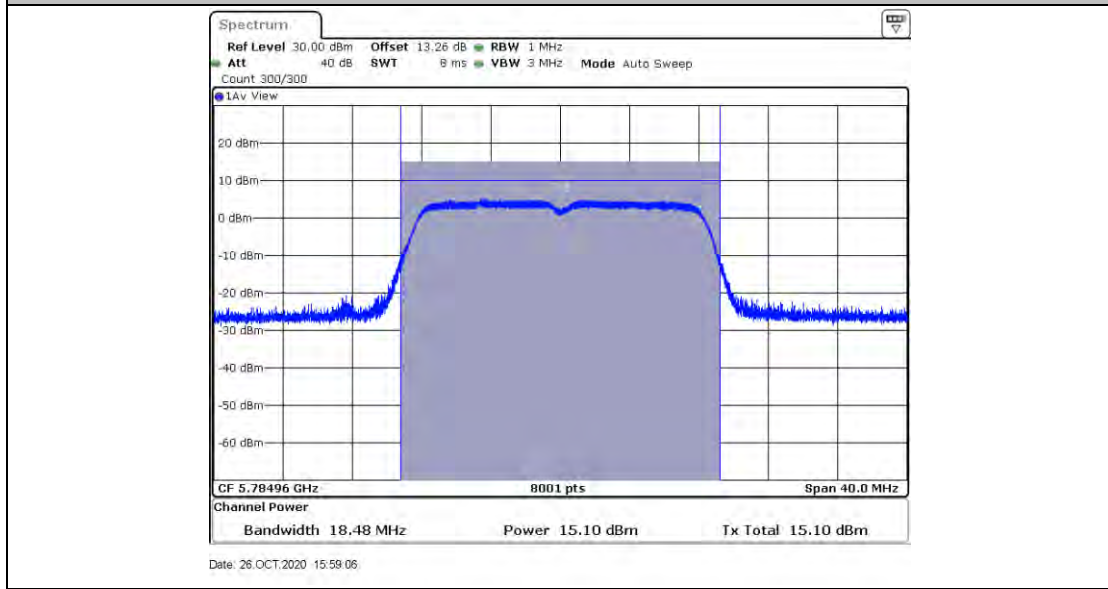
11A_Ant2_5745



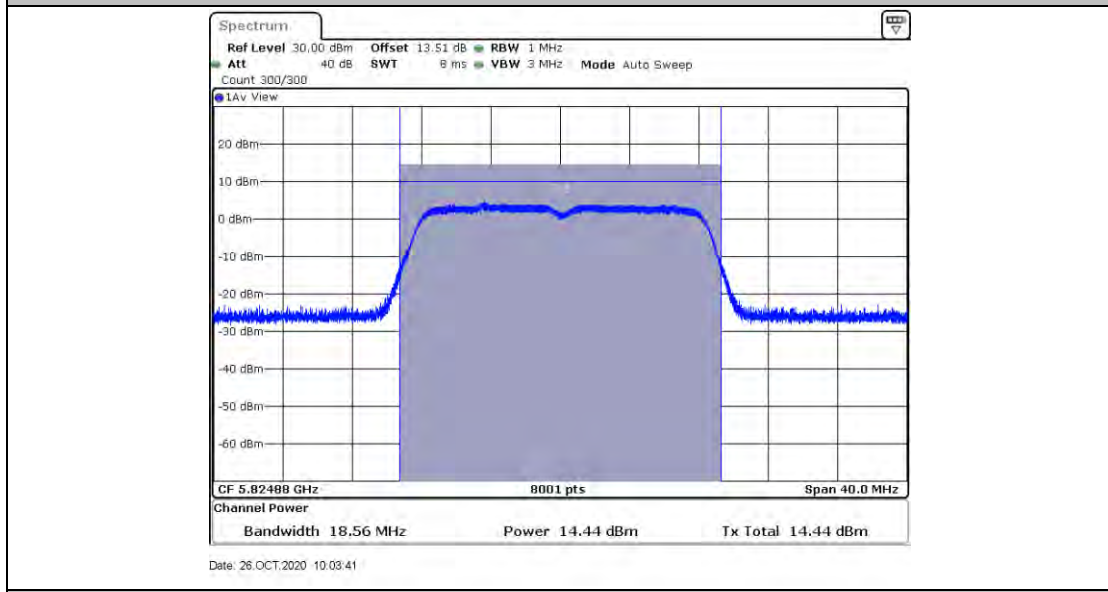
11A_Ant1_5785



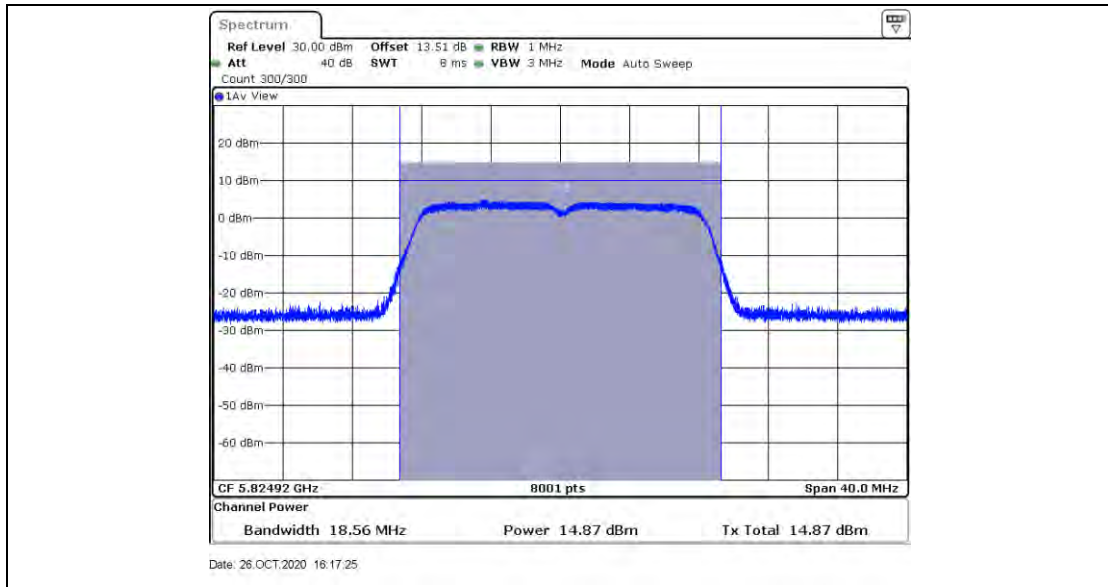
11A_Ant2_5785



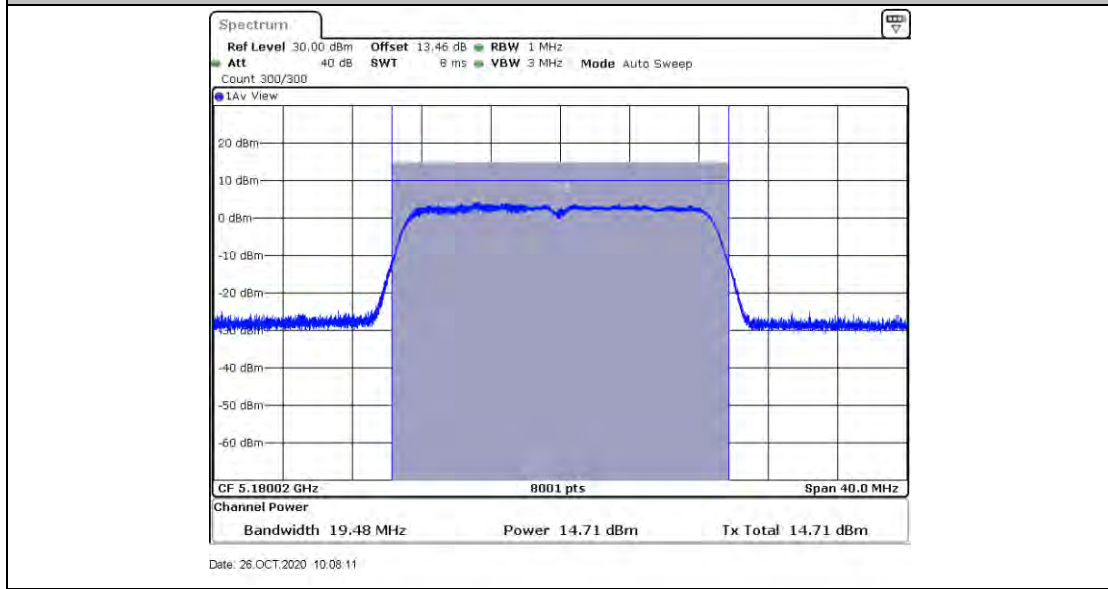
11A_Ant1_5825



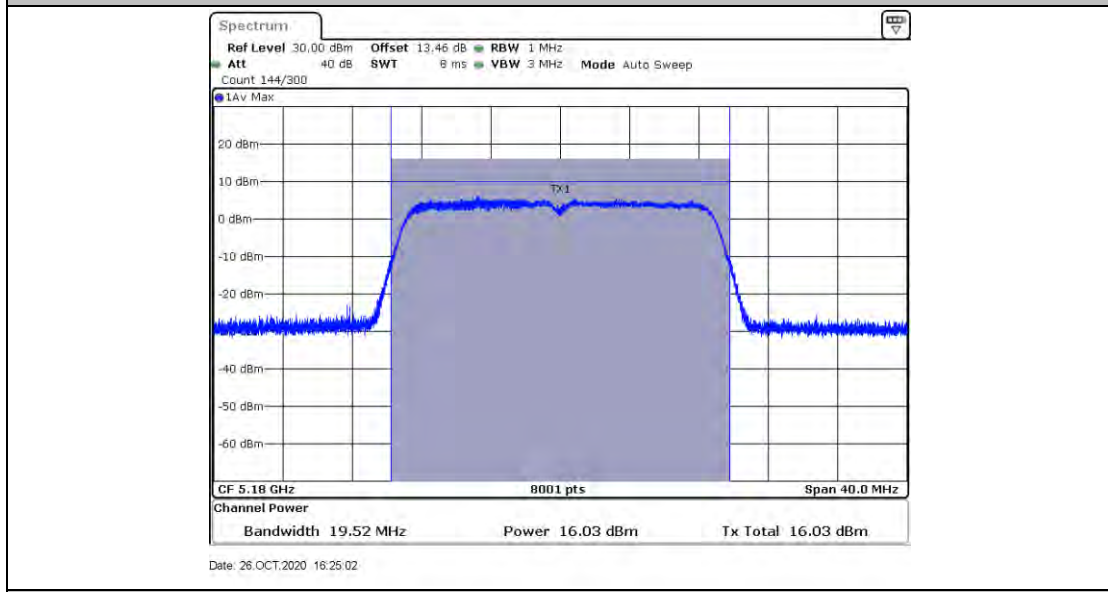
11A_Ant2_5825



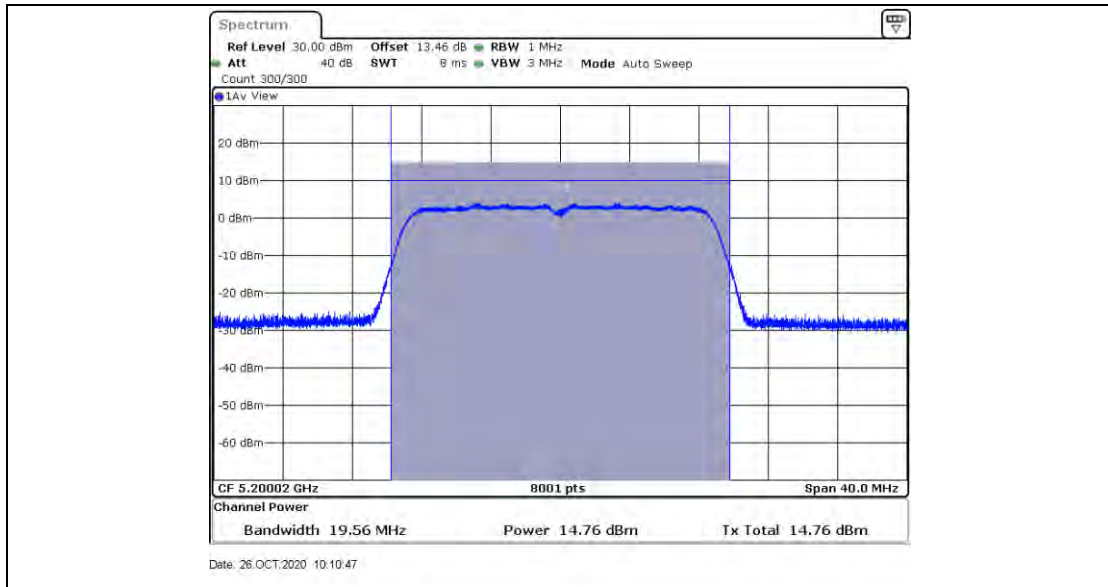
11N20SISO_Ant1_5180



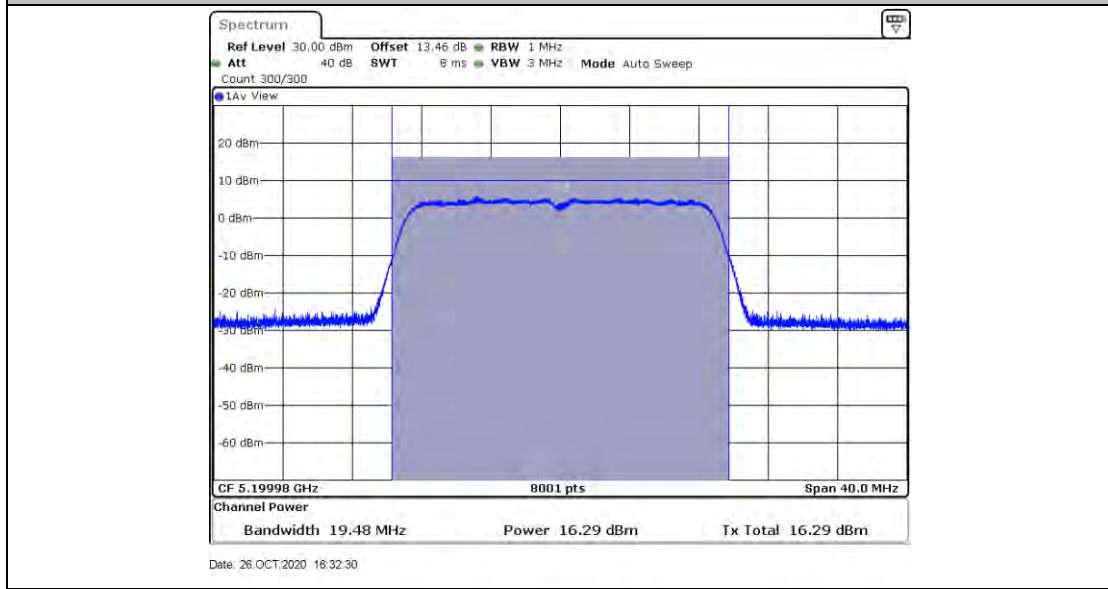
11N20SISO_Ant2_5180



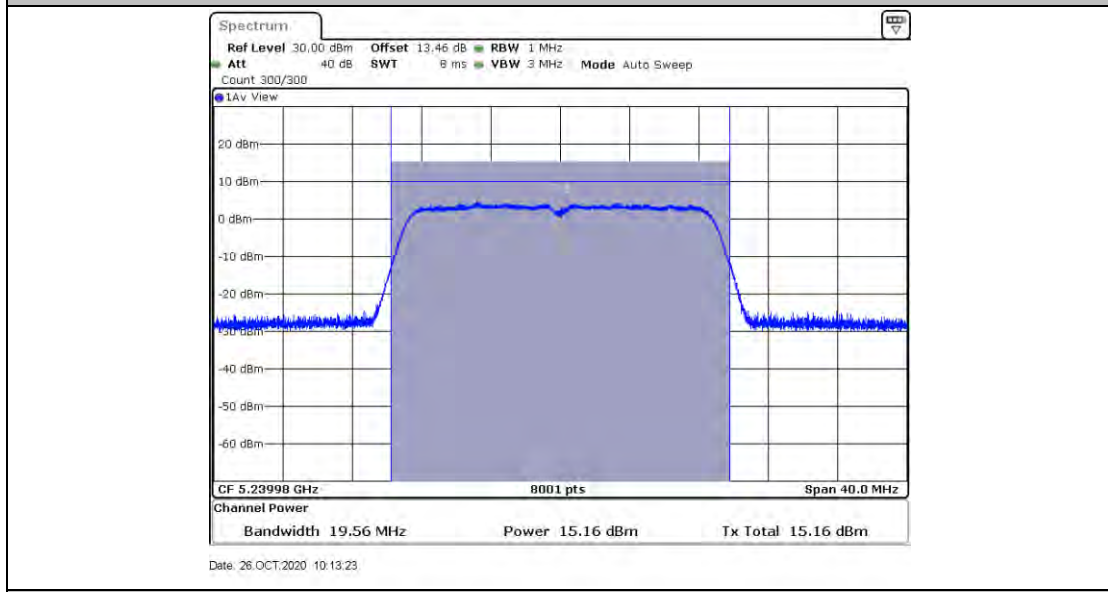
11N20SISO_Ant1_5200



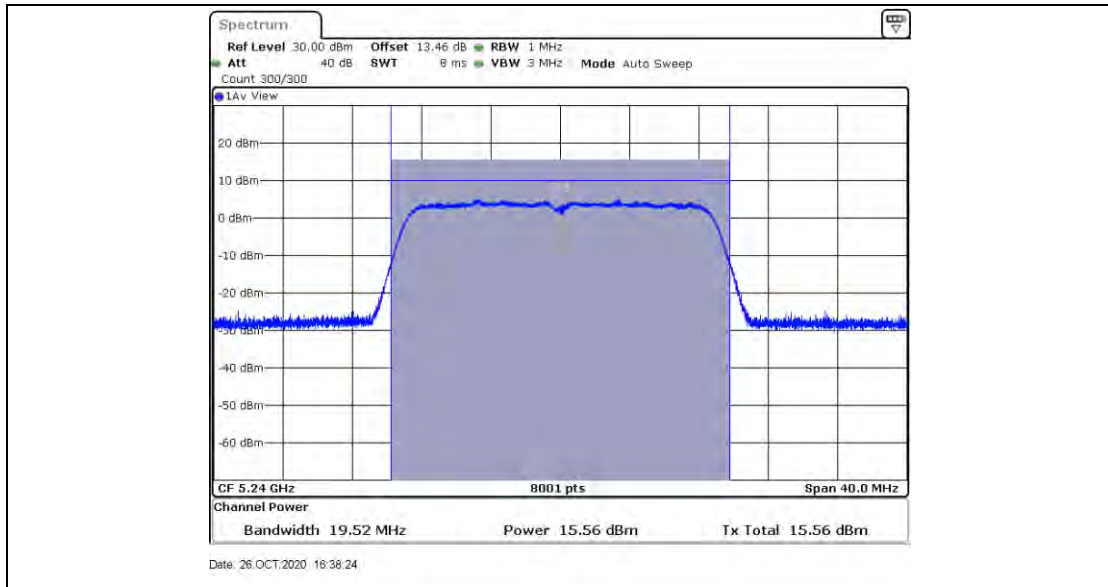
11N20SISO_Ant2_5200



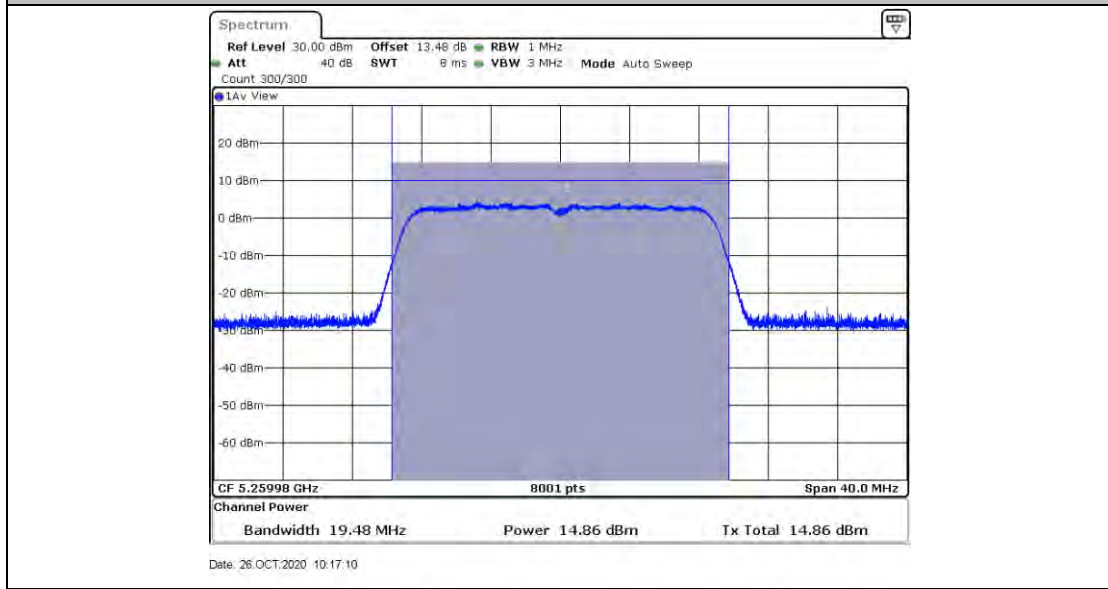
11N20SISO_Ant1_5240



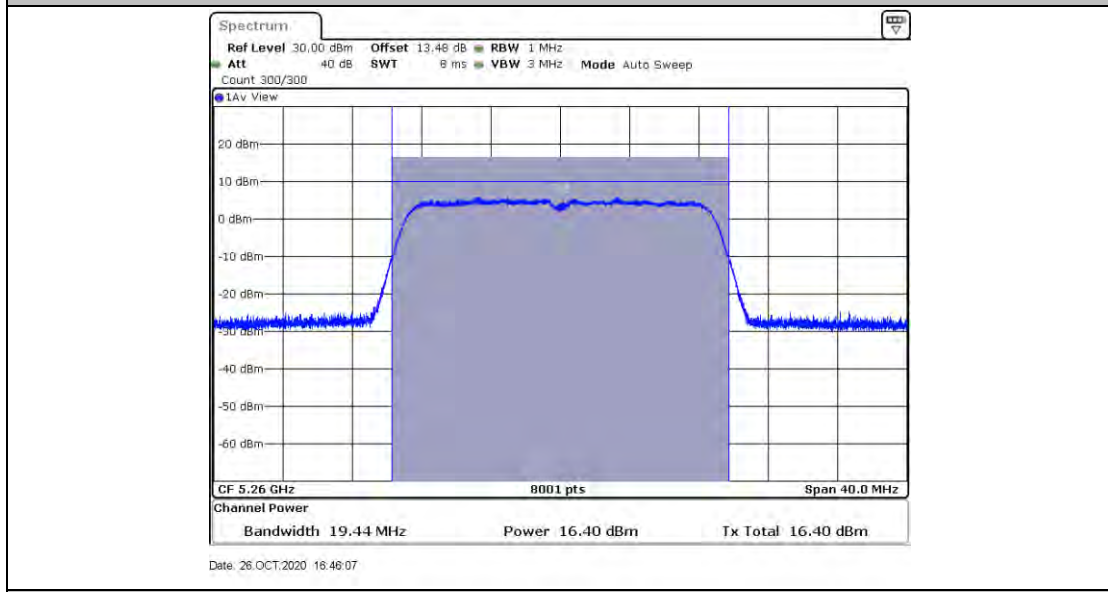
11N20SISO_Ant2_5240



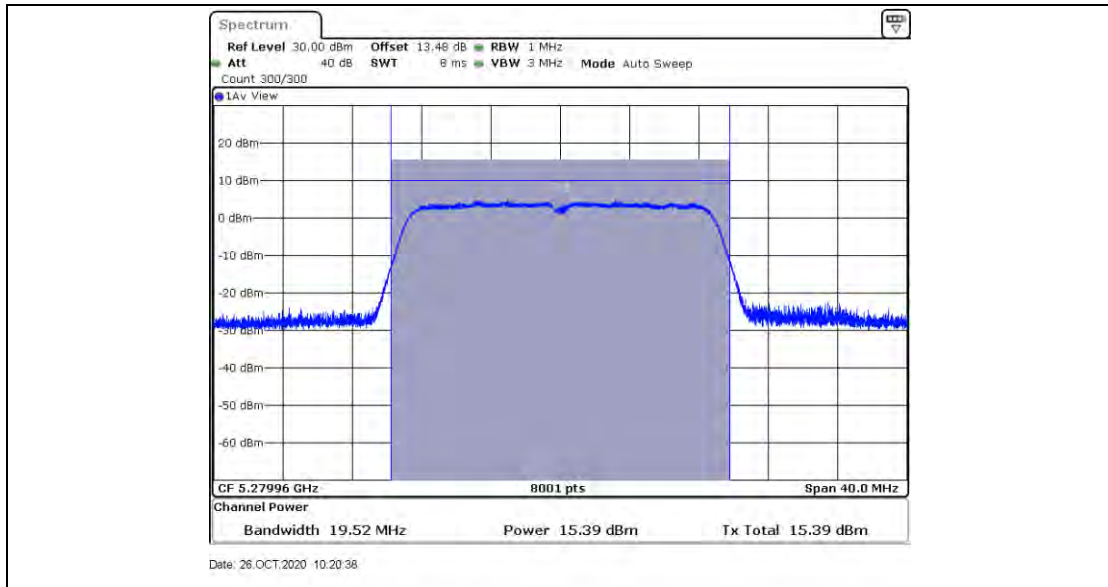
11N20SISO_Ant1_5260



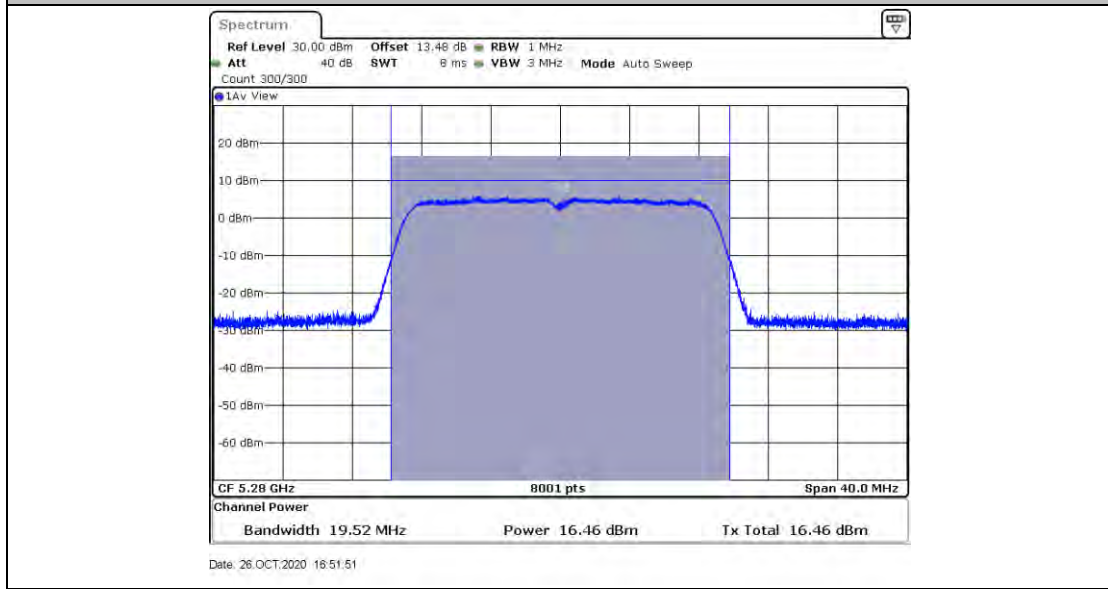
11N20SISO_Ant2_5260



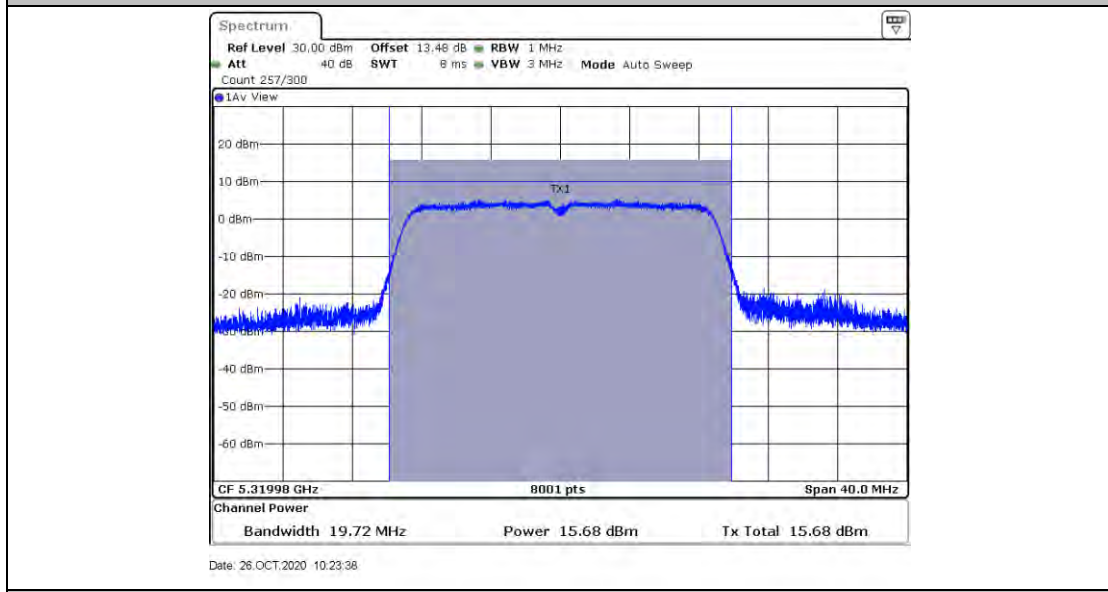
11N20SISO_Ant1_5280



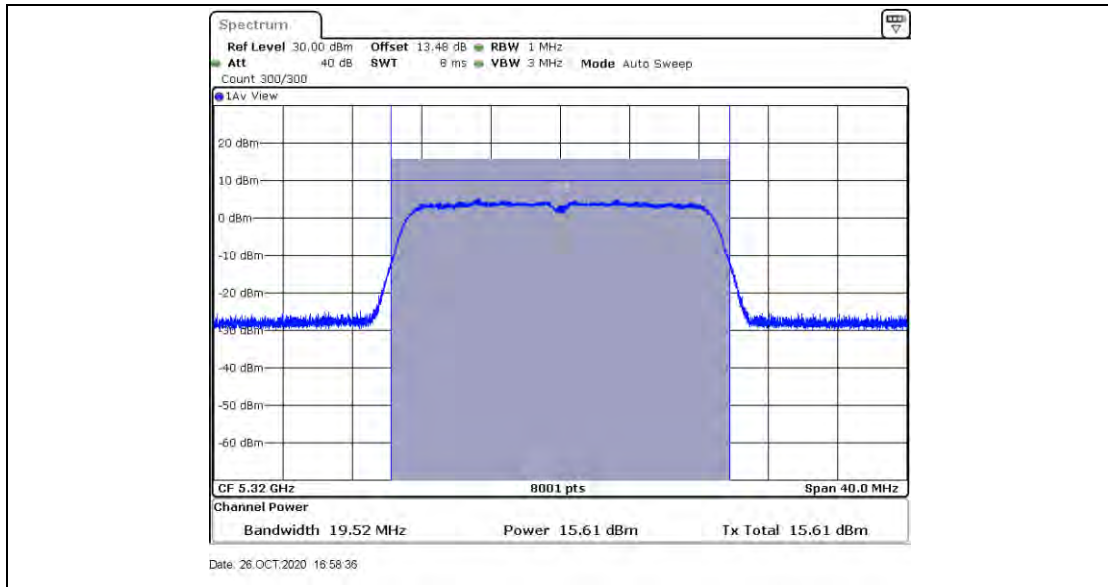
11N20SISO_Ant2_5280



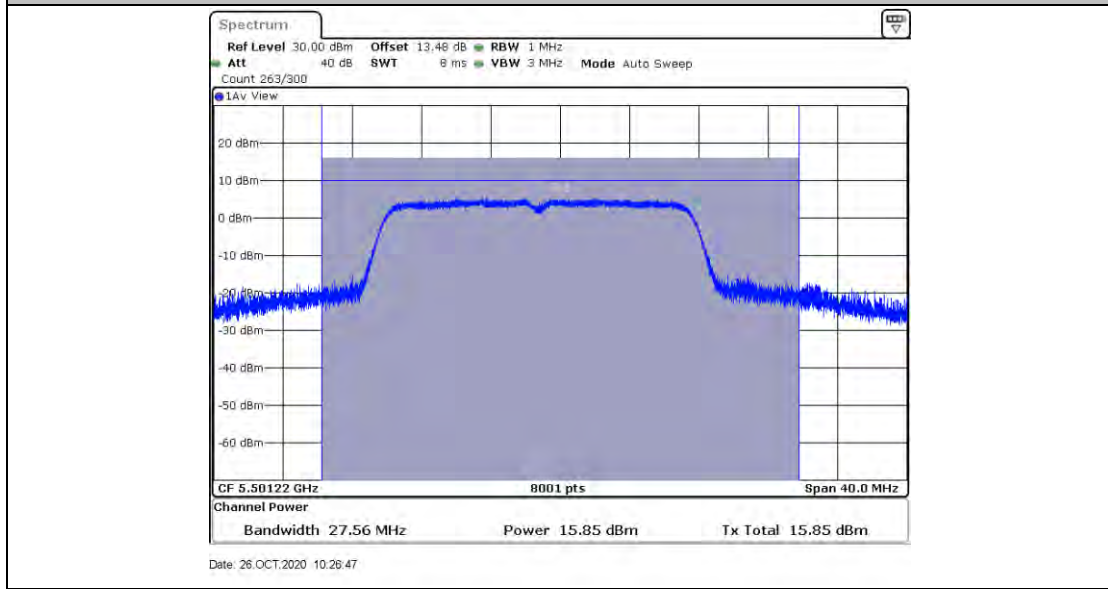
11N20SISO_Ant1_5320



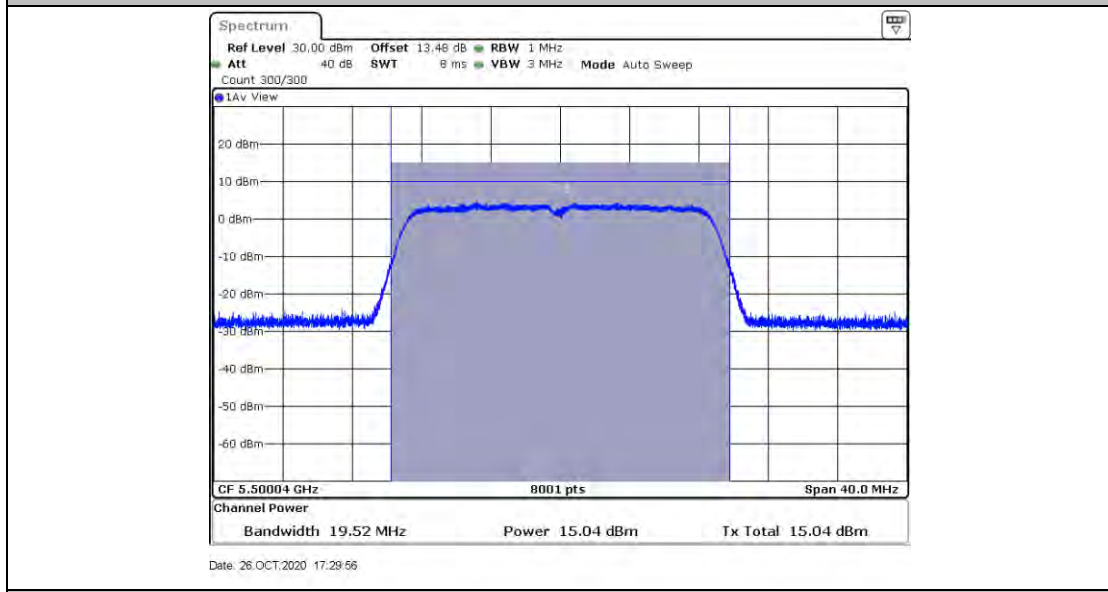
11N20SISO_Ant2_5320



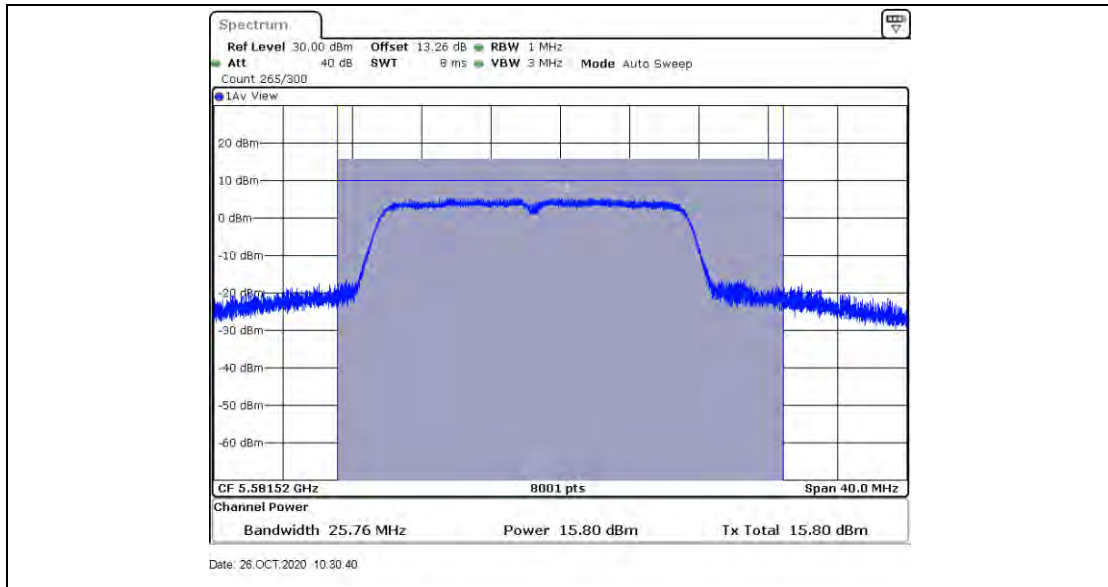
11N20SISO_Ant1_5500



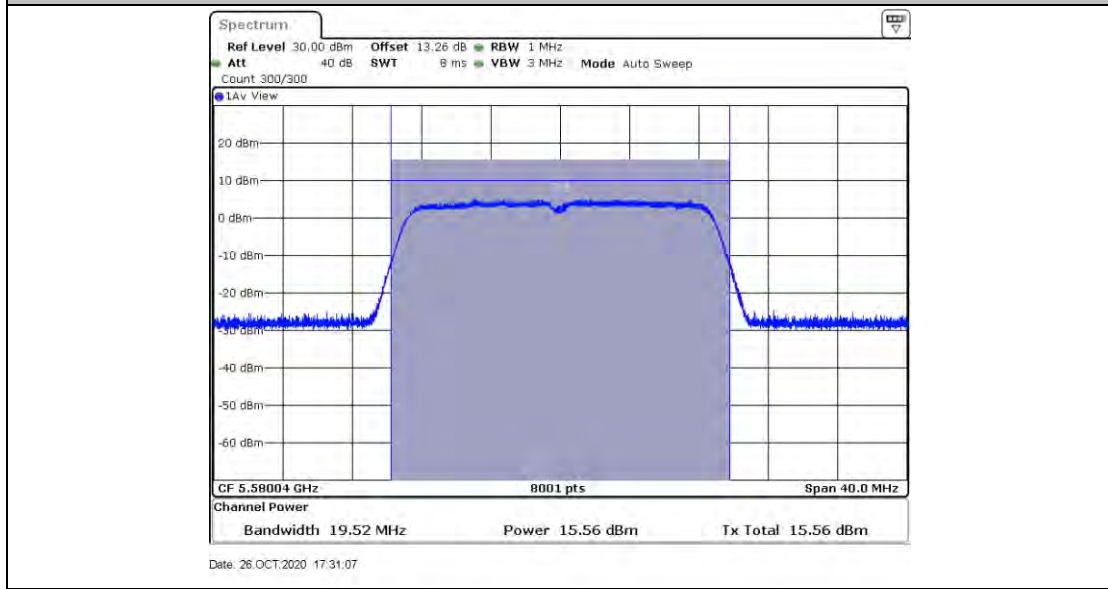
11N20SISO_Ant2_5500



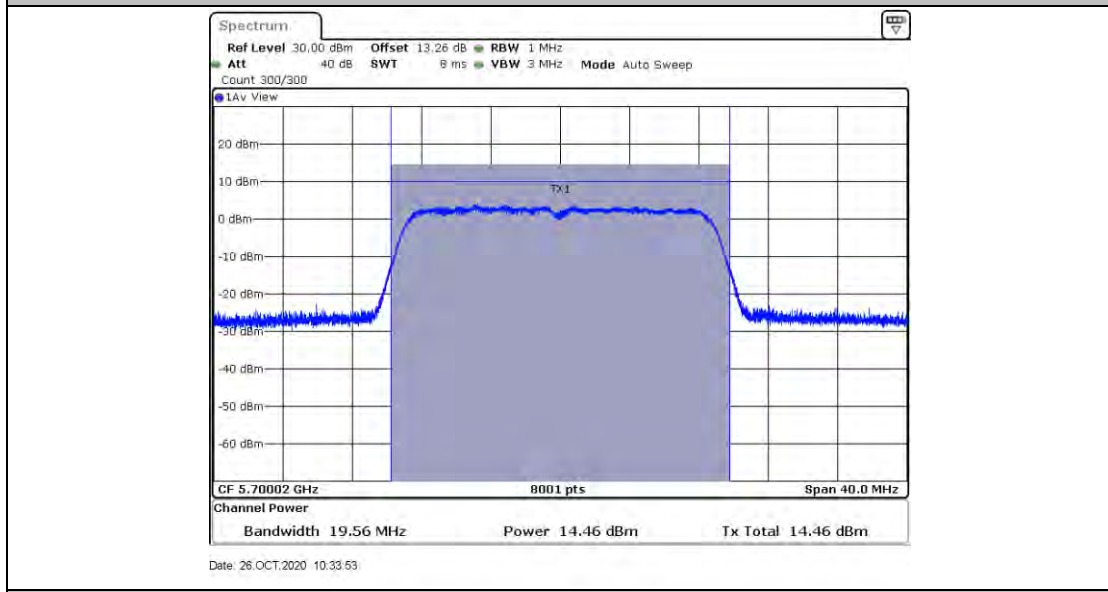
11N20SISO_Ant1_5580



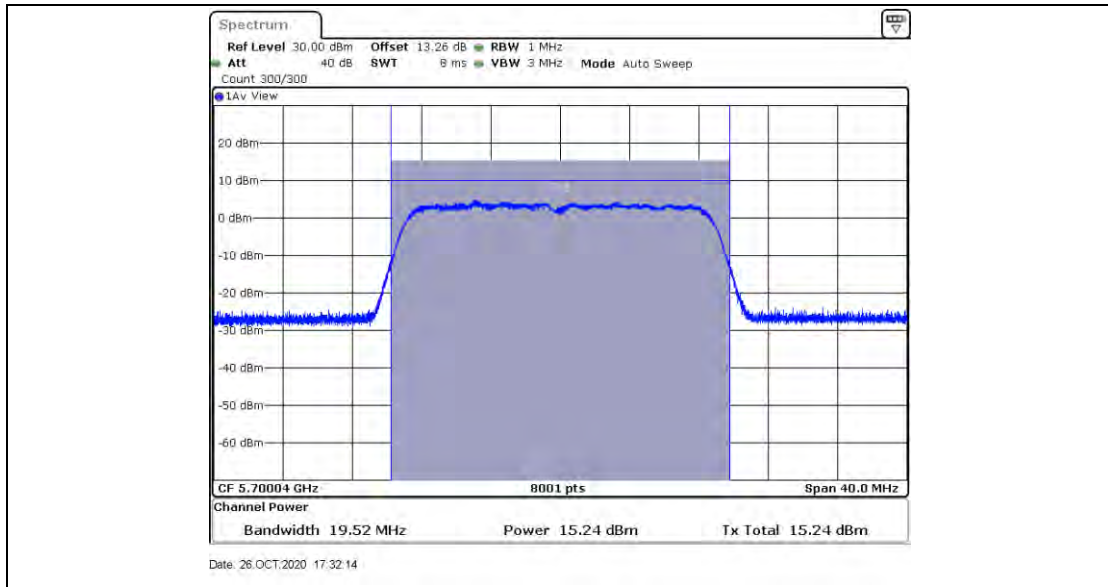
11N20SISO_Ant2_5580



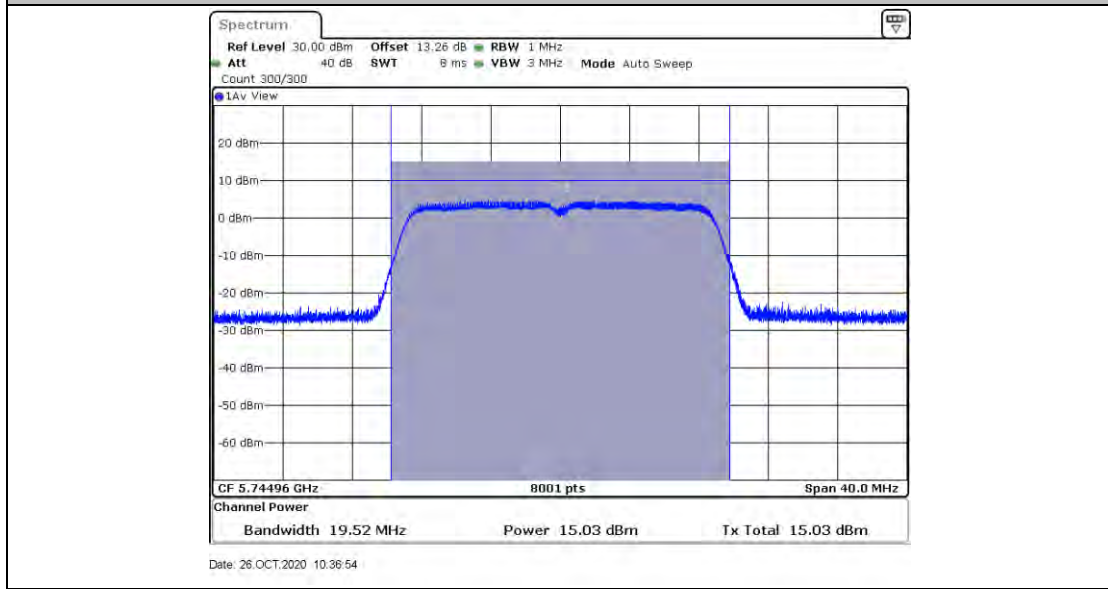
11N20SISO_Ant1_5700



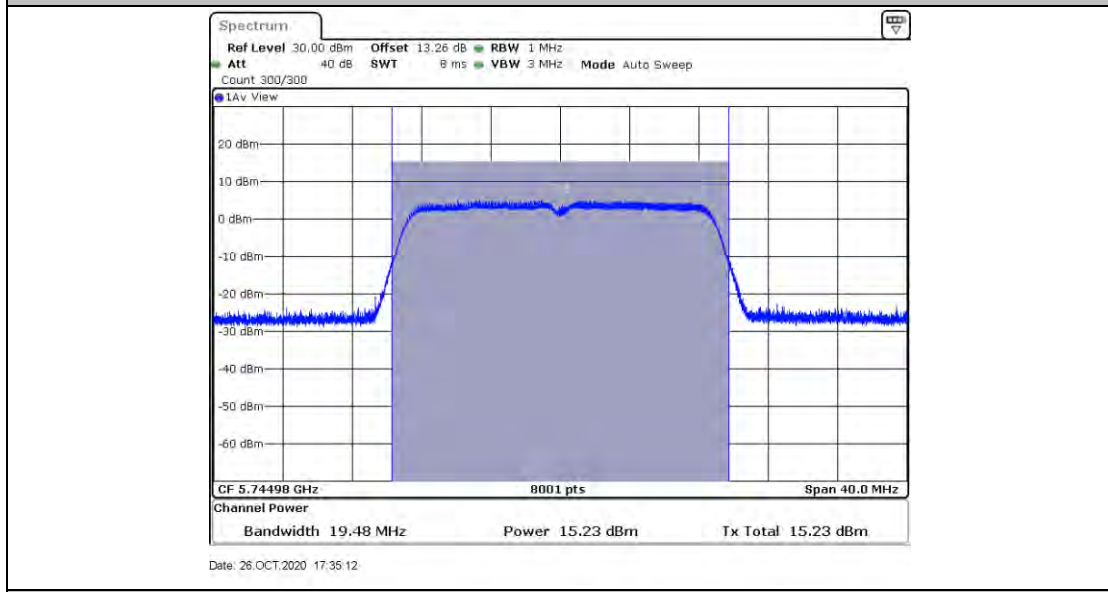
11N20SISO_Ant2_5700



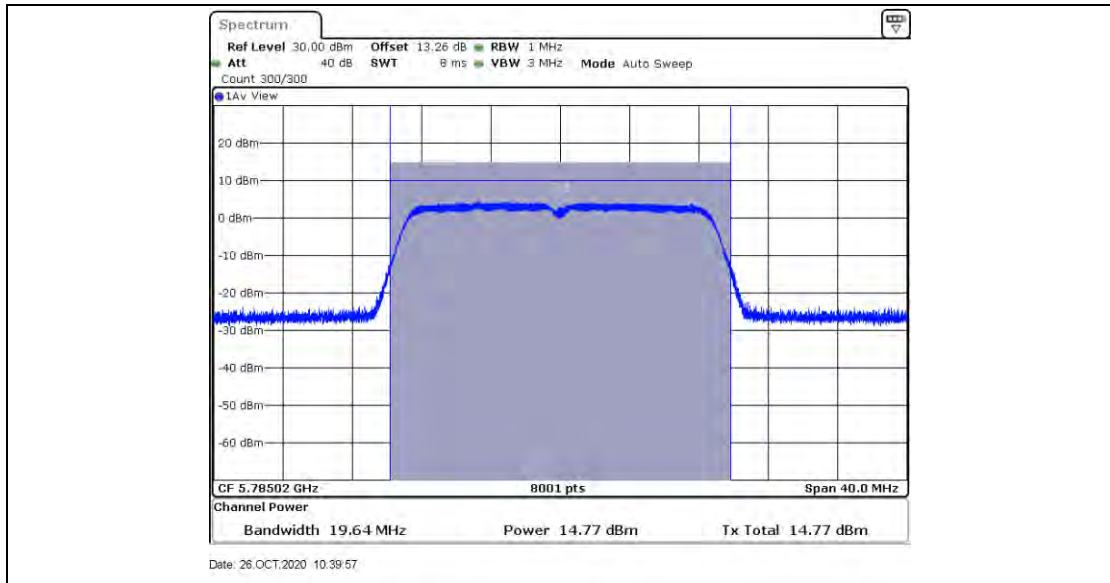
11N20SISO_Ant1_5745



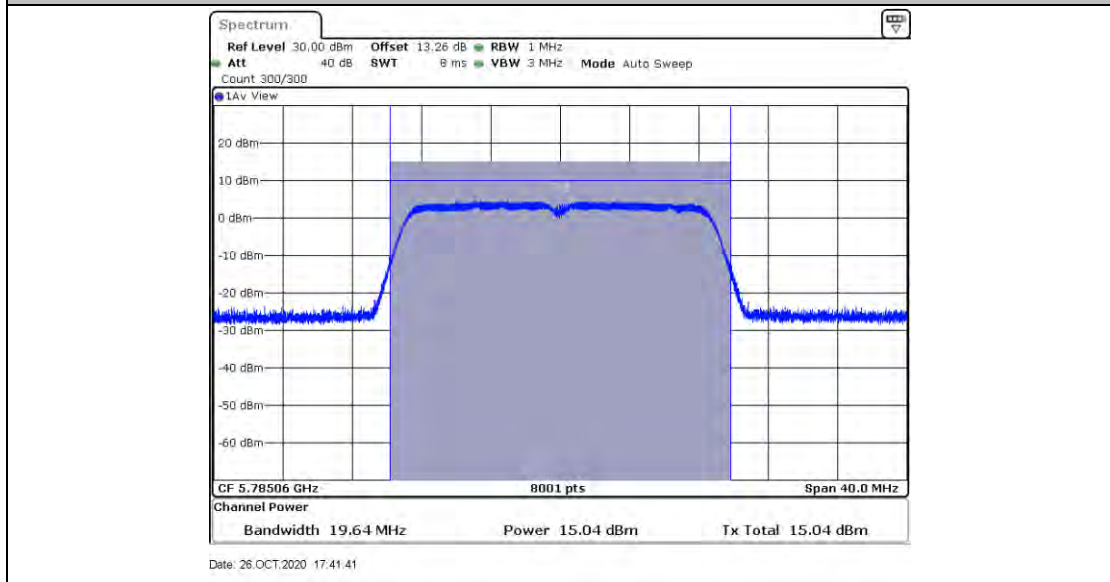
11N20SISO_Ant2_5745



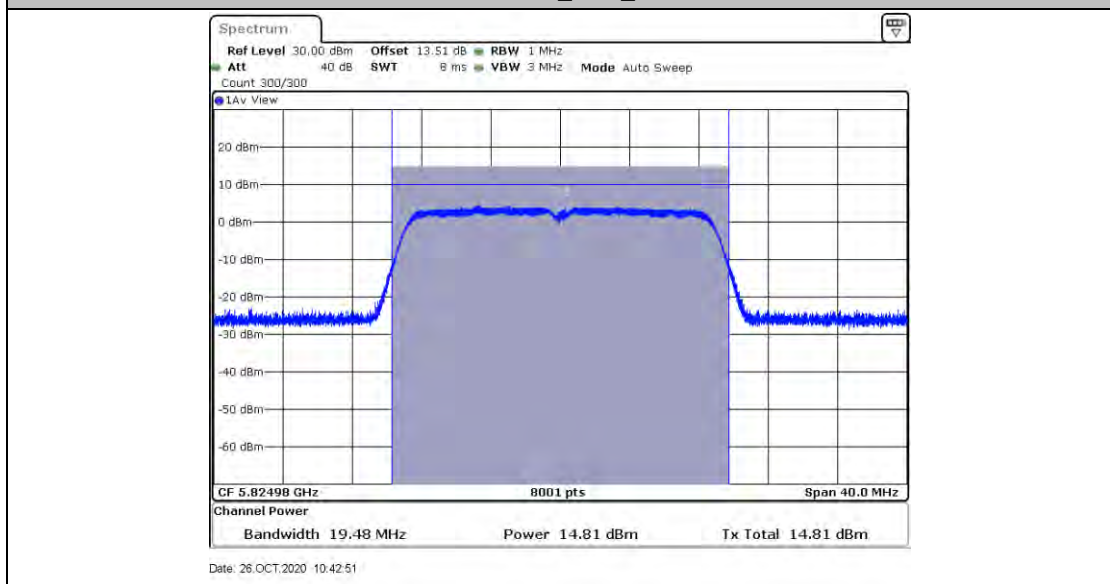
11N20SISO_Ant1_5785



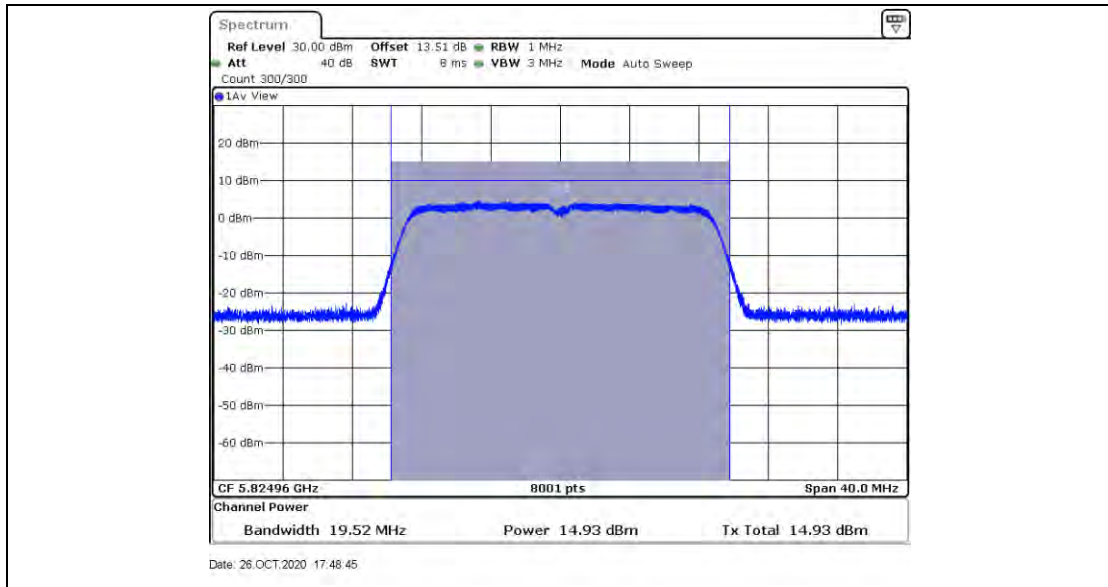
11N20SISO_Ant2_5785



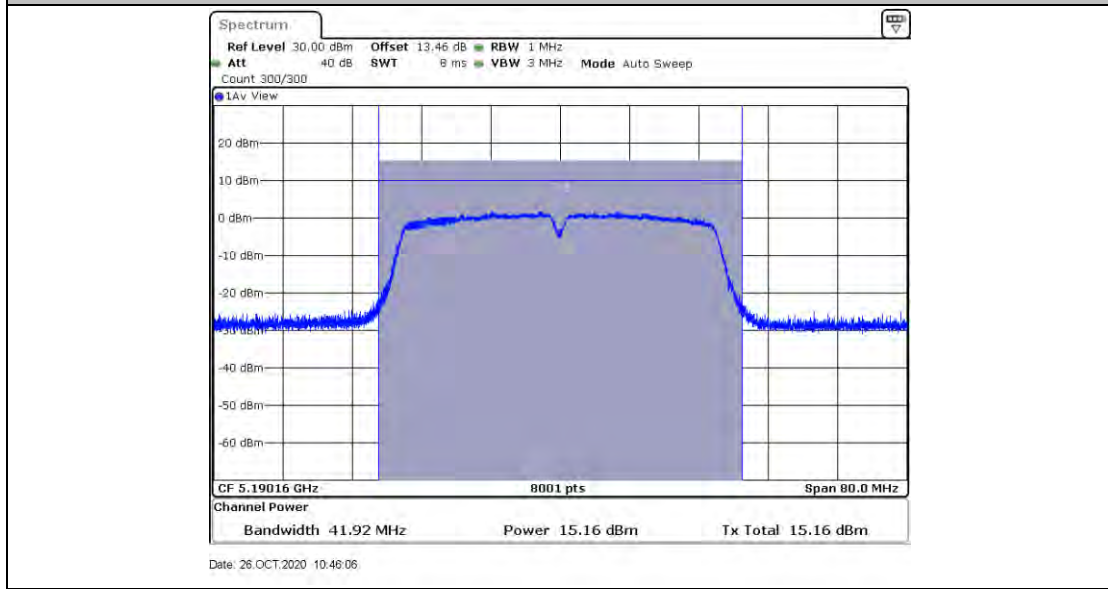
11N20SISO_Ant1_5825



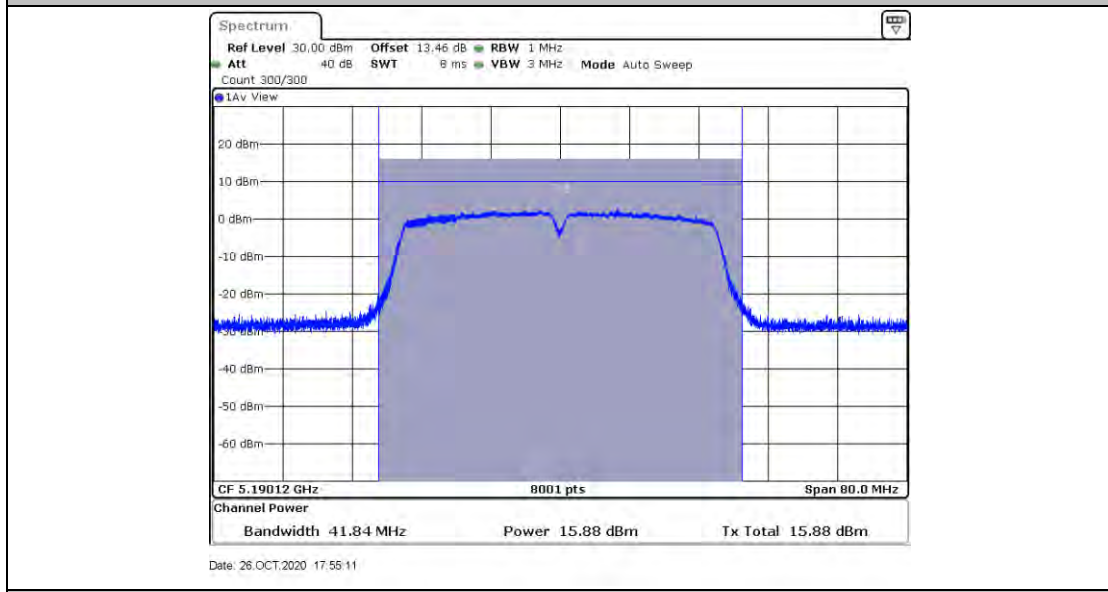
11N20SISO_Ant2_5825



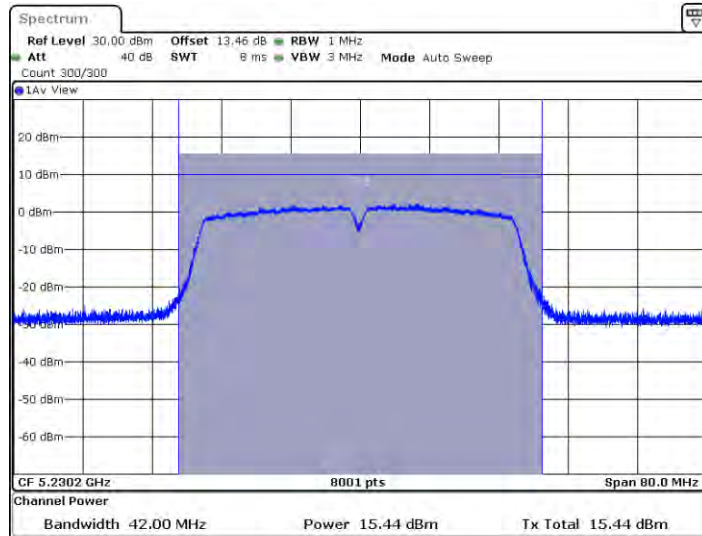
11N40SISO_Ant1_5190



11N40SISO_Ant2_5190

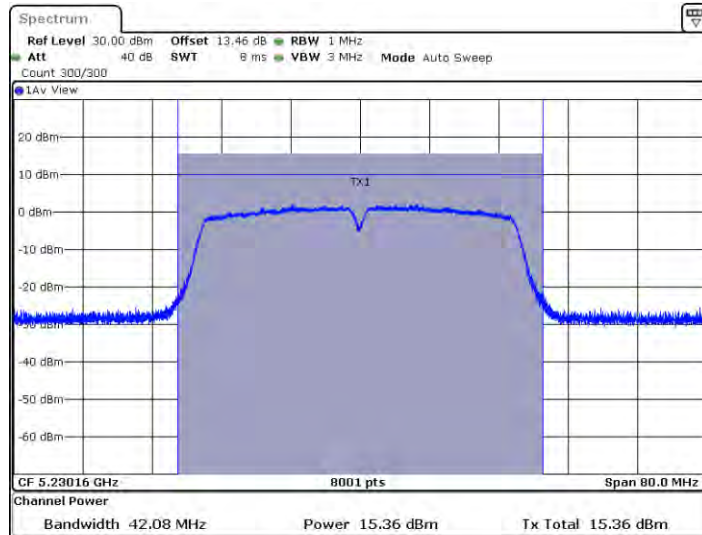


11N40SISO_Ant1_5230



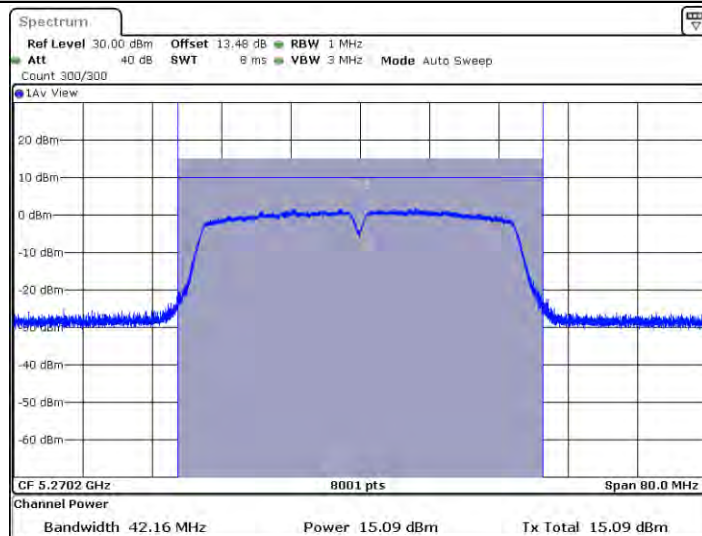
Date: 26 OCT.2020 10:48:57

11N40SISO_Ant2_5230



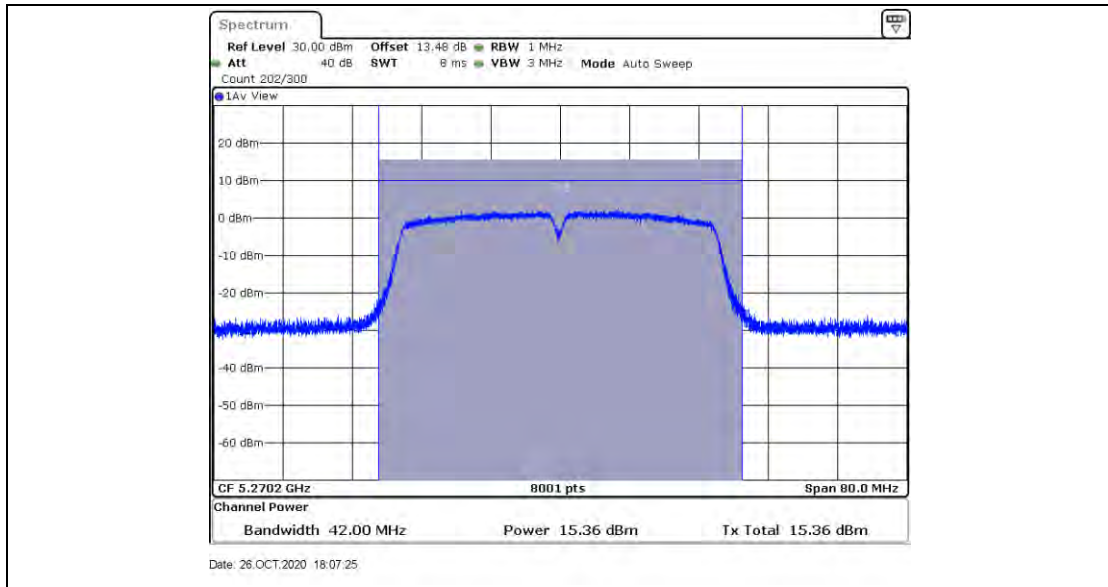
Date: 26 OCT.2020 18:01:16

11N40SISO_Ant1_5270

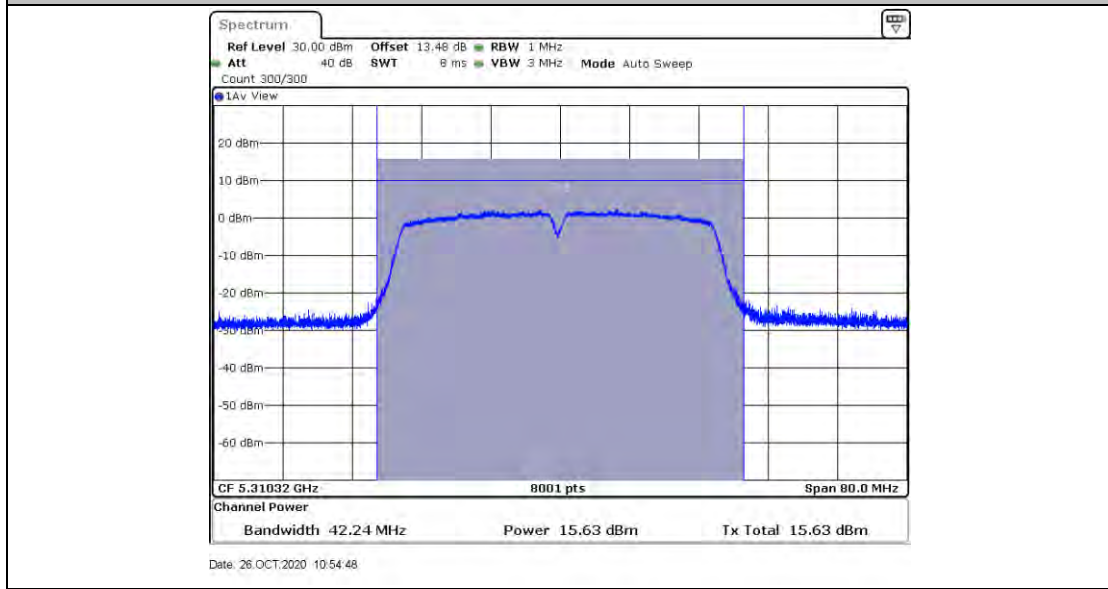


Date: 26 OCT.2020 10:52:09

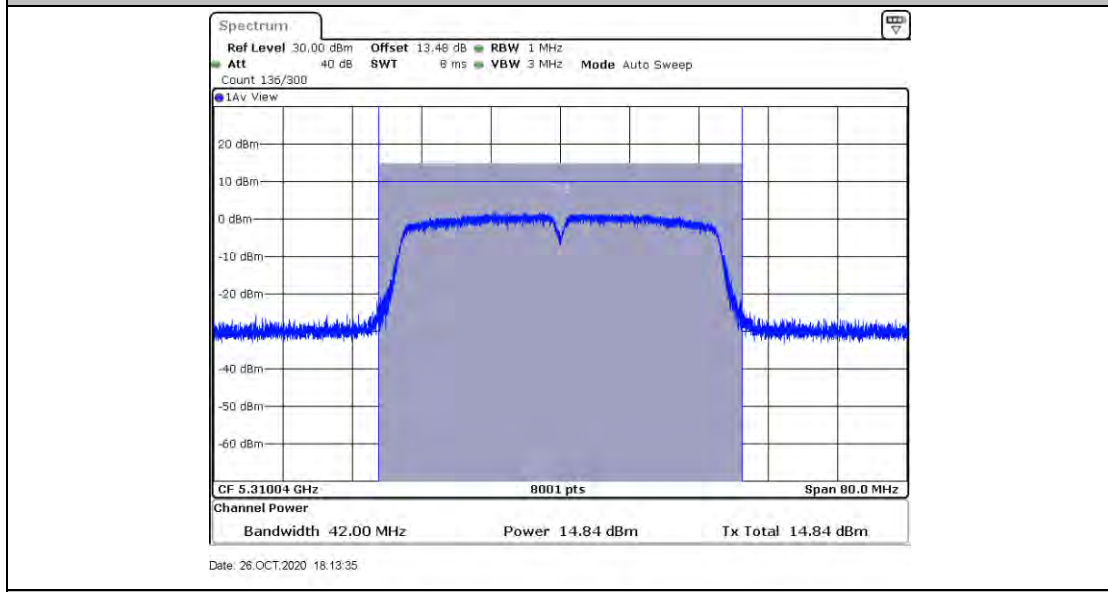
11N40SISO_Ant2_5270



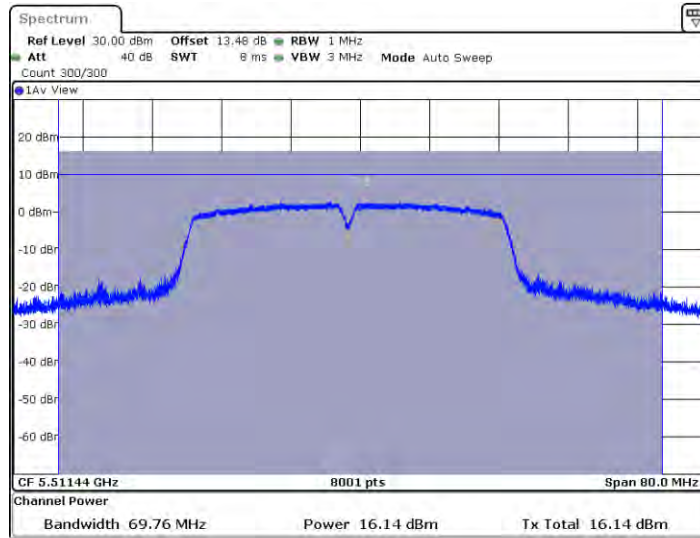
11N40SISO_Ant1_5310



11N40SISO_Ant2_5310

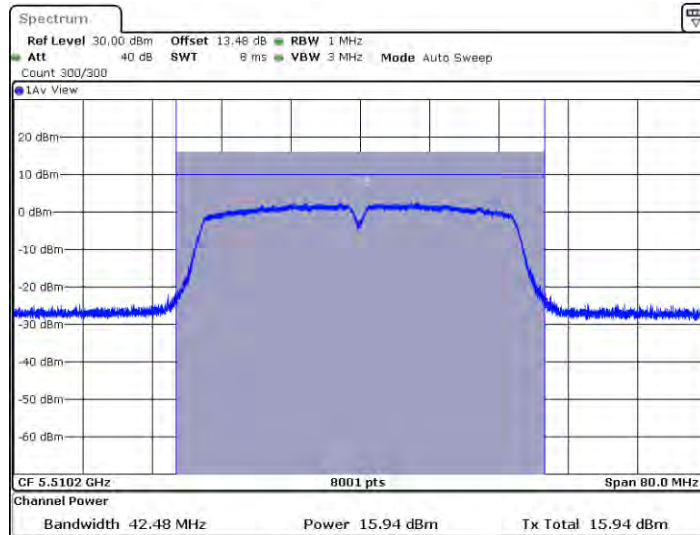


11N40SISO_Ant1_5510



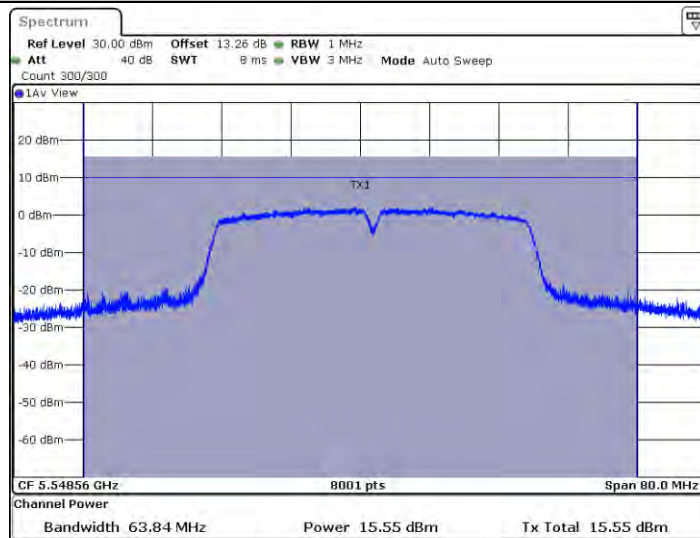
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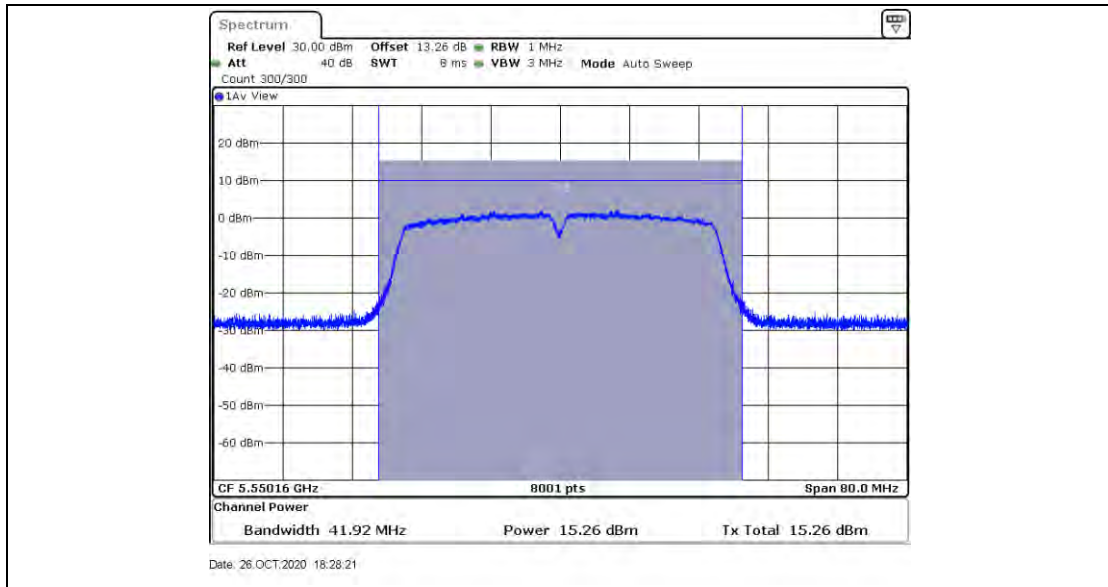
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11N40SISO_Ant1_5550

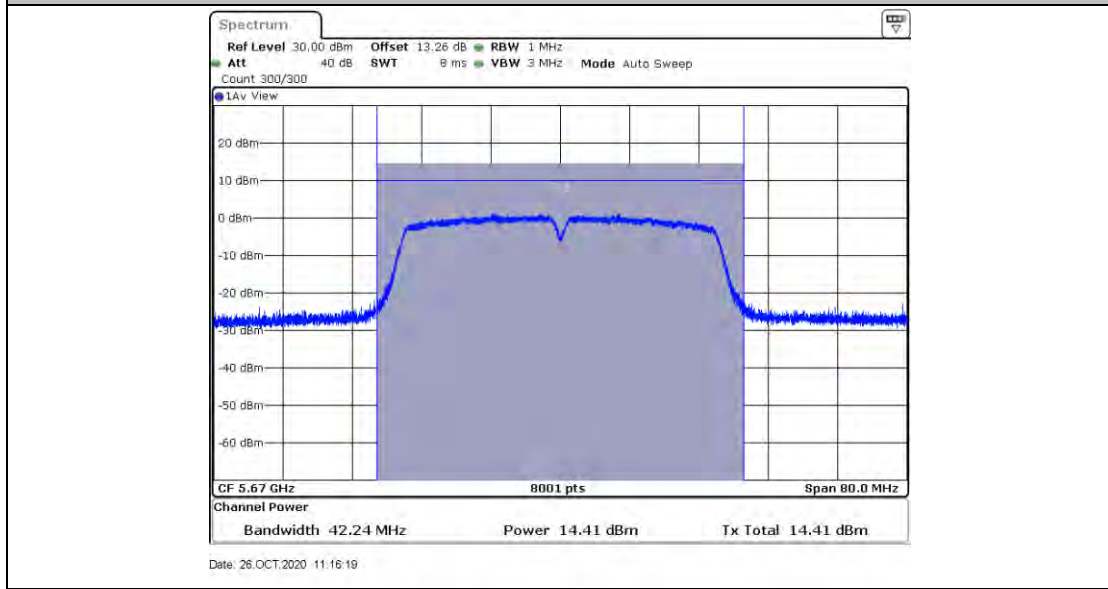


Date: 26 OCT.2020 11:09:04

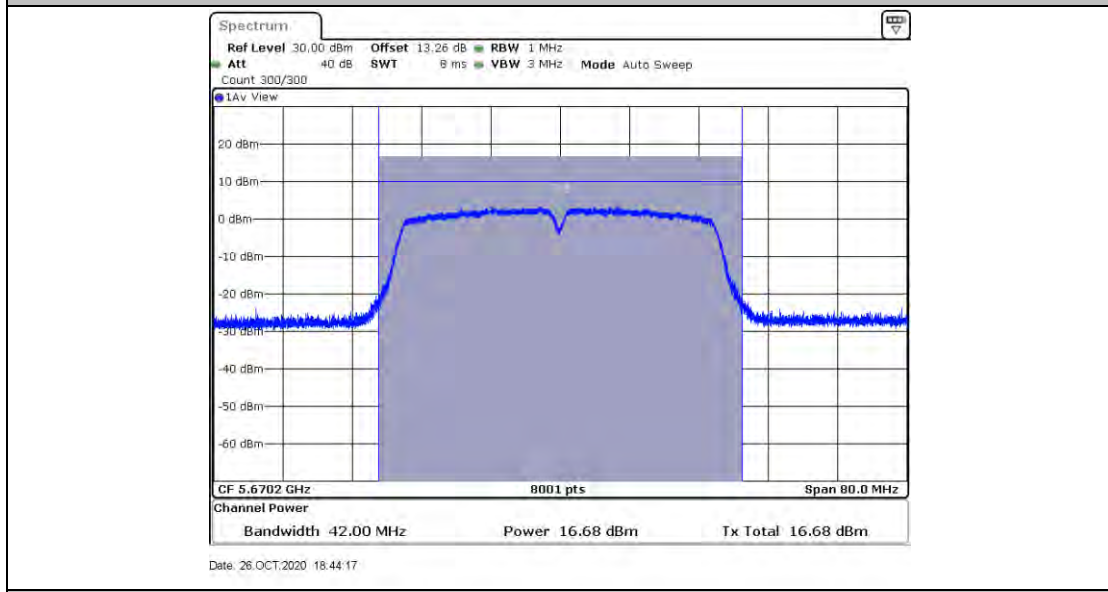
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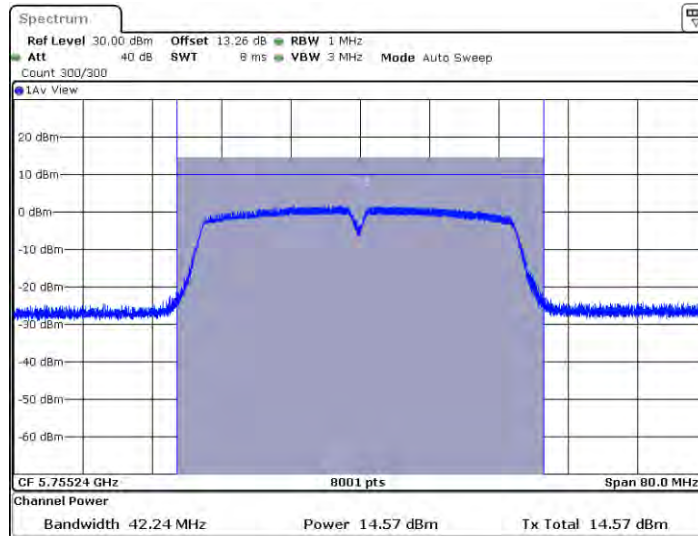
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11N40SISO_Ant2_5670

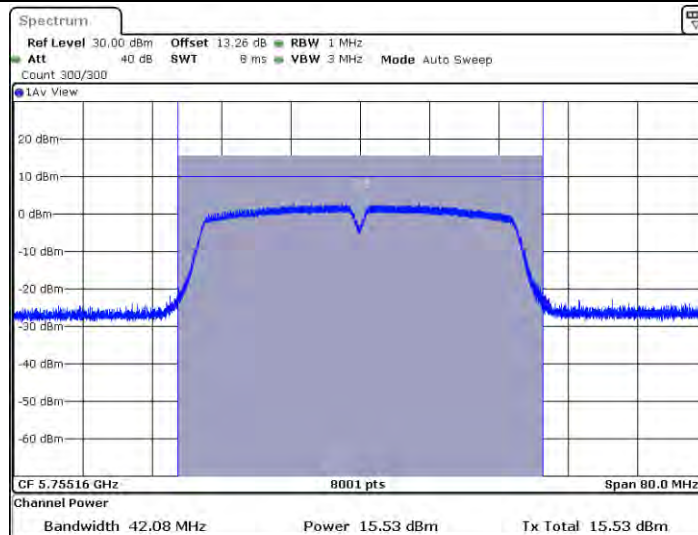


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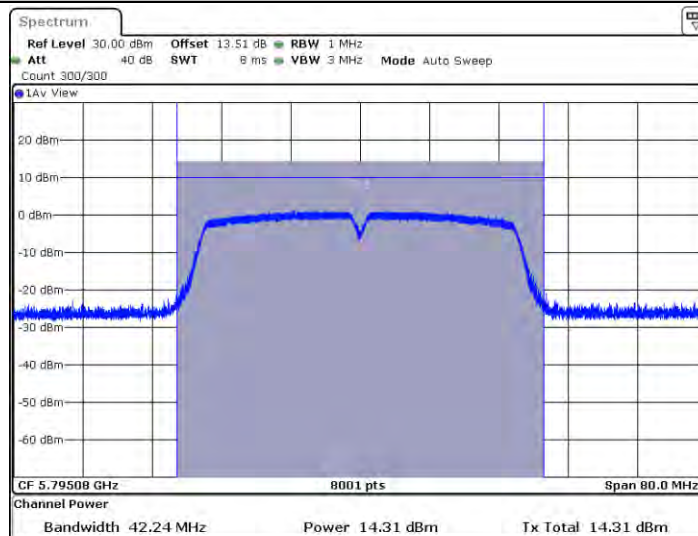
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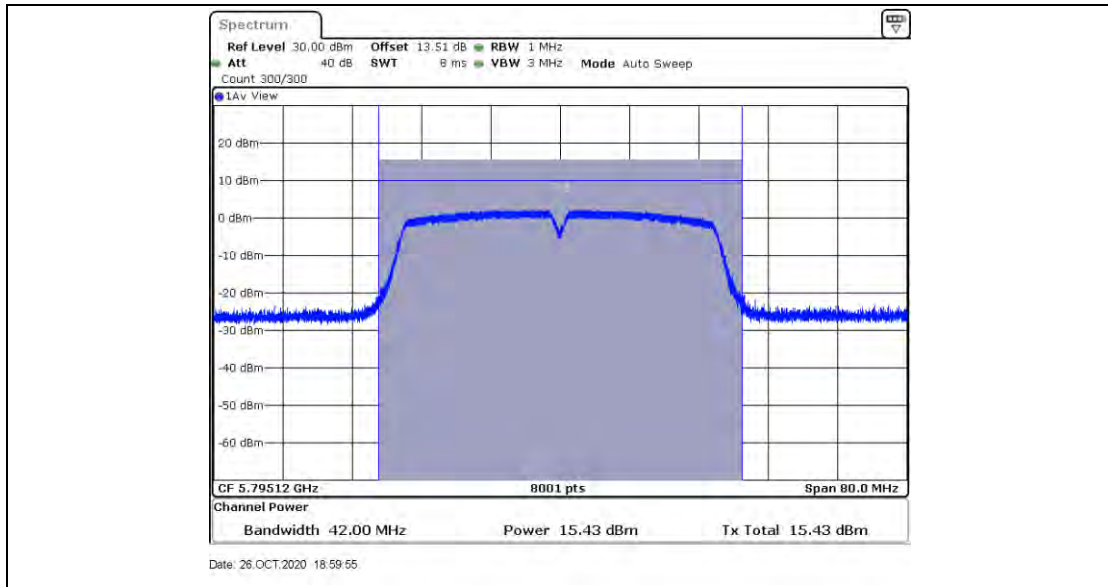
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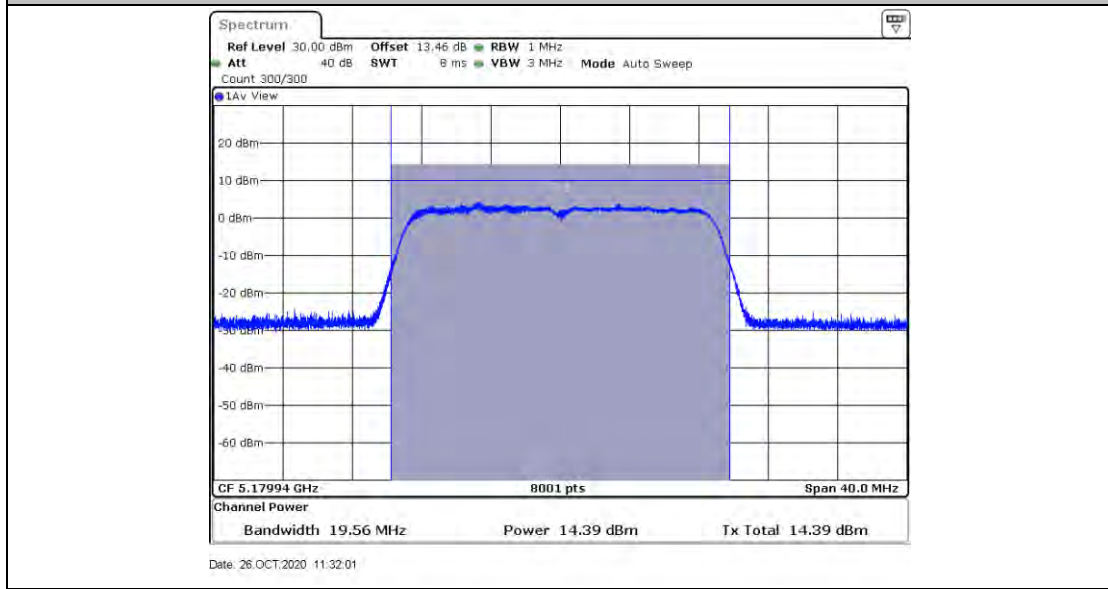


Date: 26 OCT.2020 11:22:49

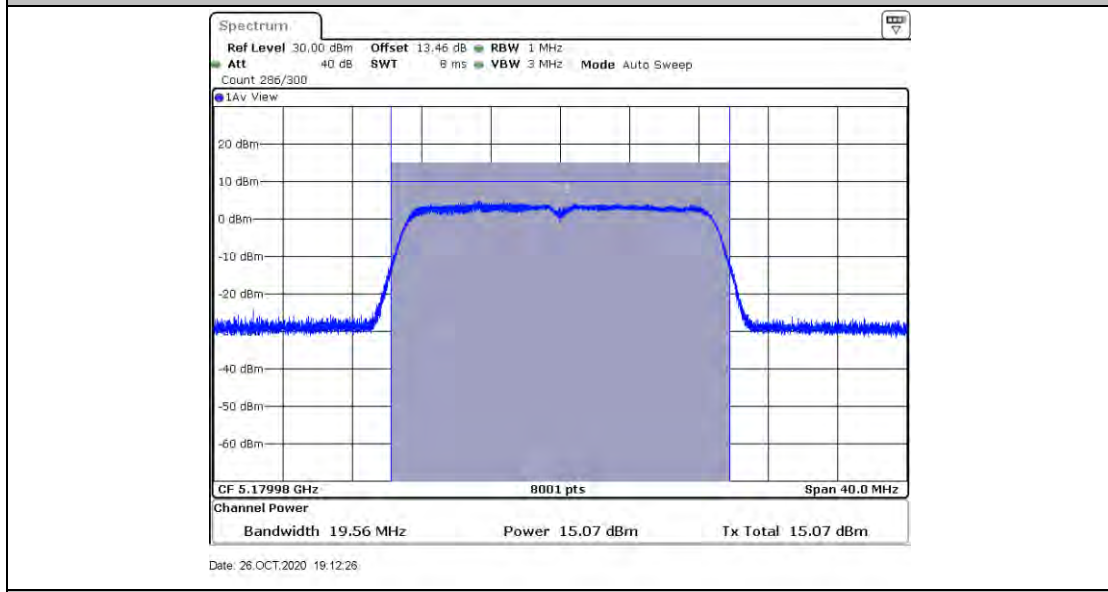
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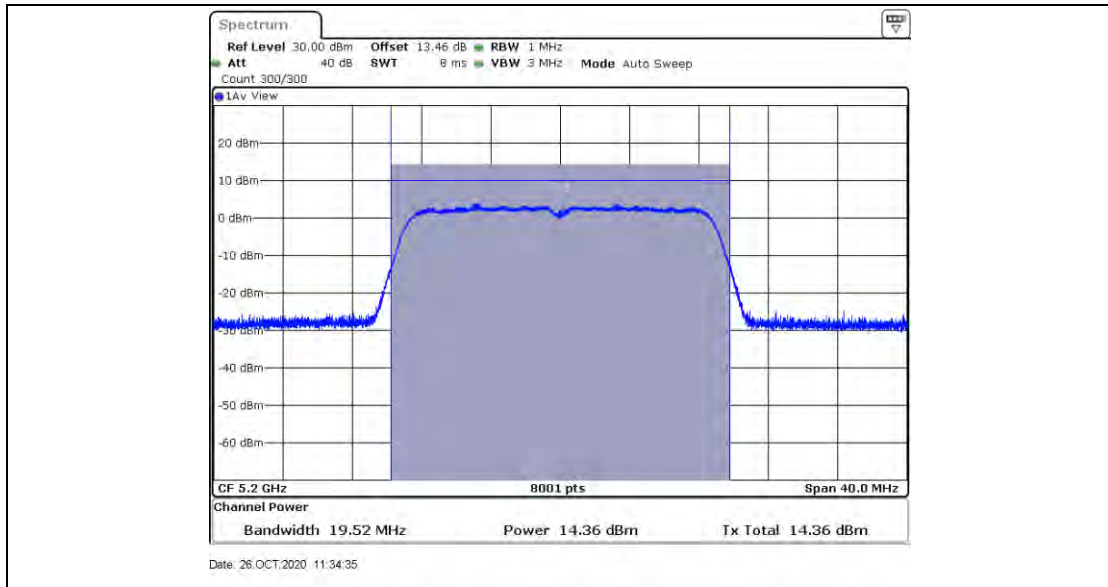
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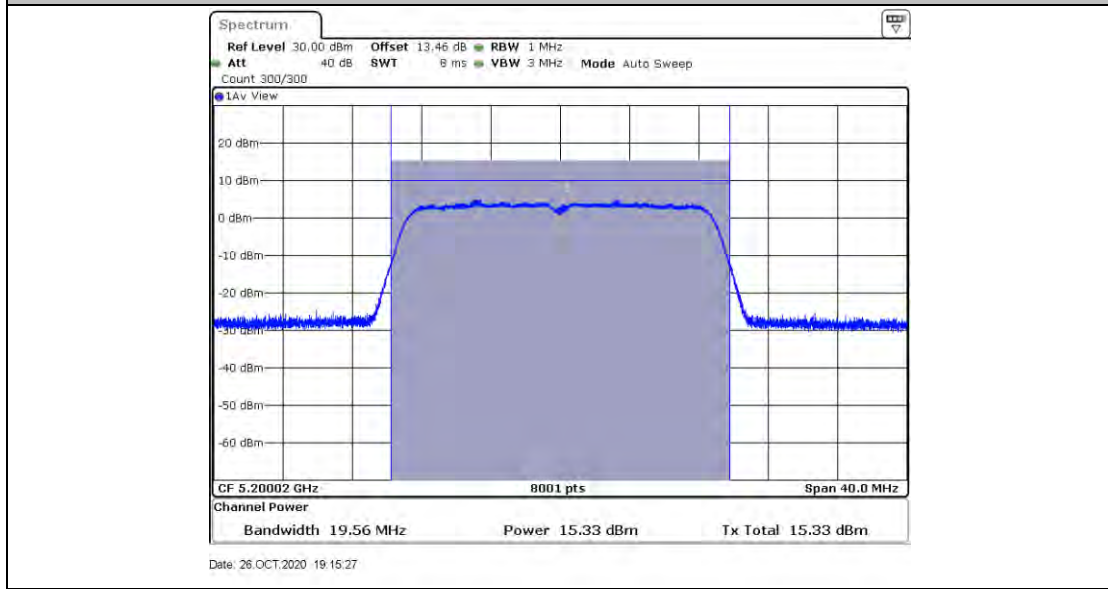
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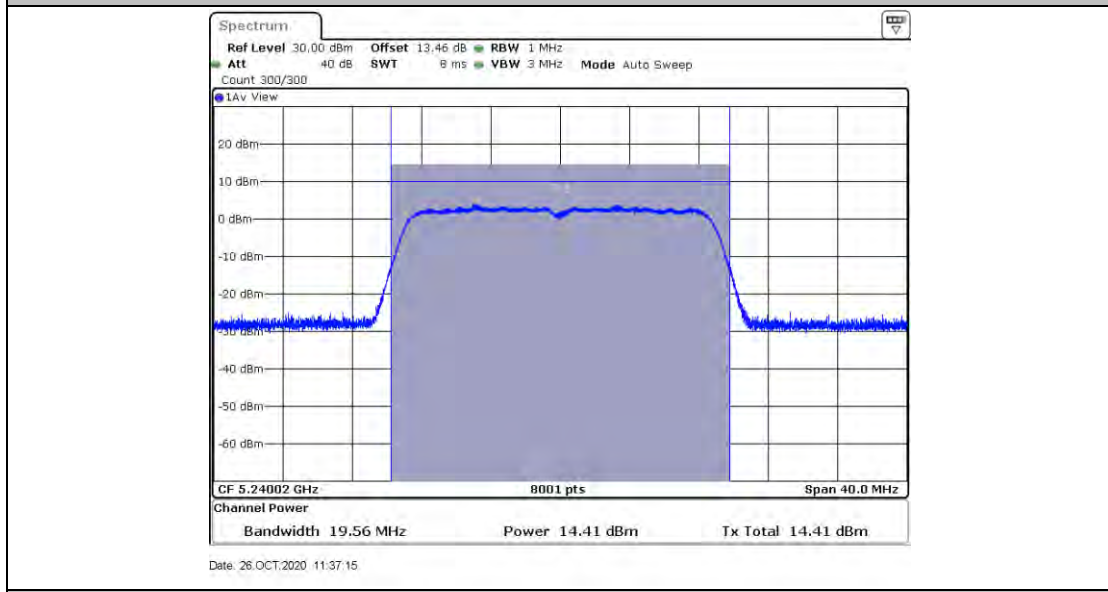
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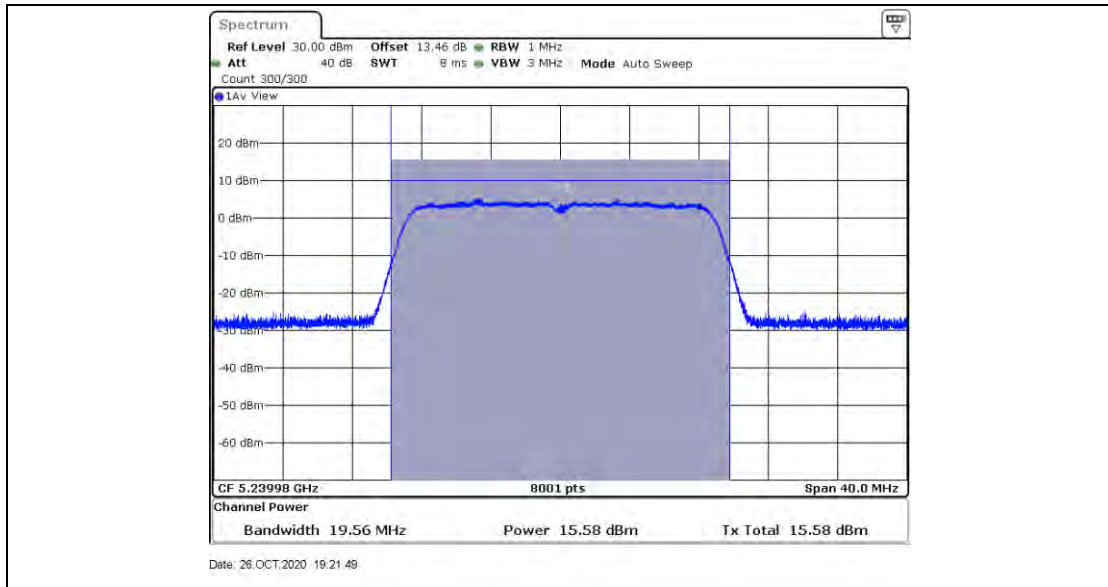
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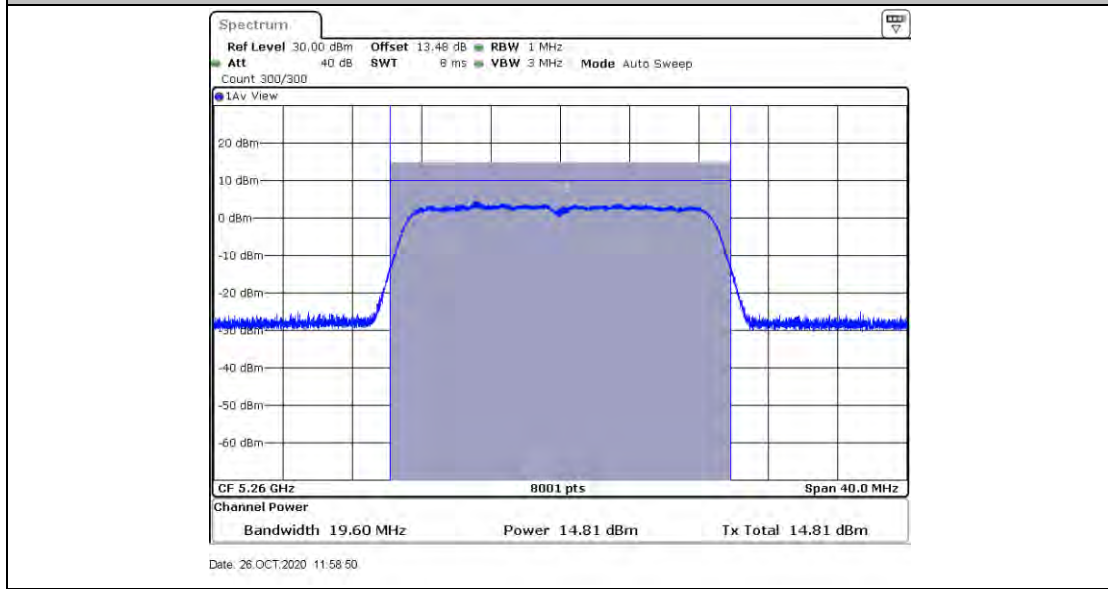
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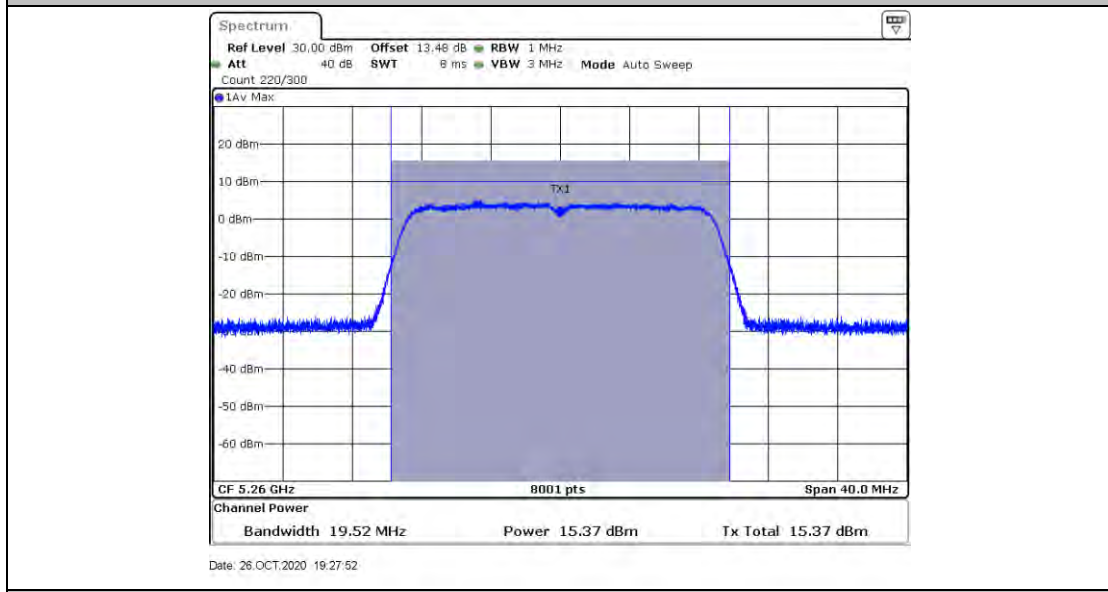
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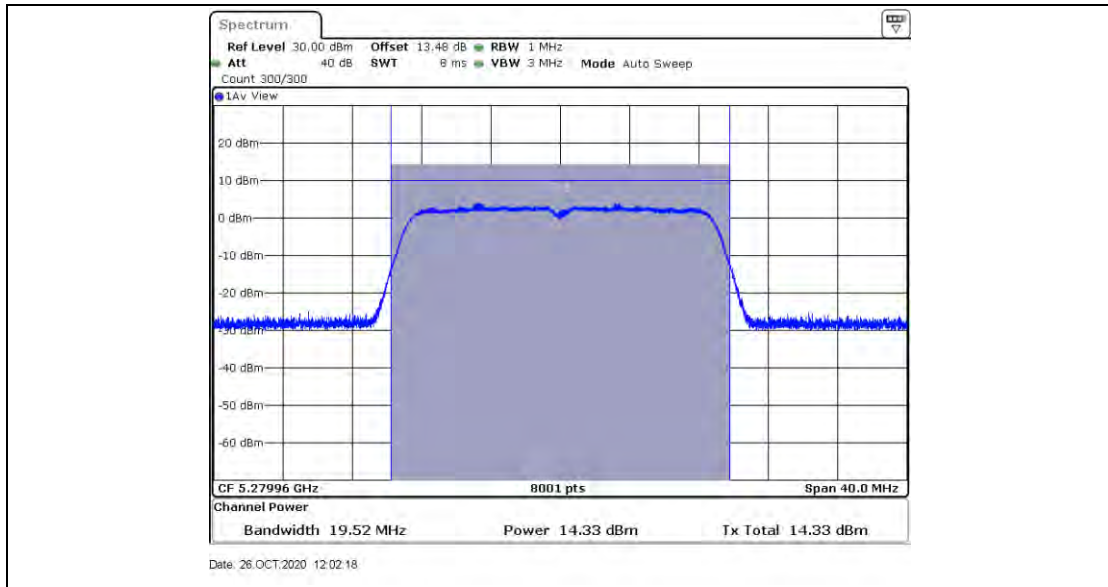
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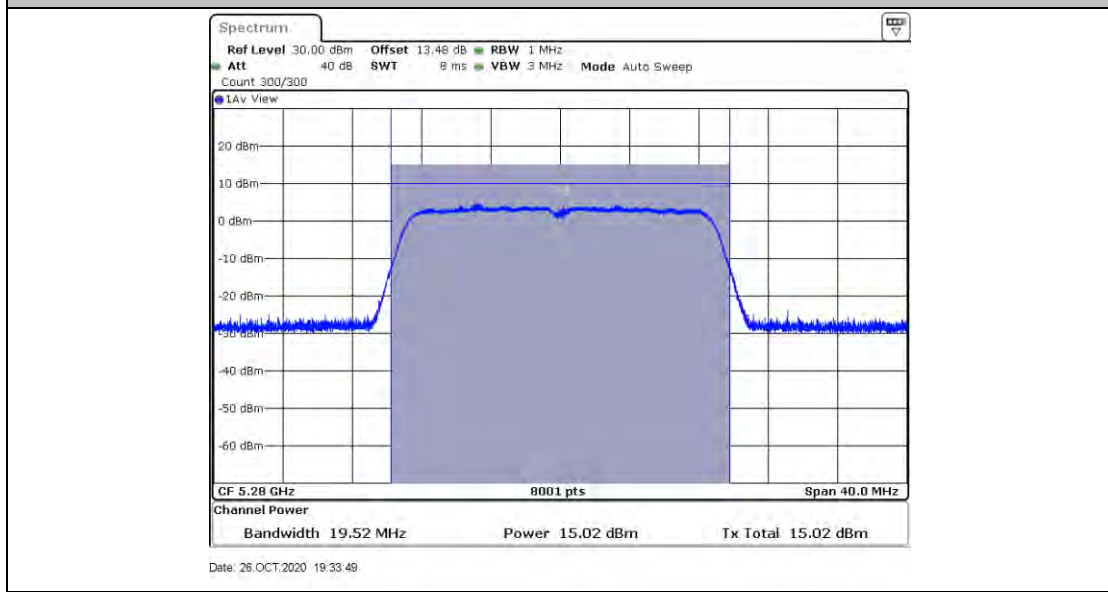
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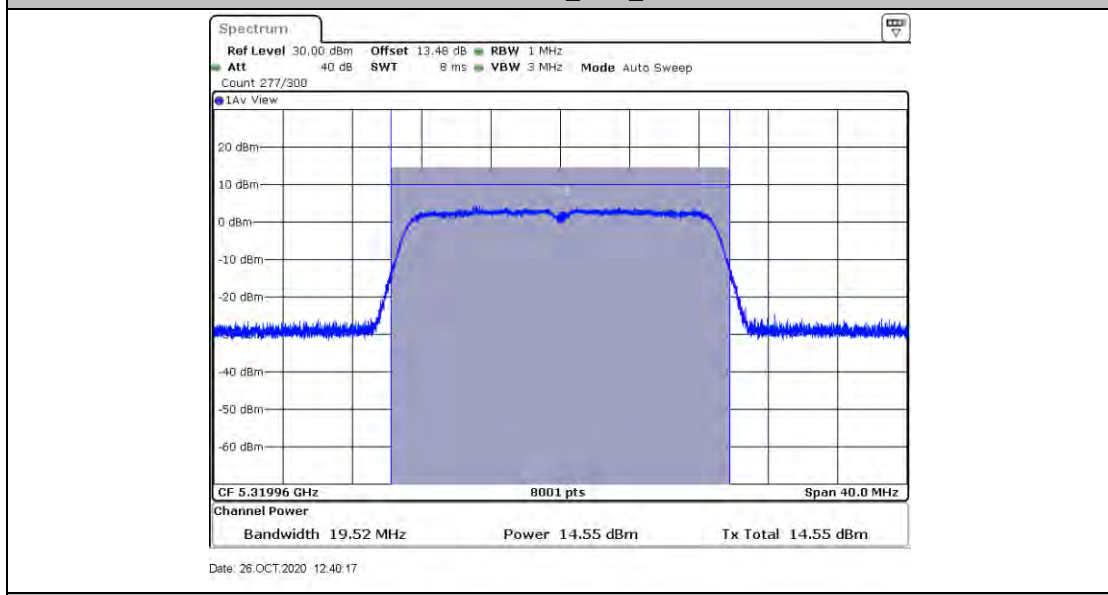
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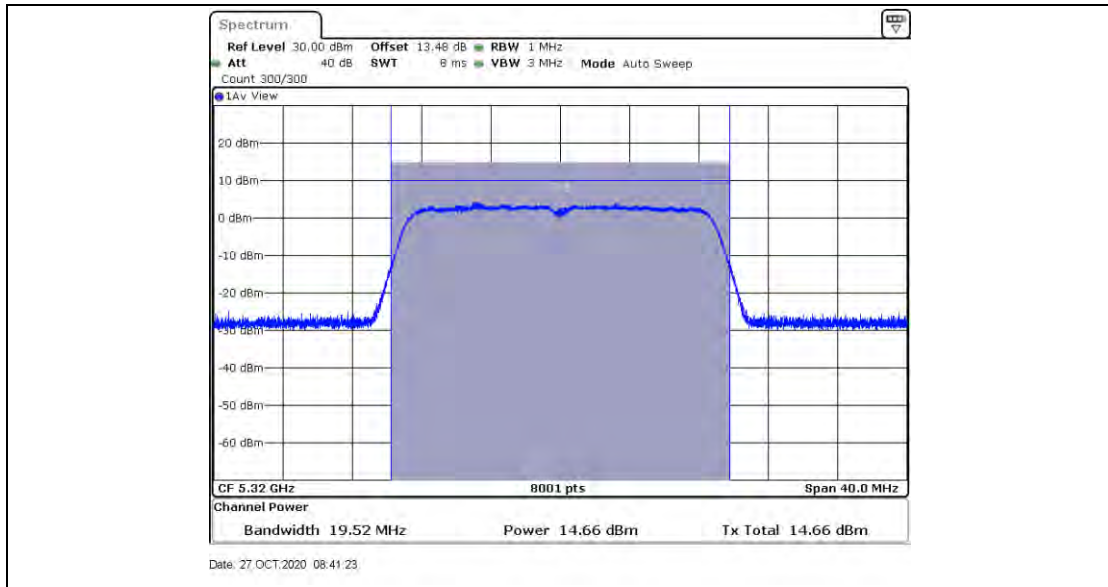
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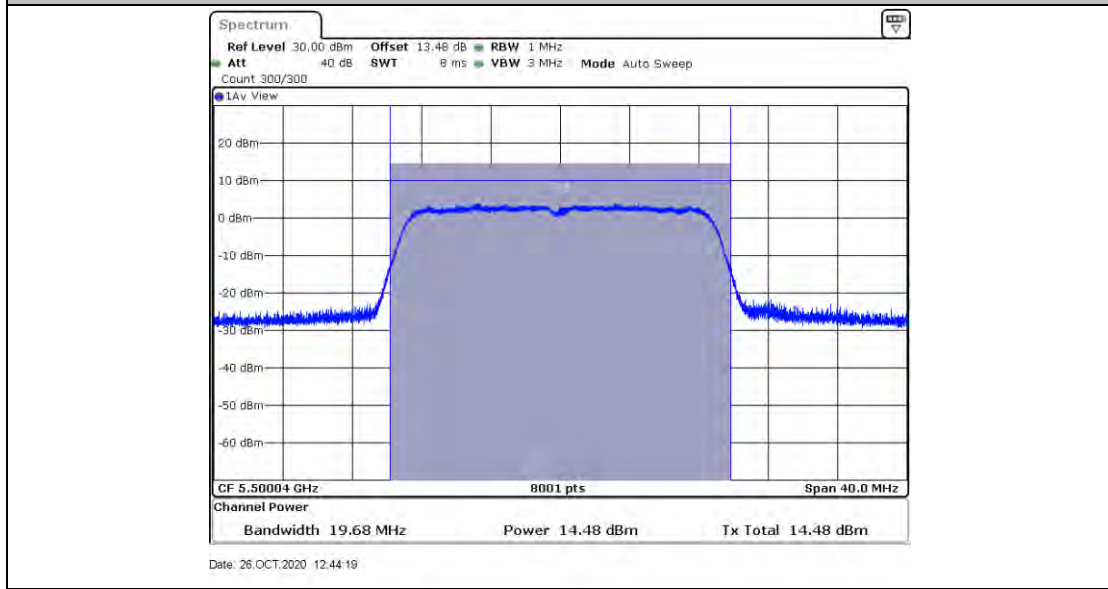
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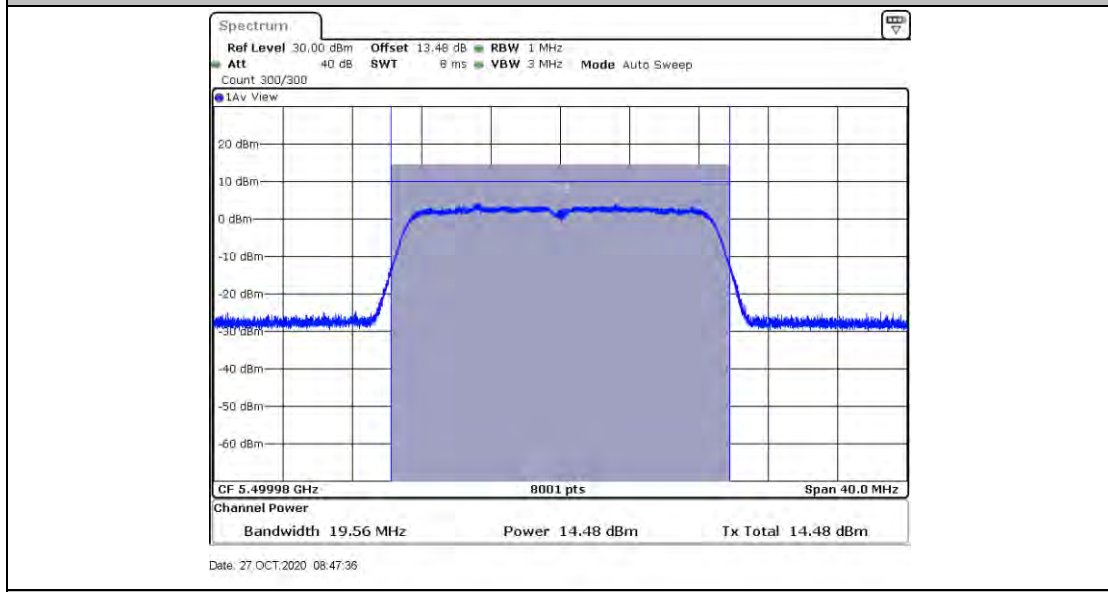
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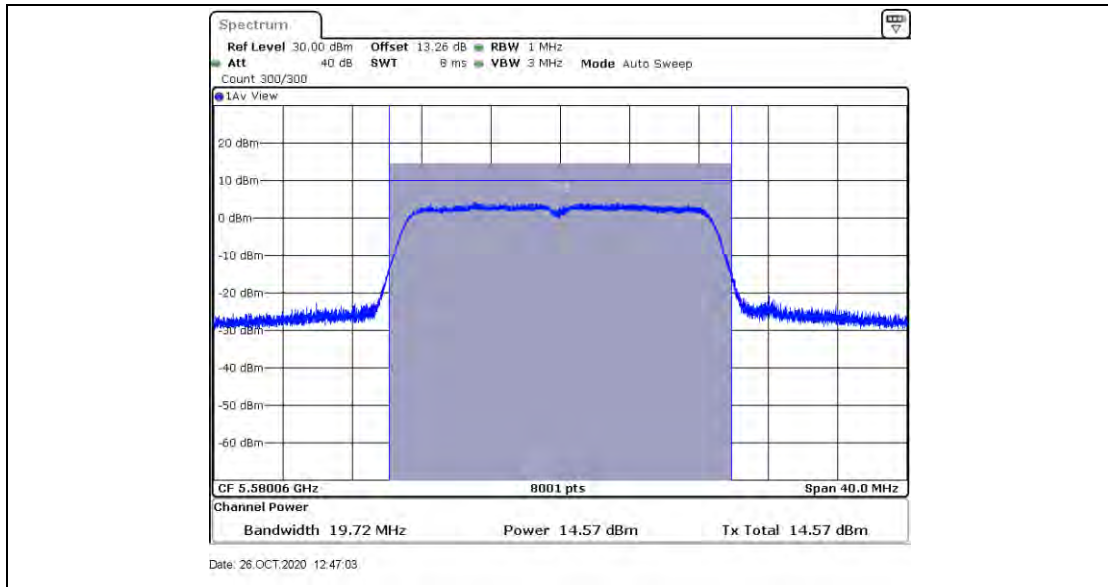
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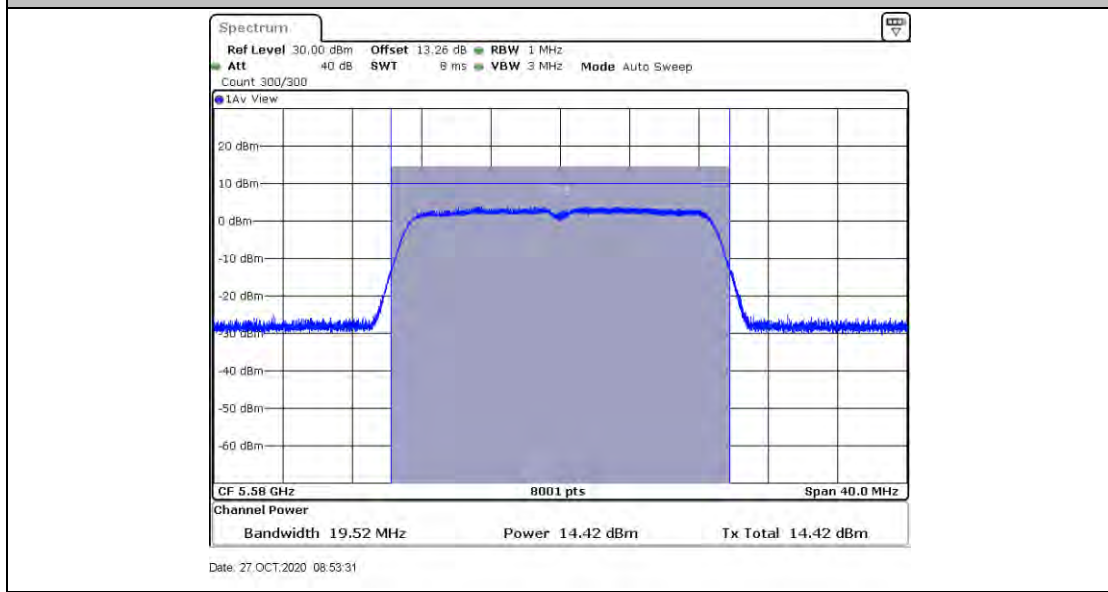
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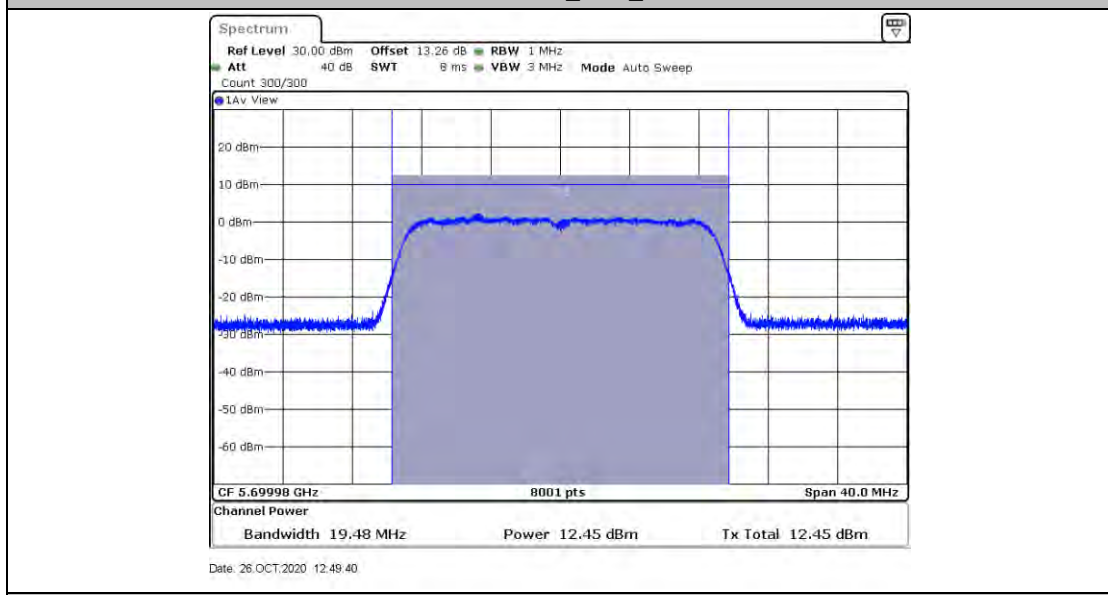
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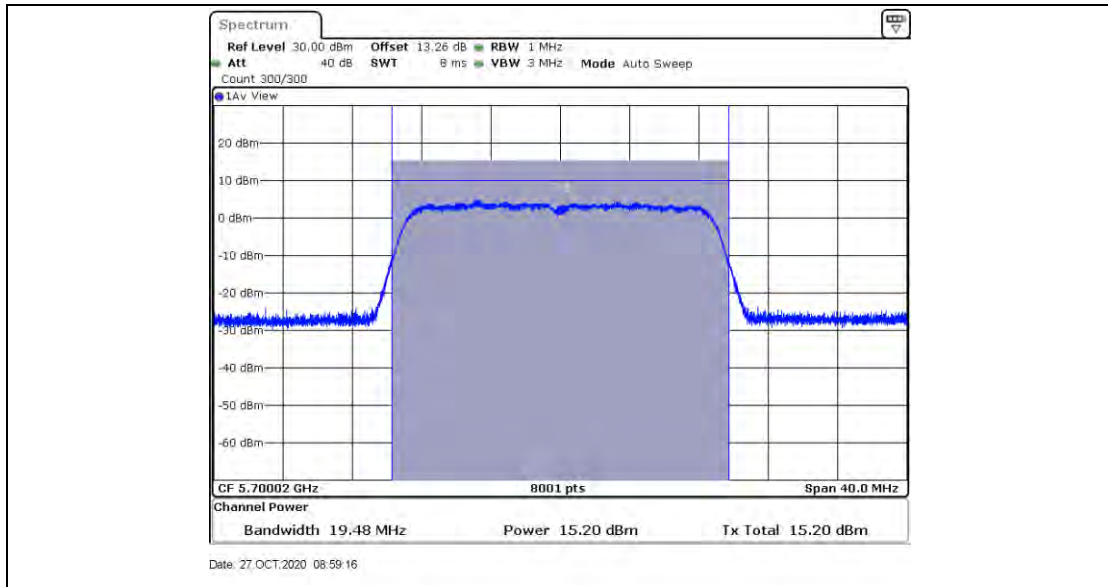
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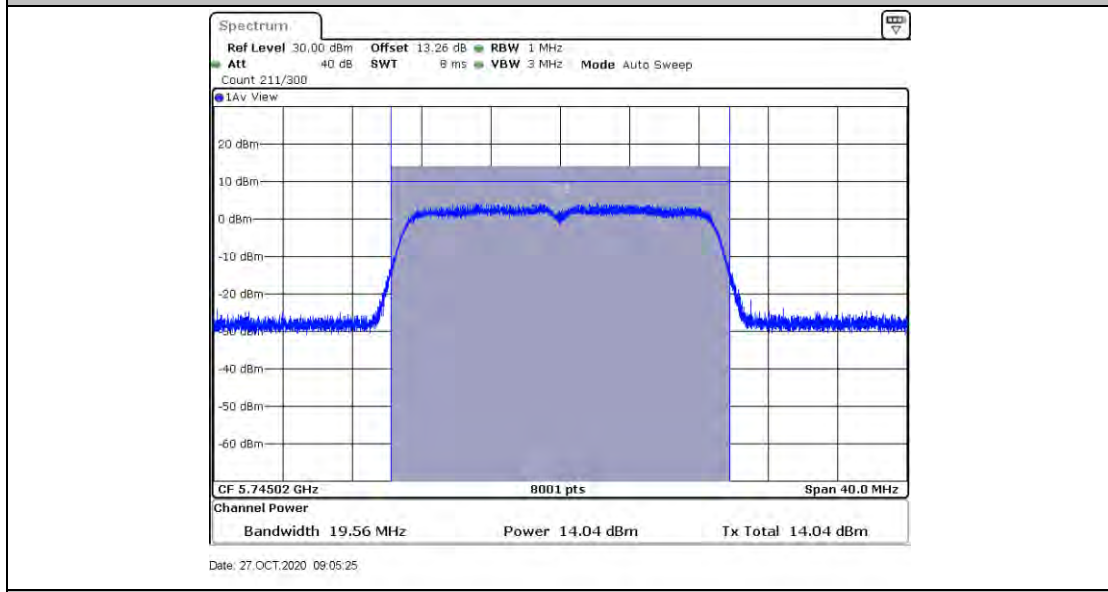
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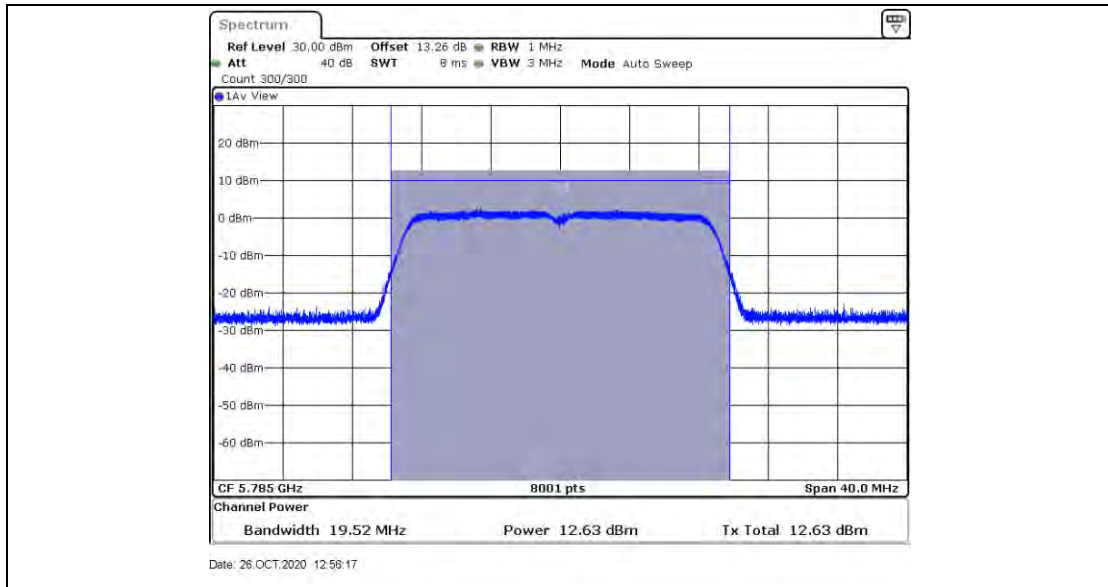
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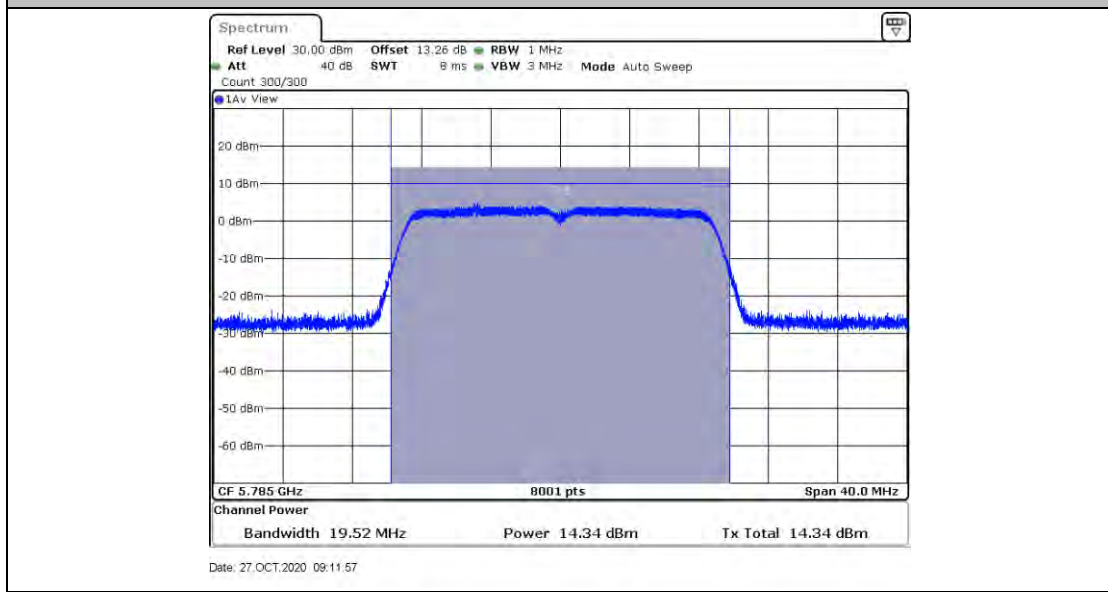
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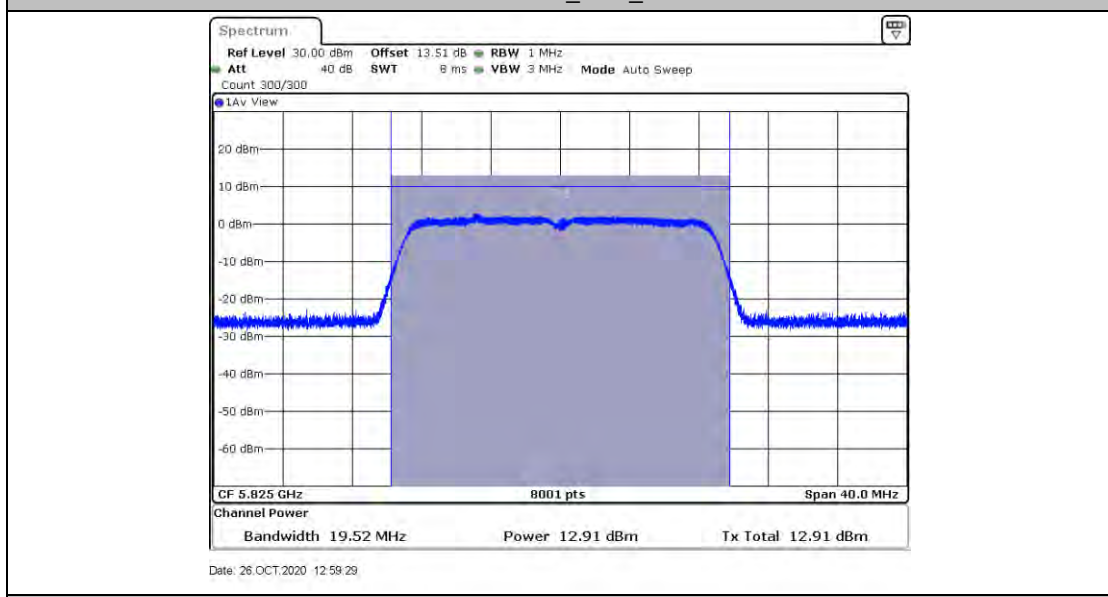
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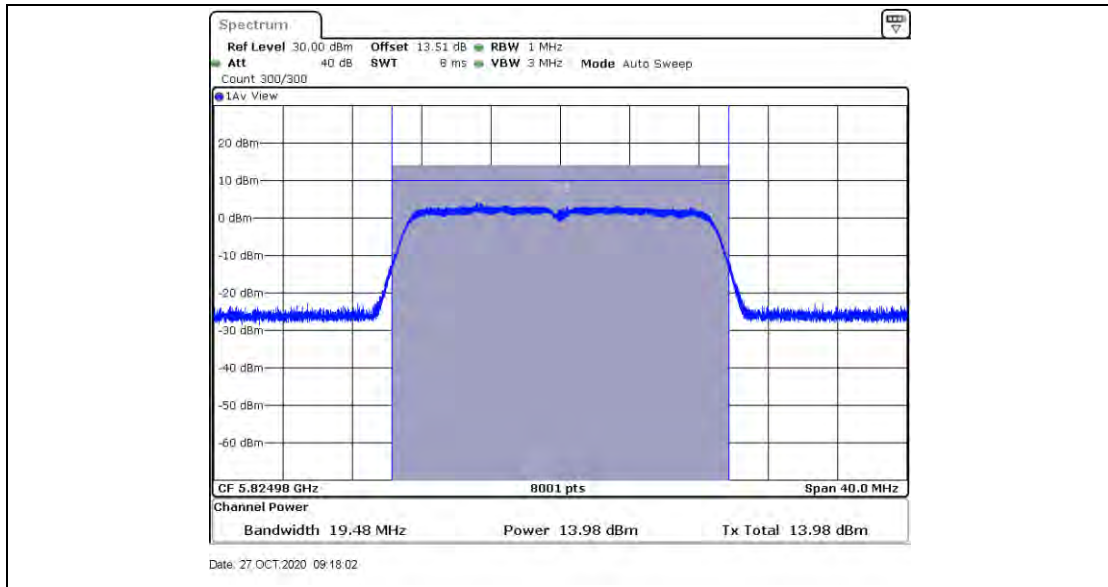
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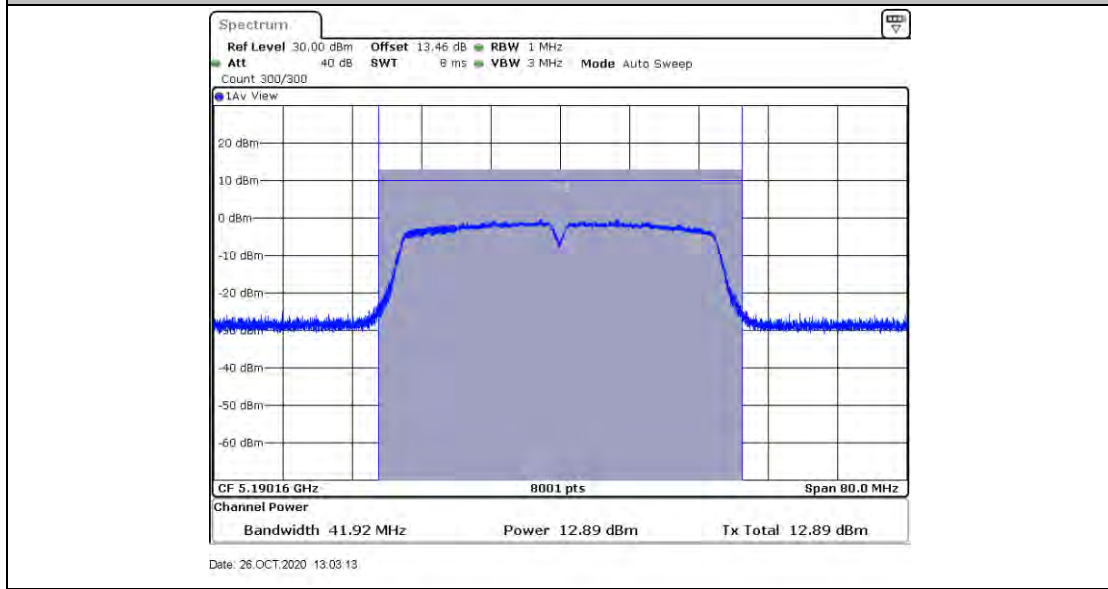
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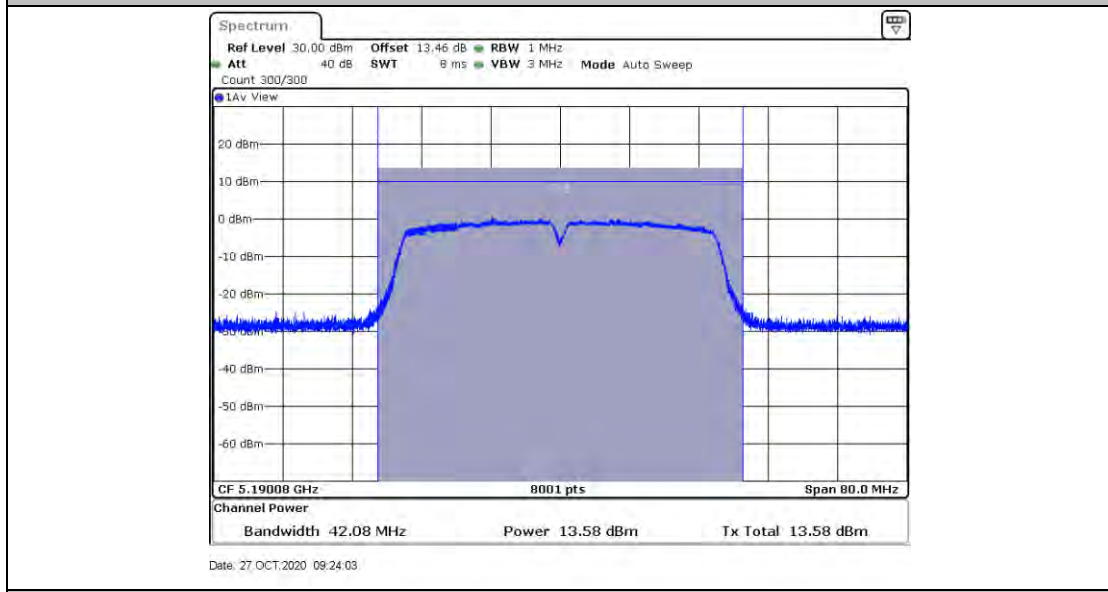
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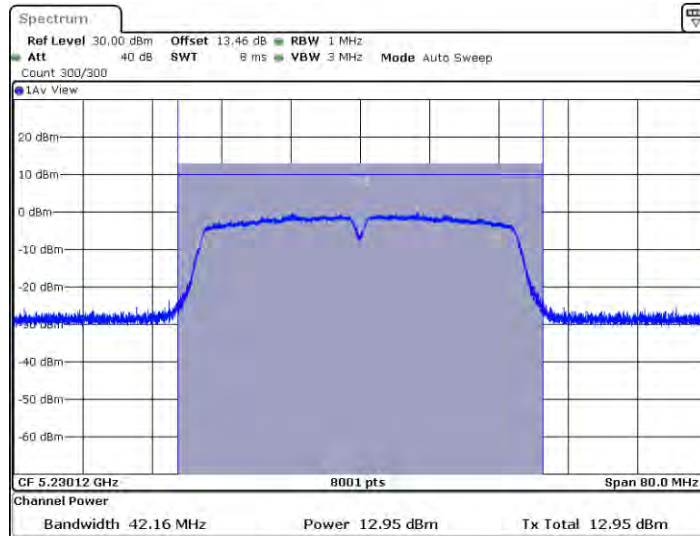
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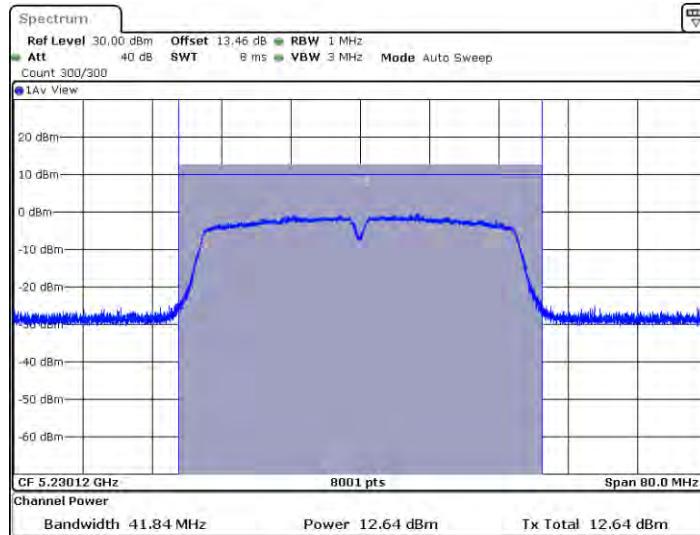


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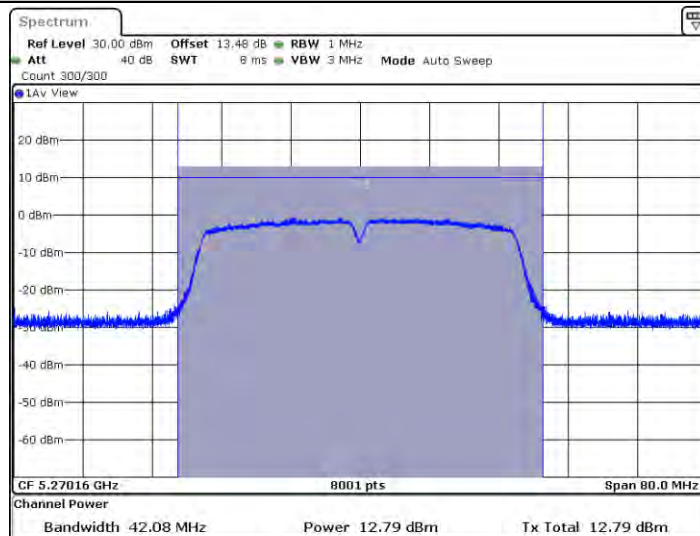
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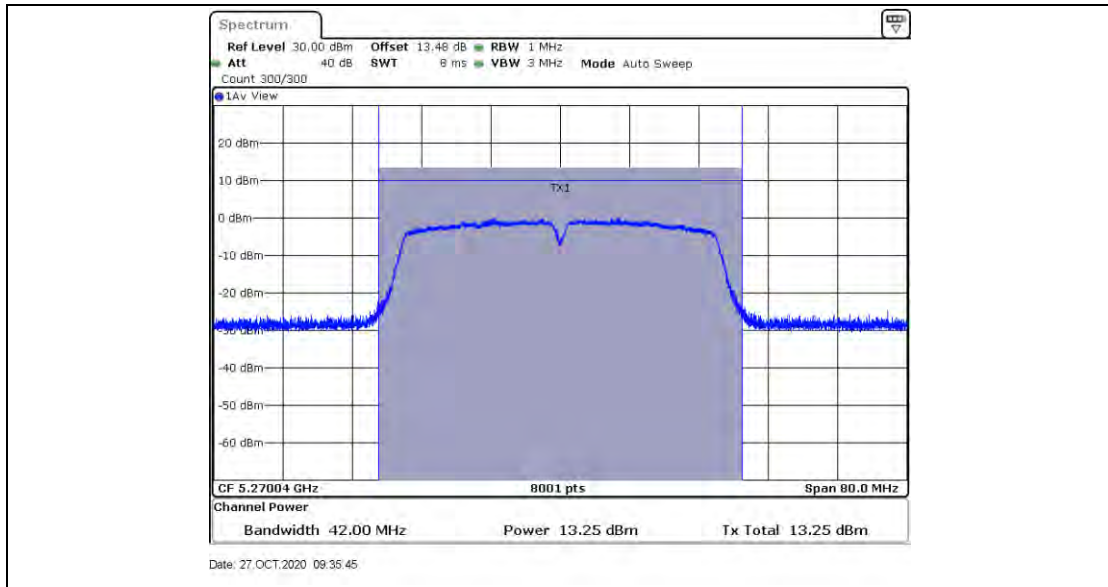
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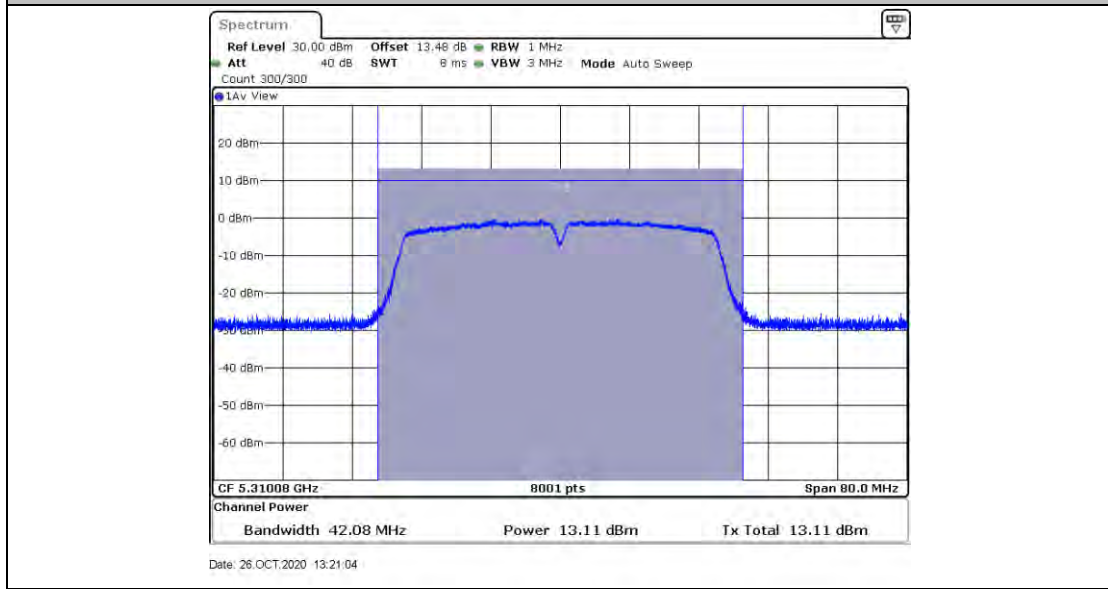


Date: 26 OCT.2020 13:15:06

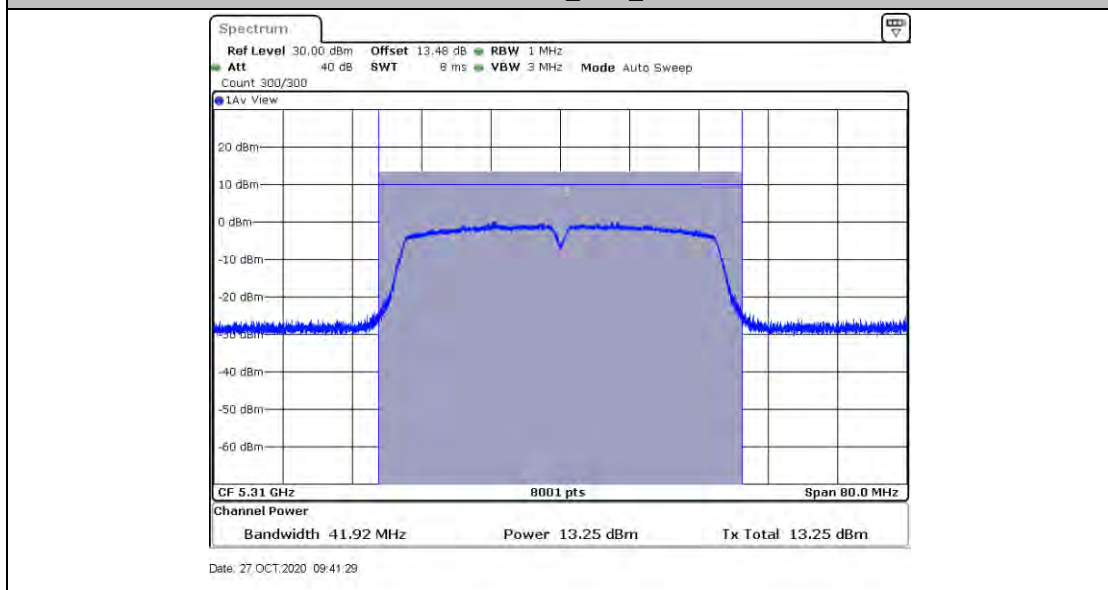
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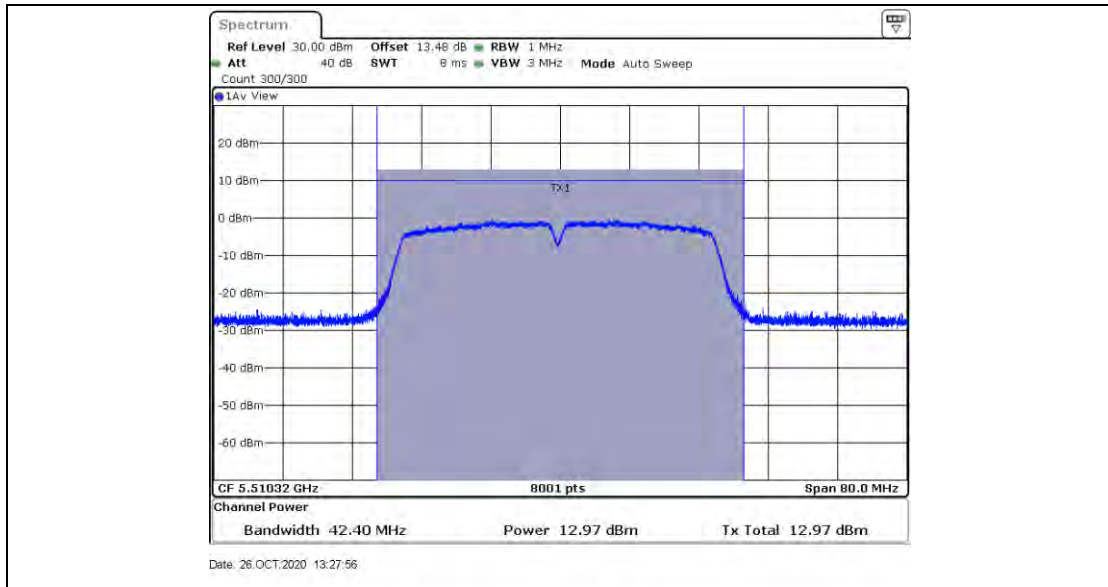
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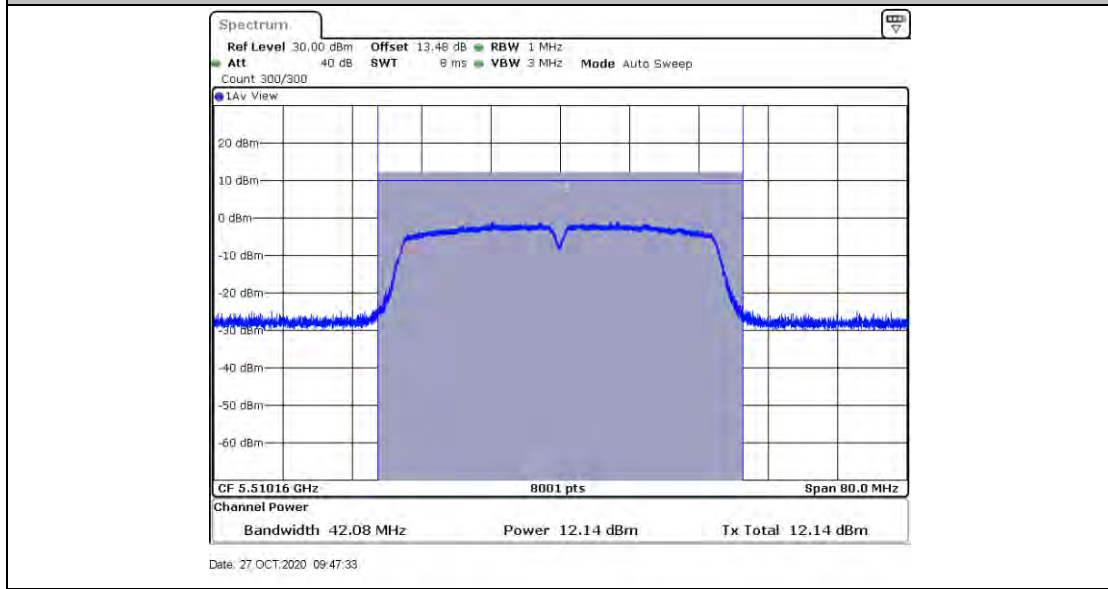
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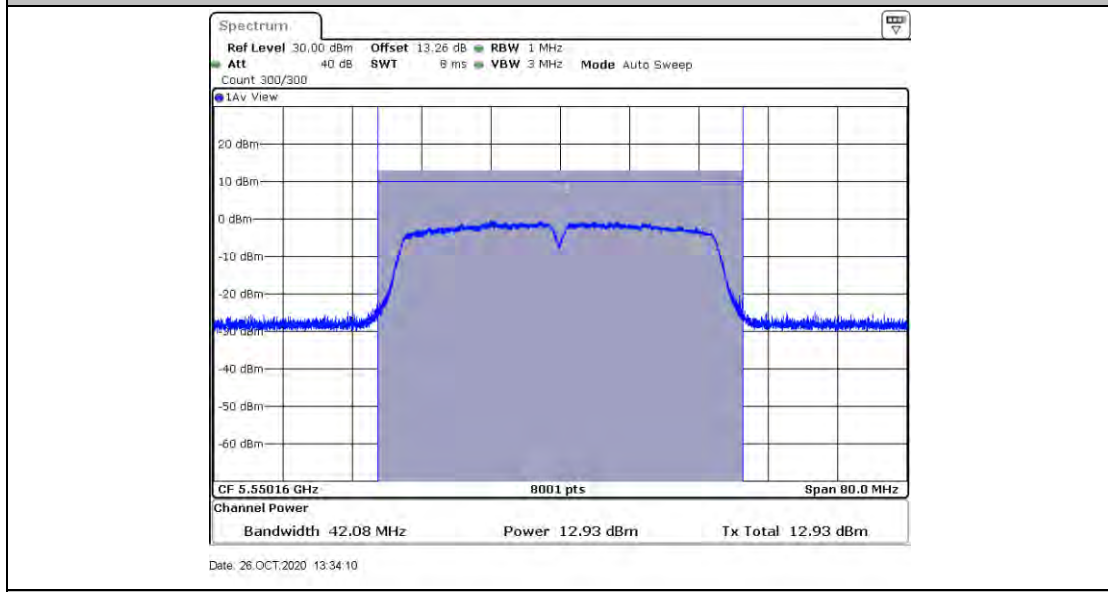
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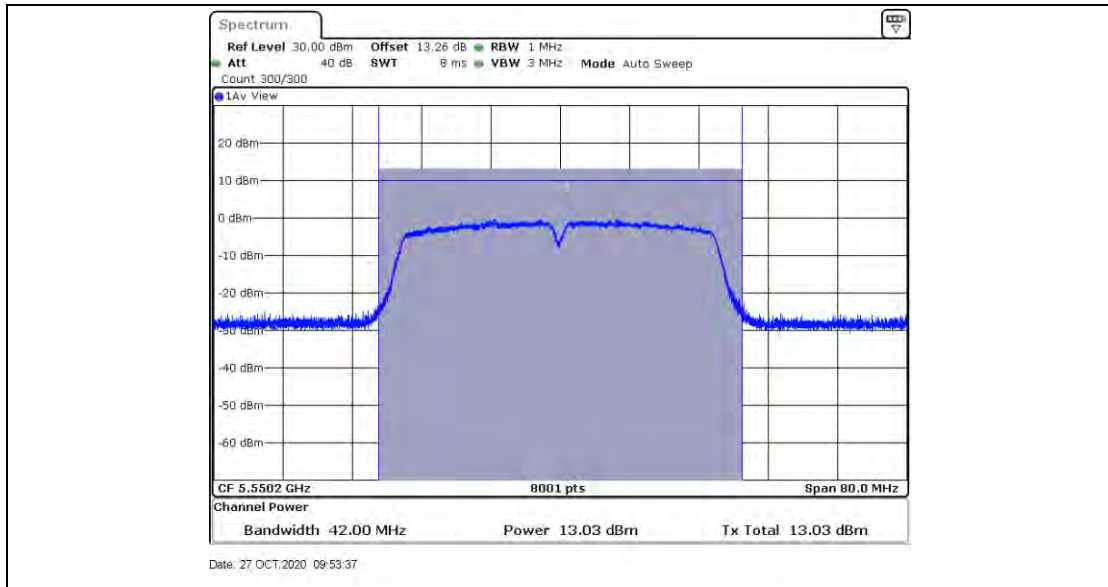
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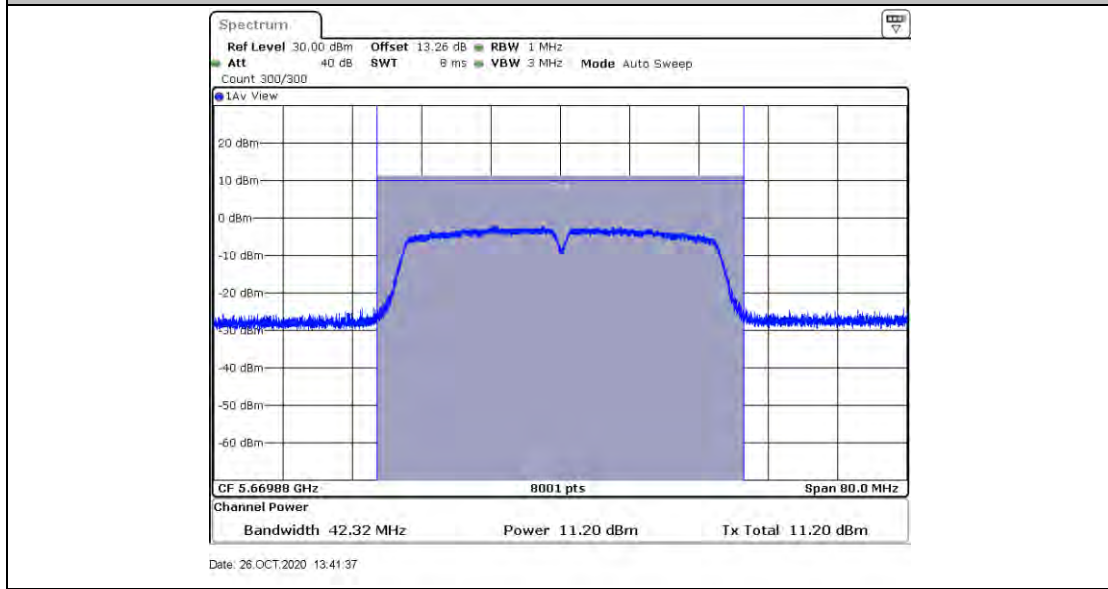
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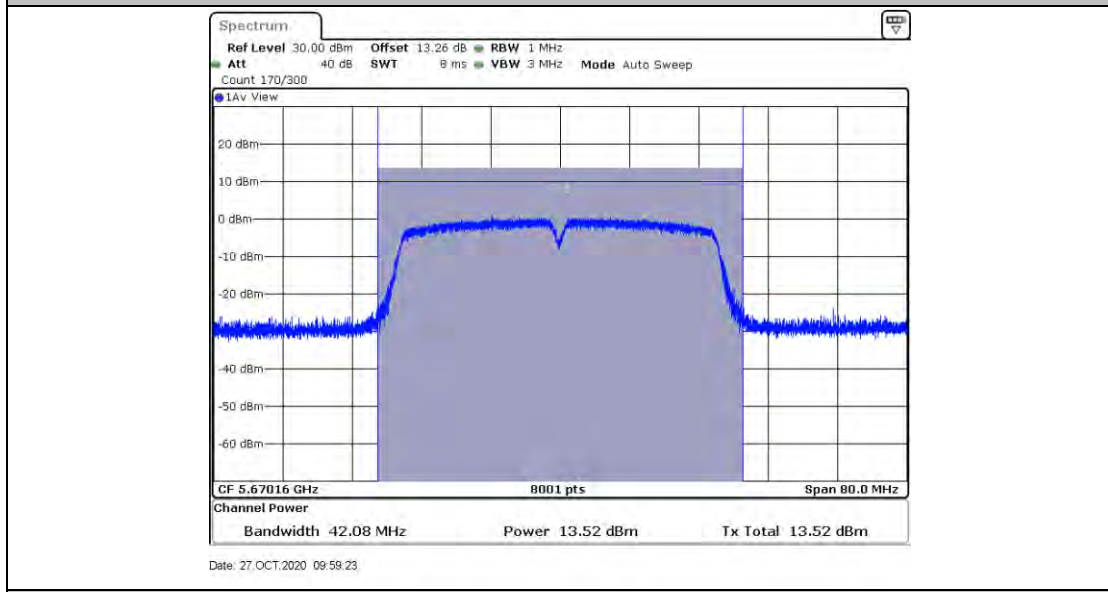
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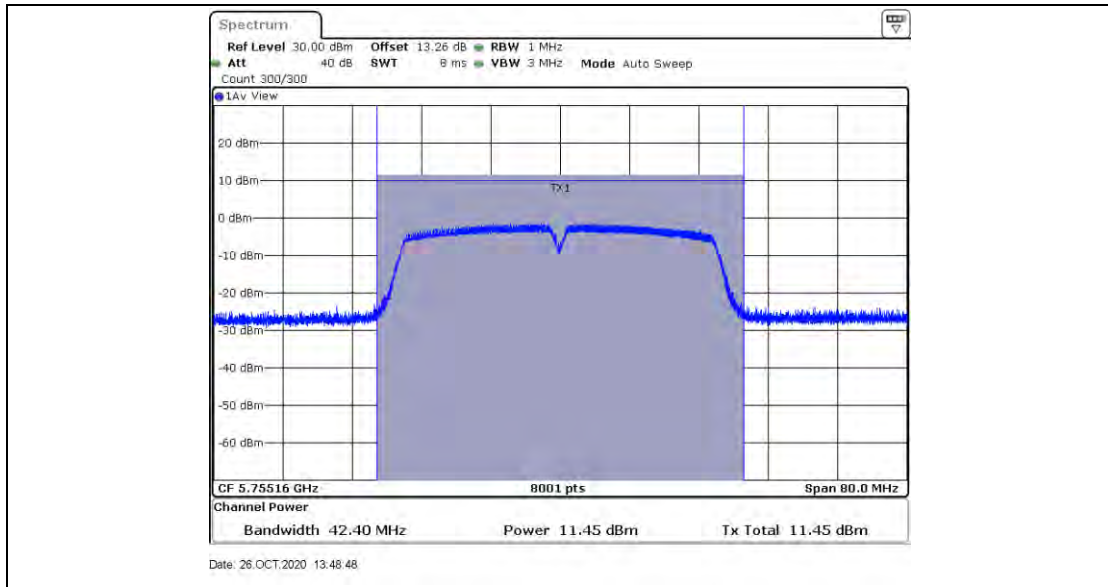
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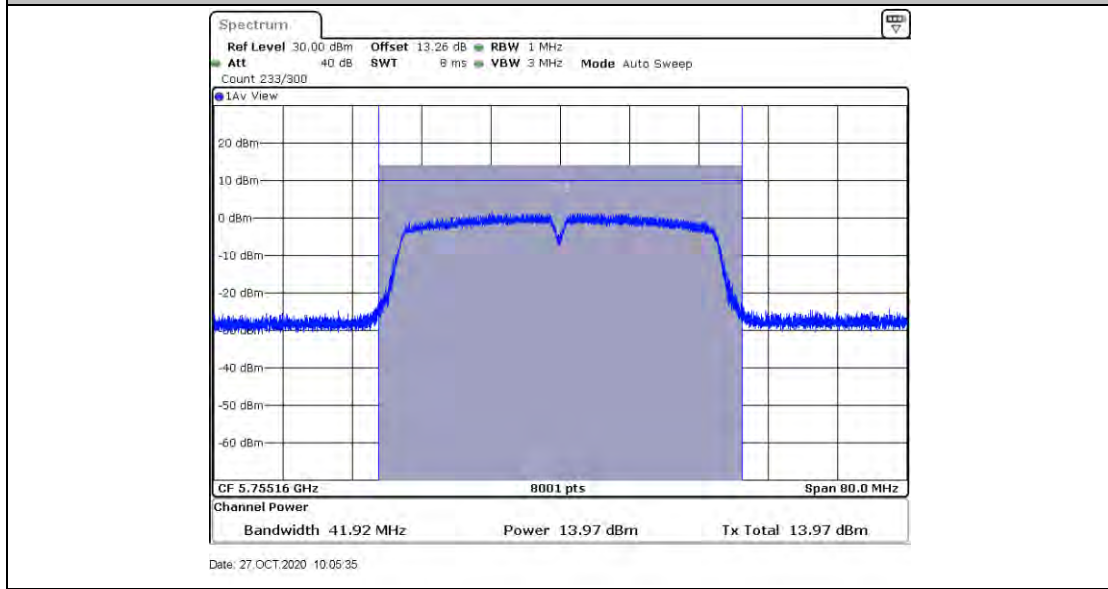
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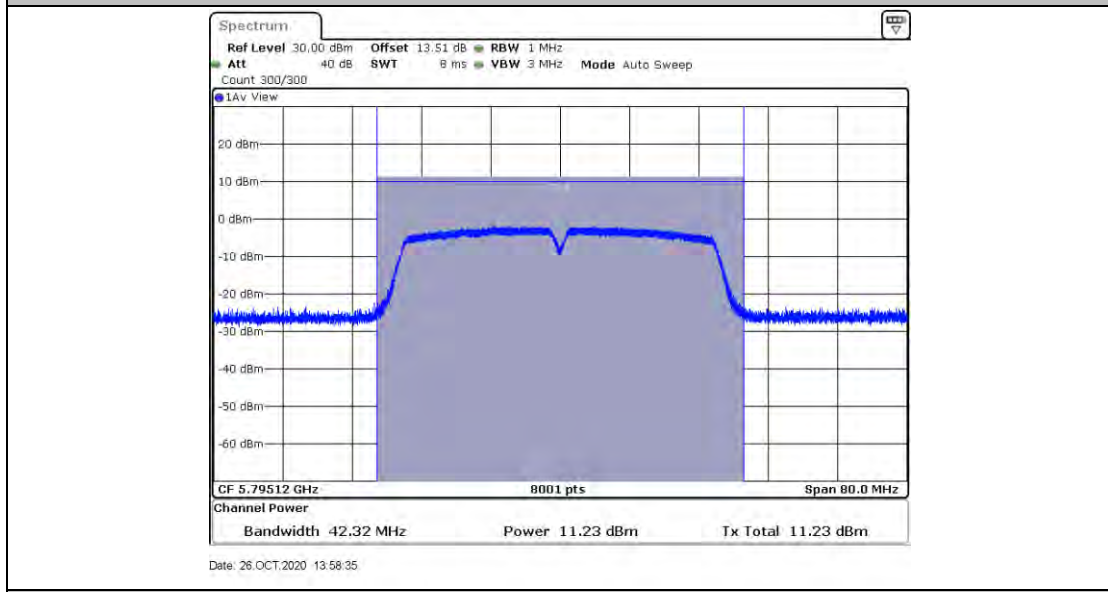
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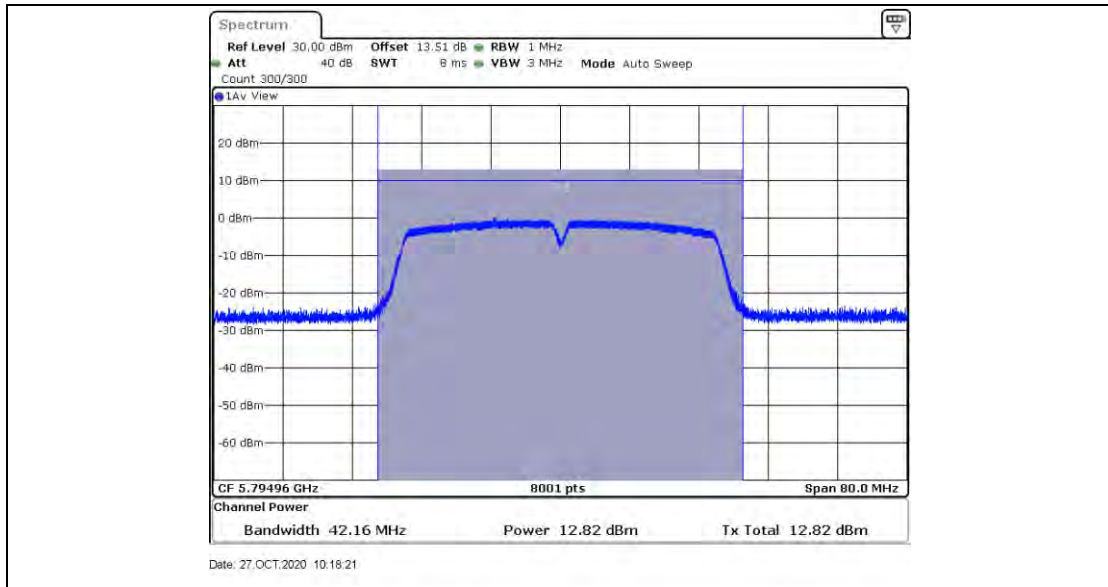
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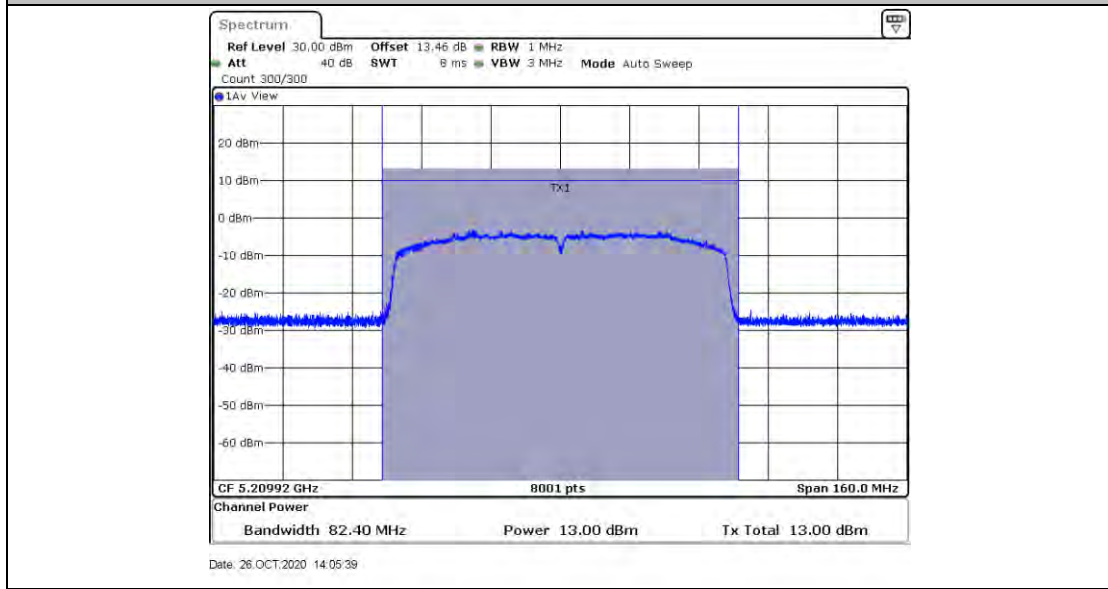
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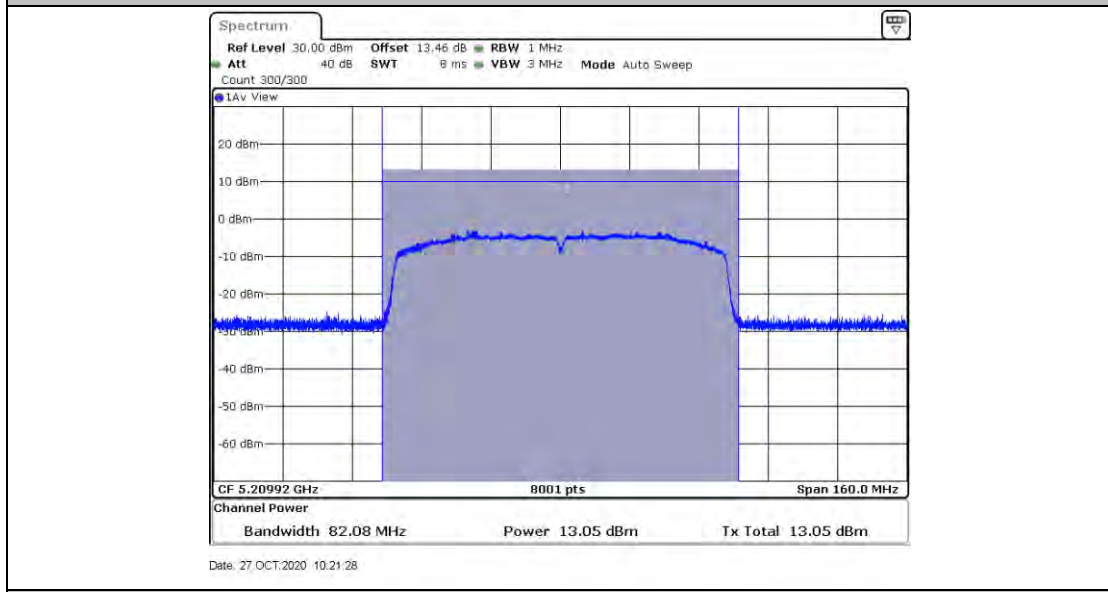
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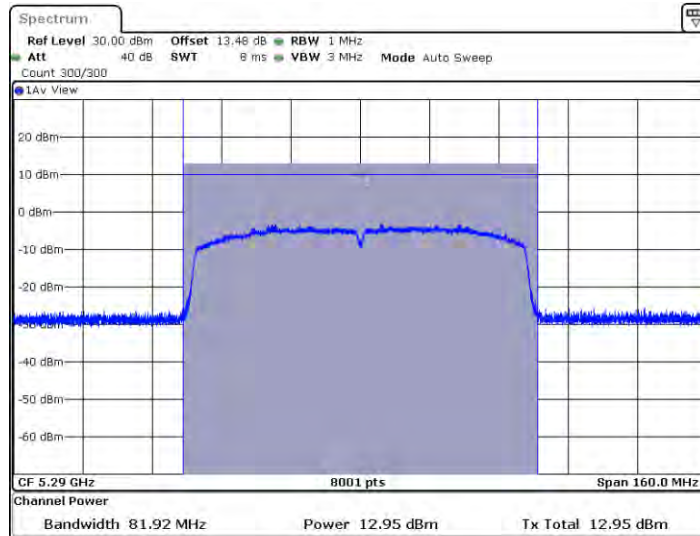
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11AC80SISO_Ant2_5210

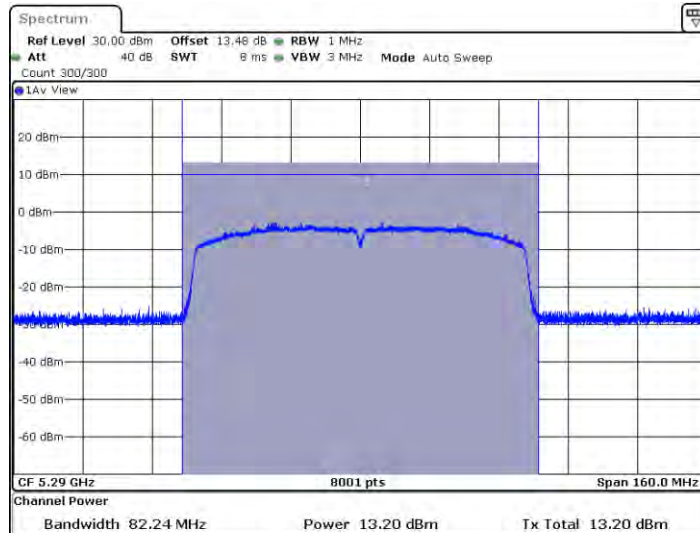


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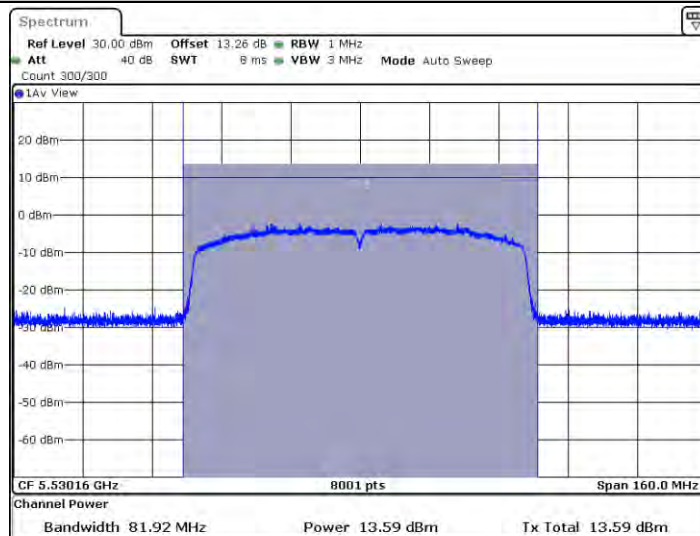
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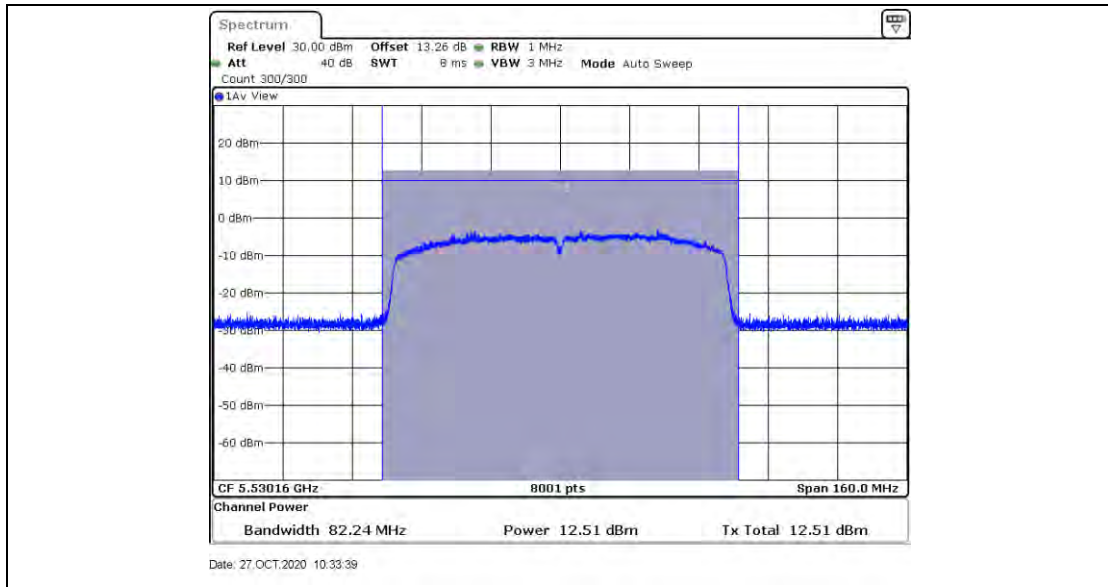
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11AC80SISO_Ant1_5530

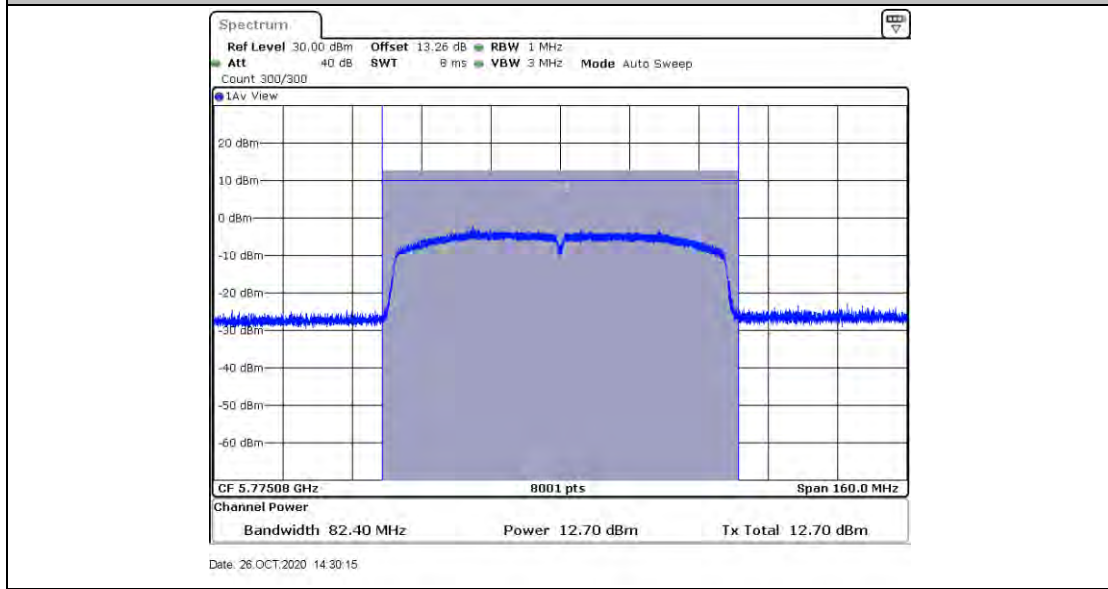


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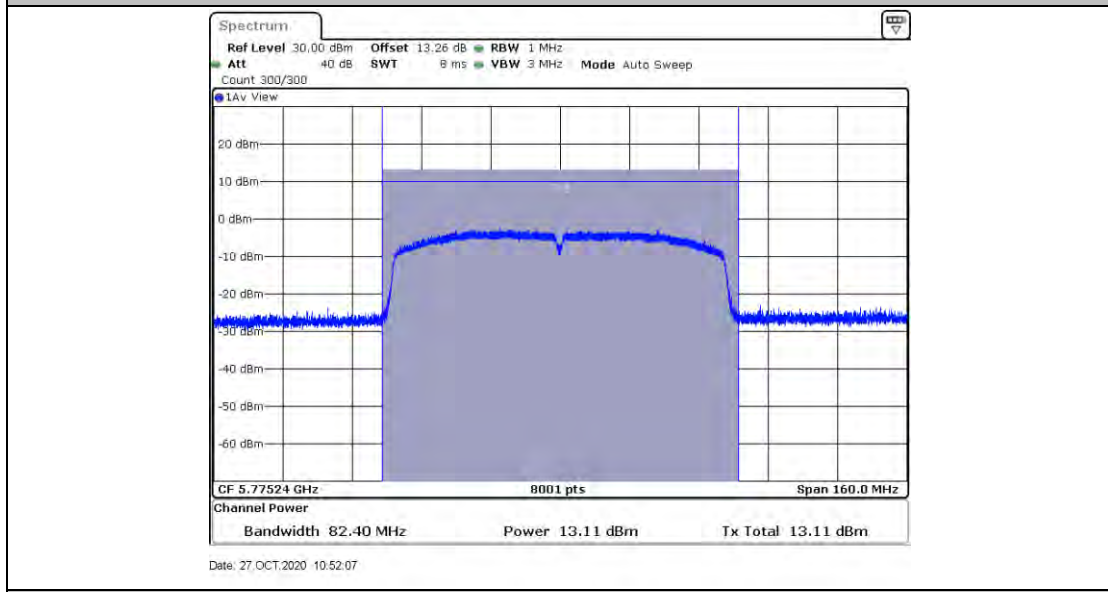
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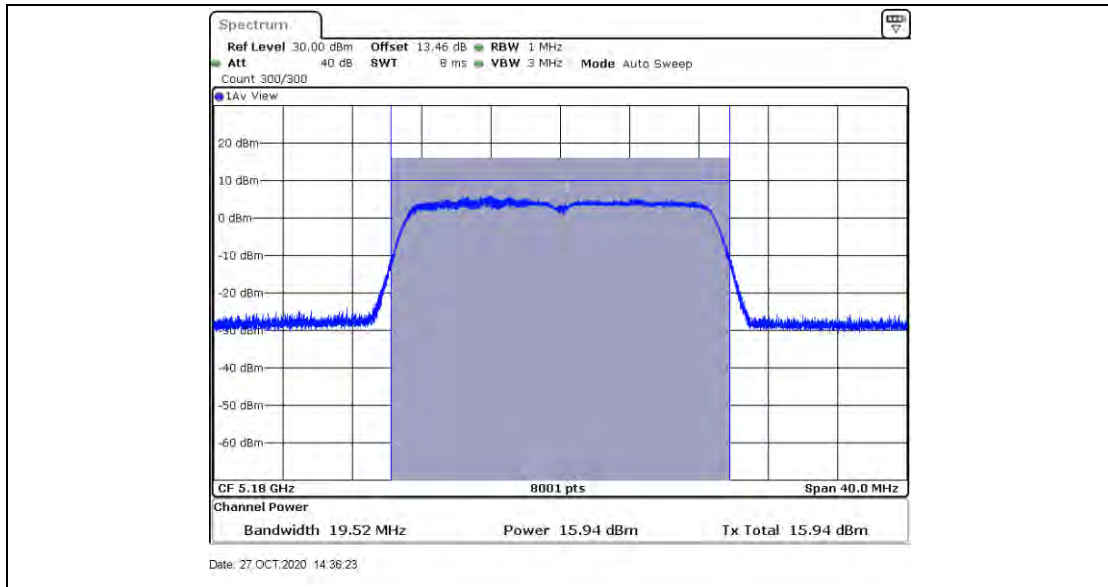
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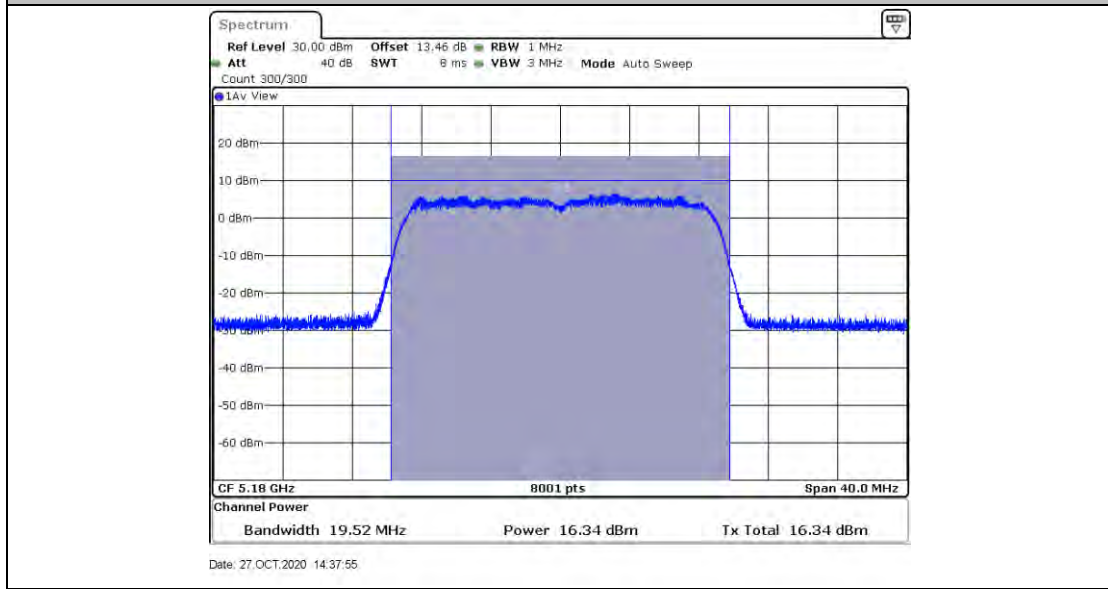
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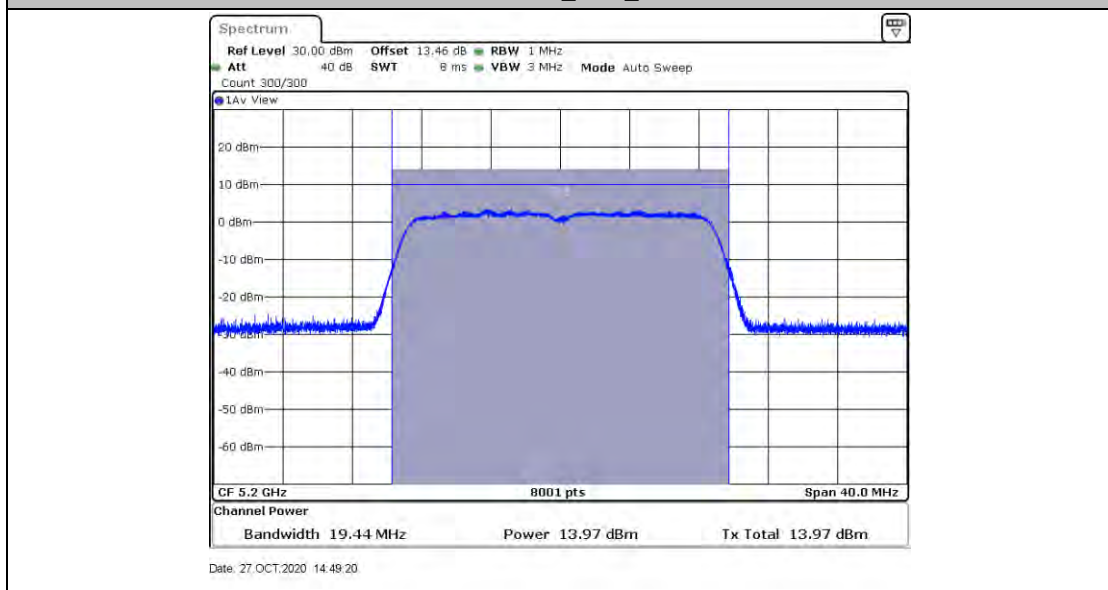
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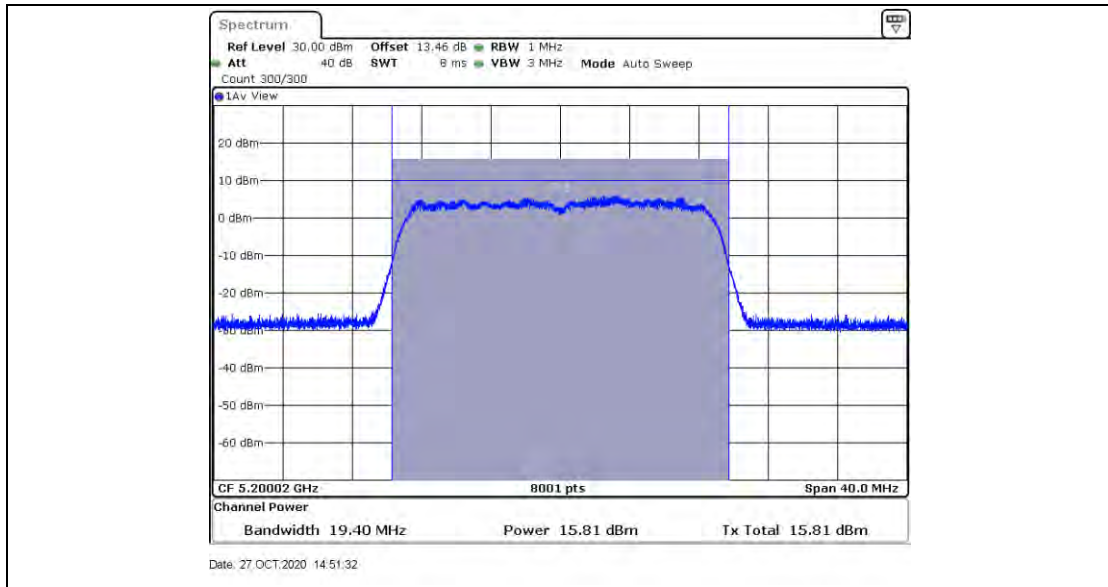
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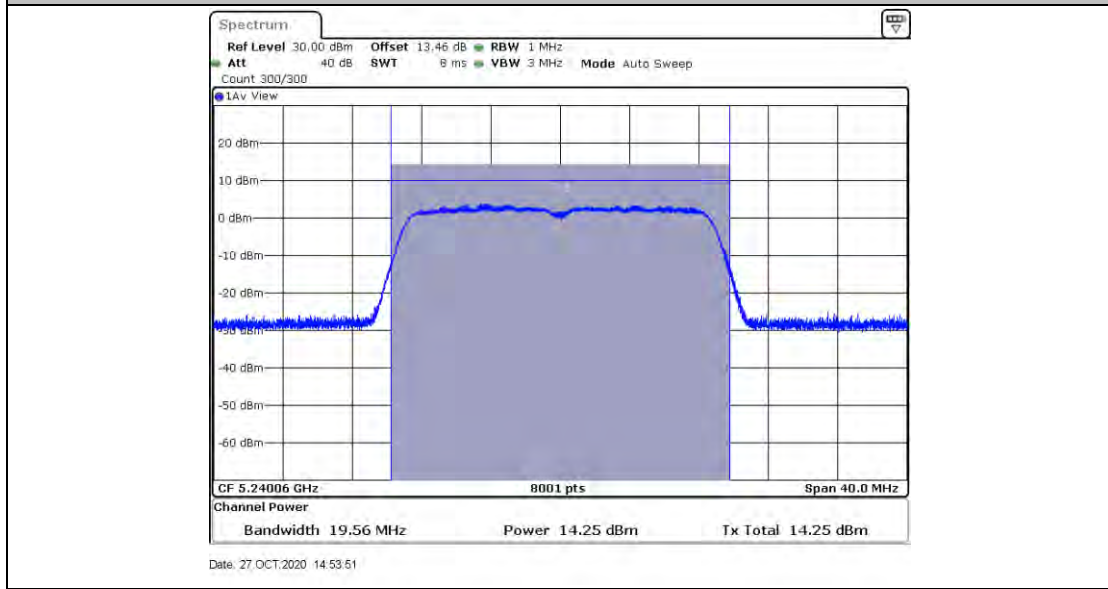
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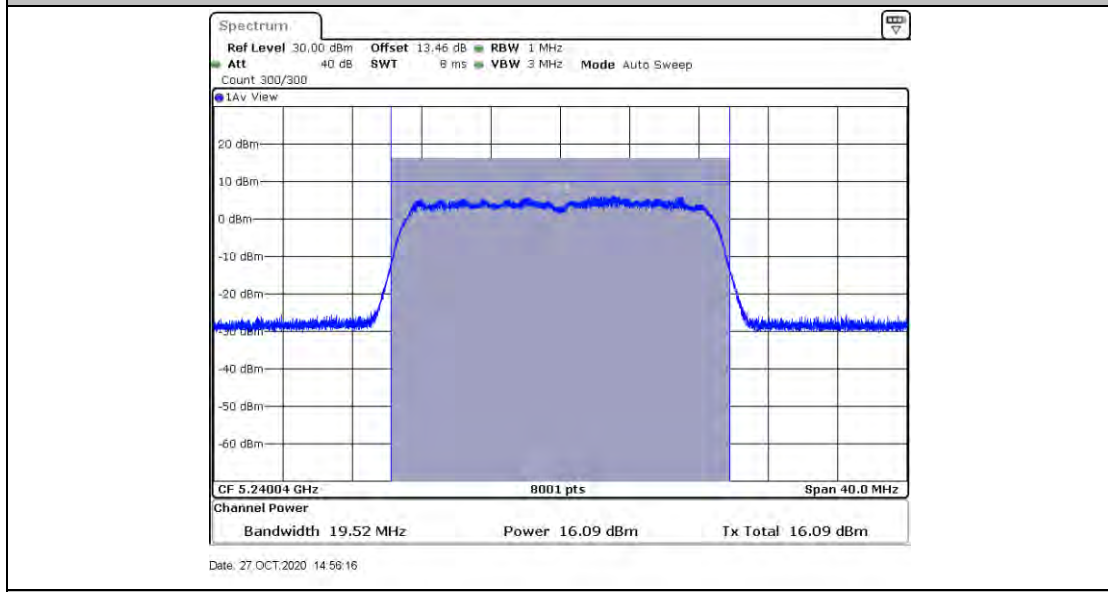
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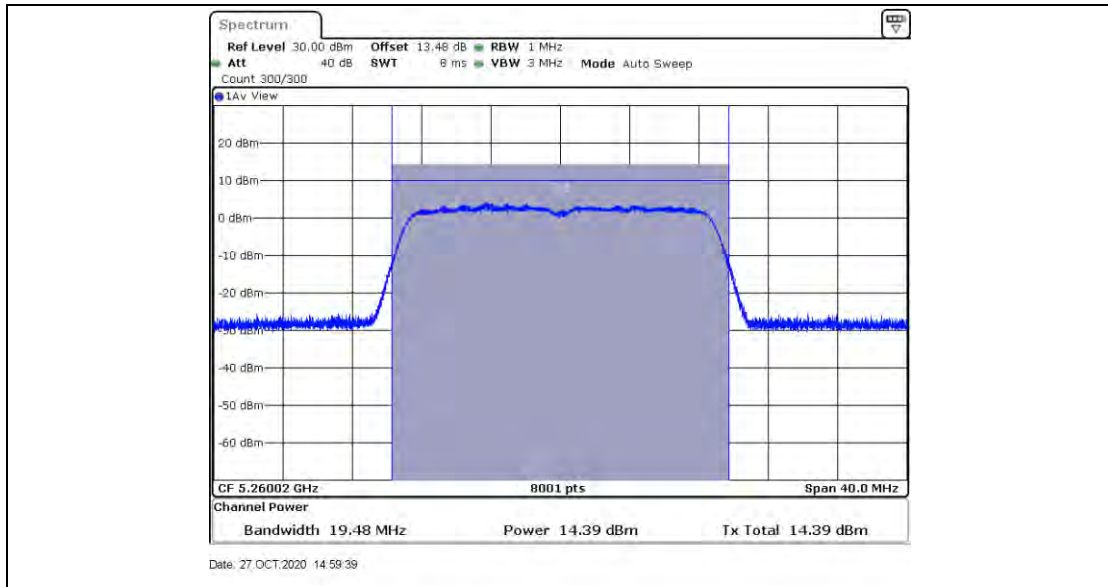
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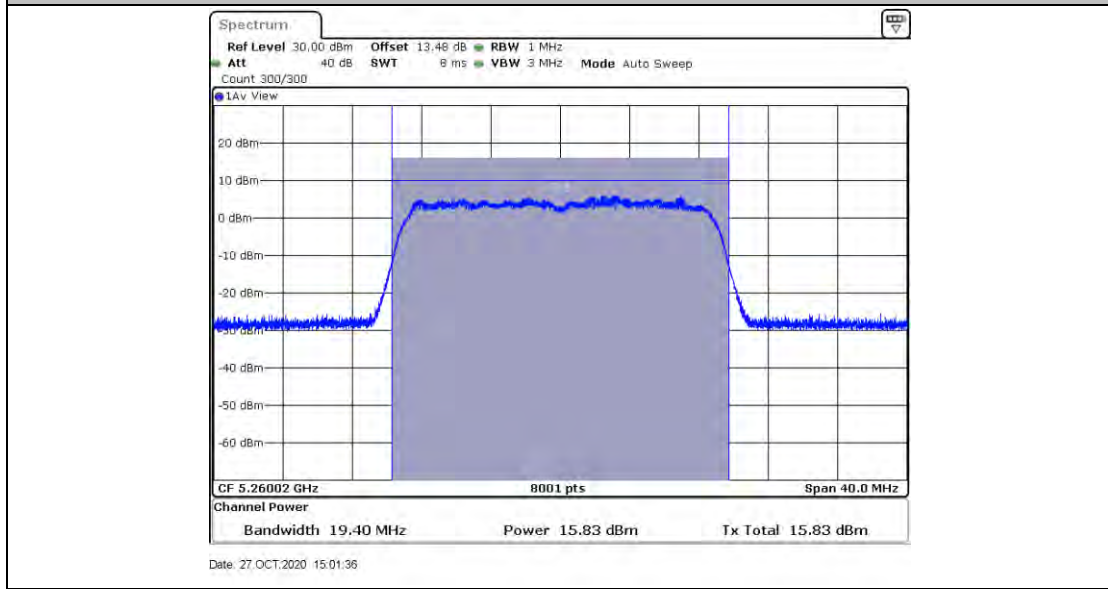
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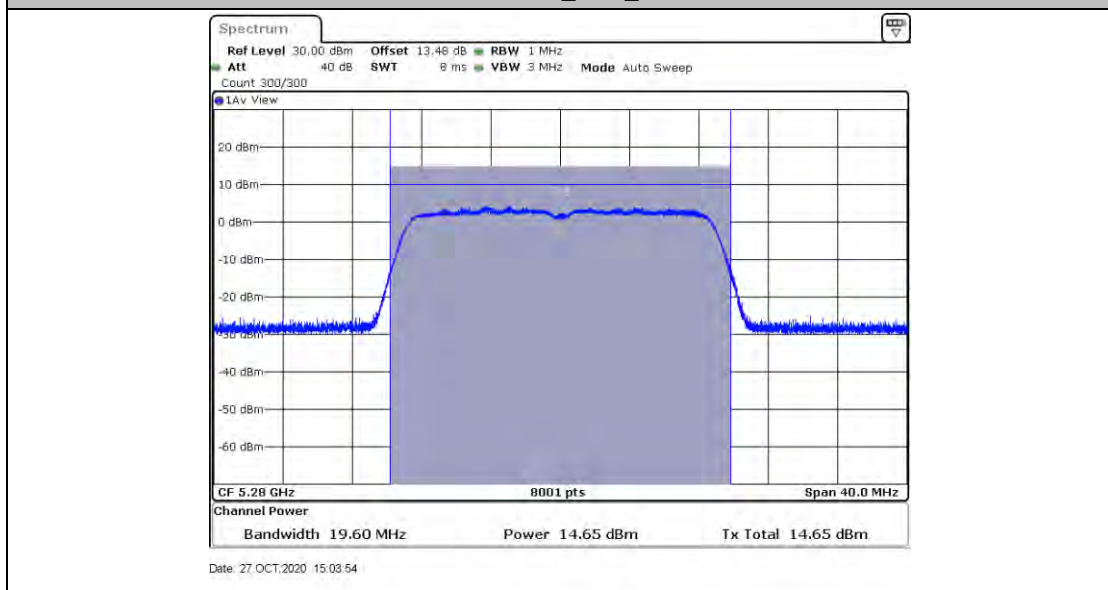
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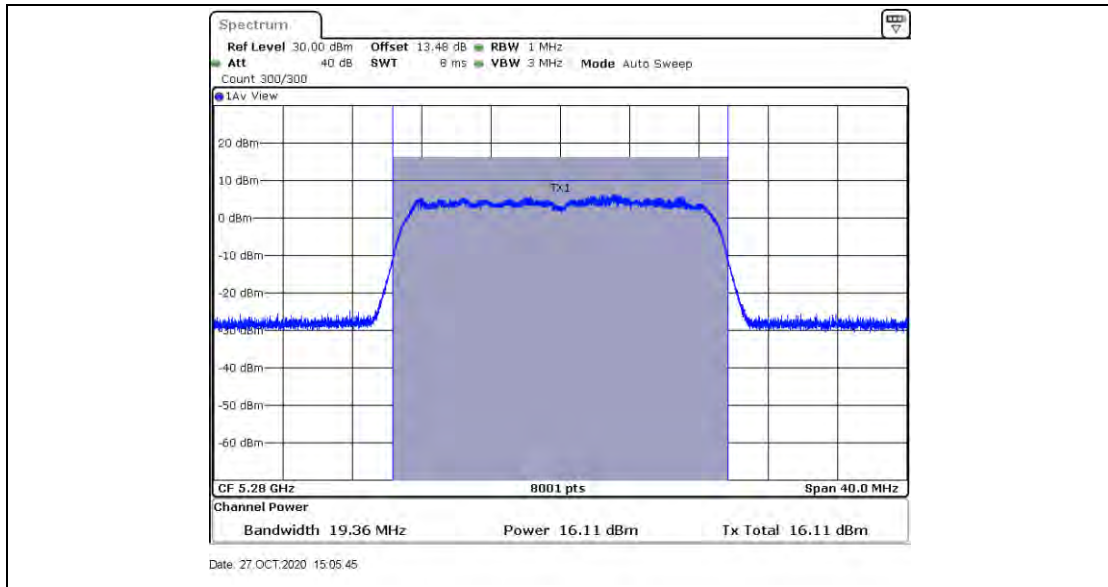
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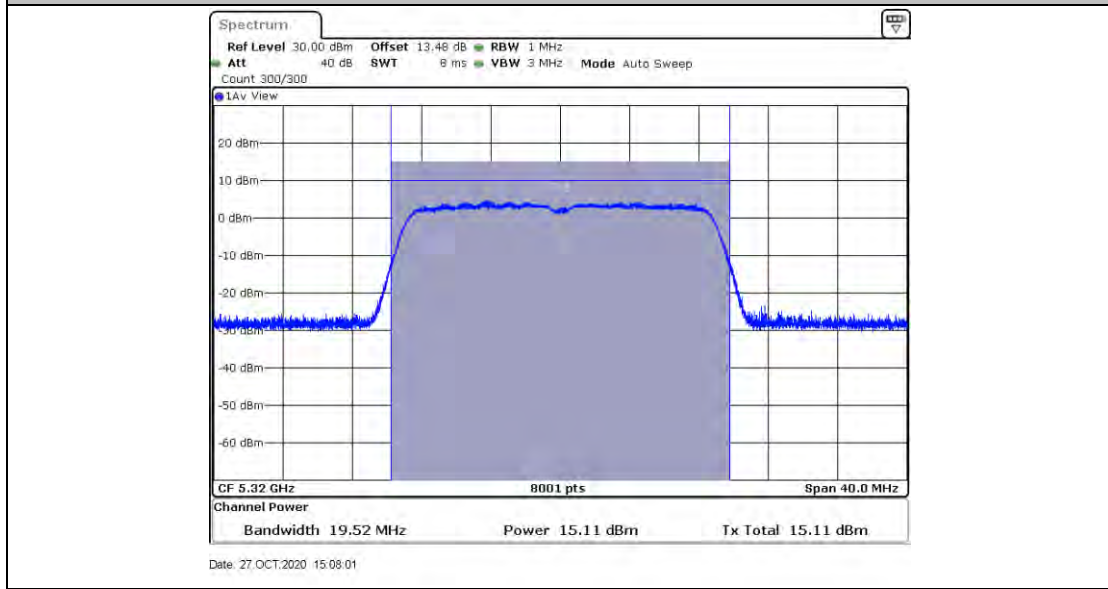
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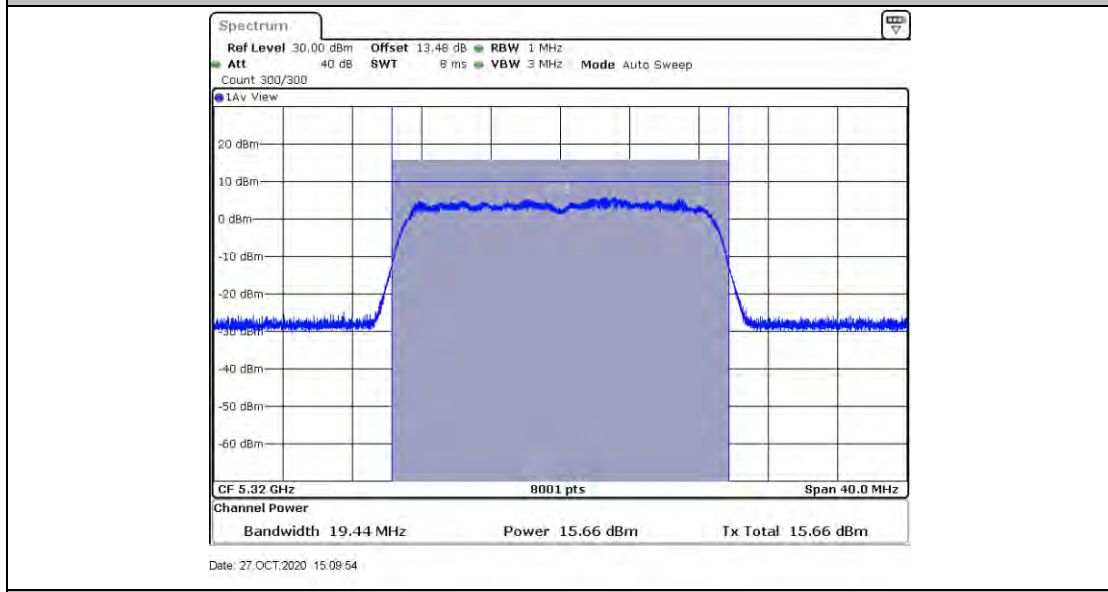
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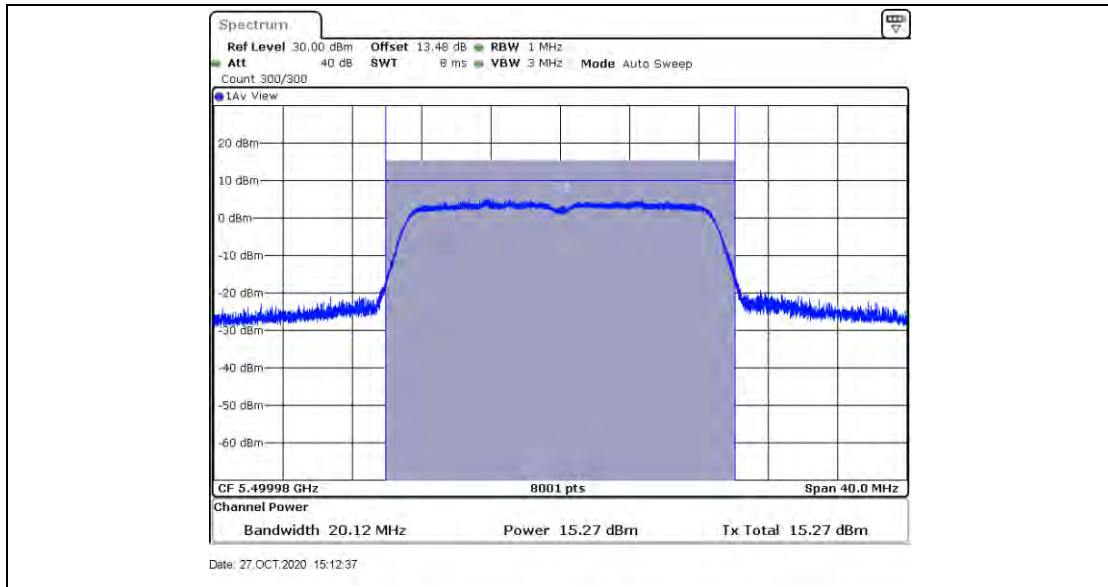
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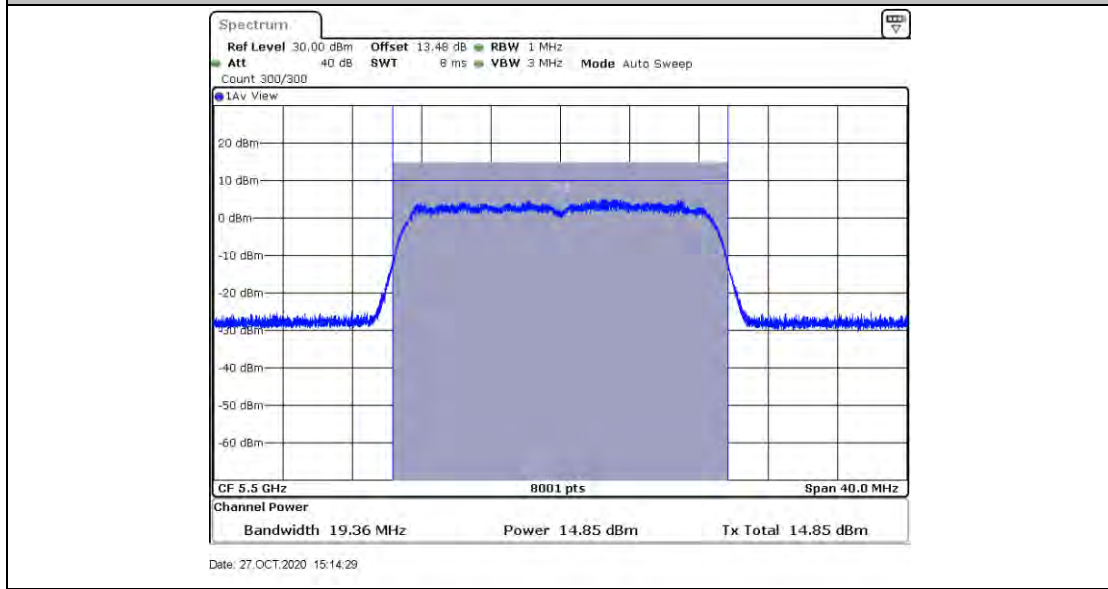
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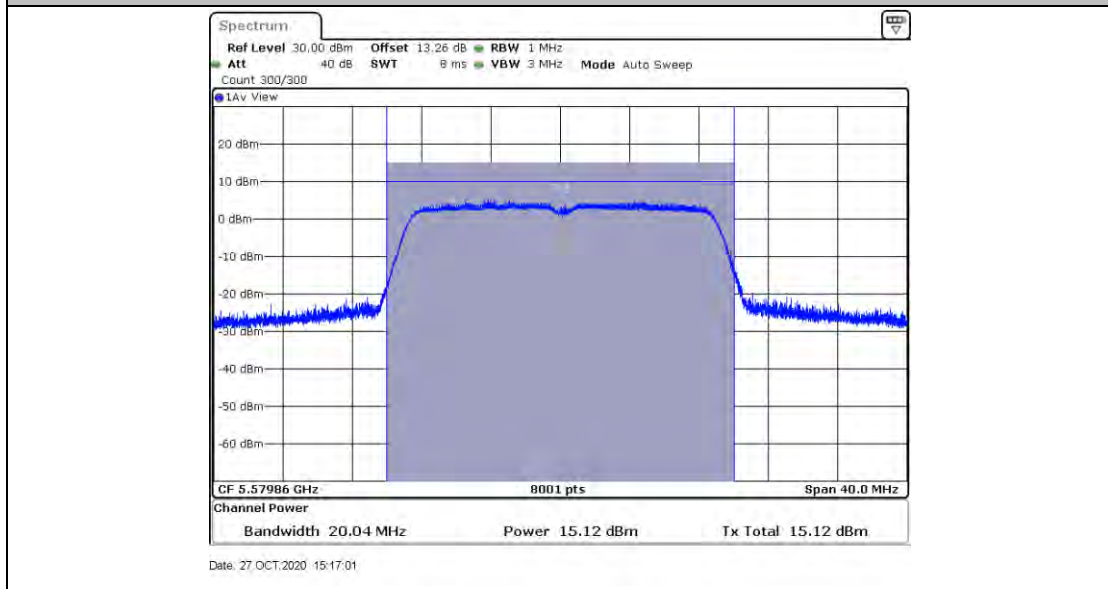
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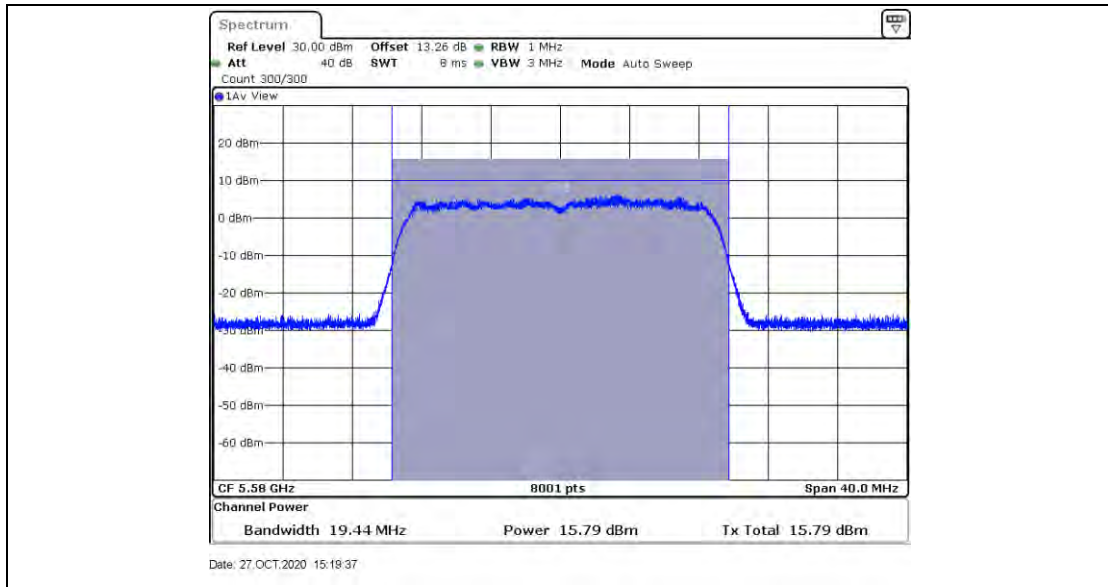
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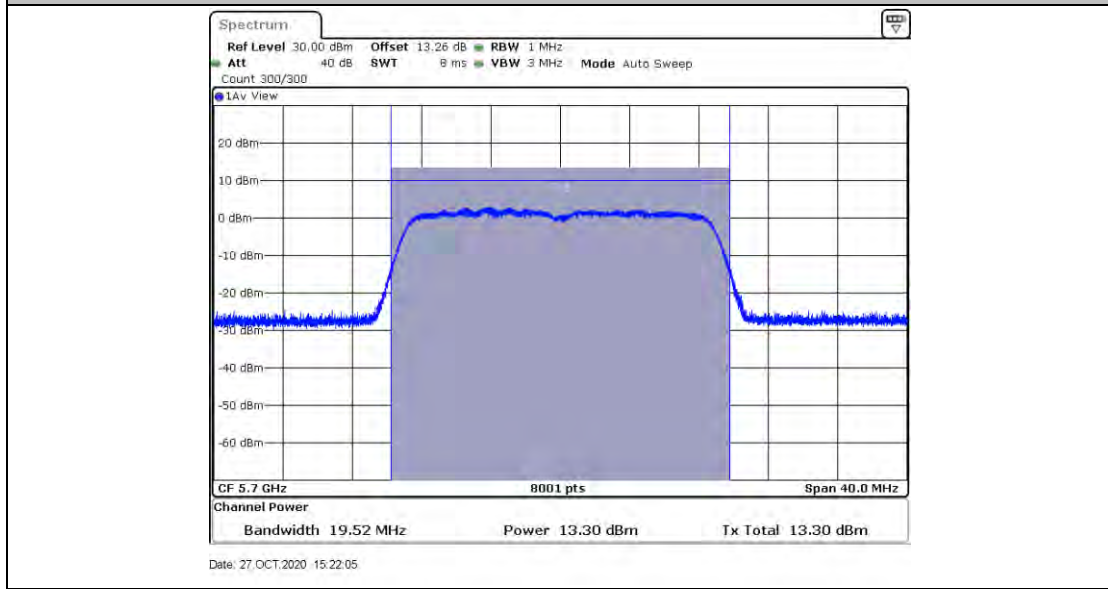
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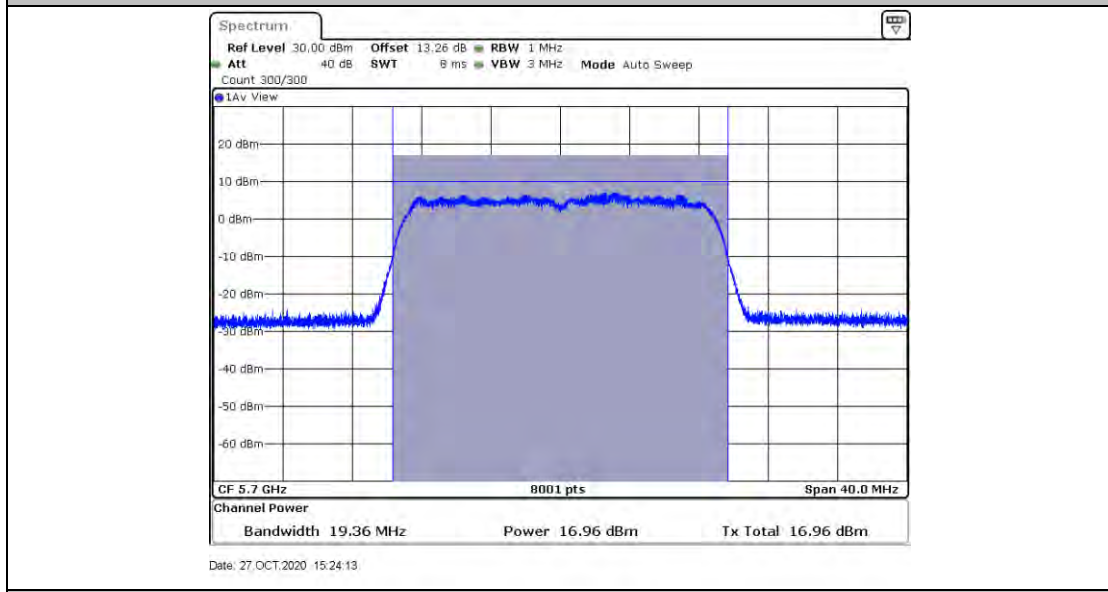
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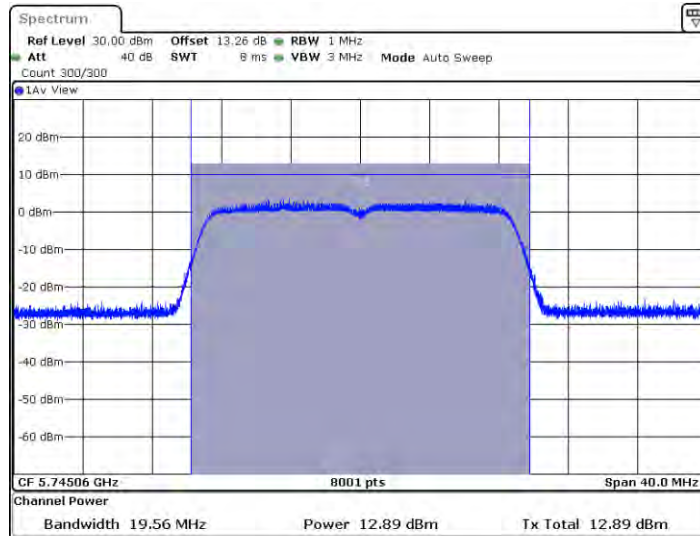
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11N20MIMO_Ant2_5700

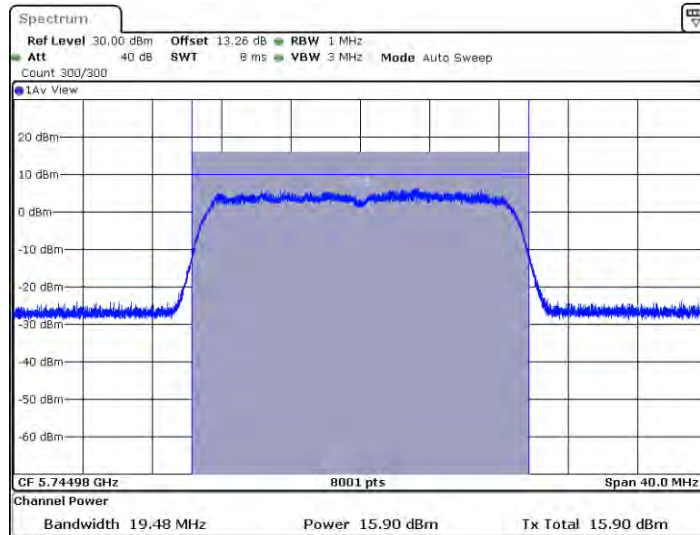


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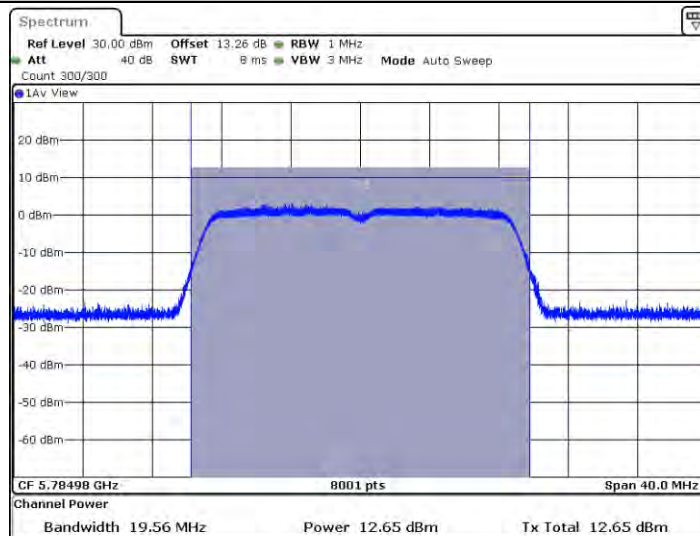
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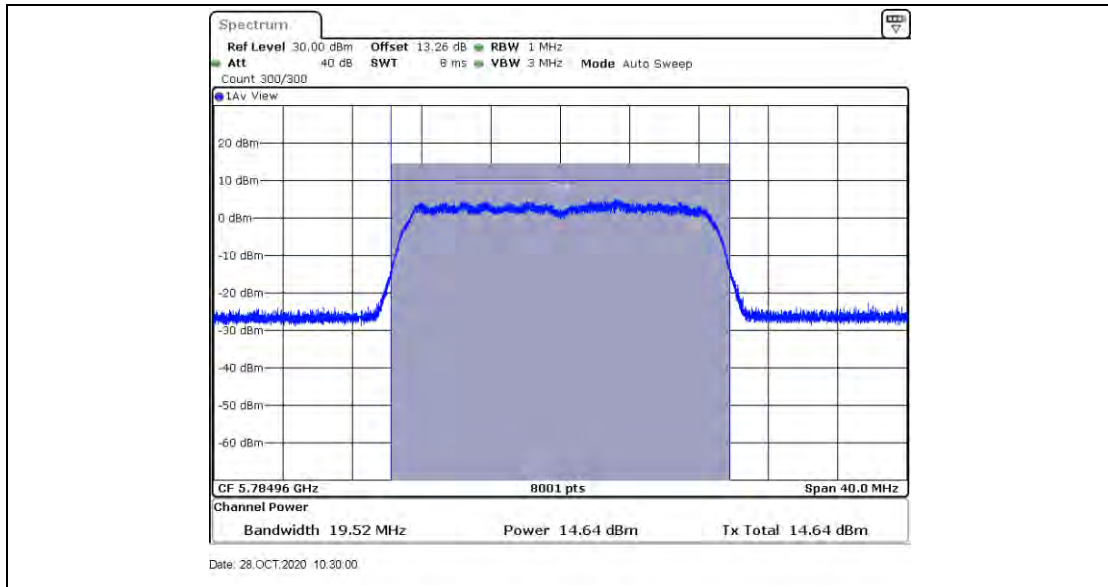
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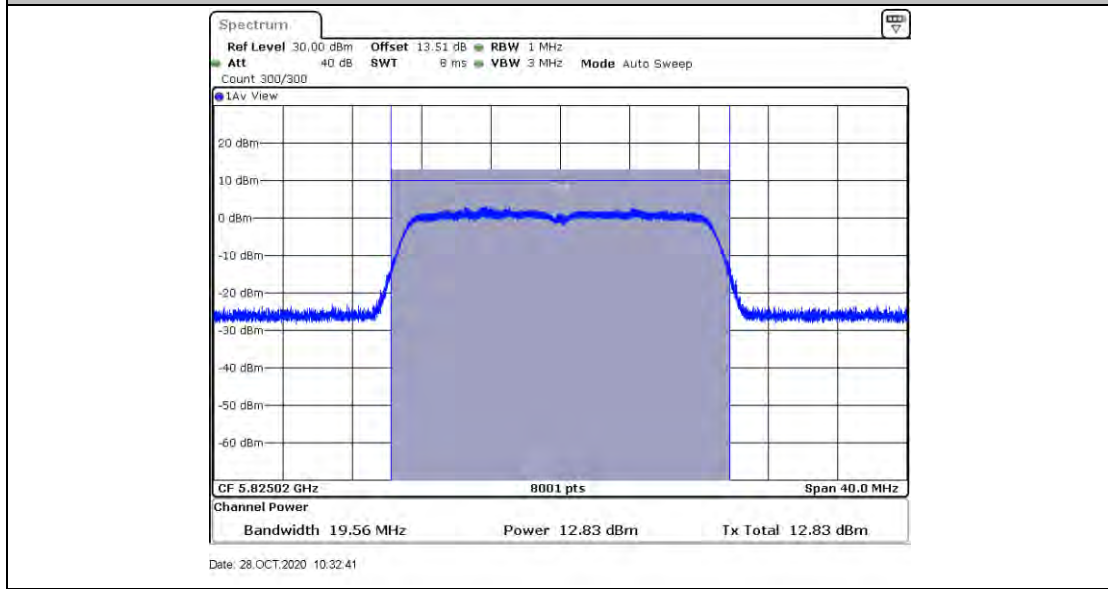


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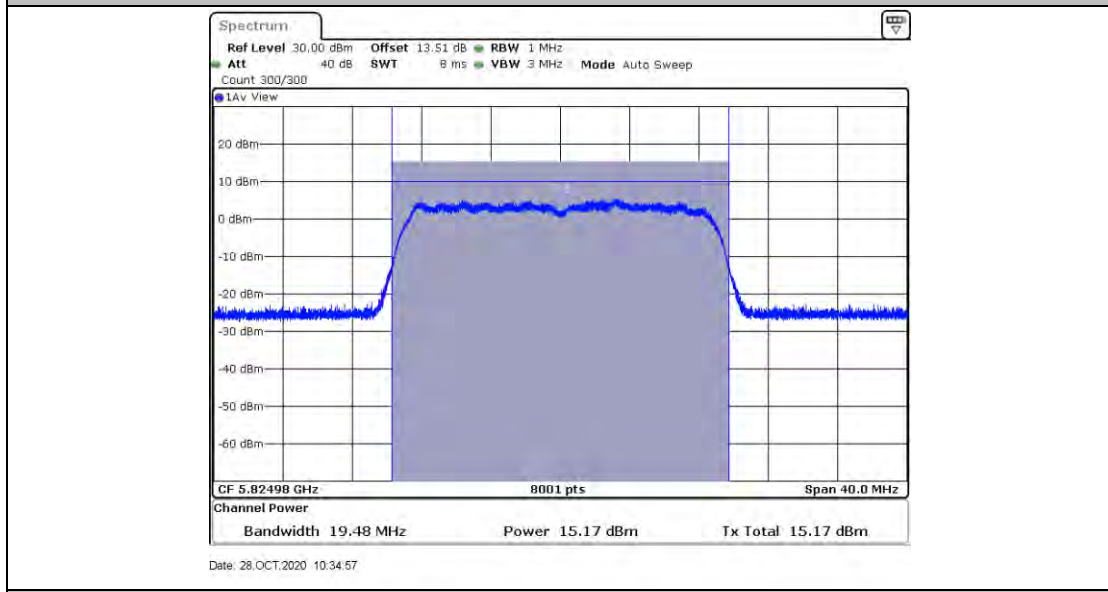
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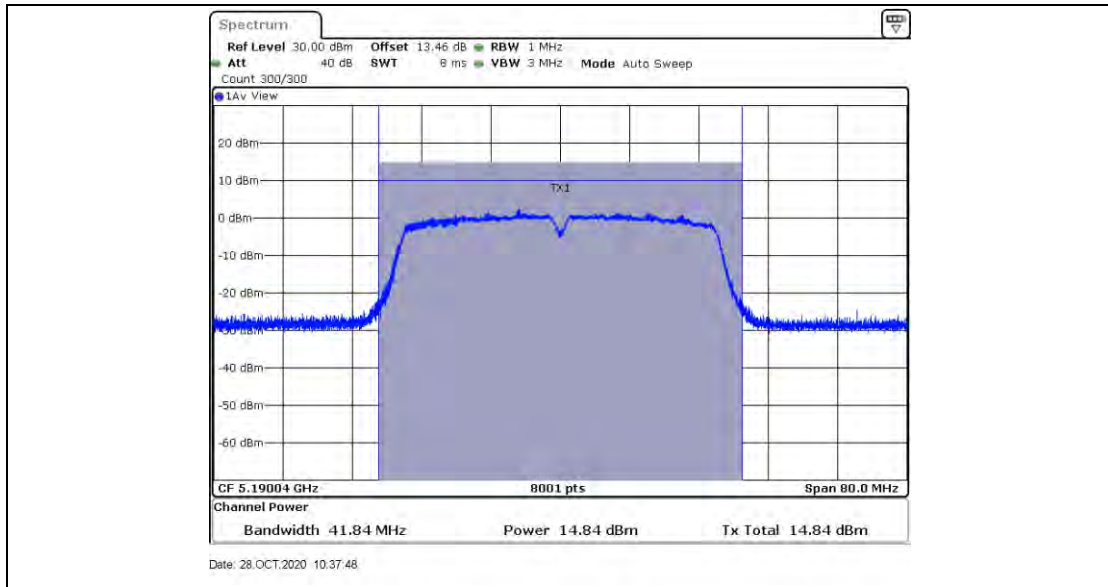
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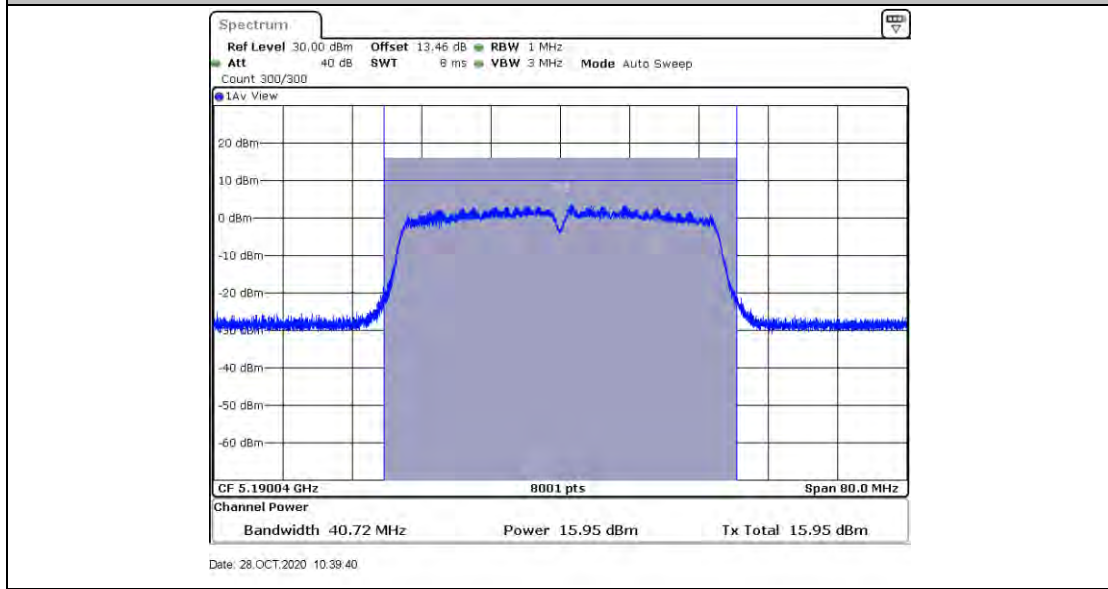
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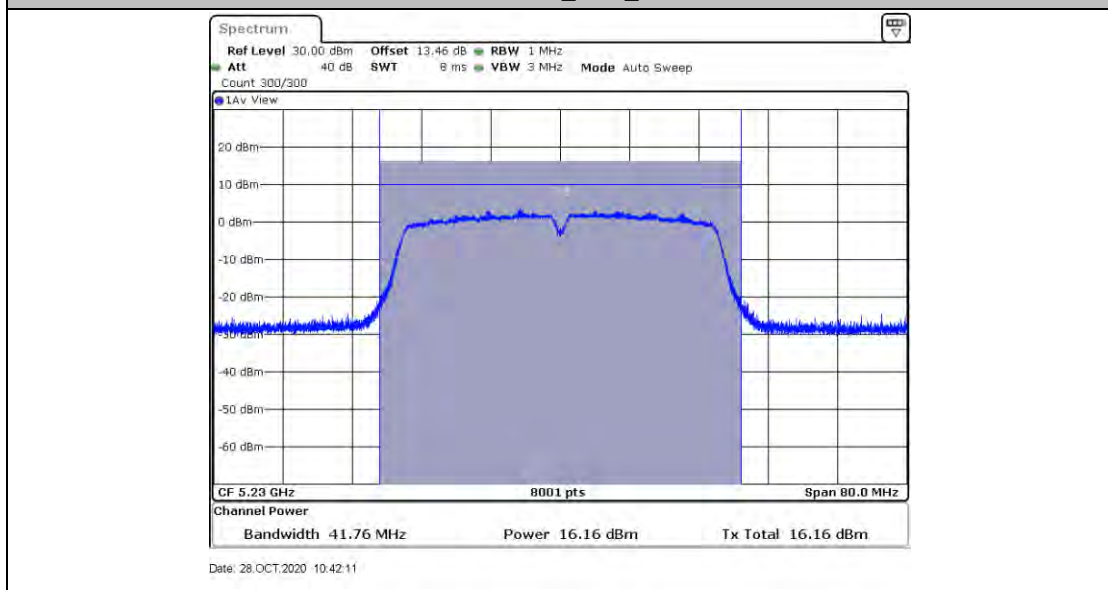
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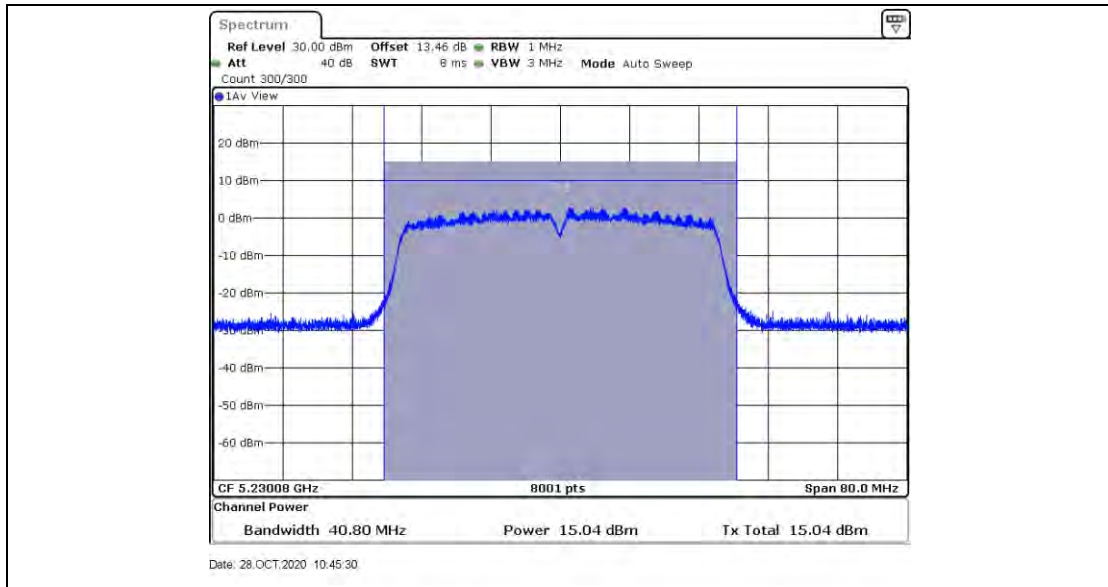
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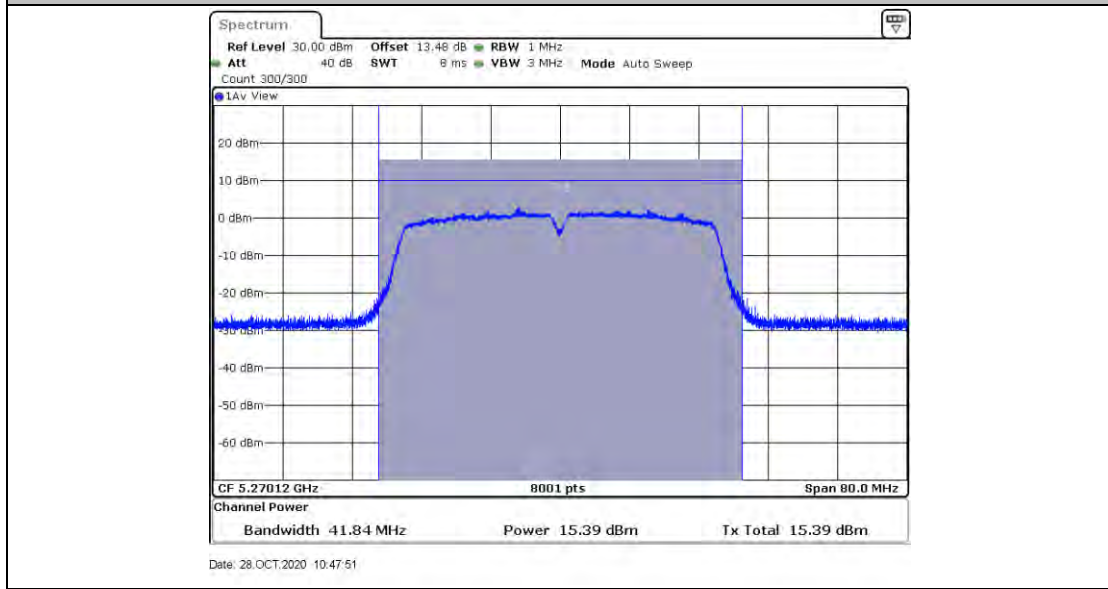
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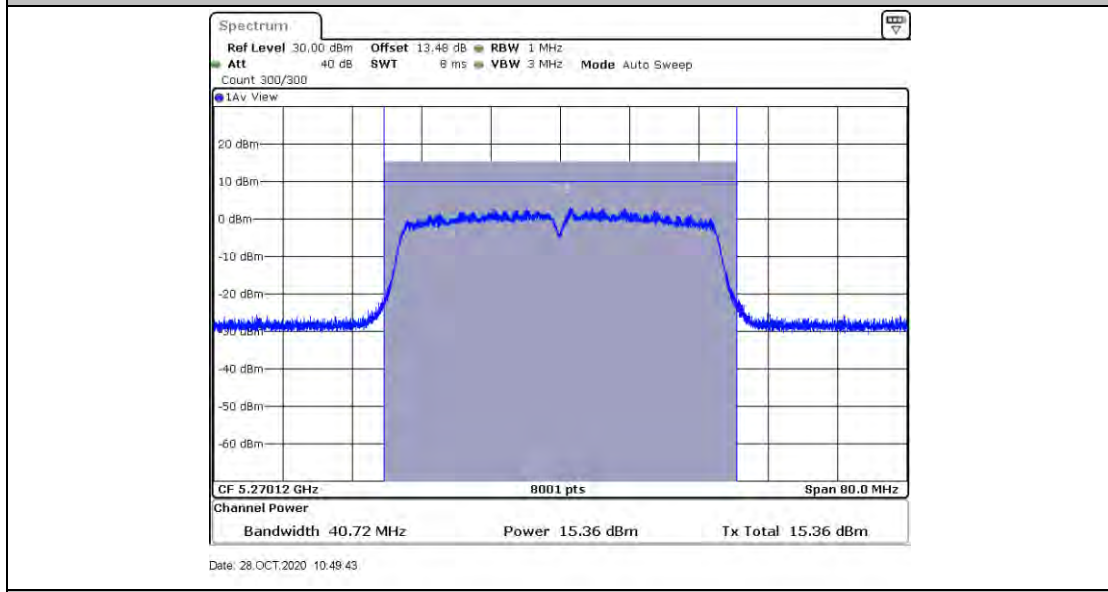
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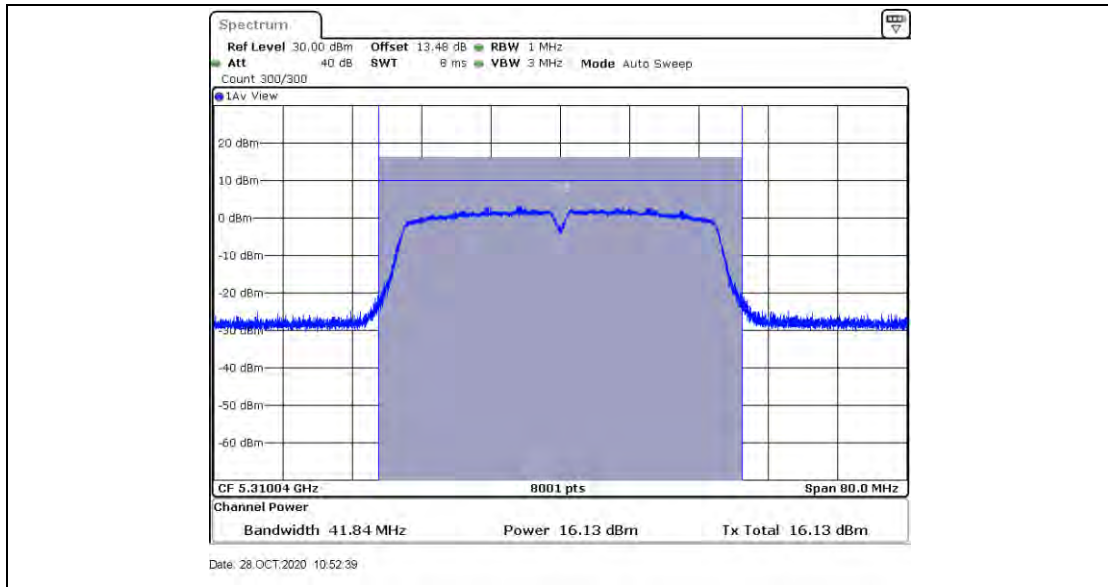
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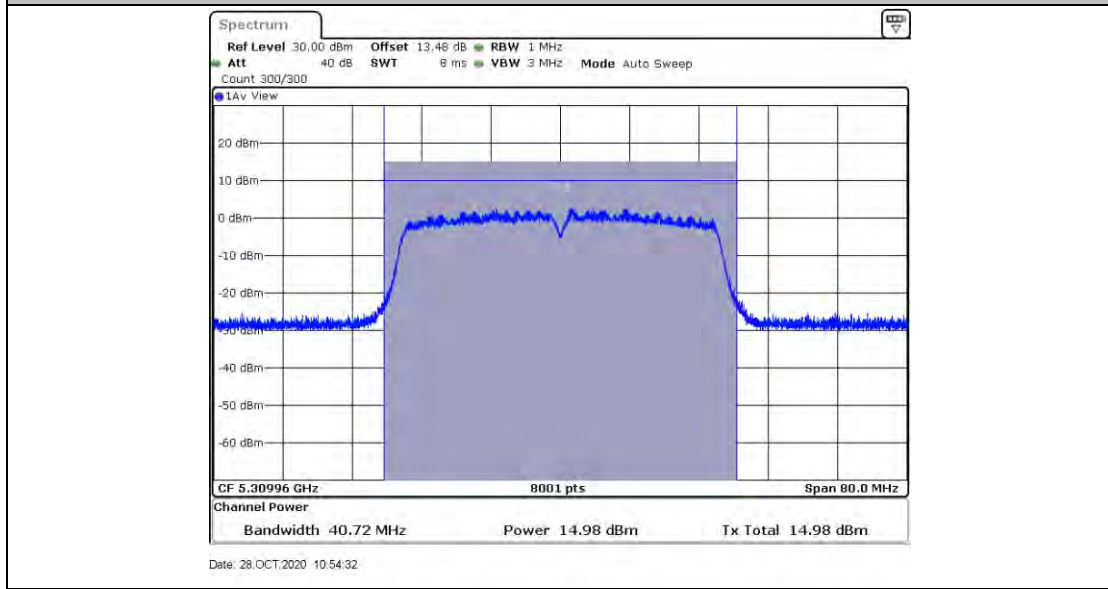
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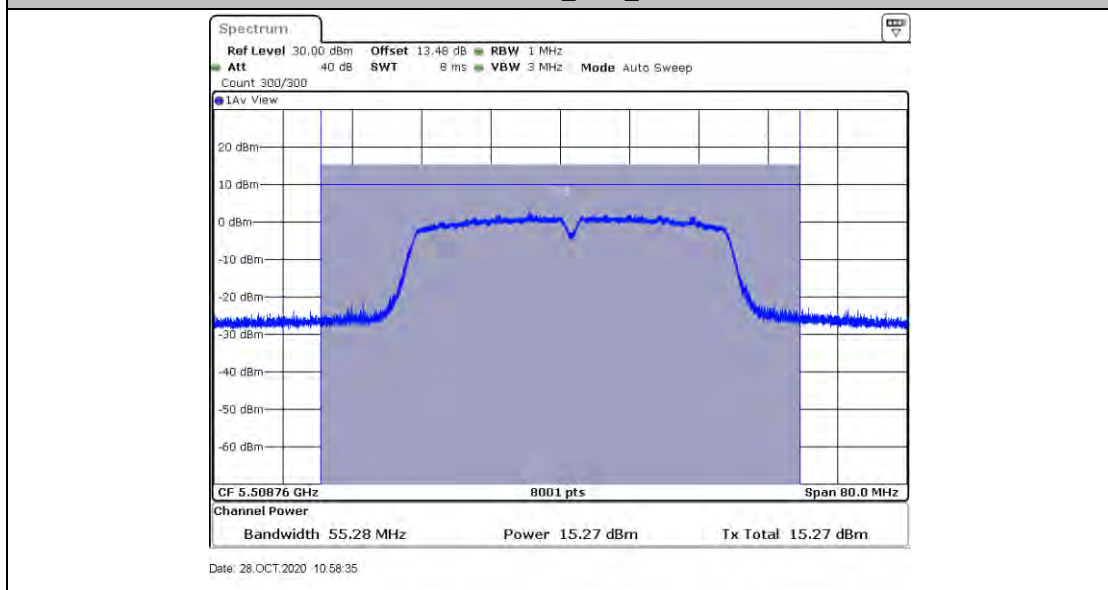
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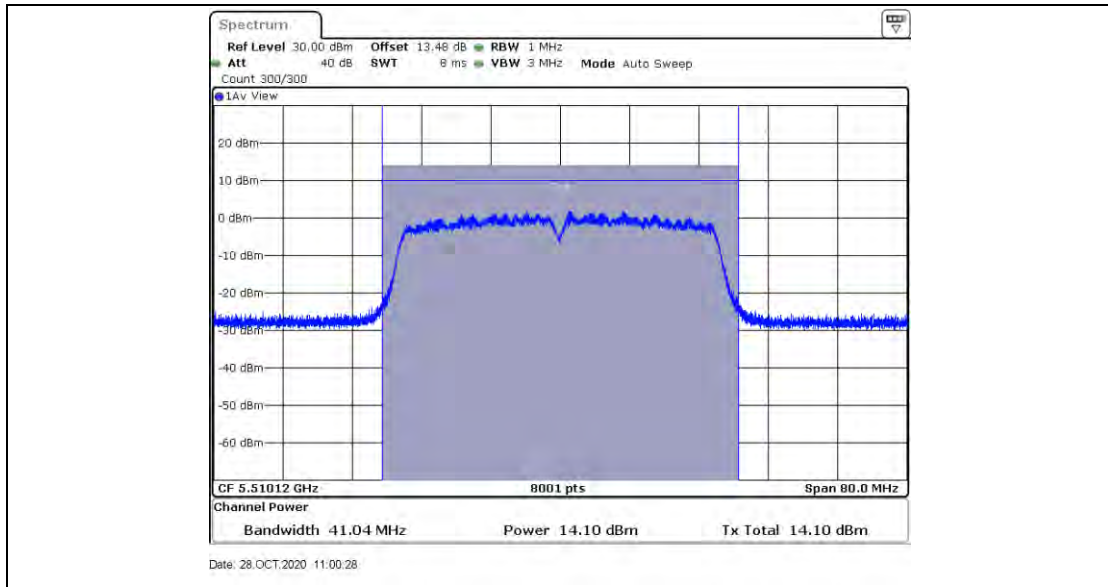
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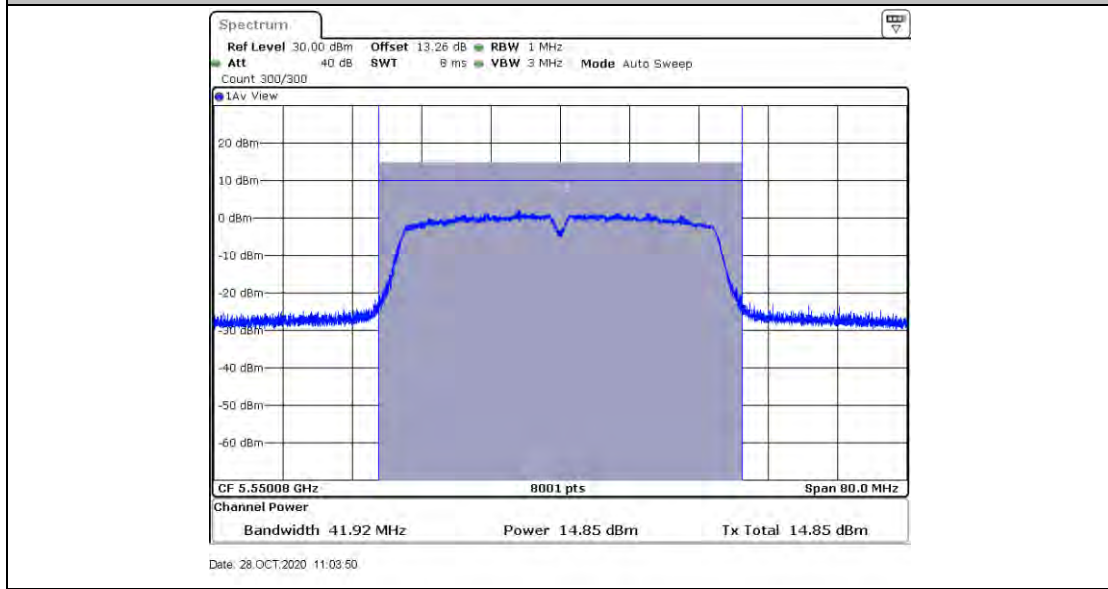
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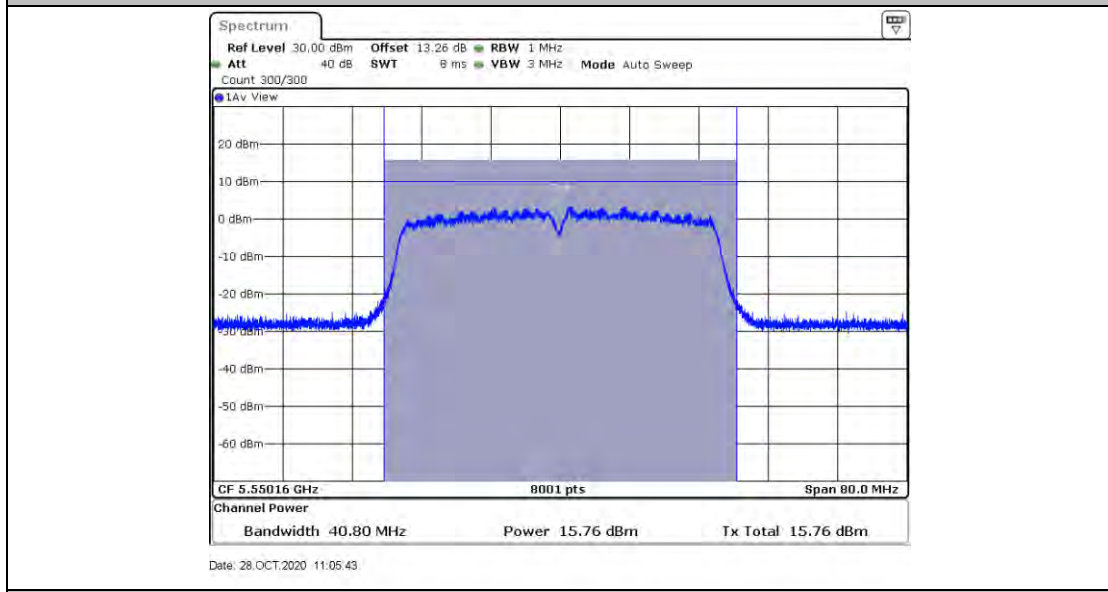
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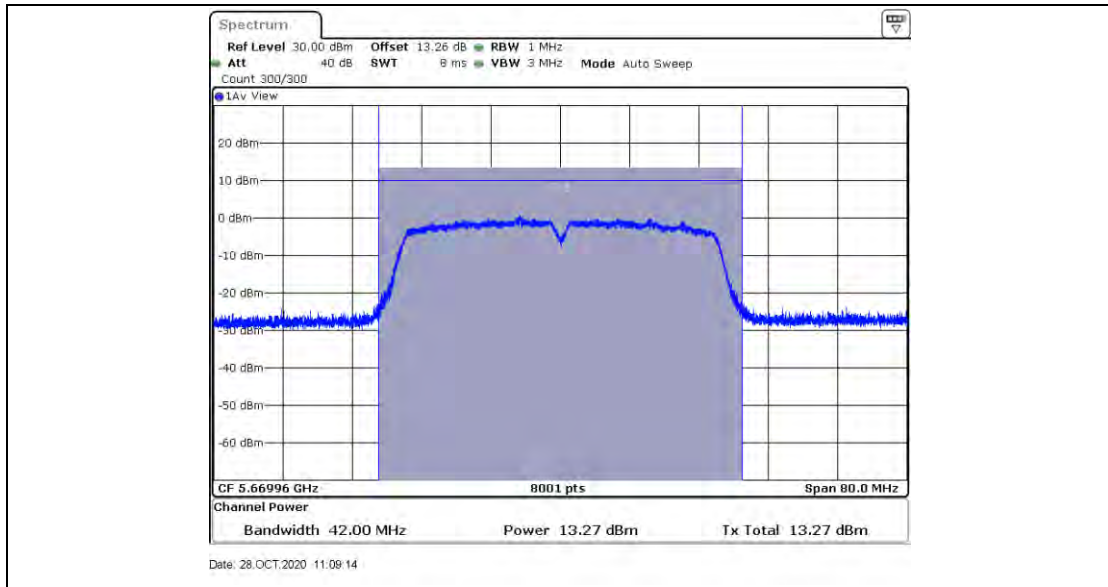
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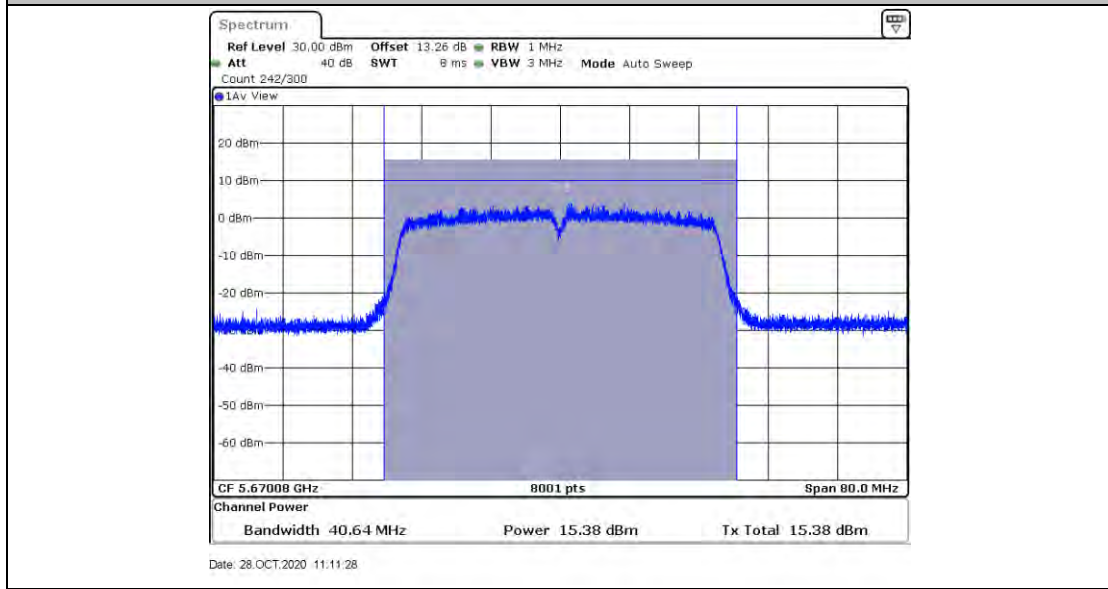
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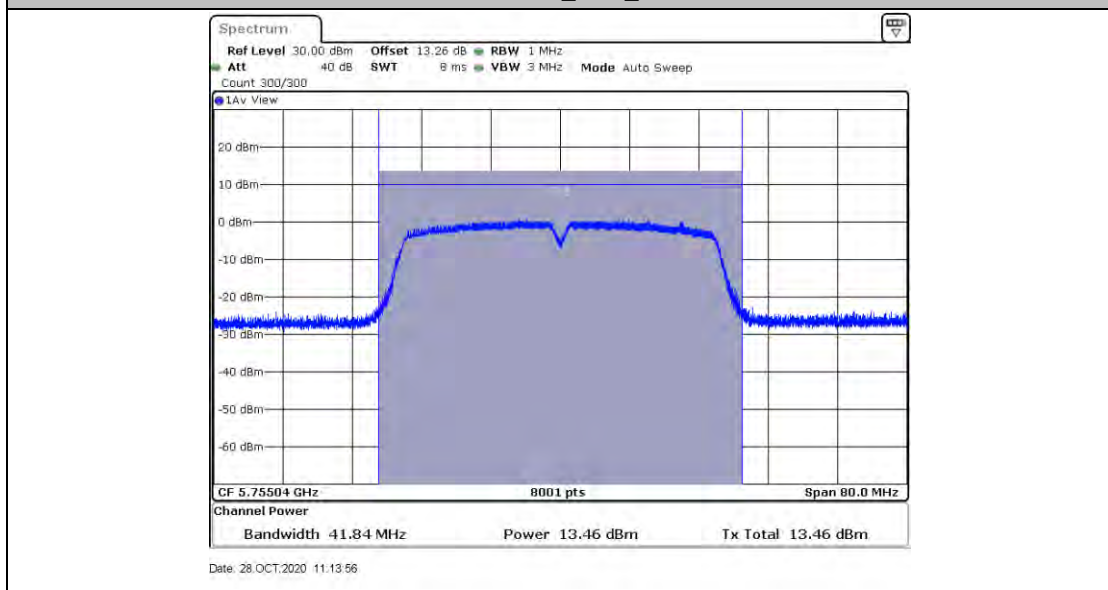
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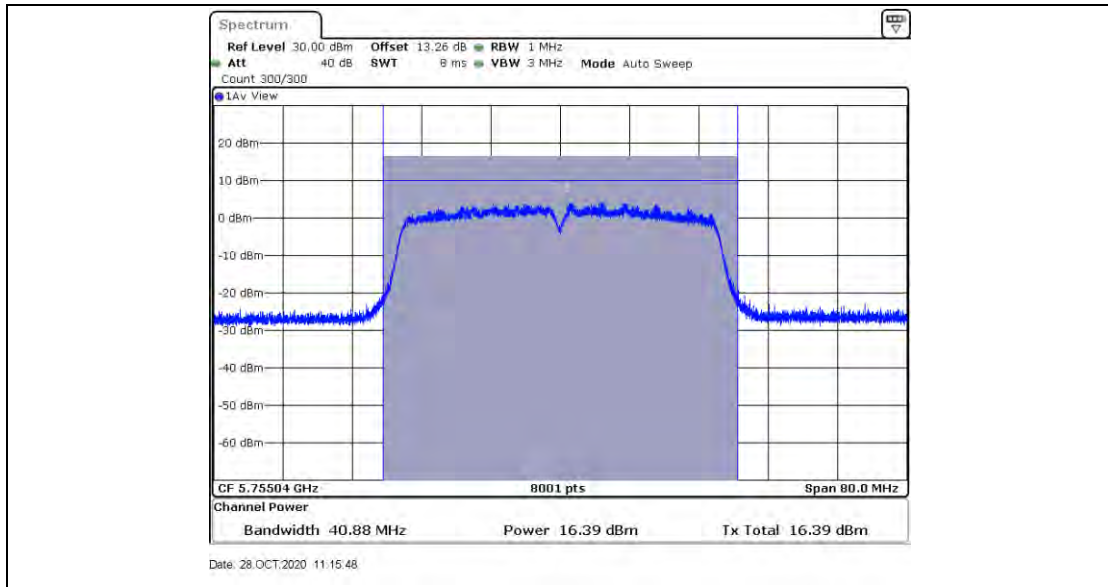
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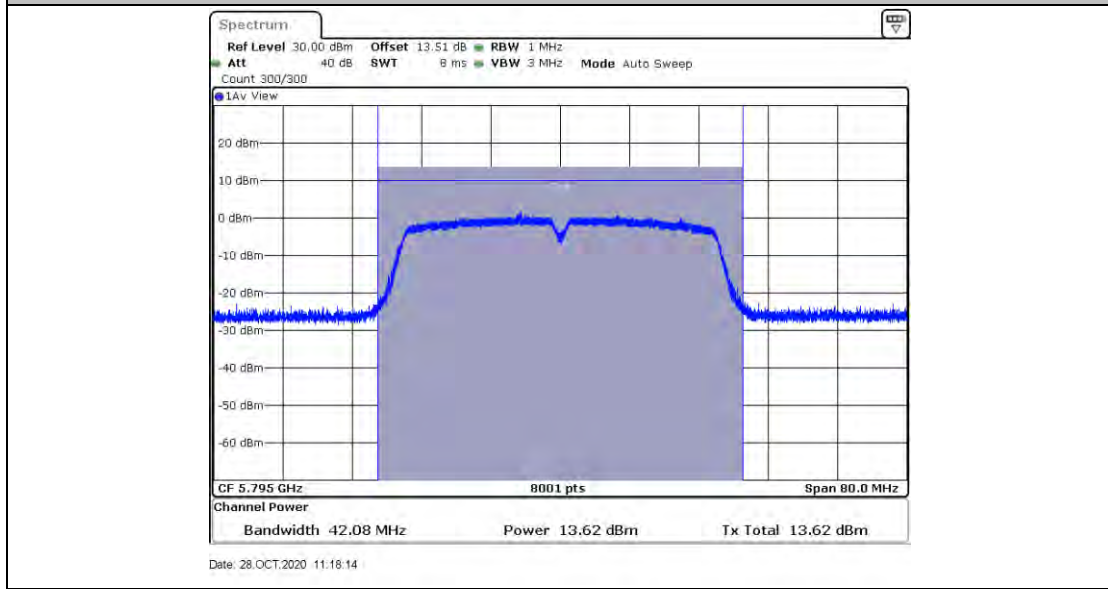
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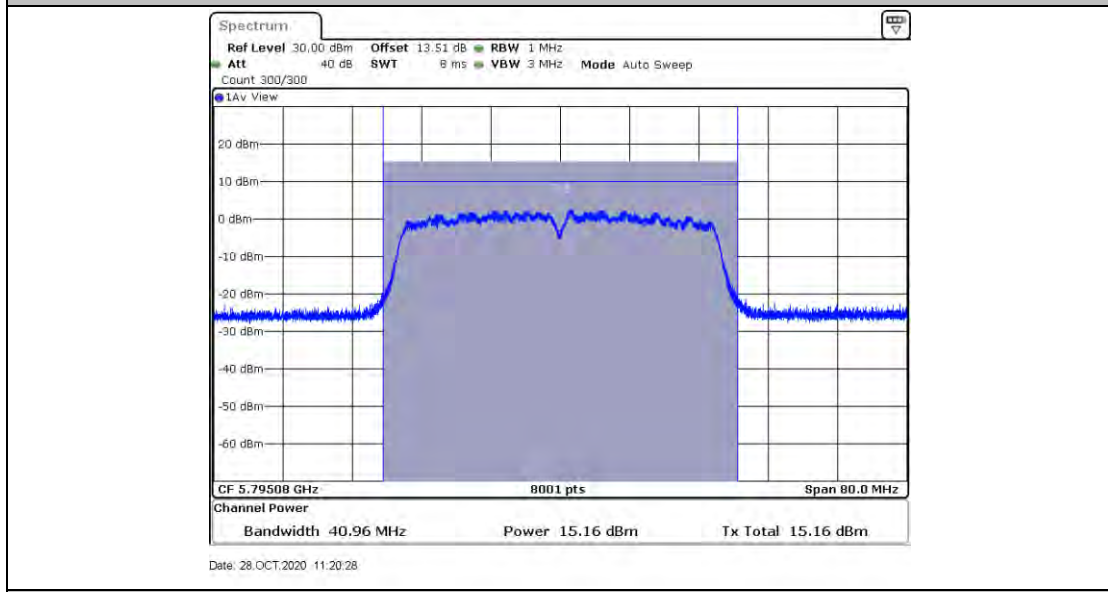
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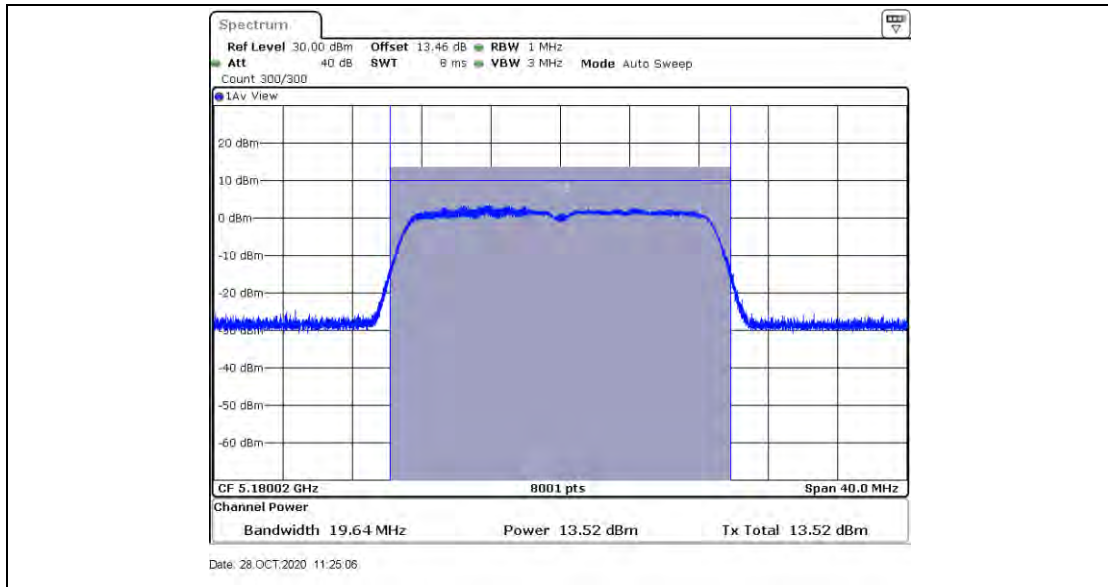
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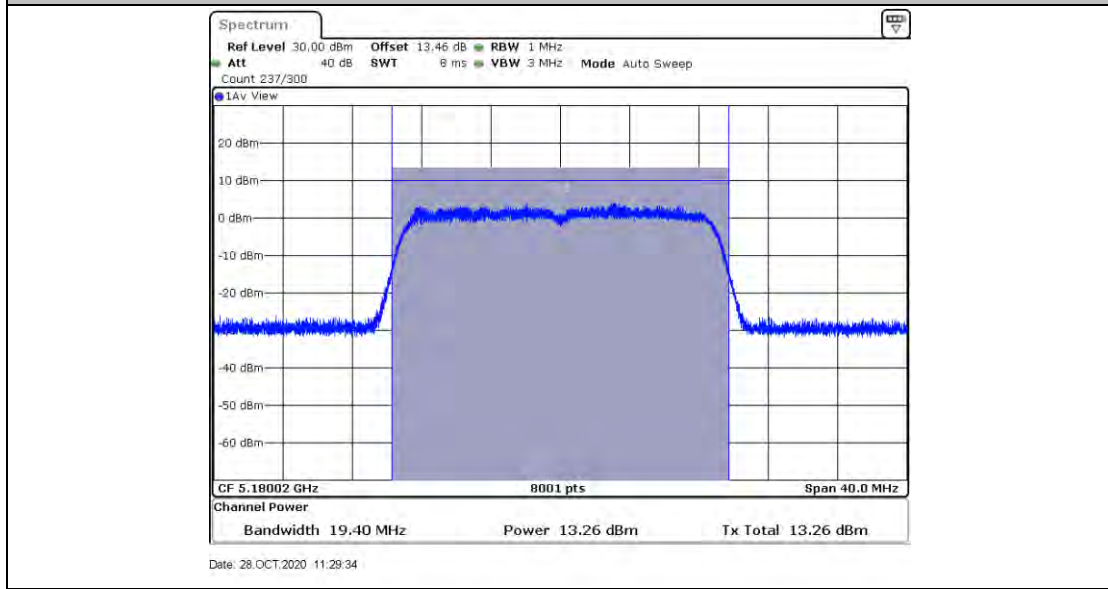
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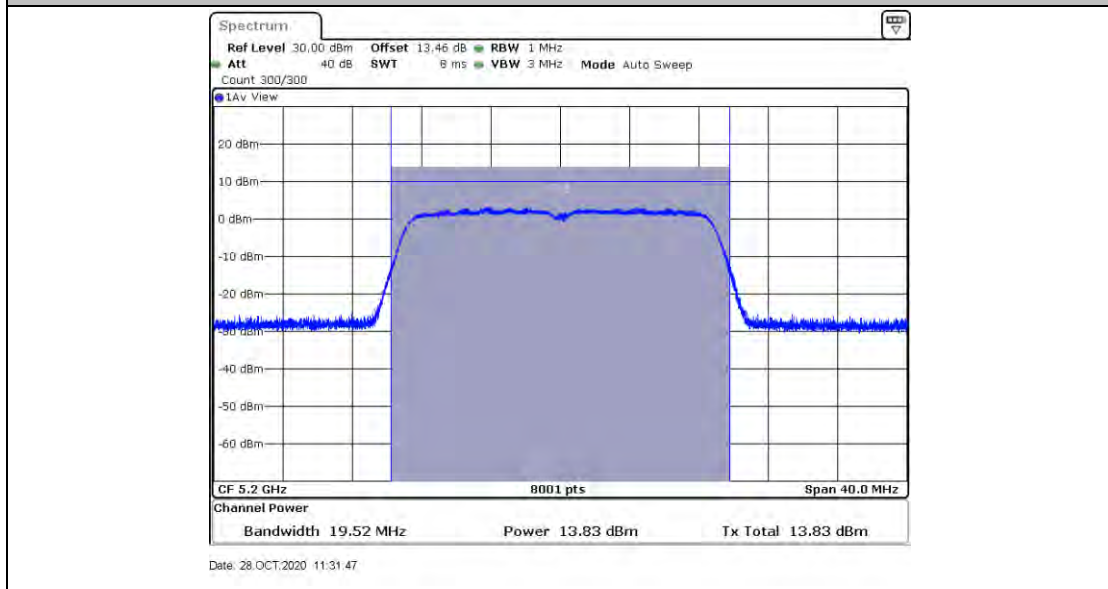
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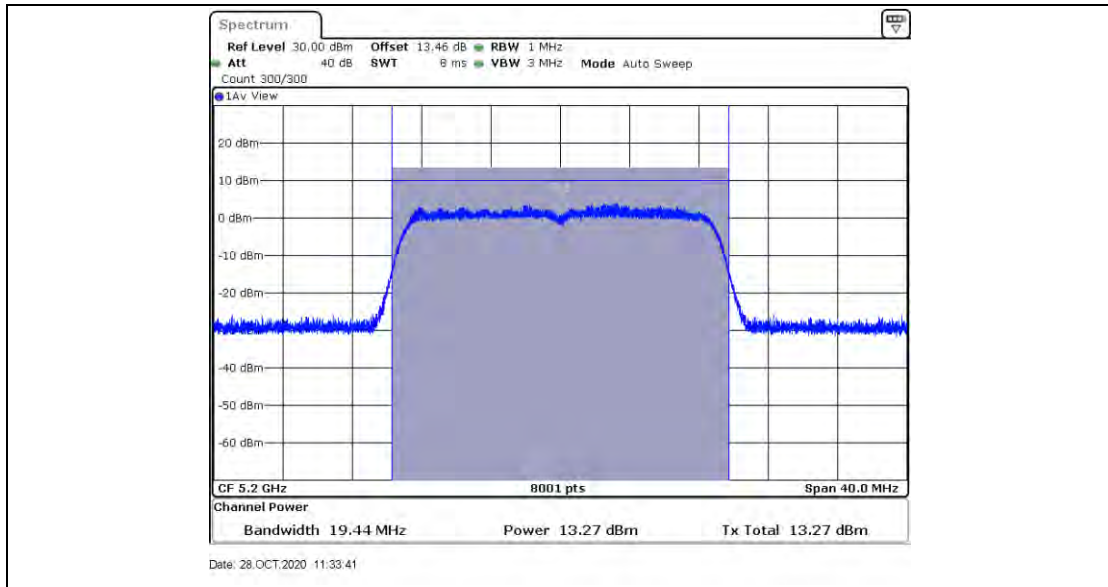
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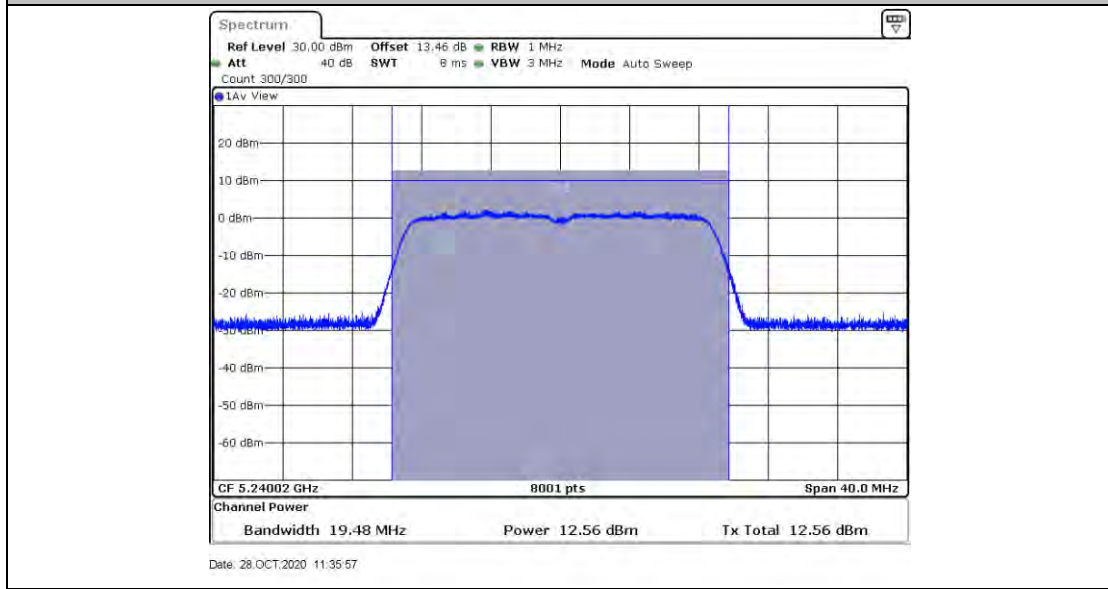
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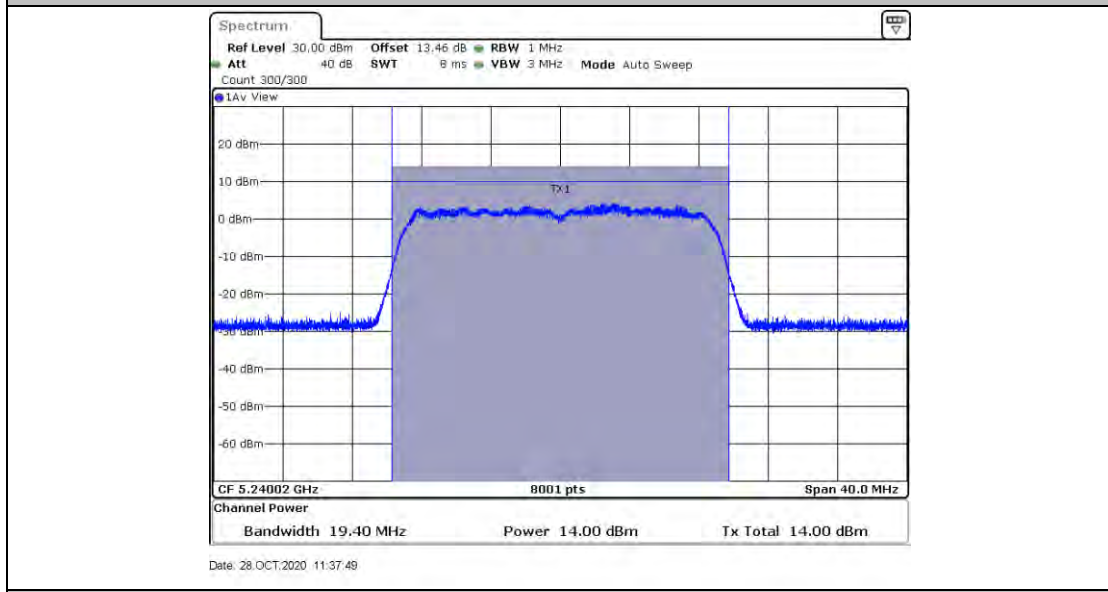
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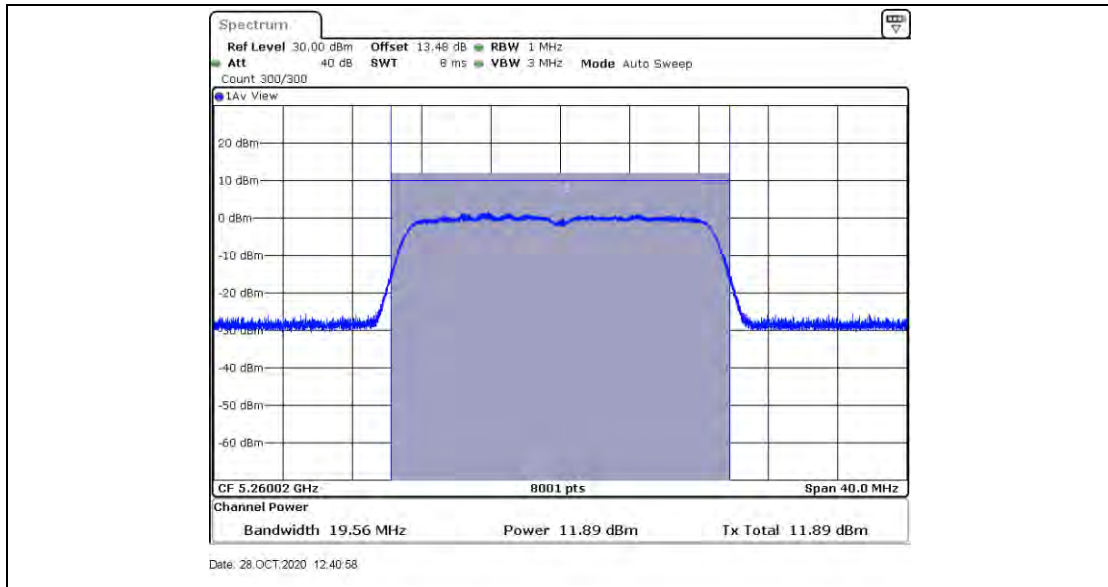
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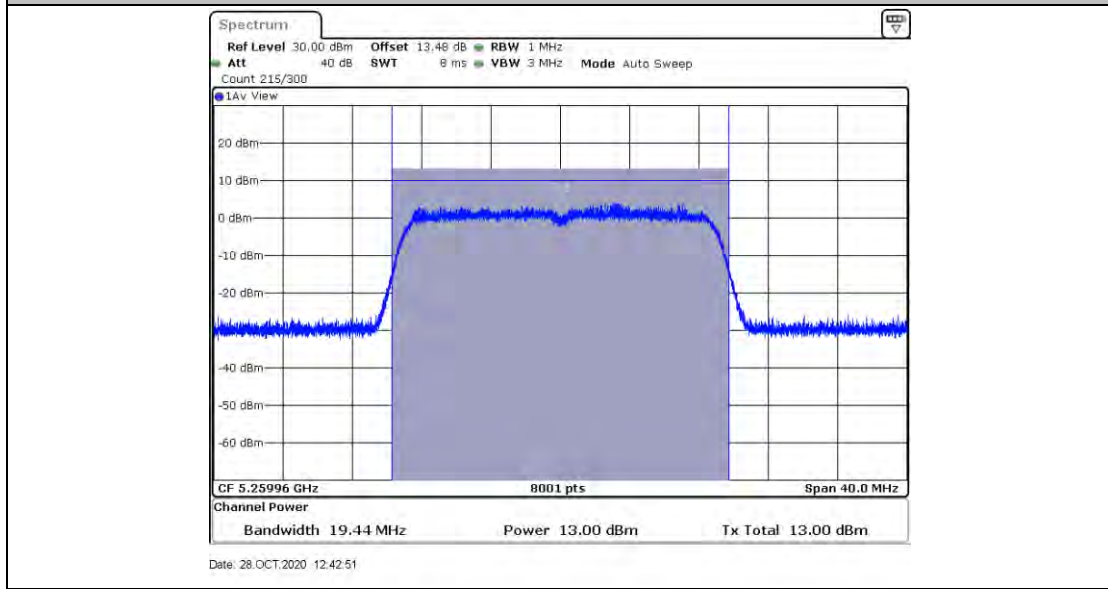
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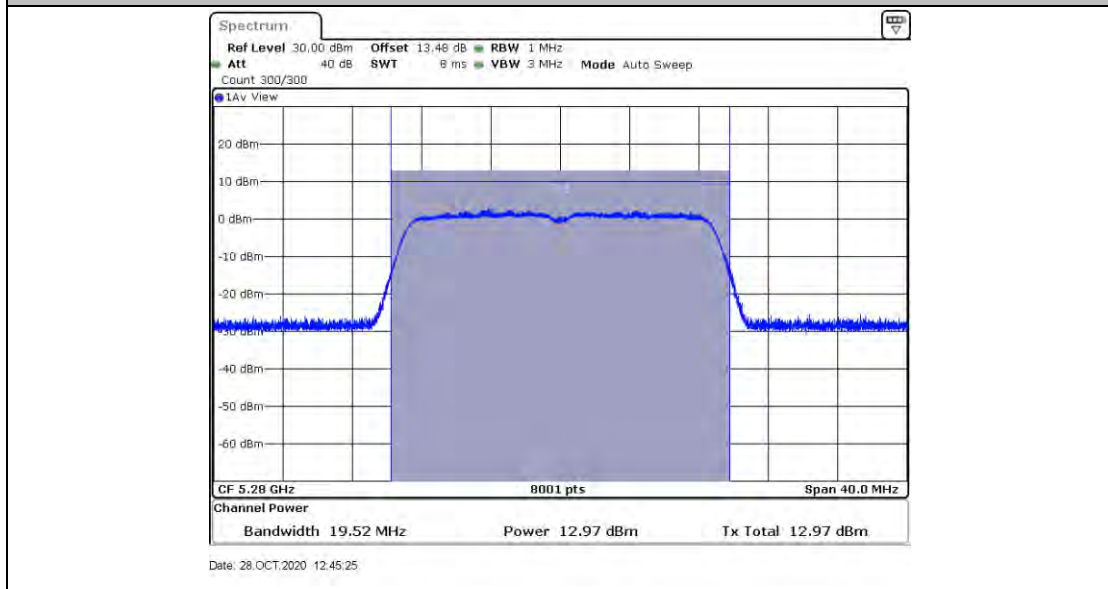
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11AC20MIMO_Ant2_5260



11AC20MIMO_Ant1_5280



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