# SPECIFICATIONS

Customer							
Product Name		Chip Antenna					
SunlordPart Number	,	SLDA	31-2R450G-S1T	F			
CustomerPart Numb	er						
□New Released, ⊠Revised] SPEC No.: SLDA190008							
This SPEC is total 8pag	es including	g specifications an	d appendix. ]				
[ROHS Compliant Parts]	1						
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# Shenzhen Sunlord Electronics Co., Ltd.

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[For Customer approval Only] Date:					
Qualification Status: □Full□Restricted□Rejecte			tricted⊔Rejected		
Approved By	Verified By	Re-checked By	Checked By		
Comments:					

# [Version change history]

	Rev.	Effective Date	Changed Contents	Change reasons	Approved By
ı	V01	Apr.28,2018	New release	/	Jimmy Ko
	V02	May.07,2020	Add test board	Meet customer requirements	Jimmy Ko
ı					

## 1. Scope

This specification applies to SLDA31-2R450G-S1TF of Chip Antenna.

# 2. Product Description and Identification (Part Number)

1) Description:

Multi-layer Chip Antenna

2) Product Identification (Part Number)

SLDA	<u>31</u>	-2R450G	<u>-S1</u>	I	<u>F</u>
1	2	3	4	(5)	6

1)	Туре	
SLDA	Multilayer Chip Antenna	

3	Center Frequency
2R450G	2450MHz

(5)	Packing
Т	Tape Carrier Package

② External Dimensions (L×W) (mm)		sions (L×W) (mm)
	31	3.2×1.6

4	Series Code	
	S1	

6	Hazardous Substance
	Free Products
	F

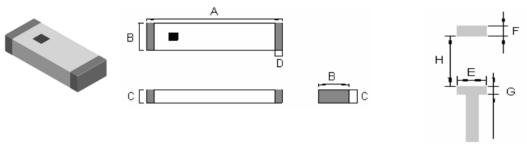
# 3. Electrical Characteristics

Part Number	SLDA31-2R450G-S1TF	
Center Frequency	2450 MHz	
Bandwidth	100 MHz min.	
Peak Gain(V-XZ)	-3.53 dBi typ.	
Average Gain(V-XZ)	-1 dBi typ.	
VSWR in BW	2.0 max.	
Port Impedance	50 ohm	
Power Capacity	3 W max.	

- a) Operating temperature range (individual chip without packing):-40°C~+85°C.
- b) Storage temperature range (individual chip without packing):-40  $^{\circ}$ C ~+85  $^{\circ}$ C.
- c) Storage temperature range (packaging conditions): -10  $^{\circ}$ C ~+40  $^{\circ}$ C and RH 70% (Max.).
- d) Test equipment: Network Analyzer:E5071C.

#### 4. Shape and Dimensions

1) Dimensions and recommended PCB pattern for reflow soldering: See Fig. 4-1, Fig. 4-2 and Table 4-1



Unit: mm

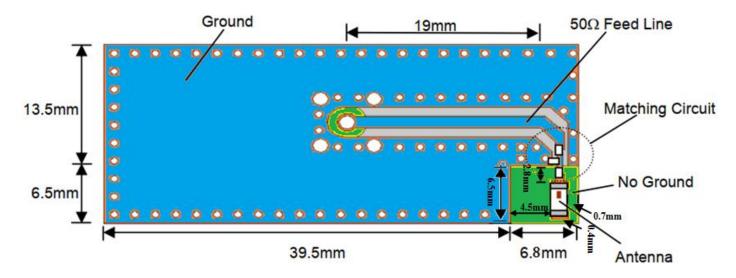
	А	В	С	D.	E	F	G	Н
SLDA31	3.2±0.2	1.6±0.2	1.2±0.2	0.5±0.2	1.6±0.2	0.8±0.2	0.8±0.2	2.6±0.2

# 2) Terminal Configuration:



No.	Terminal Name	No.	Terminal Name
(1)	Feeding Point	(2)	NC

# 3) Test Board



#### . Test and Measurement Procedures

#### 5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

a. Ambient Temperature:  $20\pm15^{\circ}$ C b. Relative Humidity:  $65\pm20\%$ 

c. Air Pressure: 86 KPa to 106 KPa

If any doubt on the results, measurements/tests should be made within the following limits:

a. Ambient Temperature: 20±2°C
b. Relative Humidity: 65±5%
c. Air Pressure: 86KPa to 106 KPa

# 5.2 Visual Examination

a. Inspection Equipment: 20 X magnifier

#### 5.3 Reliability Test

3 Reliability Test		
Items	Requirements	Test Methods and Remarks
5.3.1 Terminal Strength	No visible mechanical damage.	<ul> <li>Solder the inductor to the testing jig (glass epoxy board shown as the following figure) using leadfree solder. Then apply a force in the direction of the arrow.</li> <li>10N force for 3216 series.</li> <li>Keep time: 10±1sec.</li> </ul> Chip <ul> <li>10N/10±1s</li> <li>Speed: 1.0mm/s</li> <li>Glass Epoxy Board</li> </ul> Mounting Pad
5.3.2 Resistance to Flexure	No visible mechanical damage.	<ol> <li>Solder the chip to the test jig (glass epoxy board) using a leadfree solder. Then apply a force in the direction shown as the following figure. Solder the chip to the test jig (glass epoxy board) using leadfree solder. Then apply a force in the direction.</li> <li>Flexure: 2mm</li> <li>Pressurizing Speed: 0.5mm/sec</li> </ol>

The chip shall be stabilized at normal condition for 1~2

hours before measuring.

# 6. Packaging and Storage

#### 6.1 Packaging

There is one type of packaging for the Chip Antenna. Please specify the packing code when ordering.

Satisfy electrical Characteristic.

6.1.1 Tape Carrier Packaging:

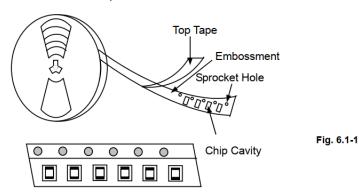
Packaging code: T

- i. Tape carrier packaging are specified in attached figure Fig.6.1-1~3
- ii. Tape carrier packaging quantity please see the following table:

Type	3216[1206]
Tape	Embossed Tape
Quantity	3К

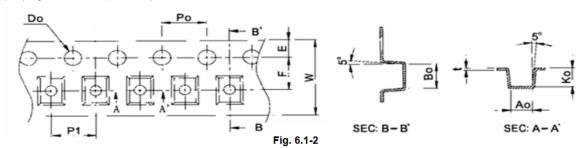
#### a) Taping Drawings (Unit: mm)

#### **Embossed Tape**



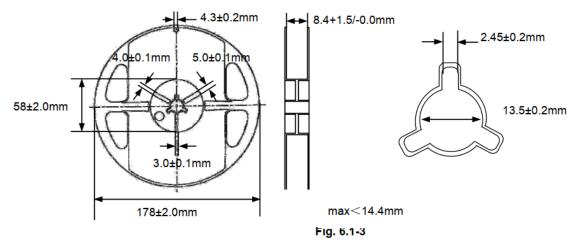
Remark: The sprocket holes are to the right as the tape is pulled toward the user.

# b) Taping Dimensions (Unit: mm)



Туре	W	P1	E	F	D0	PO	КО	A0	ВО	t
Tolerance	±0.1	±0.1	±0.1	±0.15	+0.1/	±0.1	±0.1	±0.1	±0.1	±0.05
SLDA31	8.00	4.00	1.75	3.5	1.5	4.0	1.50	1.80	3.50	0.22

## c) Reel Dimensions (Unit: mm)



#### 6.2 Storage

- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at -55 °C  $\sim+125$  °C or less and 70% RH or less
- c. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H<sub>2</sub>S).
- d. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- e. Solderability specified in Clause 5.3.6shall be guaranteed for 12months from the date of delivery on condition that they are stored at the

environment specified in Clause 3. For those parts, which passed more than 12 months shall be checked solder-ability before use.

#### 7. Recommended Soldering Technologies

#### 7.1 Reflow Profile

1. Preheat condition: 150 ~200°C/60~120sec.

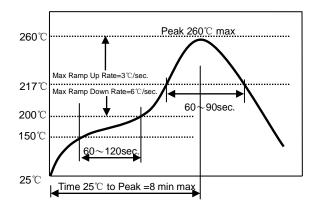
2. Allowed time above 217°C: 60~90sec.

3. Max temp: 260°C

4. Max time at max temp: 10sec.5. Solder paste: Sn/3.0Ag/0.5Cu

6. Allowed Reflow time: 3x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



# 7.2 Iron Soldering Profile

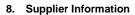
7. Iron soldering power: Max.30W8. Pre-heating: 150 °C / 60 sec.

9. Soldering tip temperature: 350°C Max.

10. Soldering time: 3 sec Max.11. Solder paste: Sn/3.0Ag/0.5Cu12. Max 1 times for iron soldering

12. Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



a) Supplier:

# Shenzhen Sunlord Electronics Co., Ltd.

b) Manufacturer:

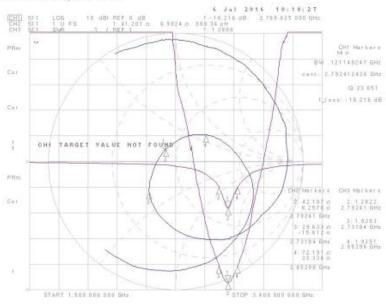
#### Shenzhen Sunlord Electronics Co., Ltd.

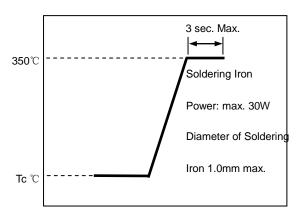
c) Manufacturing Address:

### Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110

#### Appendix 1

1. Without Matching circuit electrical performance:

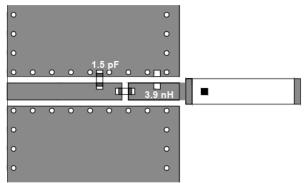




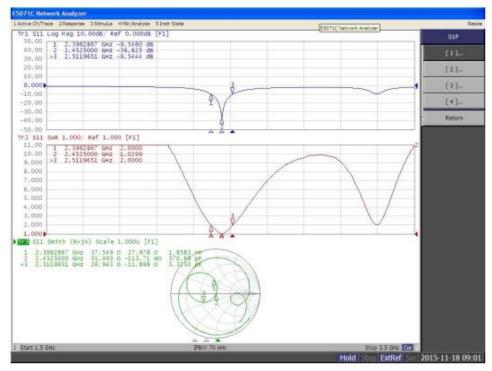
# 2.With Matching Circuit: evaluation board:80\*40mm

\*Line width should be designed to match  $50\Omega$  characteristic impedance, depending on PCB material and thickness.

(Matching circuit and component values will be different, depending on PCB layout)

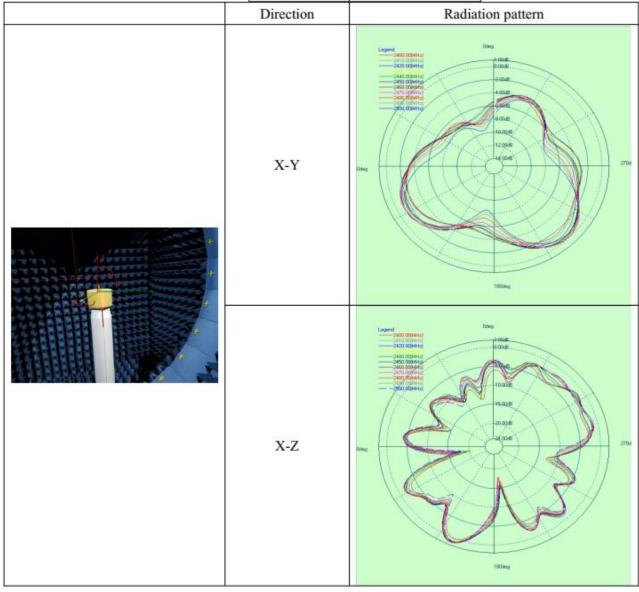


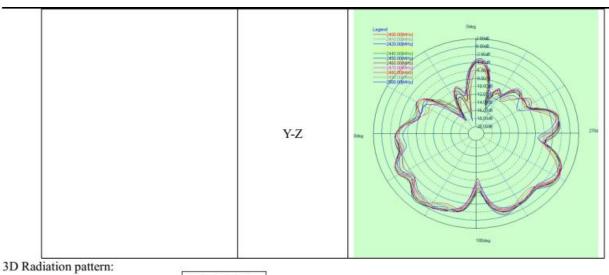
# electrical performance:



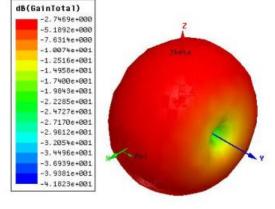
Gain & 2D Radiation pattern:

Frequency	Gain(dbi)	
2400	-4. 12543482	
2410	-4. 180958401	
2420	-3. 877599807	
2430	-3. 930632294	
2440	-3. 676540694	
2450	-3. 582882743	
2460	-3. 53429473	
2470	-3. 598055402	
2480	-3. 690017588	
2490	-3. 87625979	
2500	-3. 918544199	

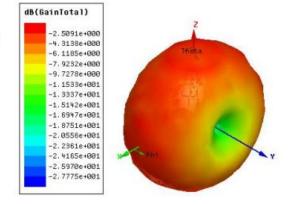








#### 2450MHz



2500MHz

