DX-BT24

MODULE SPECIFICATION

Version：2.5

Date：2022-01-05

Update record

Version	Date	Illustrate	Author
V2.1	2018/10/1	initial version	DL
V2.2	2020/8/5	Optimize the amount of data Update frequency offset parameters	DL
V2.3	2020/10/30	Modify the reset pin from high level reset to low level reset	DL
V2.4	2020/11/30	Correct the description error of the pin function	DL
V2.5	2022/01/05	new version	DL

Contact Us

Shenzhen DX-SMART Technology Co., Ltd.

Email：sales@szdx-smart.com

Tel：0755-29978125

Web：www.szdx-smart.com

Add：Room 601, Block A1, Huafeng Zhigu, Hangkong Road, Baoan District, Shenzhen

Contents

Table index

Picture index

1. Introduction

1.1. Overview

DX-BT24 Bluetooth module is built by Shenzhen DX-SMART Technology Co., Ltd. for intelligent wireless data transmission，It uses the DIALOG 14531 chip and follows the Bluetooth BLE 5.1 specification. Support AT commands, users can change the serial port baud rate, device name and other parameters according to their needs, flexible to use。This module supports UART, SPI, I2C and other interfaces, supports IO port control, ADC acquisition，It has the advantages of low cost, low power consumption, high receiving sensitivity, etc，Only a few peripheral components are required to realize its powerful functions, and various projects can be customized and developed according to customer needs.

1.2. Key Features

- DIALOG 14531 main control chip
- Bluetooth BLE 5.1
- Power consumption can be as low as 2 uA
- Open distance 90 M
- Transmission rate 10K Byte/s
- Optional on-board antenna or external antenna
- Working temperature -40-+85 ℃
- Support UART, IIC, SPI, GPIO hardware interface
- Have CE, FCC, SRRC certification
- Optional iBeacon mode program
- Optional direct drive mode program
- Optional single master mode program
- Optional one-master multi-slave mode program
- Customizable Bluetooth gateway

1.3. Application

- Smart home
- Smart education equipment
- Medical equipment monitoring and wireless control
- Measurement and monitoring system
- Industrial sensors and controls
- Tracking and positioning

1.4. Block Diagram

The following figure shows the functional block diagram of the DX-BT24 Bluetooth module，Explains the following main functions：

- Power section
- Baseband part
- Memory
- RF part
- Peripheral interface
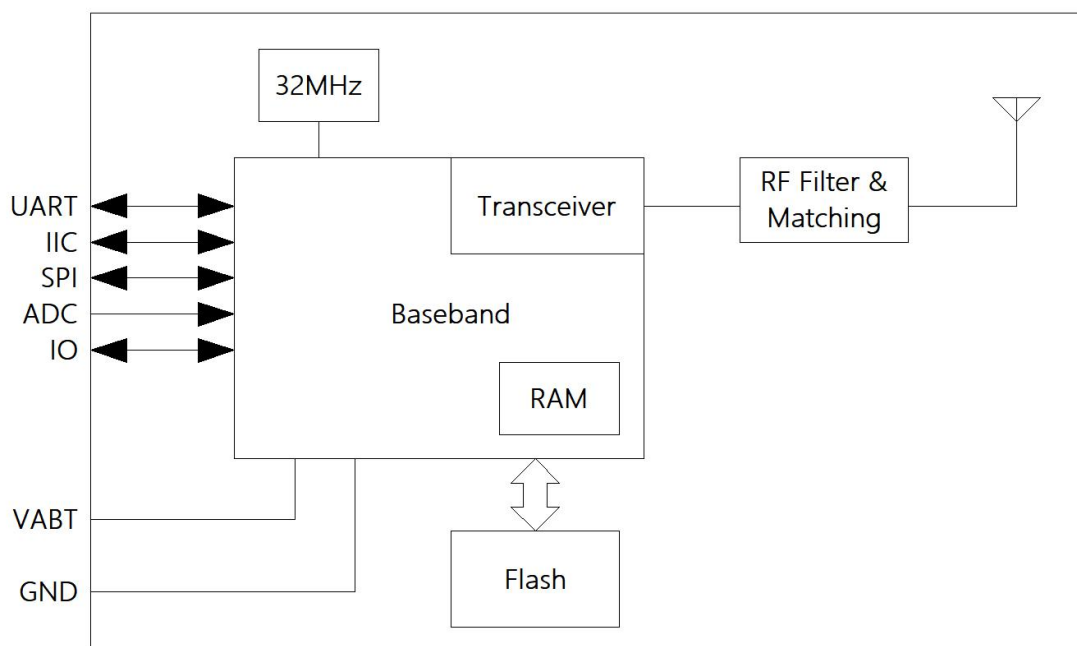
Figure 1：Functional block diagram

1.5. Basic Parameters

Table 1：Basic parameter table

Parameter	Details	Parameter	Details
Chip model	DA14531	Module model	DX-BT24
Bluetooth specifications	BLE 5.1	Module size	26.6(L) x 12.8 (W) x 2.3(H) mm
Operating Voltage	3.3V	Working current	270 uA
protocol	GATT，ATT，GAP	MTU value	253 bytes
Sensitivity	-94dBm@0.1%BER	Transmit power	-19.5~+2.5dBm
Modulation	GFSK	Frequency band	2.402GHz -2.480GHz ISM band
RF input impedance	50Ω	Frequency hopping and channel	1600hops/s 2MHz space 40 channels
Antenna interface	Onboard antenna / external antenna (optional)	Hardware interface	UART I2C GPIO
Operating temperature	MIN:-40℃ - MAX:+85℃	Humidity	10%-95% non-condensing

2. Application Interface

2.1. Module Pin Definition
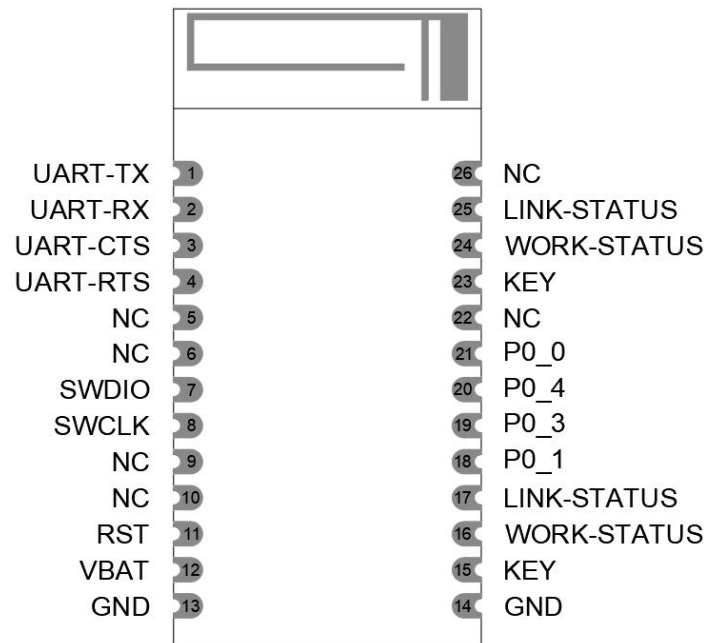
Figure 2：Module pin definition

2.2. Pin Definition Description

Table 2：Pin definition description table

Pin number	Pin name	Pin function	illustrate		
1	UART-TX	Serial data output			
2	UART-RX	Serial data input			
9	UART-CTS	UART clear to send, active low	Connect to pin 8		
10	UART-RTS	UART request to send, active low	Connect to pin 7		
5,6,9,10,22,26	NC	NC			
7	SWDIO	Debug data port	Programmable input/output line		
8	SWCLK	Debug clock port	Programmable input/output line		
11	RST	Reset			
		Wake up from hibernation mode	For details, please refer to 2.5.4		
12	VBAT	Power input pin	3.3V(Typical value)		
13	GND	Power Ground			
14	GND	Power Ground			
15	KEY	Disconnect pin	For details, please refer to 2.5.5		
		Low power mode wake up	For details, please refer to 2.5.5		
		Enter pairing mode	For details, please refer to 2.5.5		
16	WORK-STATUS	Module working status output pin	slave module	Not connected: 1s high 1s low Connection Status：3s high 50ms low Always low in low power consumption and hibernation mode	
			Master module	Searching and connecting: 300ms high 300ms low Connection status: always high	
17	LINK-STATUS	Bluetooth connection status pin	Not connected：Output low level Connection Status：output high level		

18,19,20,21	P0_1,P0_3,P0_4, P0_0	NC	Can only be left floating
23	KEY	Connect to pin 15	Programmable input/output line
24	WORK-STATUS	Connect to pin 16	Programmable input/output line
25	LINK-STATUS	Connect to pin 17	Programmable input/output line

2.3. Operating Mode

Table 3：Work mode table

mode	Function
Normal working mode	All functions and peripheral interfaces can be used normally
Low power mode	When not connected, Only open broadcast, all peripheral interfaces are closed
	In the connected state, all functions and peripheral interfaces can be used normally
Hibernation mode	All running RAM blocks of the module are closed, and only the tick clock inside the chip is reserved for wake-up. Broadcasting and all peripheral interfaces are closed

2.4. Energy-saving Mode

2.4.1. Low Power Mode

When the host and the module are connected through the serial port, the module can enter the low power consumption mode through the following steps；

- Use AT+PWRM command to enable low power consumption function. For more information about AT commands, Please refer to "DX-BT24&BT24-S Bluetooth Module_Serial UART_Application Guide".
- In low power consumption mode, the module can be found and connected.
- Send a 200ms low-level pulse to the 15-pin KEY of the module to wake up the module to normal working mode.
- To enter low power mode again：

When connected: disconnected, the module automatically enters low power consumption mode；

When it is not connected after waking up: You need to send the AT+PWRM command again to enter the low power consumption mode or reset, power off and restart to enter.

- For hardware, please refer to 2.5.5 wake-up circuit below.

2.4.2. Hibernation Mode

When the host and the module are connected through the serial port, the module can enter the hibernation mode through the following steps；

- Use the AT+PWRM command to enable the hibernation function. For detailed information about the AT command, Please refer to "DX-BT24&BT24-S Bluetooth Module_Serial UART_Application Guide".
- In hibernation mode, the module cannot be found and connected.
- Send a 200ms low-level pulse to the 11-pin reset pin to reset and wake up the module to normal working mode.
- After waking up, if you need to enter the hibernation mode again, you need to send the AT+PWRM command；
- For hardware, please refer to 2.5.4 reset circuit below.
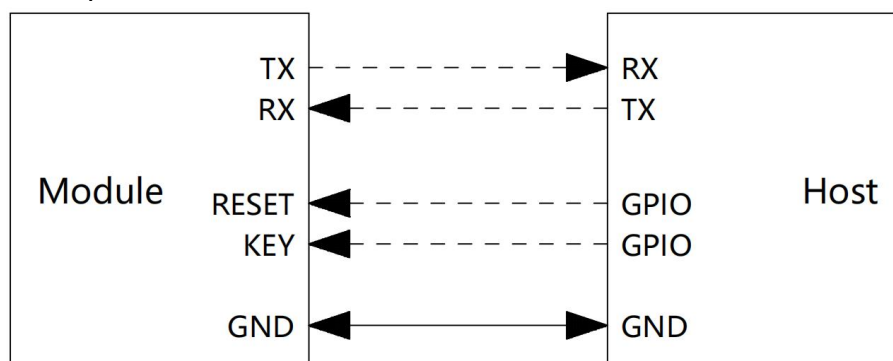
Figure 3：Energy-saving mode control diagram

2.5. Power Design

2.5.1. Power Interface

Table 4：Power interface pin definition table

Pin name	Pin number	describe	Minimum	Typical value	Max	unit
VBAT	12	Module power	2.8	3.3	3.6	V
GND	13,14	Land	-	0	-	V

2.5.2. Power Supply Stability Requirements

The power supply range of DX-BT24 is 2.8~3.6V. It is necessary to ensure that the input voltage is not lower than 2.8V. The following figure shows the voltage drop of VBAT during radio frequency burst transmission.
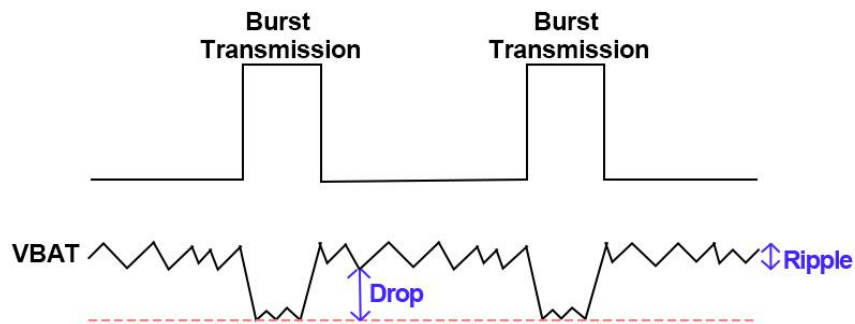
Figure 4：Burst transmission power supply requirements

In order to reduce the voltage drop, it is recommended to reserve 2 (22uF, 0.1uF) chip multilayer ceramic capacitors (MLCC) with the best ESR performance for VBAT, and the capacitors should be placed close to the VBAT pin. The reference circuit is as follows：
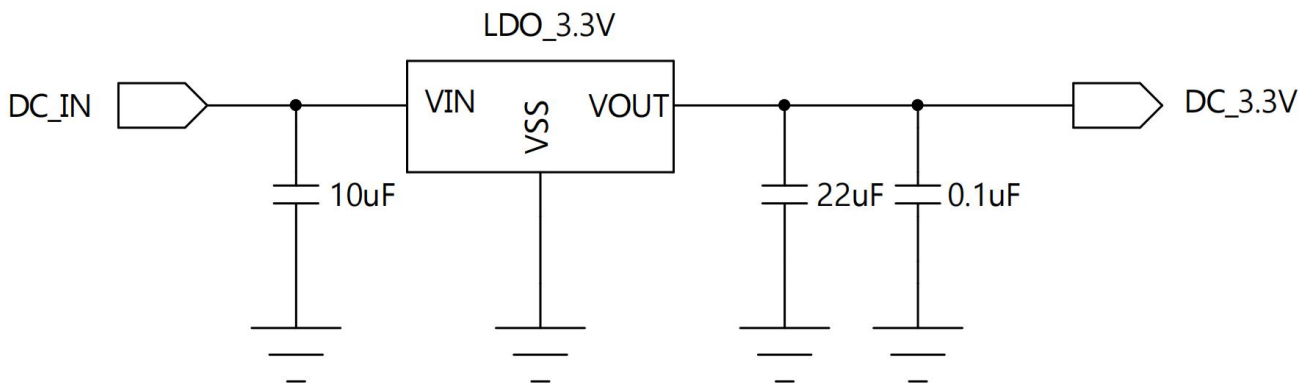
Figure 5：powered reference circuit

2.5.3. Power Down Requirements

Since the working current of the chip of this module is very low, when the power is disconnected, if the IO port of the peripheral MCU has a weak current supplying the module, the module is likely to be in an abnormal working state at this time, so after disconnecting the power of the module, the MCU is connected to the module All IO ports must be configured as pull-down.

2.5.4. RST Reset Pin Description

Table 5：RST pin definition table

Pin name	Pin number	I/O	describe	Remark
RST	11	DI	Module reset	Active low；NC if not used

Table 6：RST pin definition

Module status	Method of operation	Result
Hibernation	Pull down the RST pin for at least 200ms and then release	Reset wake
Other status	Pull down the RST pin for at least 200ms and then release	Reset

Remark

The RST signal is more sensitive to interference, so it is recommended that the trace should be as short as possible, and it needs to be handled with ground.
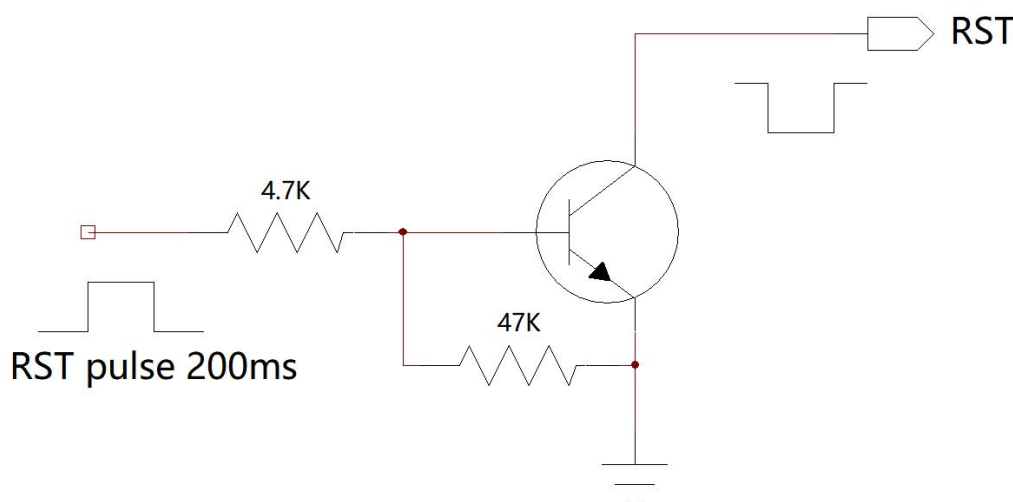
Figure 6：Reset reference circuit
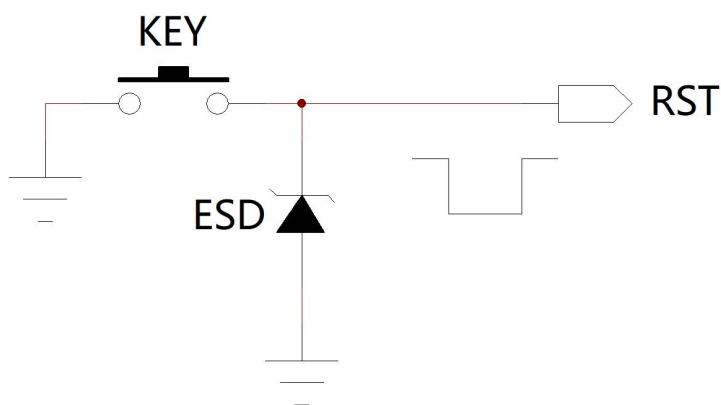
Figure 7：Key reset reference circuit

2.5.5. KEY Pin Definition

Table 7：KEY Pin definition table

Pin name	Pin number	I/O	describe	Remark
KEY	15	DI		

Table 8：KEY pin function definition table

Program	State	Method of operation	result
slave module	Connection Status	Pull down the KEY pin for at least 200ms and then release	disconnect
	Low power state	Pull down the KEY pin for at least 200ms and then release	wake
	standby mode	Pull the KEY pin low for at least 100ms (can be released after being connected)	pairing mode
master module	Connection Status	Pull down the KEY pin for at least 200ms and then release	disconnect
	standby mode	Pull down the KEY pin for at least 200ms and then release	Clear memory address code

Remark

Pairing mode: After the slave module enters pairing mode, after the search mode of the master module is set to pairing mode, the master module will automatically connect to the slave module.This pairing mode is only valid for our main module.
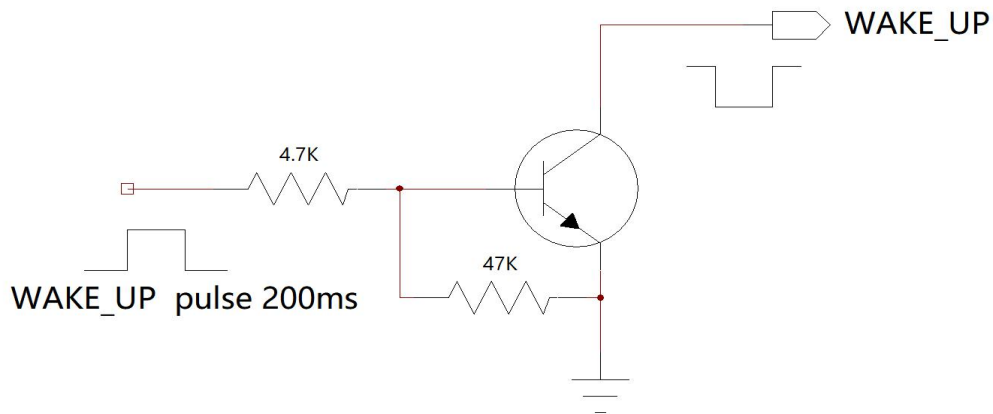
Figure 8：Open-collector drive wake-up reference circuit
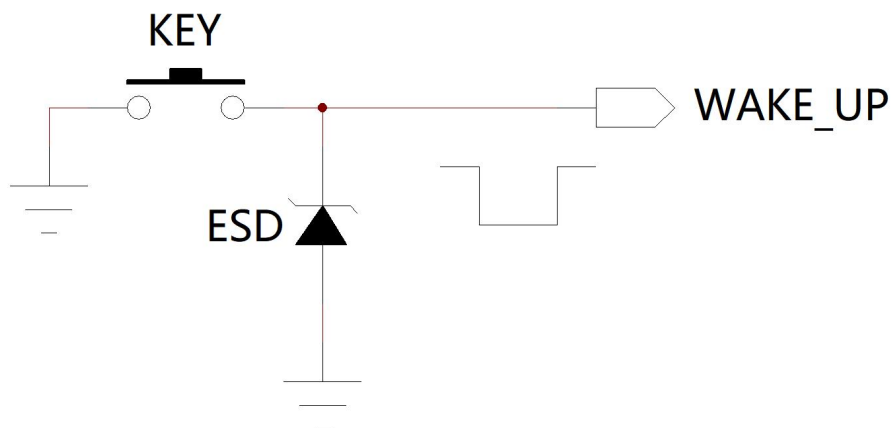
Figure 9：Key reset reference circuit

2.6. Hardware Physical Interface

2.6.1. UART Interface

Four signal pins are used to realize UART function. When DX-BT24 is connected to another digital device, UART_RX and UART_TX transfer data between the two devices. The remaining two pins, UART_CTS and UART_RTS, can be used to implement RS232 hardware flow control, and they are both active at low level, that is, transmission is allowed at low level, and transmission is stopped at high level.

| Possible UART settings		
parameter	**probable value**	
BAUD	minimum	2400baud （≤1%Error）
	standard	9600baud （≤1%Error）
	maximum	115200baud （≤1%Error）
flow control	RTS/CTS，or none	
Parity check	None, odd or even	
Number of stop bits	1/2	
Number of bits per channel	8	

2.6.2. Universal Digital IO Port

Five general digital lO ports are defined in the module. All these IO ports can be configured through software to realize various functions, such as button control, LED drive or interrupt signal of the main controller, etc. Keep floating when not in use

2.6.3. I2C Interface

● Two-wire I2C serial interface composed of serial data line (SDA) and serial clock (SCL)
● Support two speeds

standard mode　0-100Kbit/s

Fast mode　≤400Kbit/s

● 32-position sending and receiving FIFO depth （32x8-bit Rx & 32x 10-bit Tx）
● Support 7-bit and 10-bit addressing mode and regular call addressing mode.

The I2C interface is an internal circuit that allows communication with an external l2C interface. The external I2C interface is an industry standard two-wire serial interface for connecting external hardware. These two serial lines are called serial data line (SDA) and serial

clock line (SCL). The I2C module provides two data transmission rates: 100kHz in standard mode and 400kHz in fast mode. The I2C module also has an arbitration detection function to prevent multiple hosts from attempting to transmit data to the l2C bus at the same time.
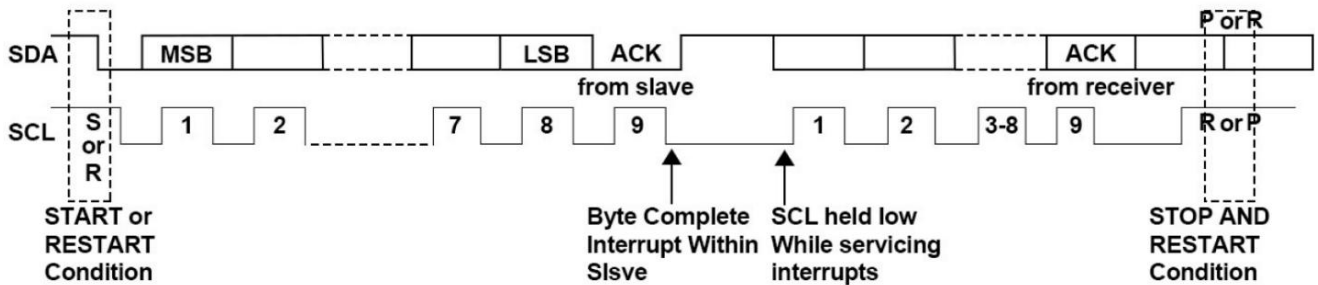
Figure 10：IIC communication sequence diagram

2.6.4. SPI Interface

- Master and slave mode
- From 4bit to 32bit operation
- Main clock up to 32MHz
- Support DMA

This controller implements serial peripheral interface (SPI™) in master-slave mode. The serial interface can transmit and receive from 4-bit to 32-bit in master/slave mode. The controller includes separate TX and RXFIFOs and DMA handshake support. From the mode clock speed is independent of the system clock speed. In addition, the master clock speed can be as fast as the system clock speed. The controller can generate an interrupt when X or RXFIFOs reach the data threshold.
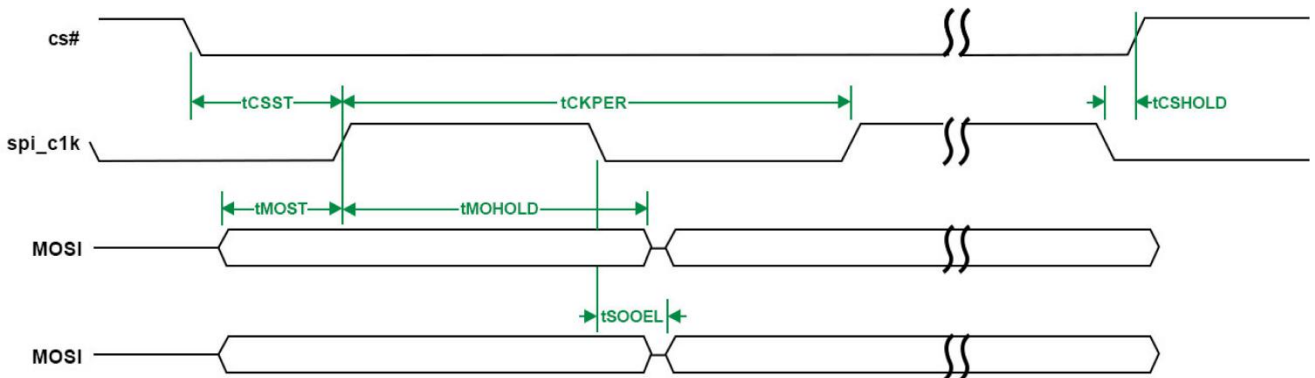
Figure 11：SPI communication timing diagram

2.6.5. Analog-to-digital Converter （ADC）

- Typical 10-bit dynamic ADC of 125 ns
- Maximum sampling rate was 1 Msample/s
- Ultra-low power consumption (20 uA Typical power supply current, 100 ksample/s)
- Configurable attenuator: 1x, 2x,3x, 4x

The device integrates a 10-bit high-speed and ultra-low-power general-purpose analog-to-digital converter (ADC). It can operate in unipolar (single-ended> mode and bipolar (differential) mode. ADC has its own 0.9v regulator (LDO), which represents the full-scale reference voltage

2.7. Reference Connection Circuit
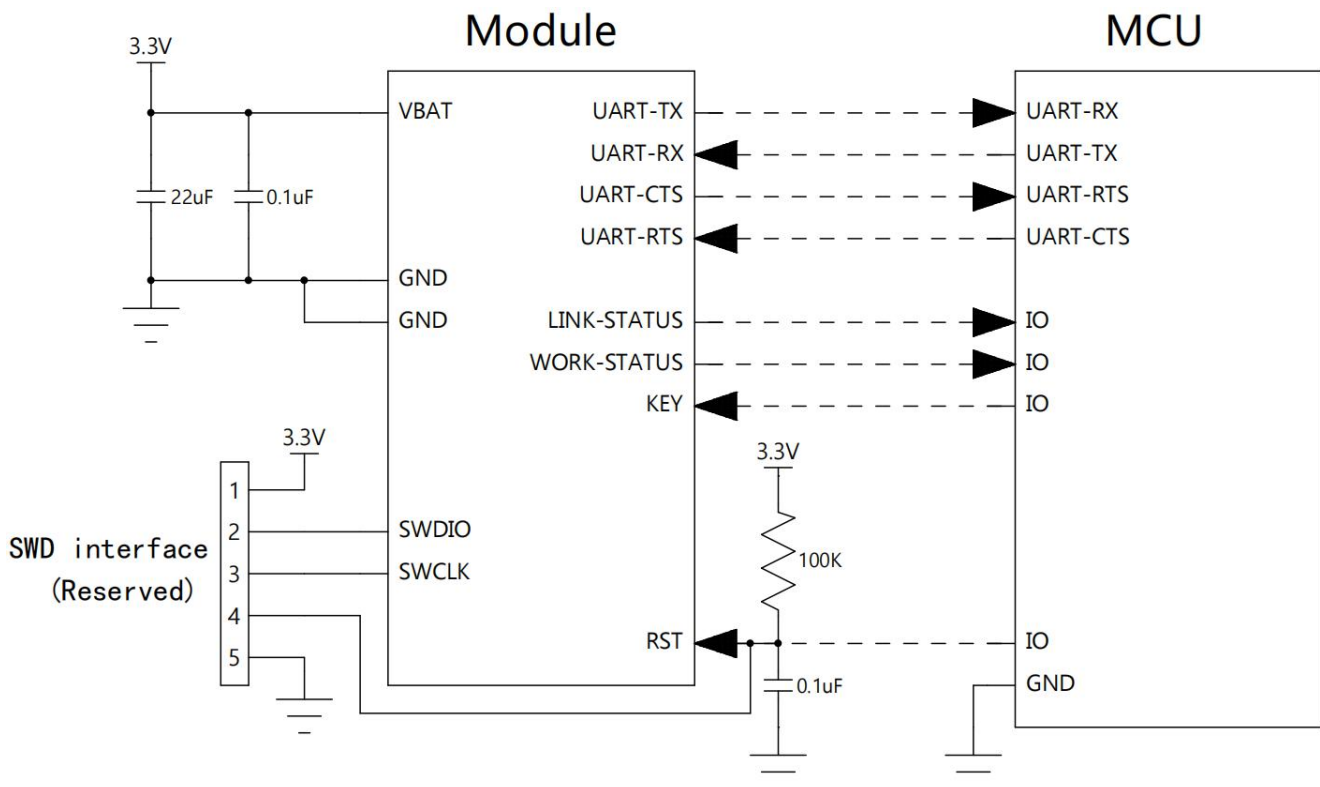
Figure 12： Typical application circuit
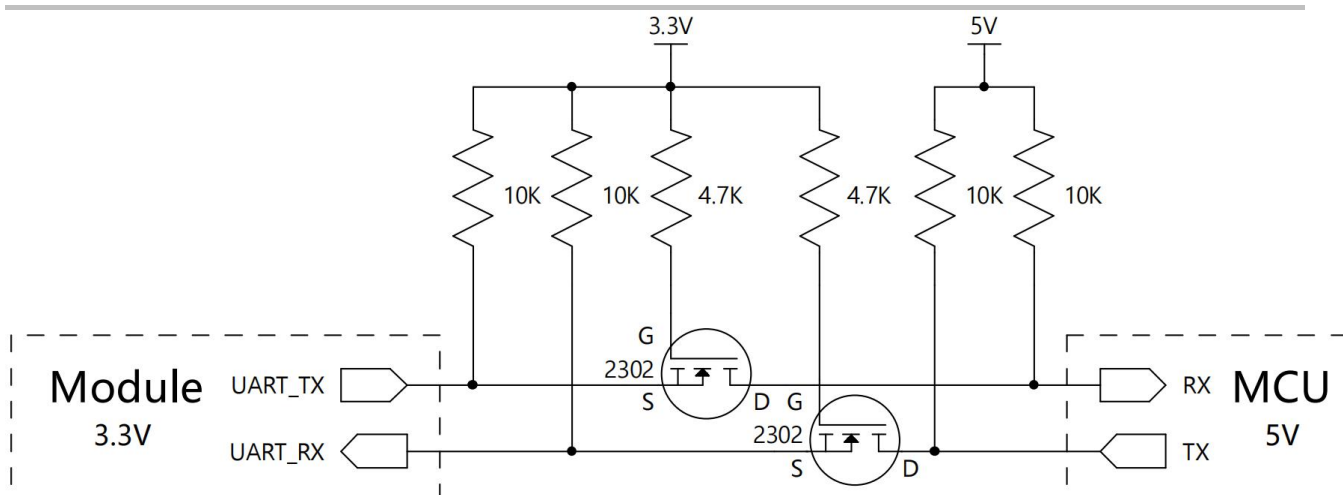
Figure 13：Reference circuit for serial port level conversion

3. Electrical Characteristics & Reliability

3.1. Maximum Rating

The absolute maximum ratings of the supply voltage and voltage on the digital and analog pins of the module are listed below. Exceeding these values will cause permanent damage. The average GPIO pin output current is defined as the average current value flowing through any corresponding pin in a 100mS period. The total average GPIO pin output current is defined as the average current value flowing through all corresponding pins in a 100ms period. The maximum output current is defined as the peak current value flowing through any corresponding pin.

Table 9：Absolute maximum rating table

Absolute maximum rating			
parameter	Minimum	Max	unit
V_{IN}- I/O power supply voltage （VDDIO）	-0.1	+3.6	V
V_{IN}- Analog digital power/voltage （VDD）	-0.1	+3.6	V

Table 10：**Working voltage table**

Operating Voltage				
parameter	Minimum	Typical	Max	unit
V_{IN}- Core supply voltage （VDD）	2.8	3.3	3.6	V
V_{IN}- I/O power supply/voltage （VDDIO）	2.8	3.3	3.6	V

3.2.   Working And Storage Temperature

Table 11：**Working and storage temperature table**

parameter	Minimum	Typical	Max	unit
Normal working temperature	-40	-	85	ºC
storage temperature	-50	-	150	ºC

3.3.   Power Consumption

Table 12：**Power consumption table**

mode	state	Current	Unit
Hibernation mode	-	2	uA
Low power mode	not connected	19	uA
	connected	341	uA
Normal working mode	not connected	270	uA
	connected	341	uA
When transparently transmitting data	connected	MIN：341uA MAX：986uA	MIN is the power consumption when not sending data MAX is the power consumption when the data volume sent by the module is 10626bytes/s

Remark

The power consumption in the above table is measured at a broadcast interval of 540ms, for reference only. The power consumption of this module in different broadcast intervals is different, the specific power consumption is subject to the actual.

3.4. RF Characteristics

Table 13：RF characteristics table

Function	Value
BLE transmit power	-19.5 ~2.5dBm
BLE sensitivity	-94dBm@0.1%BER

3.5. Static Protection

In the application of the module, due to the static electricity generated by human body static electricity and charged friction between microelectronics, it is discharged to the module through various ways, which may cause certain damage to the module, so ESD protection should be taken seriously. In the process of R&D, production, assembly and testing, especially in product design, ESD protection measures should be taken. For example, at the interface of the circuit design and the points that are easily damaged or affected by electrostatic discharge, anti-static protection should be added; anti-static gloves should be worn during production.

Table 14：Table of ESD withstand voltage of module pins

Test interface	Contact discharge	Air discharge	unit
VBAT & GND	+4	+8	kV
Main antenna interface	+2.5	+4	kV

4. Mechanical Size And Layout Recommendations

This section introduces the mechanical dimensions of the module, all dimensions are in millimeters; for all tolerances that are not marked, the tolerance is ±0.3 mm

4.1. Module Mechanical Size

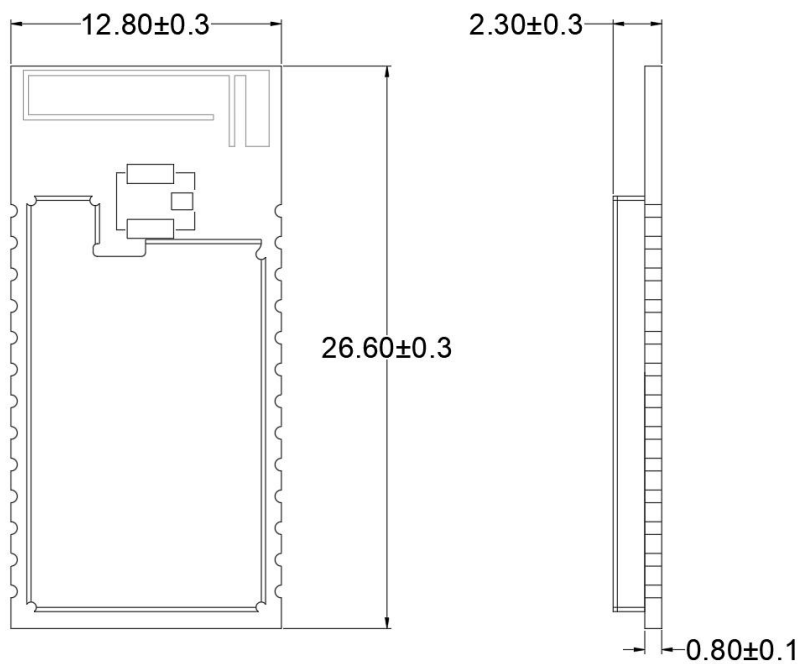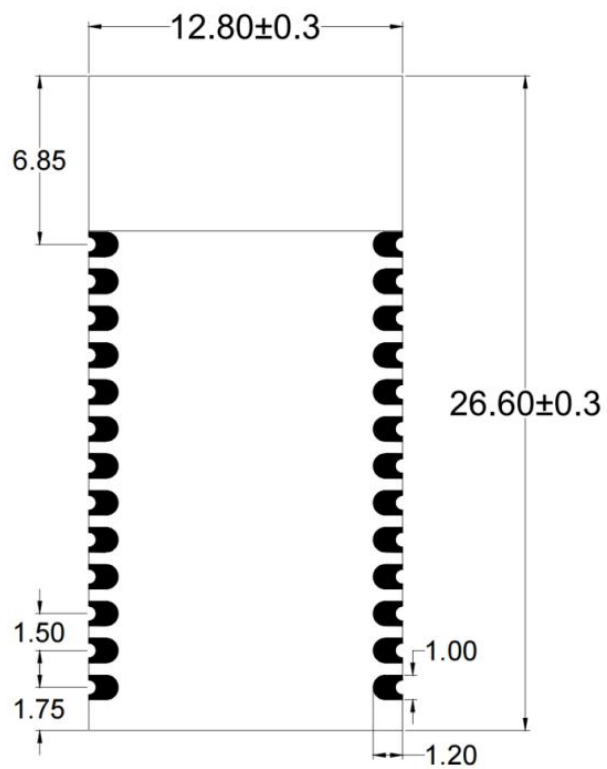**Figure 14：Module top and side dimension drawing**

Figure 15：Bottom view size of the module

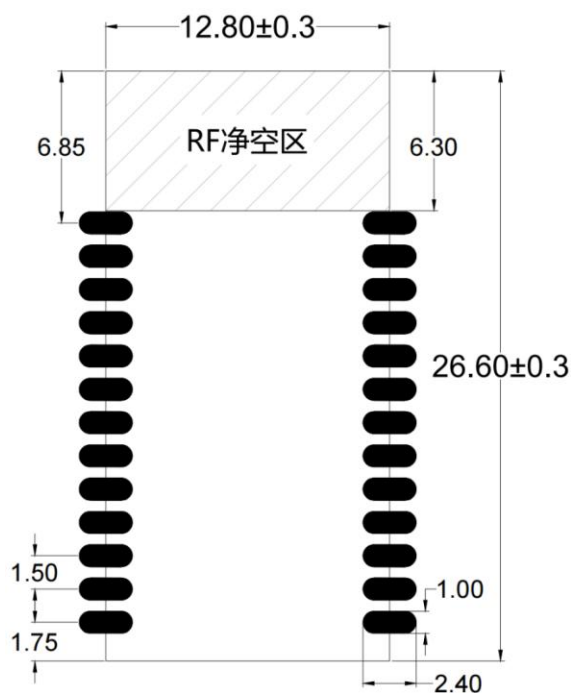## 4.2. Recommend Encapsulation

Figure 16：Recommended package dimensions
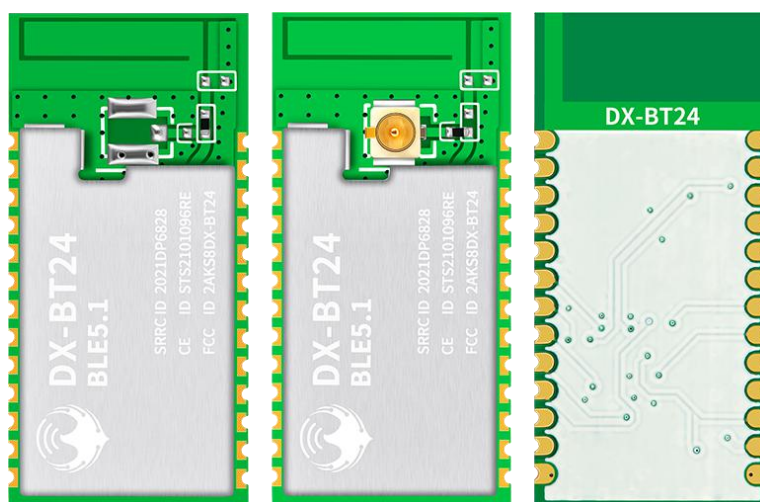
4.3. Module Top View & Bottom View

Figure 17：Top and bottom views of the module

Remark

The above picture is for reference only. For the actual product appearance and label information, please refer to the actual module.

4.4. Hardware Design Layout Recommendations

The DX-BT24 Bluetooth module works in the 2.4G wireless frequency band and uses an on-board antenna. The standing wave ratio (VSWR) and efficiency of the antenna depend on the placement of the patch. Various factors should be avoided as far as possible to the wireless signal transmission and reception. Pay attention to the following What time is it:

1、Avoid using metal for the product shell surrounding the Bluetooth. When using a part of the metal shell, try to keep the antenna part of the module away from the metal part.The metal connecting wire or metal screw inside the product should be as far away as possible from the antenna part of the module。

2、The antenna part of the module should be placed against the edge of the carrier board PCB or directly exposed to the carrier board, and it is not allowed to be placed in the middle of the board，There is at least 5mm of free space in the antenna direction, and the carrier board under the antenna is milled out. Copper and wiring are not allowed in the direction parallel to the antenna。

3、It is recommended to use insulating material to isolate the module mounting position on the substrate, for example, put a whole piece of silk screen at this position（TopOverLay）
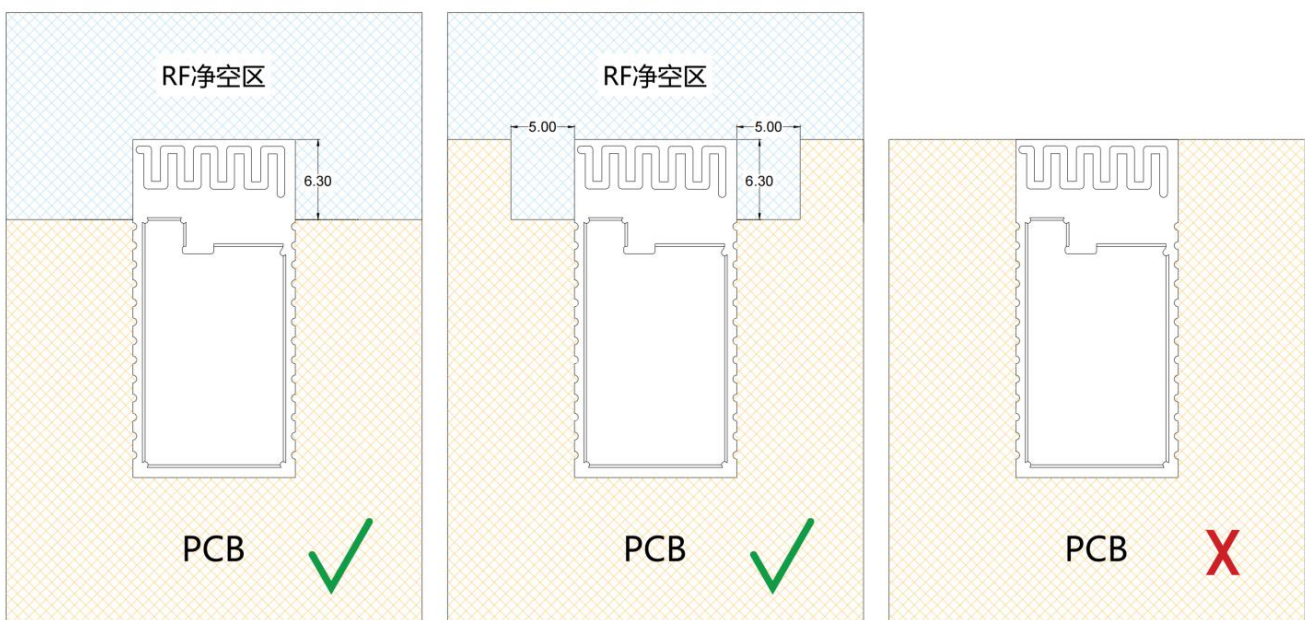

Figure 18：Reference position for module placement

5. Patch Requirements

5.1. Storage Conditions

Modules are shipped in vacuum sealed bags. The humidity sensitivity level of the module is 3 (MSL 3), and its storage must comply with the following conditions：

1. Recommended storage conditions: temperature 23±5°C, and relative humidity 35~60%.

2. Under the recommended storage conditions, the module can be stored in a vacuum sealed bag for 12 months.

3. Under workshop conditions with a temperature of 23±5°C and a relative humidity of less than 60%, the workshop life of the module after unpacking is 168 hours. Under this condition, the module can be directly subjected to reflow production or other high-temperature operations. Otherwise, the module needs to be stored in an environment with a relative humidity of less than 10% (for example, a moisture-proof cabinet) to keep the module dry.

4. If the module is under the following conditions, the module needs to be pre-baked to prevent the PCB from blistering, cracks and delamination that occurs after the module absorbs moisture and gets wet and then solders at a high temperature：

   - Storage temperature and humidity do not meet the recommended storage conditions；
   - The module failed to complete production or storage according to Article 3 above after unpacking；
   - Vacuum packaging leaks, bulk materials；
   - Before module repair；

5.2. Module Baking Treatment

   - Requires high temperature baking at 120 ±5 °C for 8 hours；
   - The second-baked module must be soldered within 24 hours after baking, otherwise it still needs to be stored in a dry box；

Remark

1. In order to prevent and reduce the occurrence of defective welding such as blistering and delamination caused by damp, strict control should be carried out. It is not recommended to expose to the air for a long time after unpacking the vacuum package.

2. Before baking, take the module out of the package and place the bare module on a high temperature resistant appliance to avoid high temperature damage to the plastic tray or reel; The second-baked module must be soldered within 24 hours after baking, otherwise it must be stored in a dry box. Please pay attention to ESD protection when unpacking and placing the module, for example, wear anti-static gloves。

5.3. Reflow Soldering

Use a printing squeegee to print the solder paste on the screen so that the solder paste is printed on the PCB through the opening of the screen. The force of the printing squeegee needs to be adjusted appropriately.In order to ensure the quality of the module printing paste, the thickness of the stencil corresponding to the module pad part is recommended to be 0.1~0.15mm .

The recommended reflow soldering temperature is 235~250 ℃, and the maximum should not exceed 250 ℃。In order to avoid damage to the module due to repeated heating, it is strongly recommended that customers re-attach the module after completing the reflow soldering on the first side of the PCB board. The recommended furnace temperature curve (lead-free SMT reflow soldering) and related parameters are shown in the chart below：
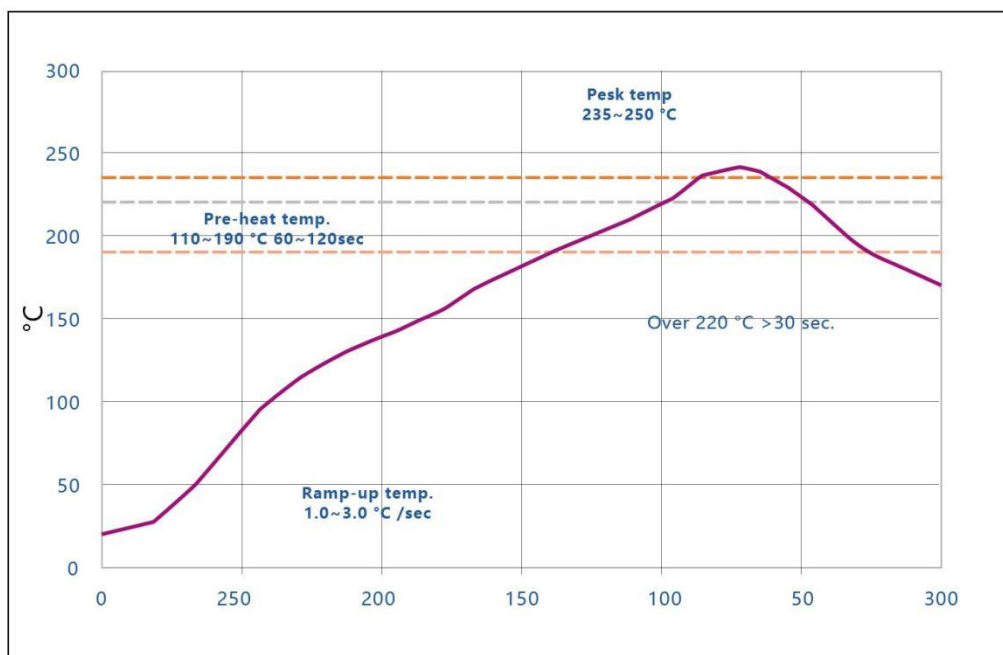
Figure 19：Recommended reflow soldering temperature profile

Table 15：Recommended reflow soldering temperature

Statistics name	Lower limit	Upper limit	unit
Slope 1 (target=2.0) is between 30.0 and 70.0	1	3	Degree/sec
Slope 2 (target=2.0) is between 70.0 and 150.0	1	3	Degree/sec
Slope 3 (target = -2.8) is between 220.0 and 150.0	-5	-0.5	Degree/sec
Constant temperature time 110-190℃	60	120	sec
@220℃ Reflow time	30	65	sec
Peak temperature	235	250	Celsius
@235℃ total time	10	30	sec

5.4. Packing Specification

The DX-BT24 modules are packaged on tape and reel and encapsulated in a vacuum-sealed bag with a desiccant and humidity card. Each carrier is 20 meters long, contains 1000 modules, and has a reel diameter of 330 mm. The specific specifications are as follows:
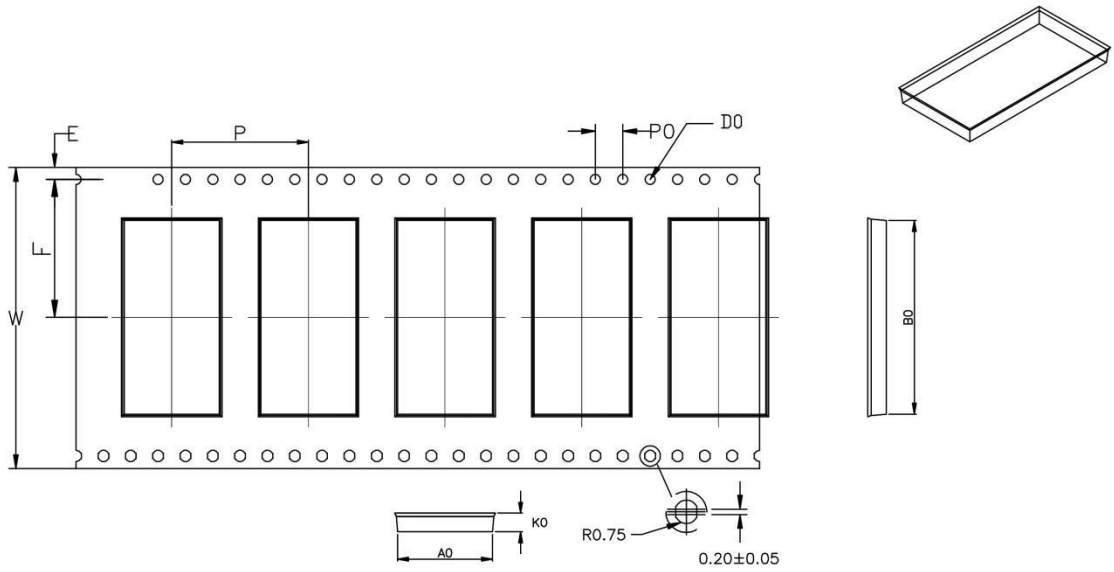
ITEM	W	A_0	A_1	A_2	K_0	S_0	F	E	D_0	P	P_0	P_2	B_0	B_1	B_2
DIM	$44.0^{+0.30}_{-0.30}$	$13.9^{+0.10}_{-0.10}$			$2.7^{+0.10}_{-0.10}$	——	$20.15^{+0.10}_{-0.10}$	$1.75^{+0.10}_{-0.10}$	$\phi1.50^{+0.10}_{-0}$	$20.0^{+0.10}_{-0.10}$	$4.00^{+0.10}_{-0.10}$	$2.00^{+0.10}_{-0.10}$	$28.30^{+0.10}_{-0.10}$		
ALTERNATE															

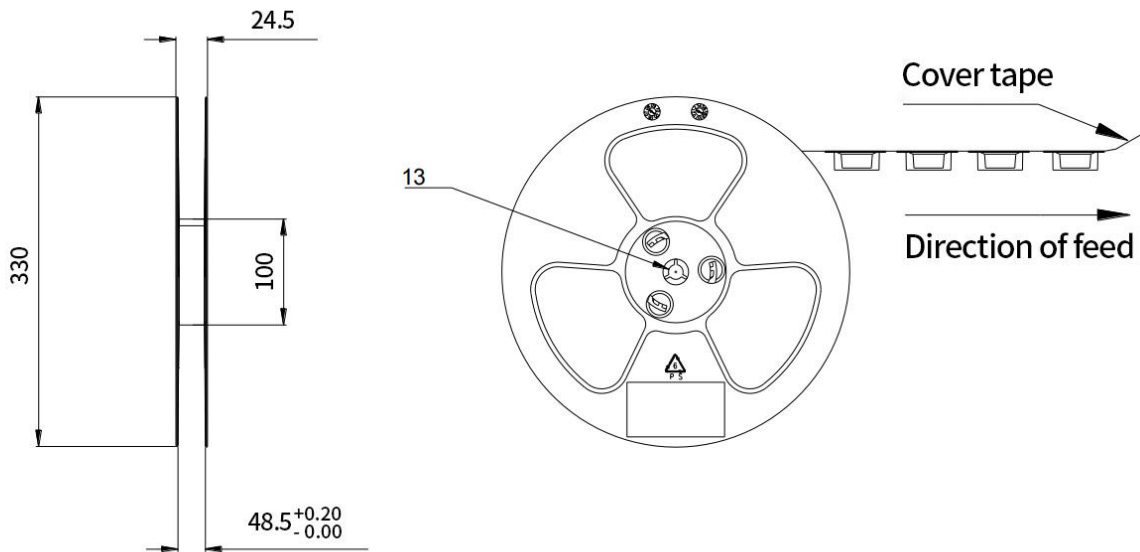**Figure 20：Carrier tape size（Unit: mm）**
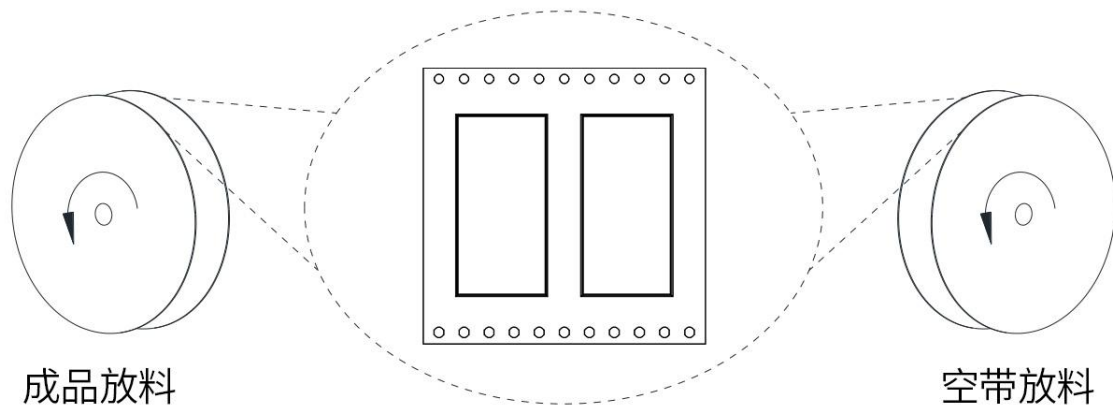

Figure 21：Reel size (unit: mm)

Figure 22：Tape direction

6. Certification

6.1. CE-(Europe)

Certificate NO：STS2101096RE

6.2. FCC-(U.S.A.)

FCC IDENTIFIDR：2AKS8-DX-BT24

6.3. SRRC-(China)

CMIIT ID：2021DP6828

FCC Statement

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions:

(1) This device may not cause harmful interference, and

(2) this device must accept

any interference received, including interference that may cause undesired operation.

Any Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

The device has been evaluated to meet general RF exposure requirement. The device can be used in portable exposure condition without restriction.

If the FCC identification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use wording such as the following: "Contains Transmitter Module FCC ID: 2AKS8-DX-BT24 or Contains FCC ID: 2AKS8-DX-BT24"

When the module is installed inside another device, the user manual of the host must contain below warning statements;

1. This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

(1) This device may not cause harmful interference.

(2) This device must accept any interference received, including interference that may cause undesired operation.

2. Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

The devices must be installed and used in strict accordance with the manufacturer's instructions as described in the user documentation that comes with the product.

Any company of the host device which install this modular with Single modular approval should perform the test of radiated emissionand spurious emission according to FCC part 15C : 15.247 and 15.209 requirement,Only if the test result comply with FCC part 15C : 15.247 and 15.209 requirement，then the host can be sold legally.

This device is intended only for OEM integrators under the following conditions: (For module device use)

1) The antenna must be installed such that 20 cm is maintained between the antenna and users, and

2) The transmitter module may not be co-located with any other transmitter or antenna.

As long as 2 conditions above are met, further transmitter test will not be required. However, the OEM integrator is still responsible for testing their endproduct for any additional compliance requirements required with this module installed.

FCC Radiation Exposure Statement

This modular complies with FCC RF radiation exposure limits set forth for an uncontrolled environment. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. This modular must be installed and operated with a minimum distance of 20 cm between the radiator and user body.

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

-Reorient or relocate the receiving antenna.

-Increase the separation between the equipment and receiver.

-Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

-Consult the dealer or an experienced radio/TV technician for help.

Integration instructions for host product manufacturers according to KDB 996369 D03 OEM Manual v01

2.2 List of applicable FCC rules
FCC Part 15 Subpart C 15.247 & 15.207 & 15.209

2.3 Specific operational use conditions
The module is a Bluetooth module with BLE function.
Operation Frequency: 2402-2480MHz
Number of Channel: 40
Modulation: GFSK
Type: PCB Antenna

Gain: 0dBi

The module can be used for mobile or portable applications with a maximum 0dBi antenna. The host manufacturer installing this module into their product must ensure that the final composit product complies with the FCC requirements by a technical assessment or evaluation to the FCC rules, including the transmitter operaition. The host manufacturer has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warning as show in this manual.

2.4 Limited module procedures
Not applicable. The module is a Single module and complies with the requirement of FCC Part 15.212.

2.5 Trace antenna designs
Not applicable. The module has its own antenna, and doesn't need a host's printed board microstrip trace antenna etc.

2.6 RF exposure considerations
The module must be installed in the host equipment such that at least 20mm is maintained between the antenna and users' body; and if RF exposure statement or module layout is changed, then the host product manufacturer required to take responsibility of the module through a change in FCC ID or new application. The FCC ID of the module cannot be used on the final product. In these circumstances, the host manufacturer will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

2.7 Antennas
Antenna Specification are as follows:
Type: PCB Antenna
Gain: 0 dBi
This device is intended only for host manufacturers under the following conditions:
The transmitter module may not be co-located with any other transmitter or antenna;
The module shall be only used with the internal antenna(s) that has been originally tested and certified with this module. The antenna must be either permanently attached or employa 'unique' antenna coupler.

As long as the conditions above are met, further transmitter test will not be required. However, the host manufacturer is still responsible for testing their end-product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.).

2.8 Label and compliance information
Host product manufacturers need to provide a physical or e-label stating "Contains FCC ID: 2AKS8-DX-BT24 " with their finished product.

2.9 Information on test modes and additional testing requirements
Operation Frequency: 2402-2480MHz
Number of Channel: 40
Modulation: GFSK
Host manufacturer must perfom test of radiated & conducted emission and spurious emission, etc according to the actual test modes for a stand-alone modular transmitter in a host, as well as for multiple simultaneously transmitting modules or other transmitters in a host product.
Only when all the test results of test modes comply with FCC requirements, then the end product can be sold legally.

2.10 Additional testing, Part 15 Subpart B disclaimer
The modular transmitter is **only** FCC authorized for FCC Part 15 Subpart C 15.247 & 15.207 & 15.209 and that the host product manufacturer is responsible for compliance to any other FCC rules that apply to the host not covered by the modular transmitter grant of certification. If the grantee markets their product as being Part 15 Subpart B compliant (when it also contains unintentional-radiator digital circuity), then the grantee shall provide a notice stating that the final host product still requires Part 15 Subpart B compliance testing with the modular transmitter installed.