

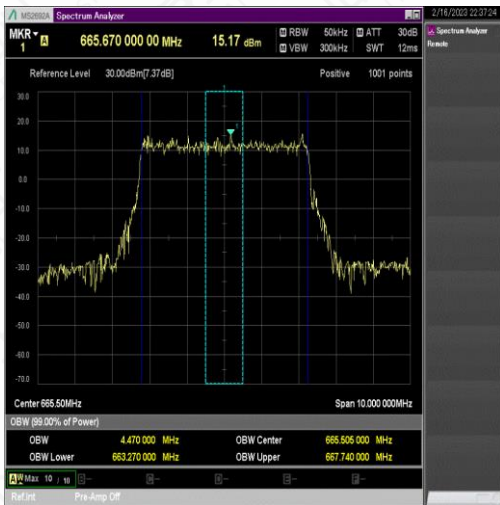
N71 Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N71	15	5	DFT-PI2BPSK	L	Outer_Full	4.47	4.47	PASS
N71	15	5	DFT-QPSK	L	Outer_Full	4.48	4.48	PASS
N71	15	5	DFT-16QAM	L	Outer_Full	4.47	4.47	PASS
N71	15	5	DFT-64QAM	L	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-256QAM	L	Outer_Full	4.47	4.47	PASS
N71	15	5	CP-QPSK	L	Outer_Full	4.46	4.46	PASS
N71	15	5	CP-16QAM	L	Outer_Full	4.47	4.47	PASS
N71	15	5	CP-64QAM	L	Outer_Full	4.47	4.47	PASS
N71	15	5	CP-256QAM	L	Outer_Full	4.48	4.48	PASS
N71	15	5	DFT-PI2BPSK	M	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-QPSK	M	Outer_Full	4.49	4.49	PASS
N71	15	5	DFT-16QAM	M	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-64QAM	M	Outer_Full	4.45	4.45	PASS
N71	15	5	DFT-256QAM	M	Outer_Full	4.47	4.47	PASS
N71	15	5	CP-QPSK	M	Outer_Full	4.46	4.46	PASS
N71	15	5	CP-16QAM	M	Outer_Full	4.46	4.46	PASS
N71	15	5	CP-64QAM	M	Outer_Full	4.47	4.47	PASS
N71	15	5	CP-256QAM	M	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-PI2BPSK	H	Outer_Full	4.44	4.44	PASS
N71	15	5	DFT-QPSK	H	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-16QAM	H	Outer_Full	4.46	4.46	PASS
N71	15	5	DFT-64QAM	H	Outer_Full	4.45	4.45	PASS
N71	15	5	DFT-256QAM	H	Outer_Full	4.48	4.48	PASS
N71	15	5	CP-QPSK	H	Outer_Full	4.45	4.45	PASS
N71	15	5	CP-16QAM	H	Outer_Full	4.46	4.46	PASS
N71	15	5	CP-64QAM	H	Outer_Full	4.46	4.46	PASS
N71	15	5	CP-256QAM	H	Outer_Full	4.46	4.46	PASS
N71	15	10	DFT-PI2BPSK	L	Outer_Full	8.92	8.92	PASS
N71	15	10	DFT-QPSK	L	Outer_Full	8.92	8.92	PASS
N71	15	10	DFT-16QAM	L	Outer_Full	8.92	8.92	PASS
N71	15	10	DFT-64QAM	L	Outer_Full	8.94	8.94	PASS
N71	15	10	DFT-256QAM	L	Outer_Full	8.92	8.92	PASS
N71	15	10	CP-QPSK	L	Outer_Full	9.3	9.3	PASS
N71	15	10	CP-16QAM	L	Outer_Full	9.28	9.28	PASS
N71	15	10	CP-64QAM	L	Outer_Full	9.26	9.26	PASS
N71	15	10	CP-256QAM	L	Outer_Full	9.28	9.28	PASS
N71	15	10	DFT-PI2BPSK	M	Outer_Full	8.92	8.92	PASS
N71	15	10	DFT-QPSK	M	Outer_Full	8.92	8.92	PASS

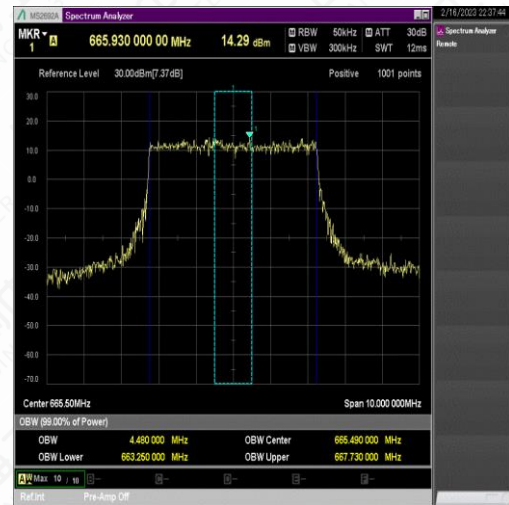
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N71	15	10	DFT-64QAM	M	Outer_Full	8.94	8.94	PASS
N71	15	10	DFT-256QAM	M	Outer_Full	8.92	8.92	PASS
N71	15	10	CP-QPSK	M	Outer_Full	9.28	9.28	PASS
N71	15	10	CP-16QAM	M	Outer_Full	9.28	9.28	PASS
N71	15	10	CP-64QAM	M	Outer_Full	9.28	9.28	PASS
N71	15	10	CP-256QAM	M	Outer_Full	9.28	9.28	PASS
N71	15	10	DFT-PI2BPSK	H	Outer_Full	8.88	8.88	PASS
N71	15	10	DFT-QPSK	H	Outer_Full	8.9	8.9	PASS
N71	15	10	DFT-16QAM	H	Outer_Full	8.9	8.9	PASS
N71	15	10	DFT-64QAM	H	Outer_Full	8.88	8.88	PASS
N71	15	10	DFT-256QAM	H	Outer_Full	8.88	8.88	PASS
N71	15	10	CP-QPSK	H	Outer_Full	9.24	9.24	PASS
N71	15	10	CP-16QAM	H	Outer_Full	9.24	9.24	PASS
N71	15	10	CP-64QAM	H	Outer_Full	9.26	9.26	PASS
N71	15	10	CP-256QAM	H	Outer_Full	9.26	9.26	PASS
N71	15	15	DFT-PI2BPSK	L	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-QPSK	L	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-16QAM	L	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-64QAM	L	Outer_Full	13.41	13.41	PASS
N71	15	15	DFT-256QAM	L	Outer_Full	13.44	13.44	PASS
N71	15	15	CP-QPSK	L	Outer_Full	14.1	14.1	PASS
N71	15	15	CP-16QAM	L	Outer_Full	14.1	14.1	PASS
N71	15	15	CP-64QAM	L	Outer_Full	14.07	14.07	PASS
N71	15	15	CP-256QAM	L	Outer_Full	14.16	14.16	PASS
N71	15	15	DFT-PI2BPSK	M	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-QPSK	M	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-16QAM	M	Outer_Full	13.47	13.47	PASS
N71	15	15	DFT-64QAM	M	Outer_Full	13.47	13.47	PASS
N71	15	15	DFT-256QAM	M	Outer_Full	13.44	13.44	PASS
N71	15	15	CP-QPSK	M	Outer_Full	14.16	14.16	PASS
N71	15	15	CP-16QAM	M	Outer_Full	14.13	14.13	PASS
N71	15	15	CP-64QAM	M	Outer_Full	14.16	14.16	PASS
N71	15	15	CP-256QAM	M	Outer_Full	14.1	14.1	PASS
N71	15	15	DFT-PI2BPSK	H	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-QPSK	H	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-16QAM	H	Outer_Full	13.41	13.41	PASS
N71	15	15	DFT-64QAM	H	Outer_Full	13.44	13.44	PASS
N71	15	15	DFT-256QAM	H	Outer_Full	13.47	13.47	PASS
N71	15	15	CP-QPSK	H	Outer_Full	14.07	14.07	PASS
N71	15	15	CP-16QAM	H	Outer_Full	14.07	14.07	PASS

N71	15	15	CP-64QAM	H	Outer_Full	14.13	14.13	PASS
N71	15	15	CP-256QAM	H	Outer_Full	14.13	14.13	PASS
N71	15	20	DFT-PI2BPSK	L	Outer_Full	17.84	17.84	PASS
N71	15	20	DFT-QPSK	L	Outer_Full	17.8	17.8	PASS
N71	15	20	DFT-16QAM	L	Outer_Full	17.84	17.84	PASS
N71	15	20	DFT-64QAM	L	Outer_Full	17.8	17.8	PASS
N71	15	20	DFT-256QAM	L	Outer_Full	17.8	17.8	PASS
N71	15	20	CP-QPSK	L	Outer_Full	18.84	18.84	PASS
N71	15	20	CP-16QAM	L	Outer_Full	18.88	18.88	PASS
N71	15	20	CP-64QAM	L	Outer_Full	18.88	18.88	PASS
N71	15	20	CP-256QAM	L	Outer_Full	18.84	18.84	PASS
N71	15	20	DFT-PI2BPSK	M	Outer_Full	17.92	17.92	PASS
N71	15	20	DFT-QPSK	M	Outer_Full	17.84	17.84	PASS
N71	15	20	DFT-16QAM	M	Outer_Full	17.92	17.92	PASS
N71	15	20	DFT-64QAM	M	Outer_Full	17.88	17.88	PASS
N71	15	20	DFT-256QAM	M	Outer_Full	17.92	17.92	PASS
N71	15	20	CP-QPSK	M	Outer_Full	18.88	18.88	PASS
N71	15	20	CP-16QAM	M	Outer_Full	18.92	18.92	PASS
N71	15	20	CP-64QAM	M	Outer_Full	18.96	18.96	PASS
N71	15	20	CP-256QAM	M	Outer_Full	18.88	18.88	PASS
N71	15	20	DFT-PI2BPSK	H	Outer_Full	17.88	17.88	PASS
N71	15	20	DFT-QPSK	H	Outer_Full	17.88	17.88	PASS
N71	15	20	DFT-16QAM	H	Outer_Full	17.92	17.92	PASS
N71	15	20	DFT-64QAM	H	Outer_Full	17.88	17.88	PASS
N71	15	20	DFT-256QAM	H	Outer_Full	17.88	17.88	PASS
N71	15	20	CP-QPSK	H	Outer_Full	18.84	18.84	PASS
N71	15	20	CP-16QAM	H	Outer_Full	18.92	18.92	PASS
N71	15	20	CP-64QAM	H	Outer_Full	18.92	18.92	PASS
N71	15	20	CP-256QAM	H	Outer_Full	18.84	18.84	PASS

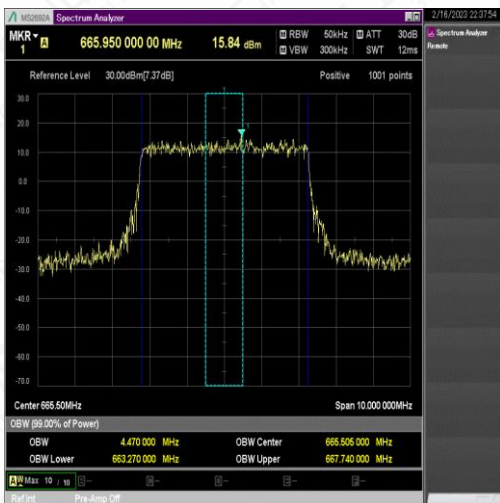
Test Graphs



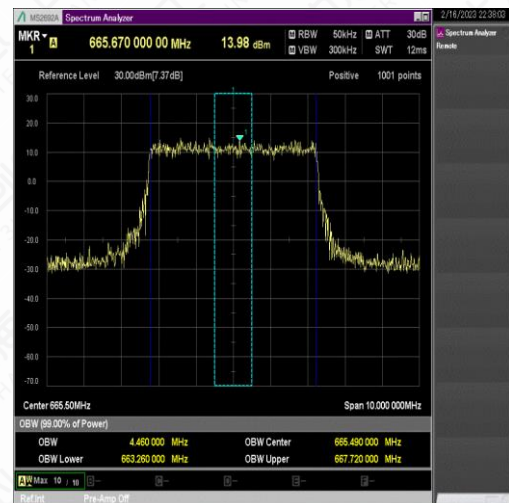
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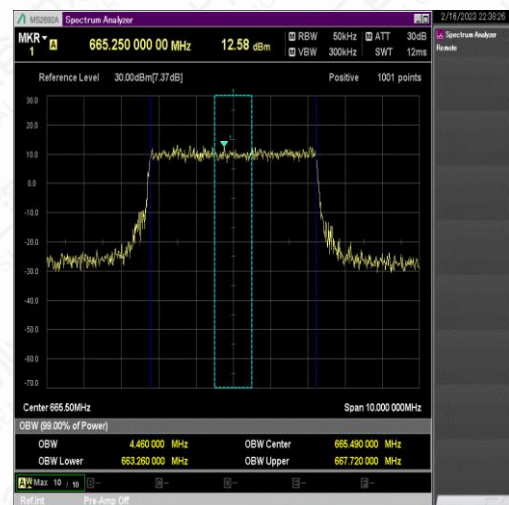
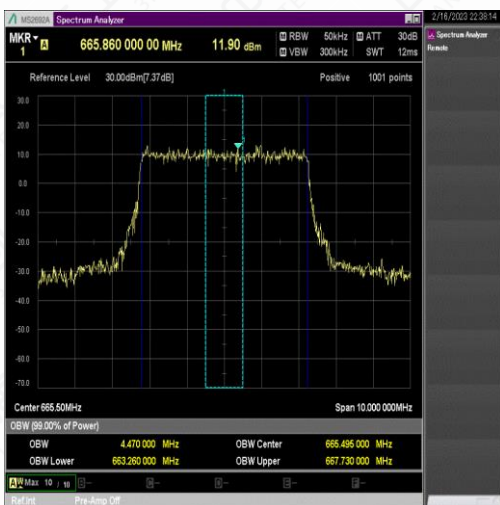
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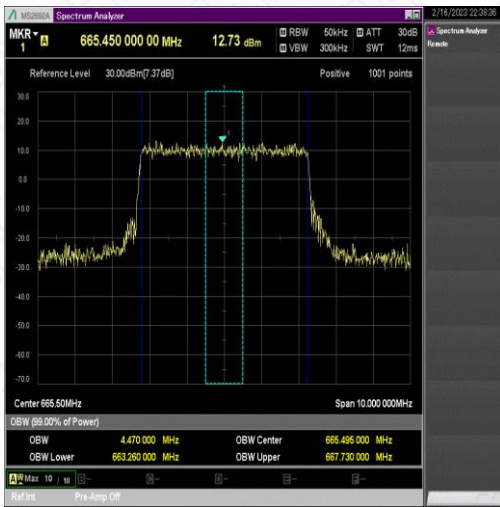
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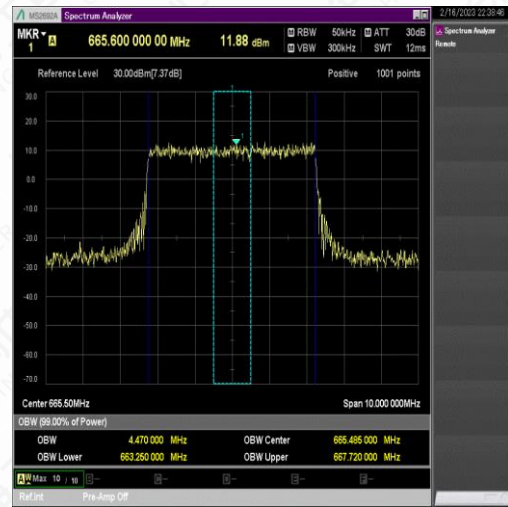
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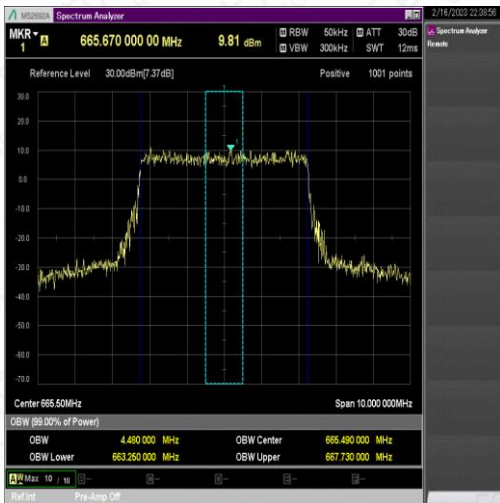
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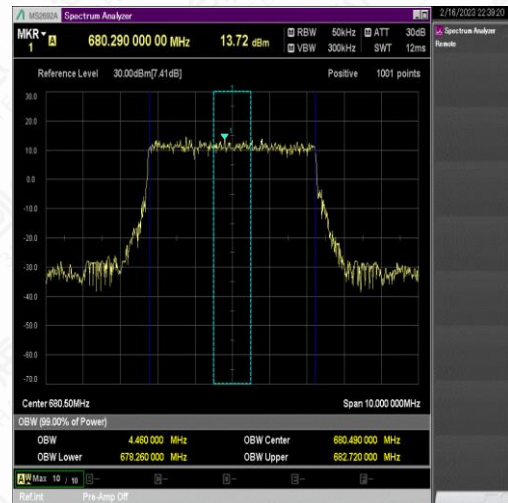
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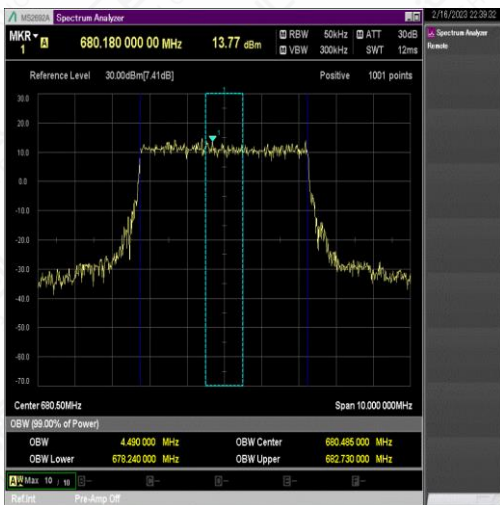
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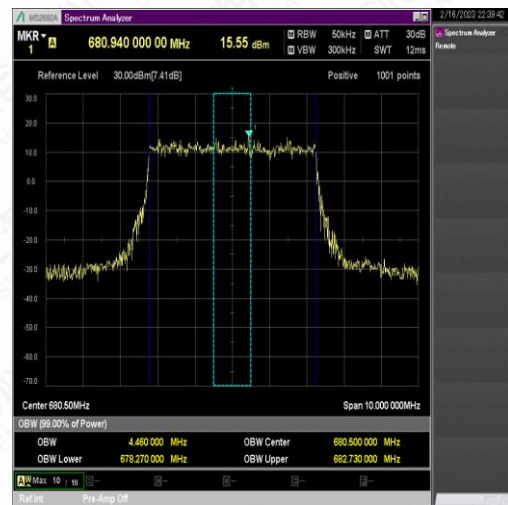
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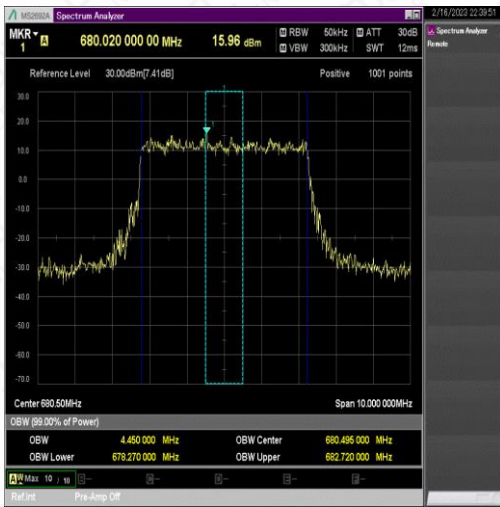
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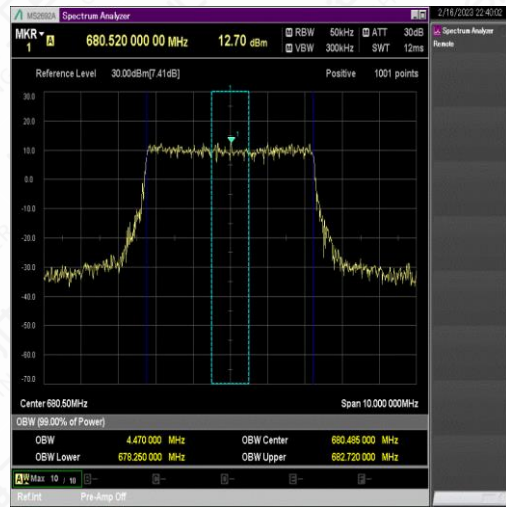
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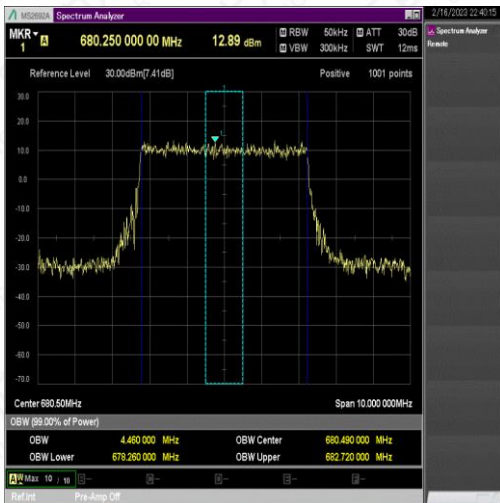
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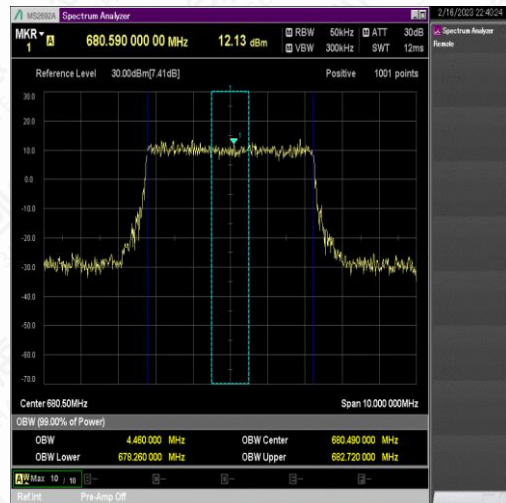
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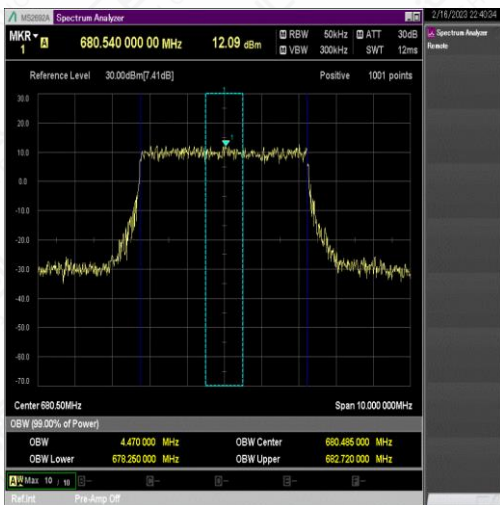
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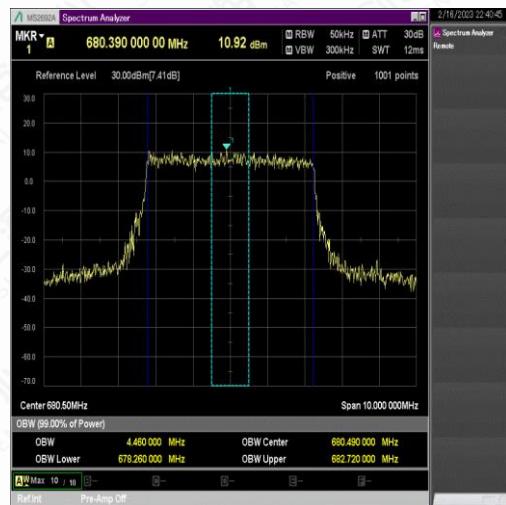
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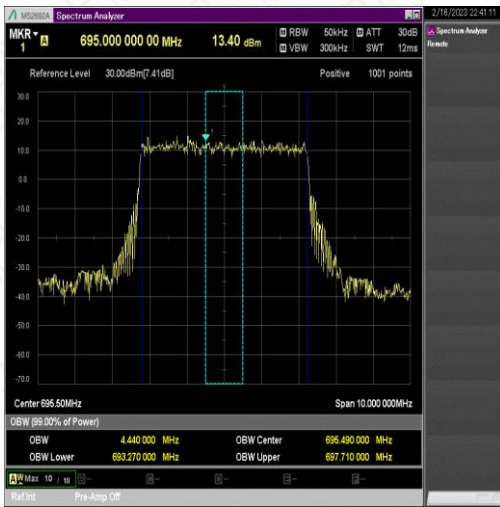
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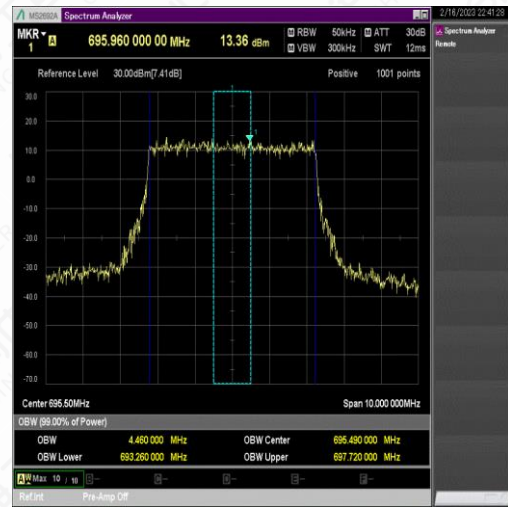
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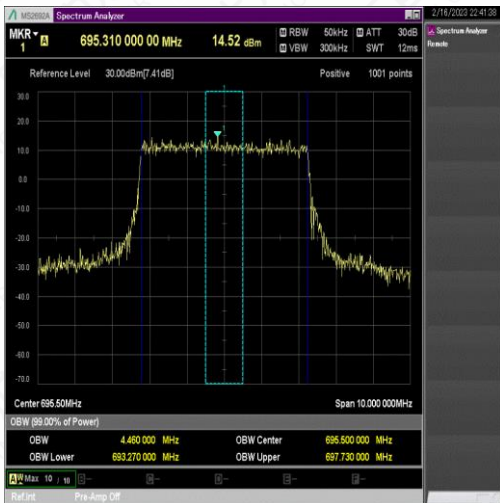
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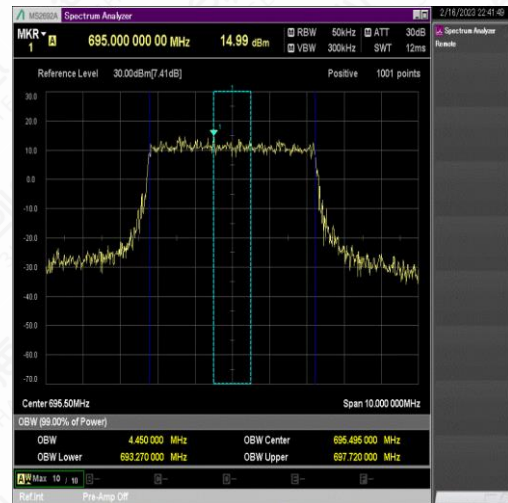
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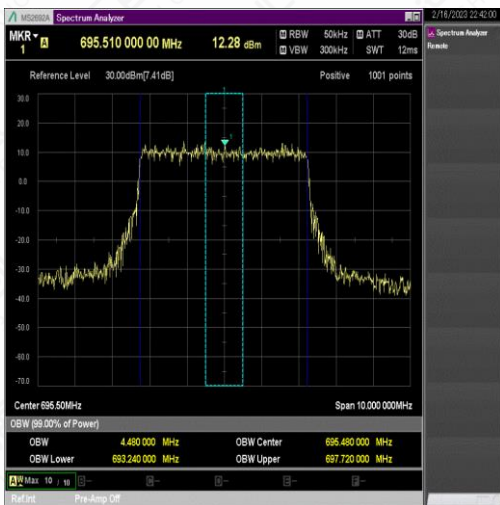
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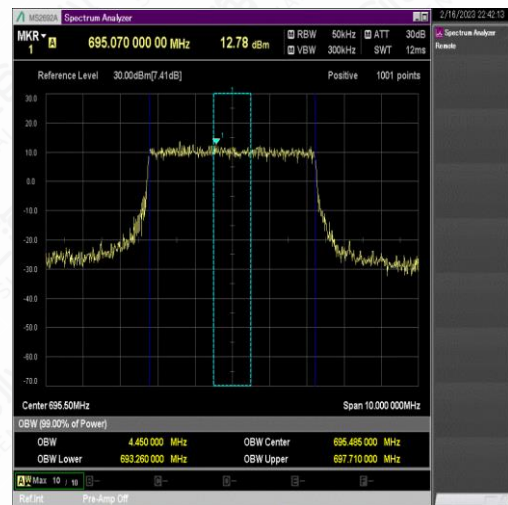
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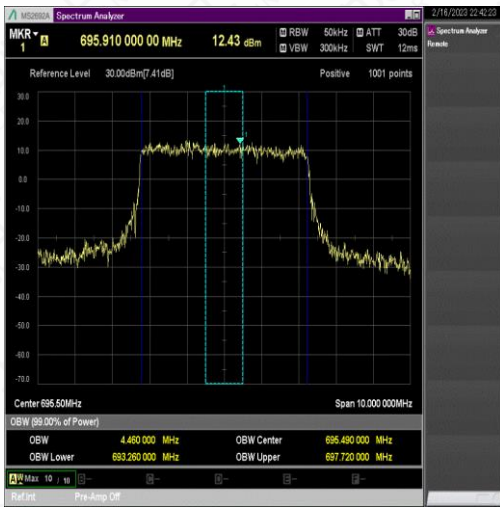
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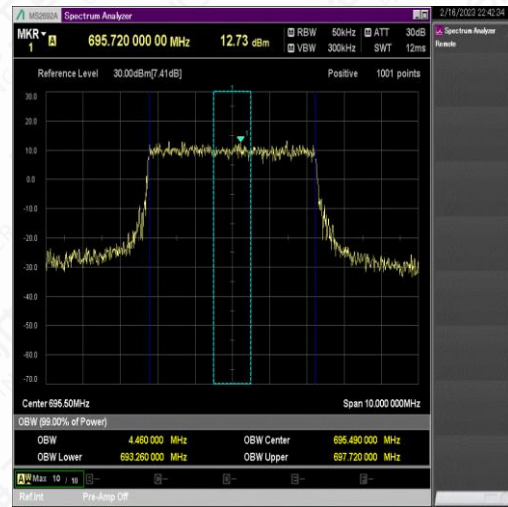
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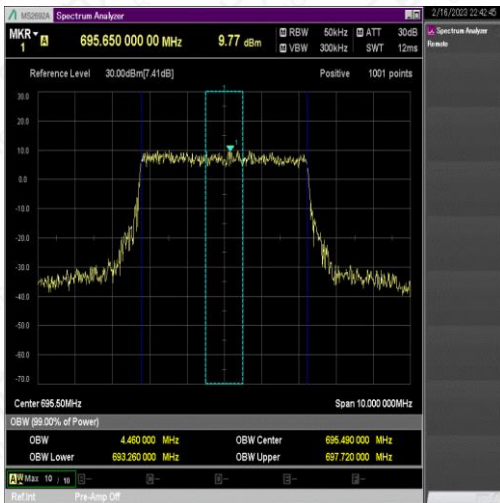
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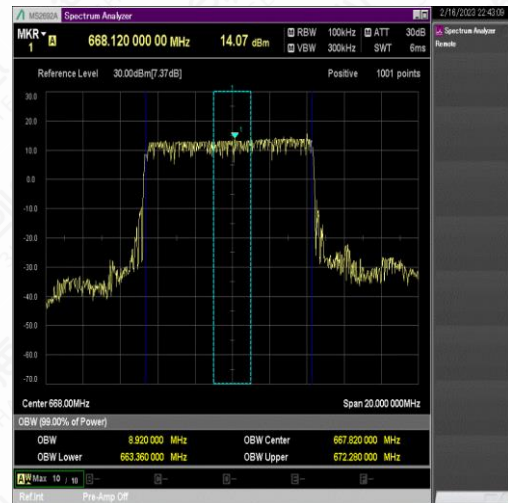
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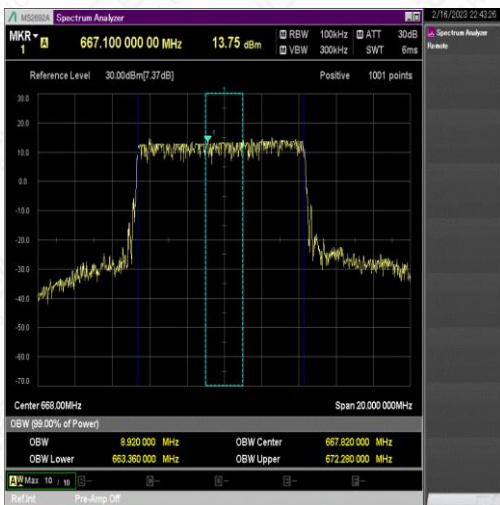
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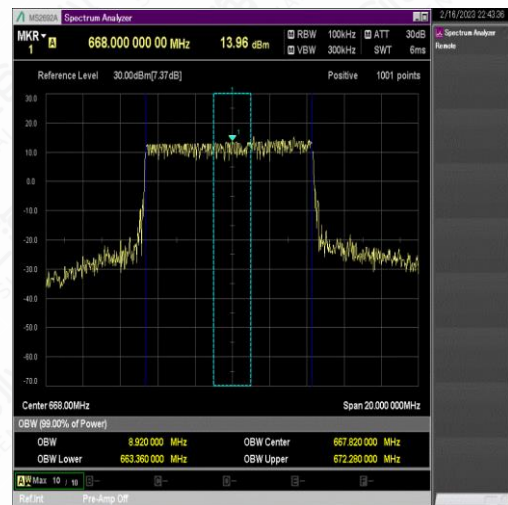
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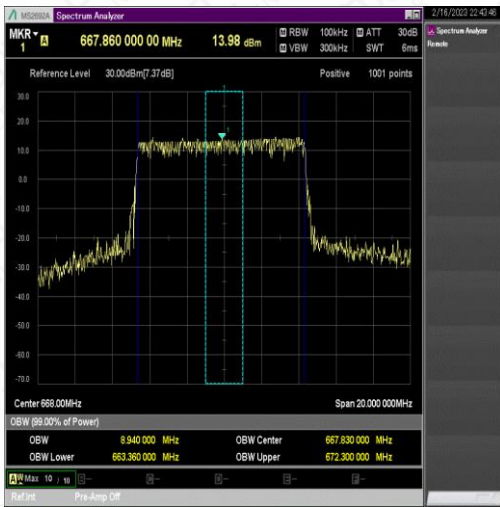
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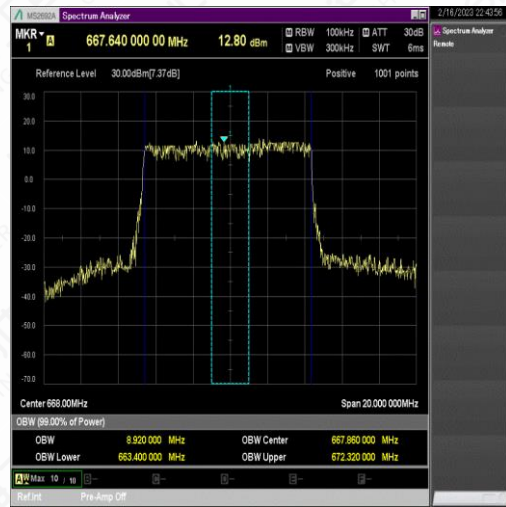
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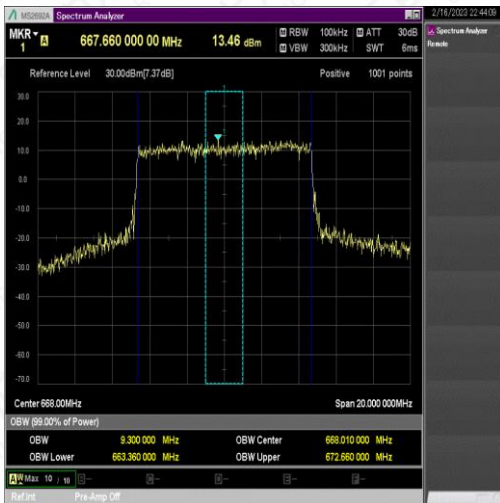
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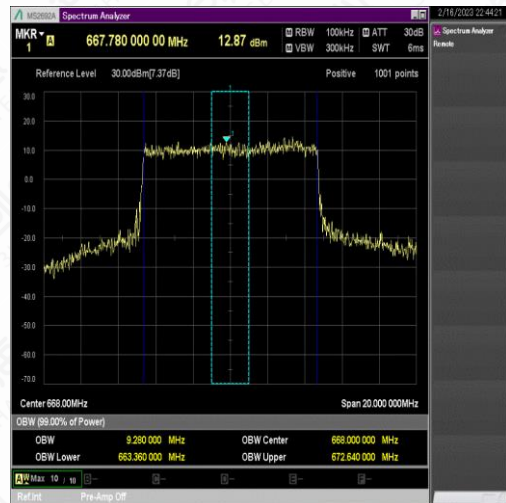
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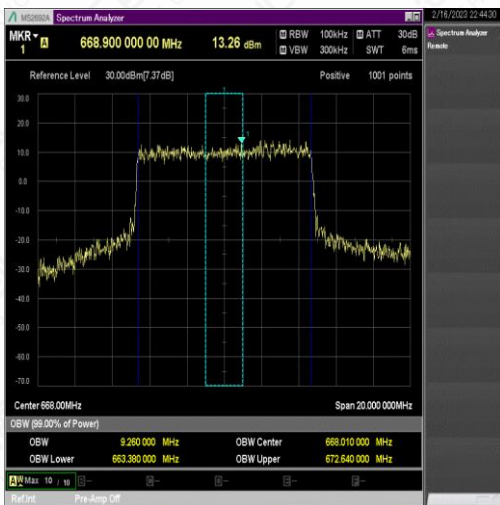
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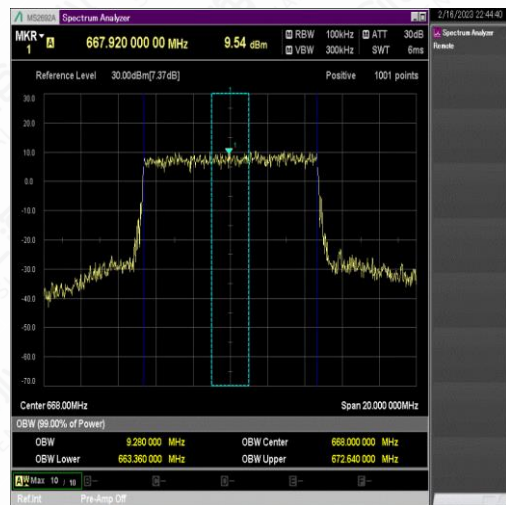
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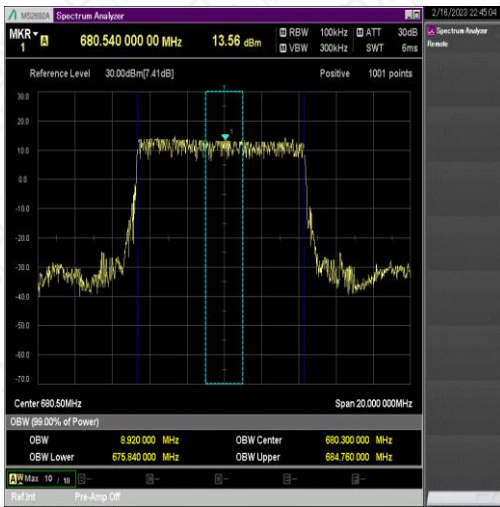
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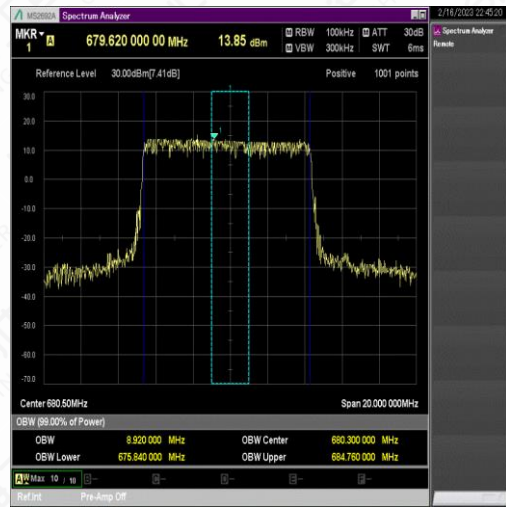
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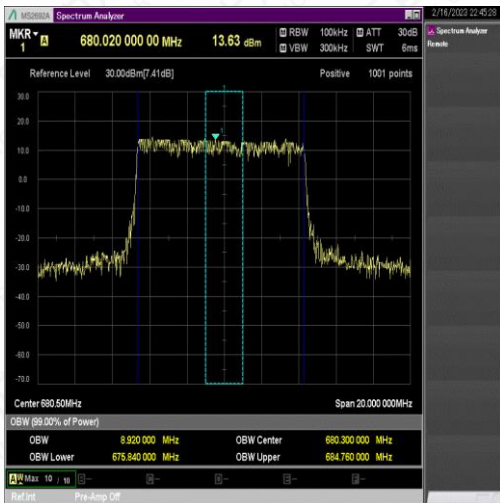
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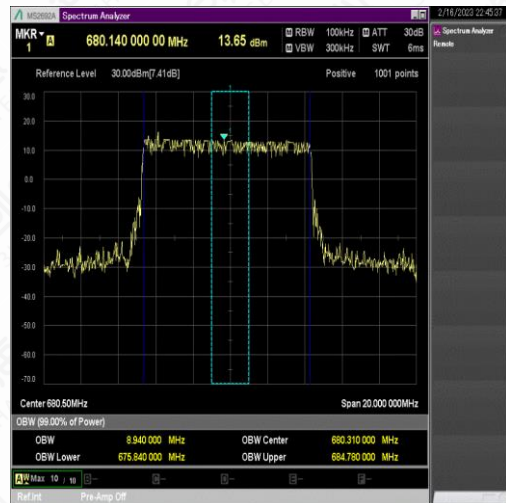
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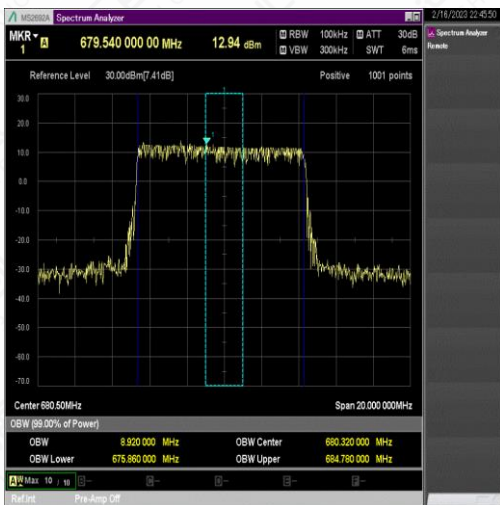
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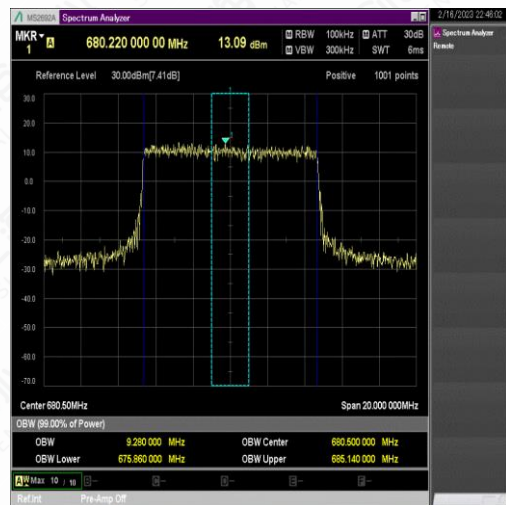
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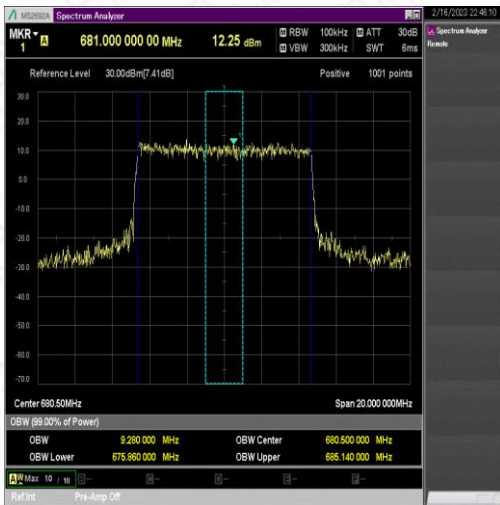
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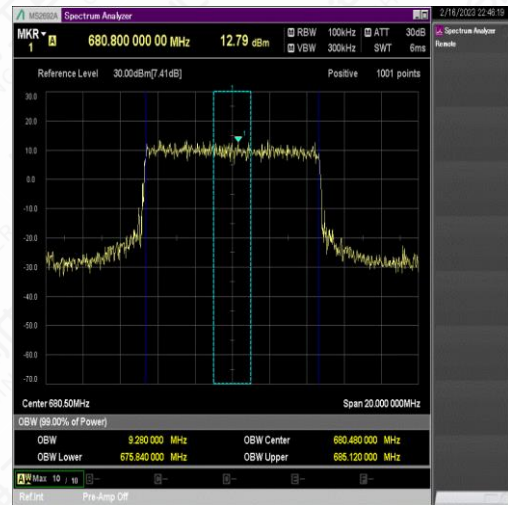
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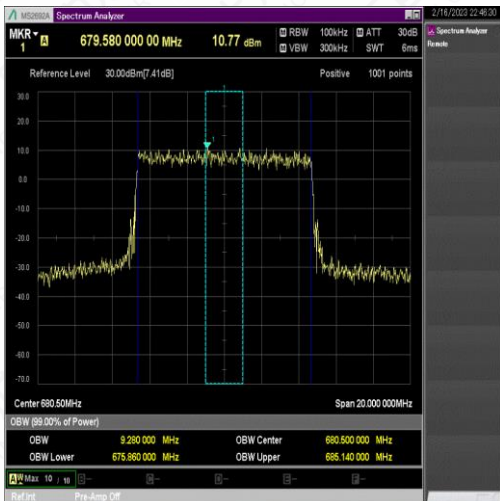
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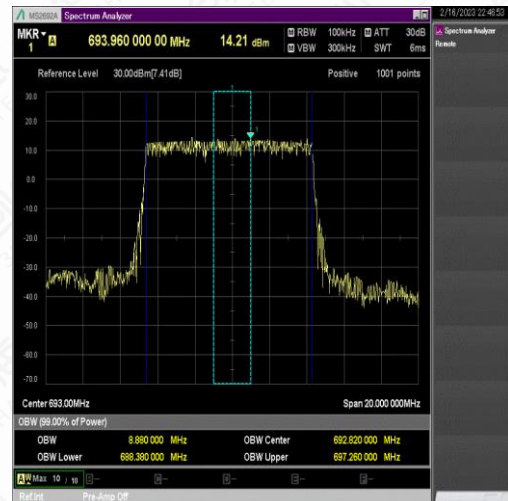
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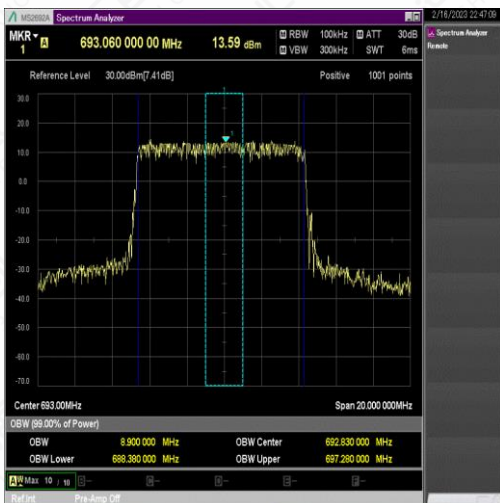
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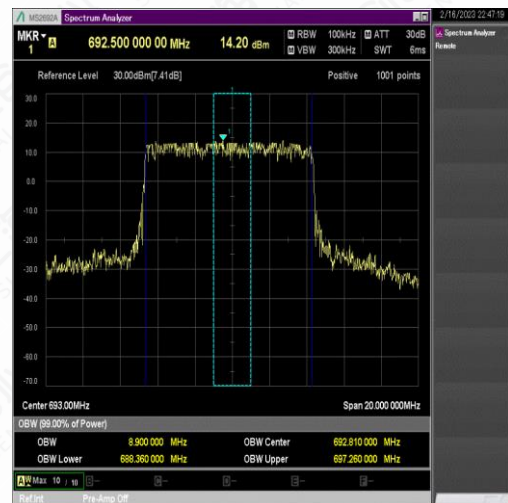
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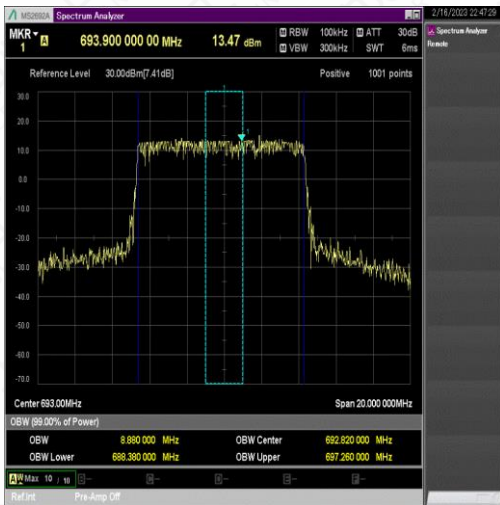
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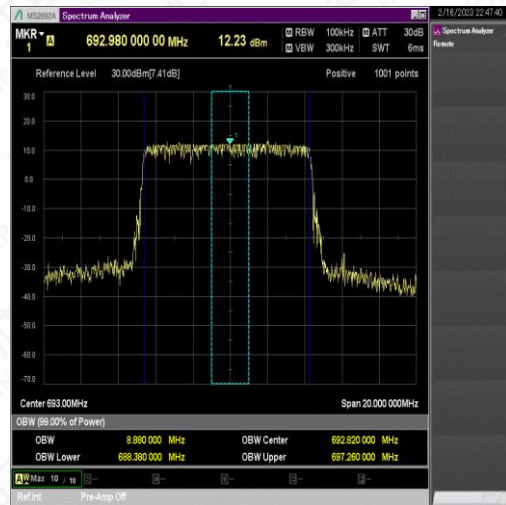
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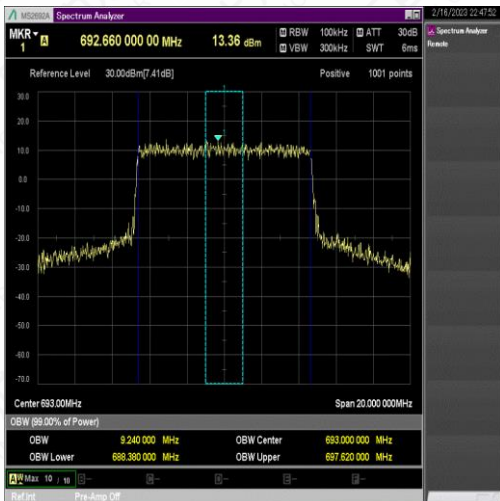
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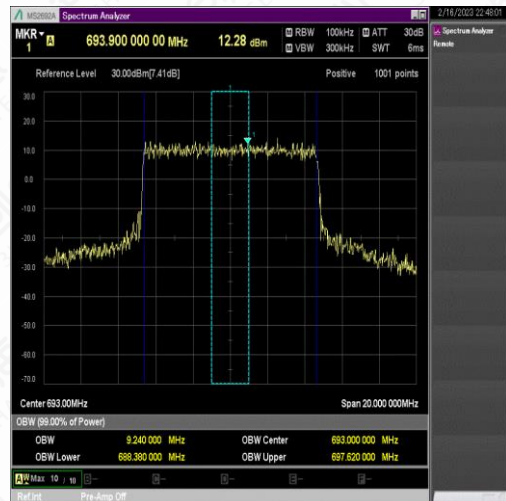
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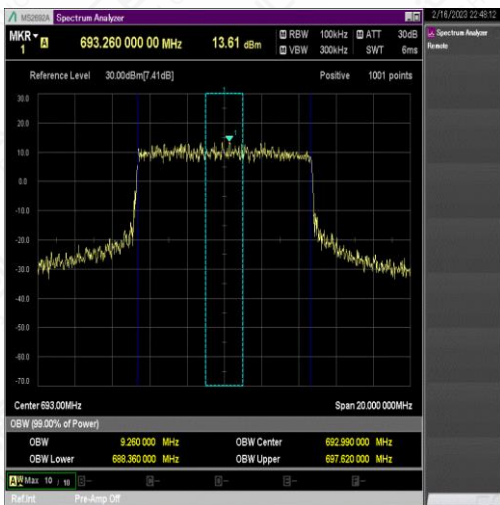
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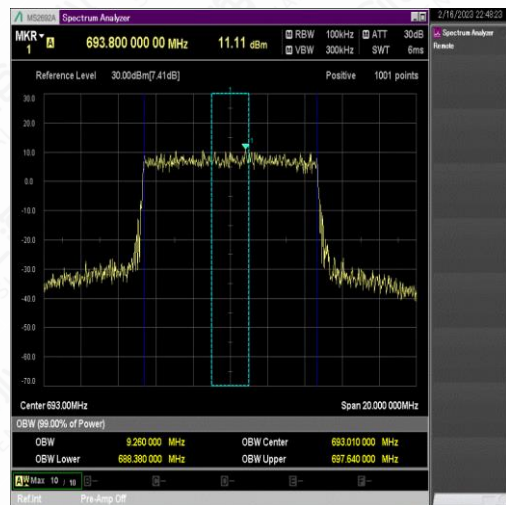
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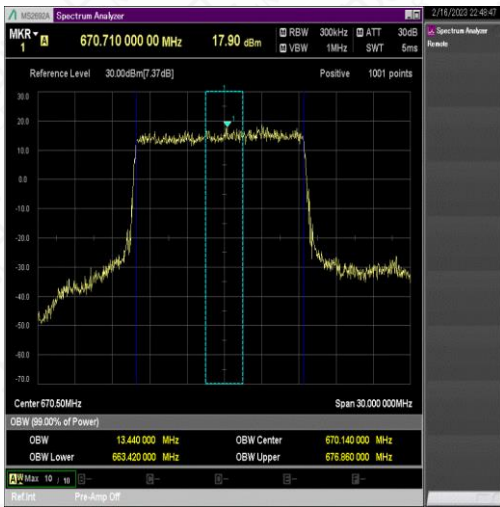
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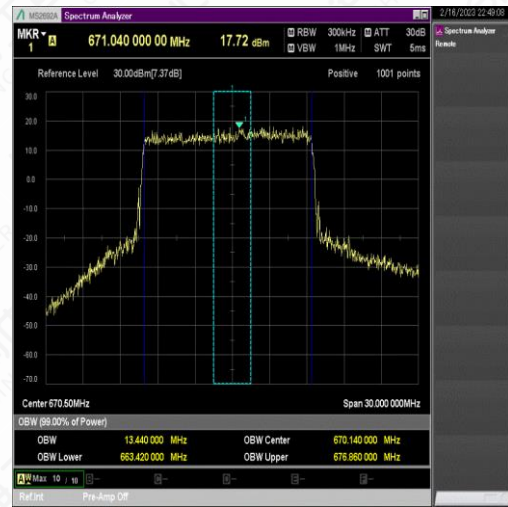
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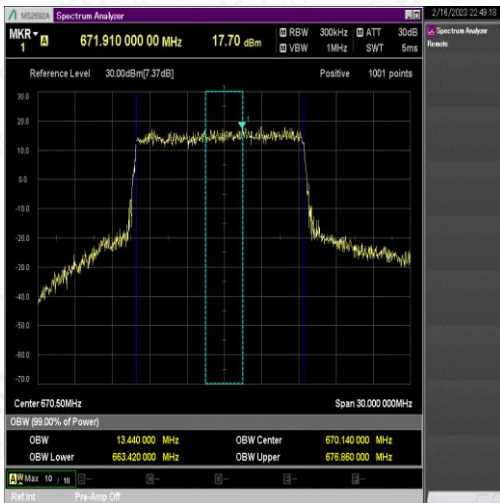
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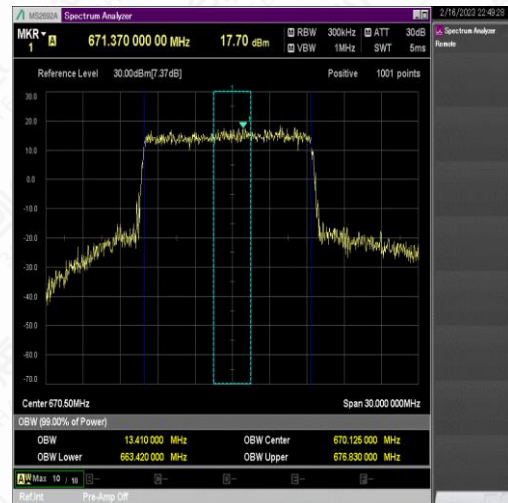
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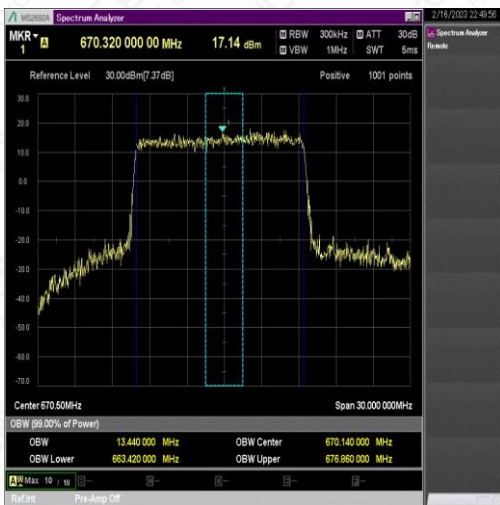
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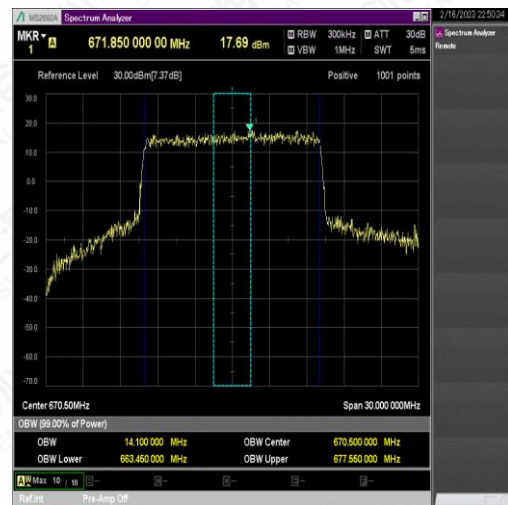
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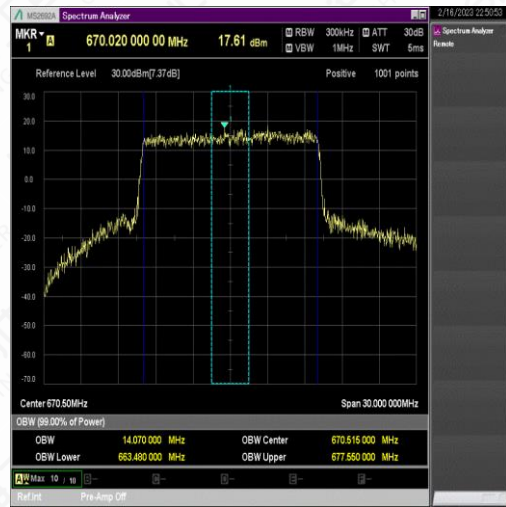
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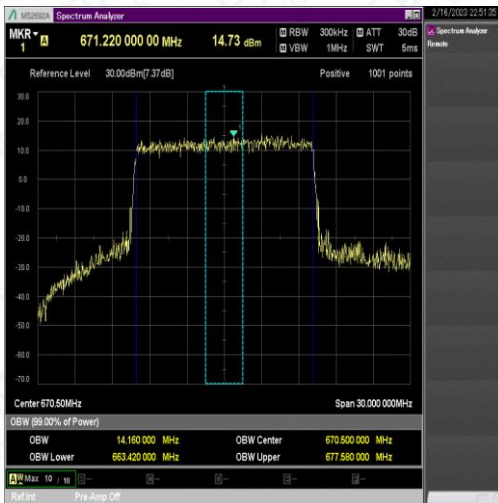
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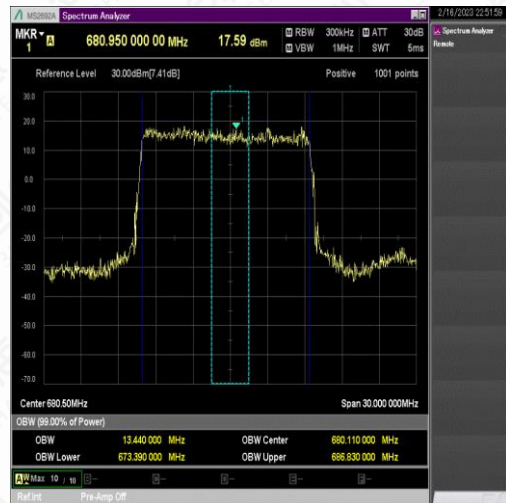
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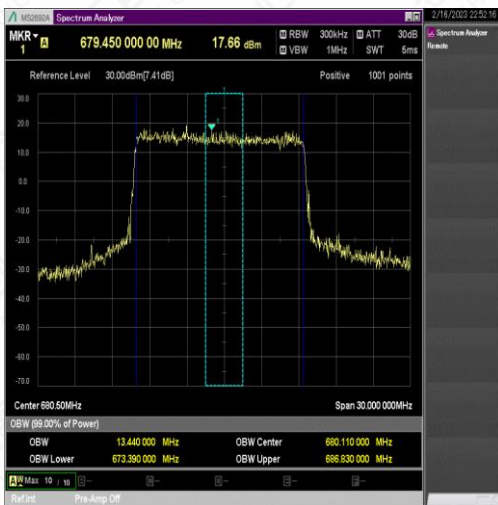
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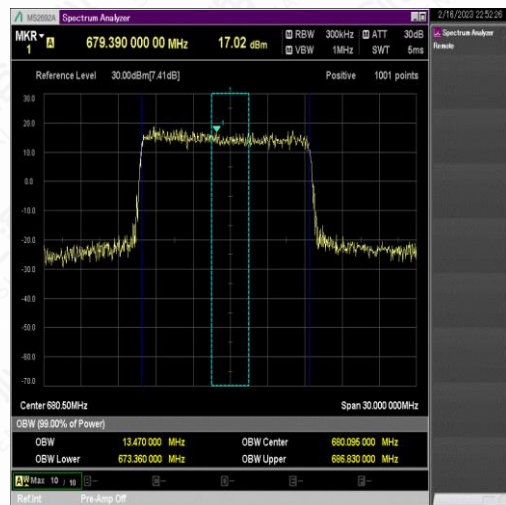
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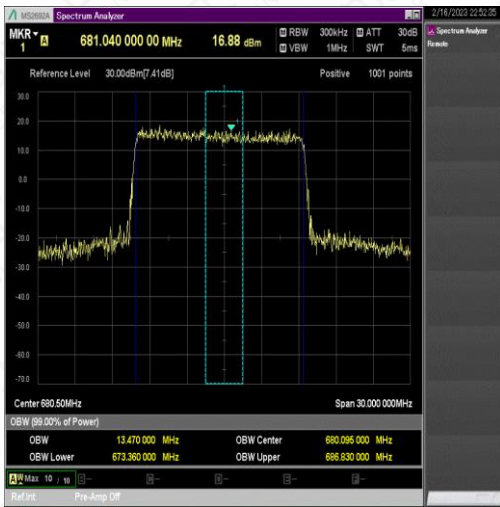
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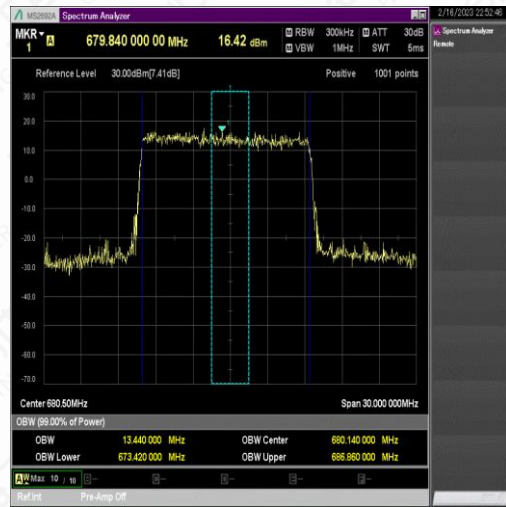
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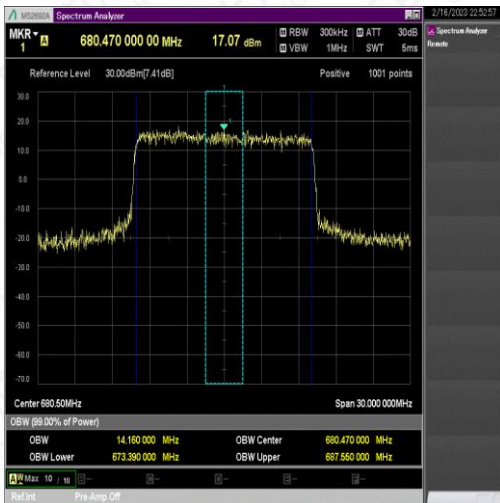
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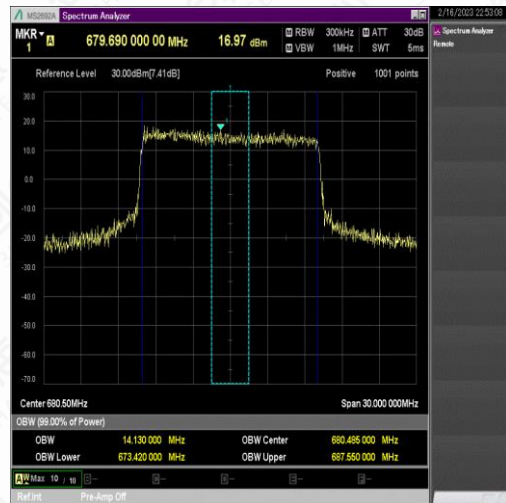
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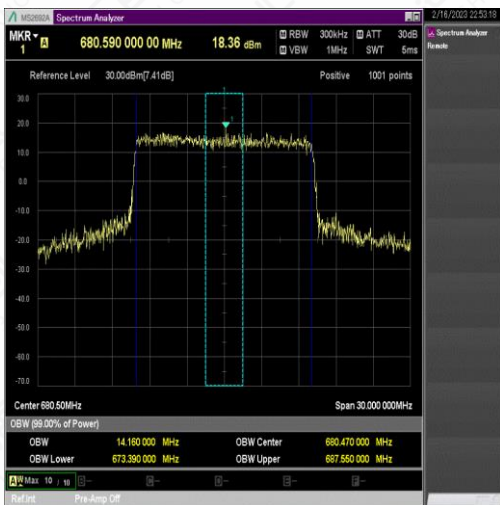
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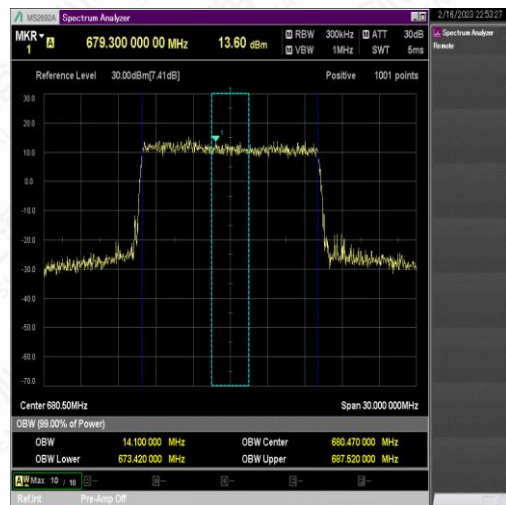
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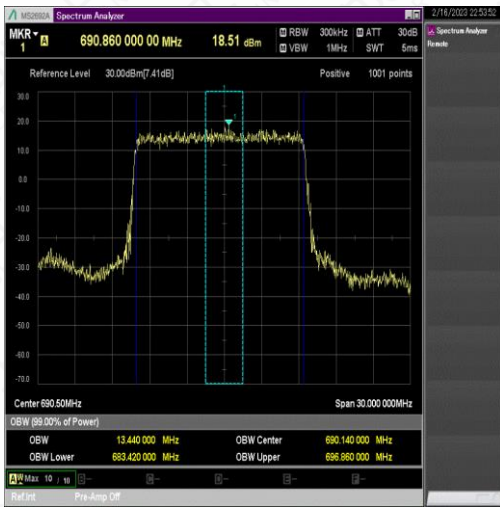
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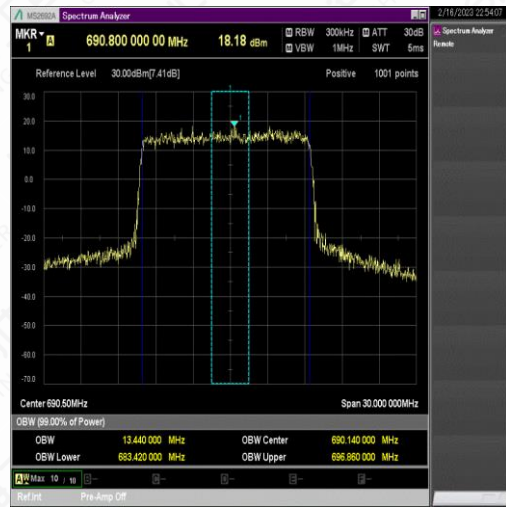
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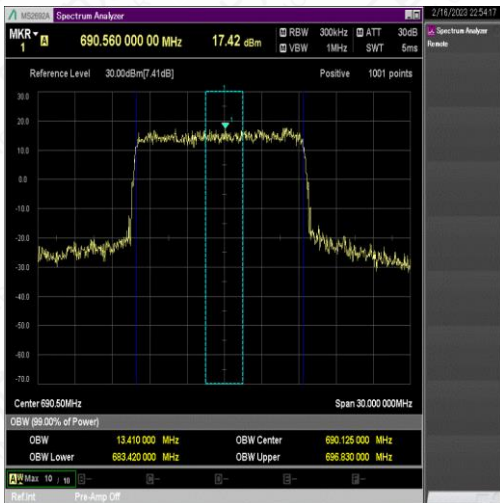
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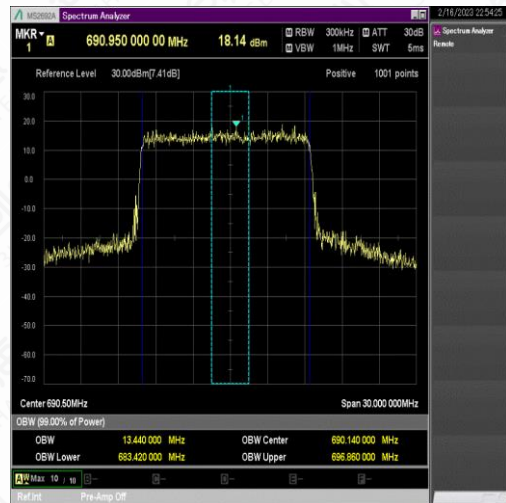
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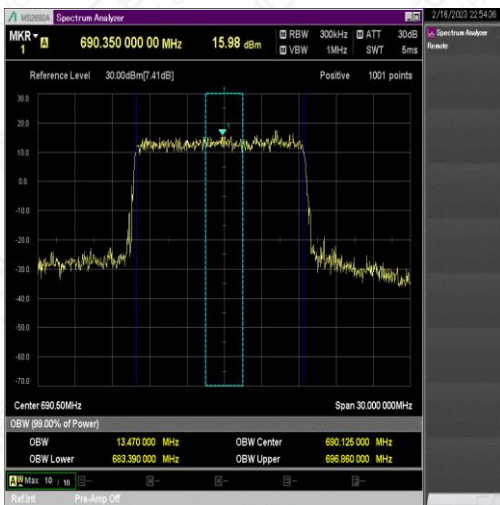
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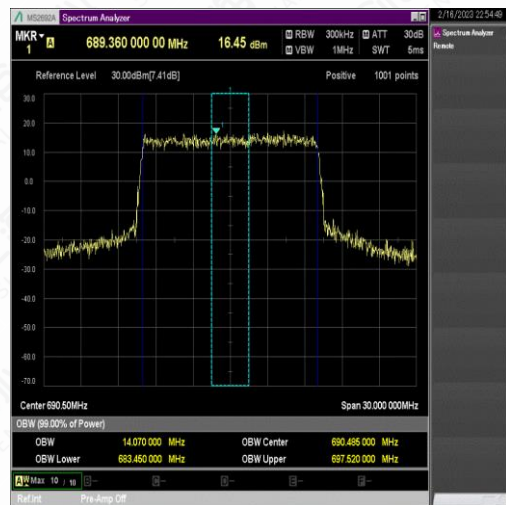
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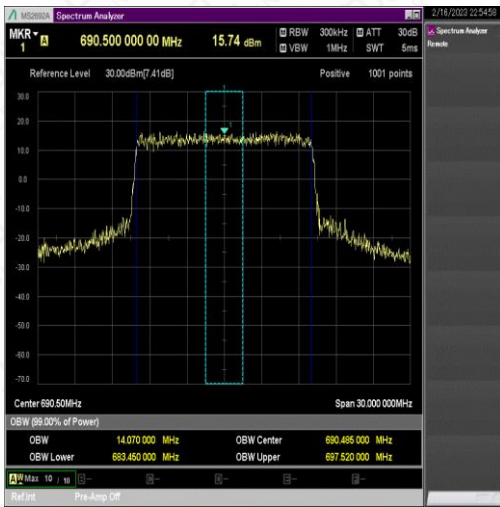
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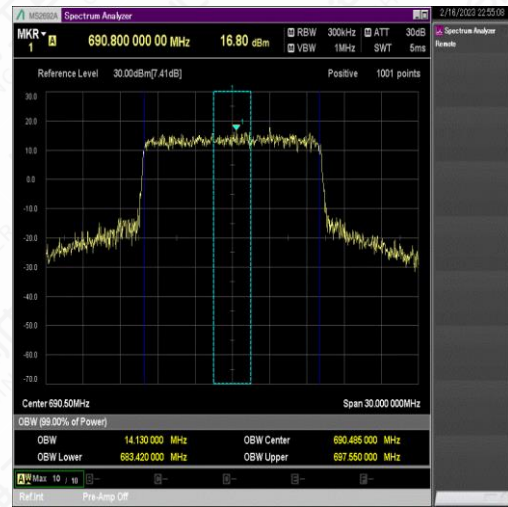
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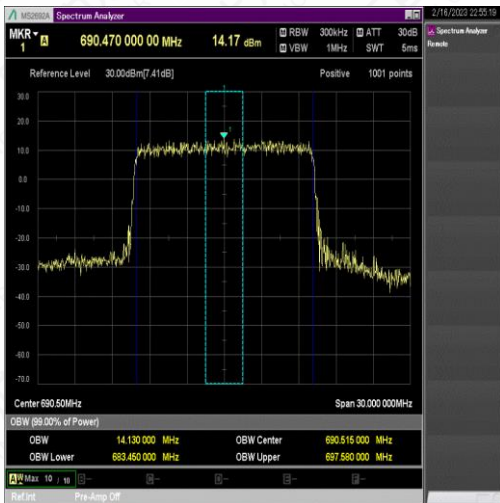
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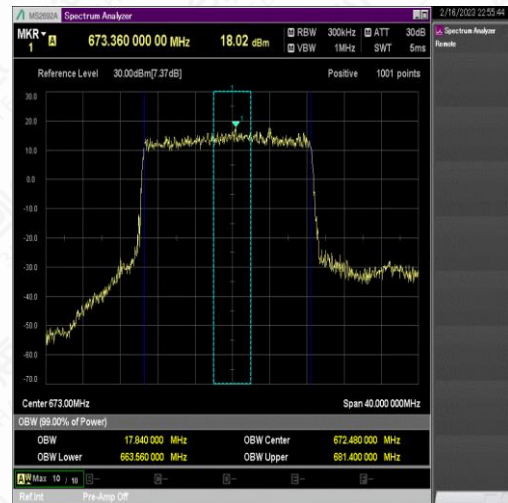
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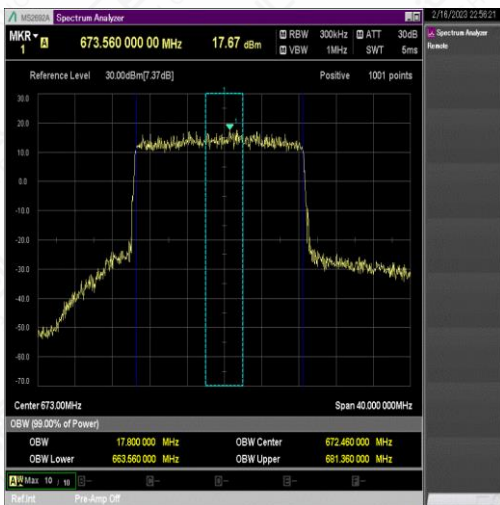
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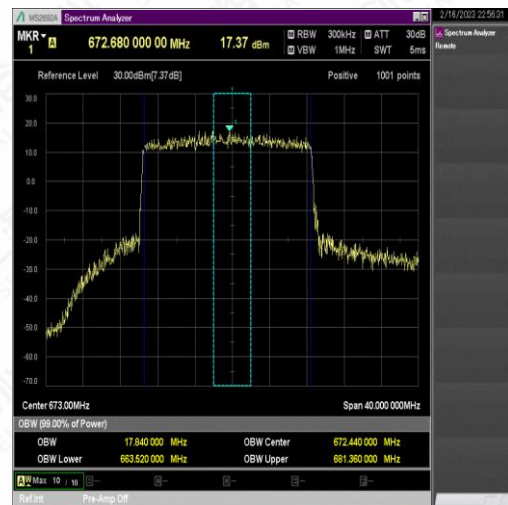
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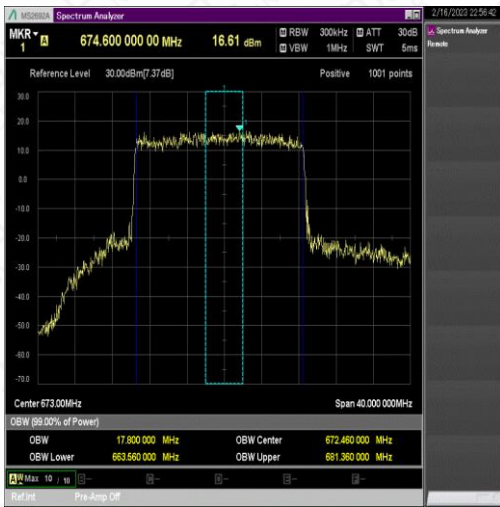
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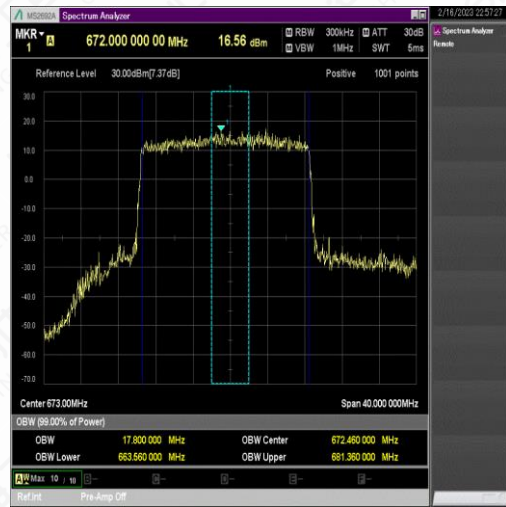
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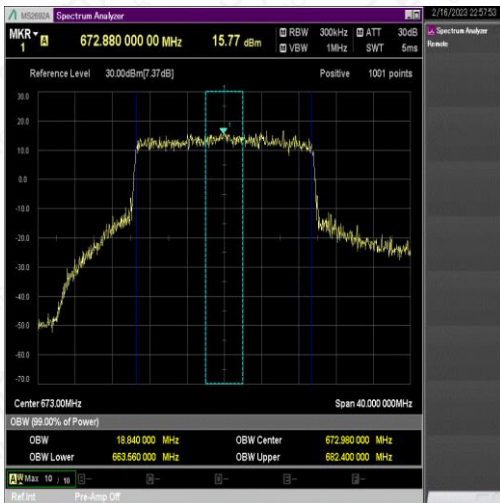
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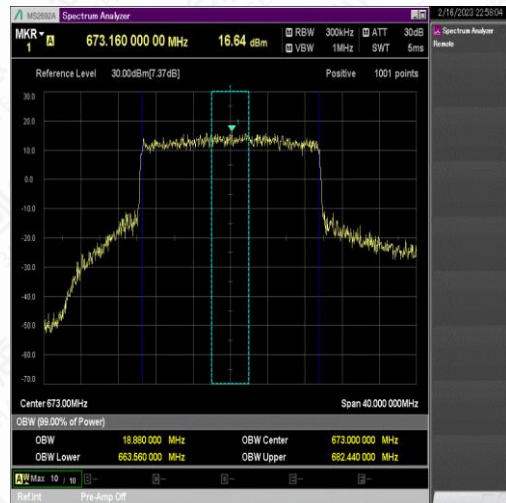
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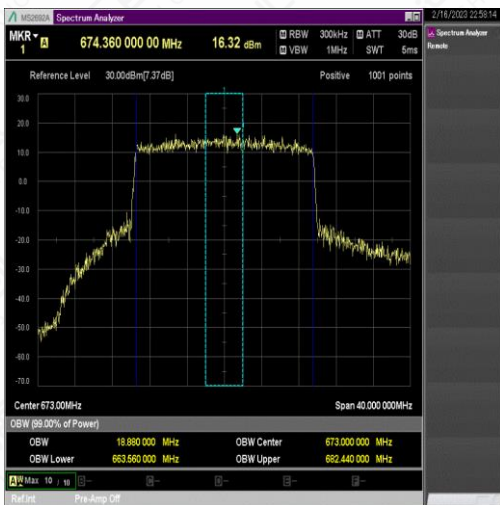
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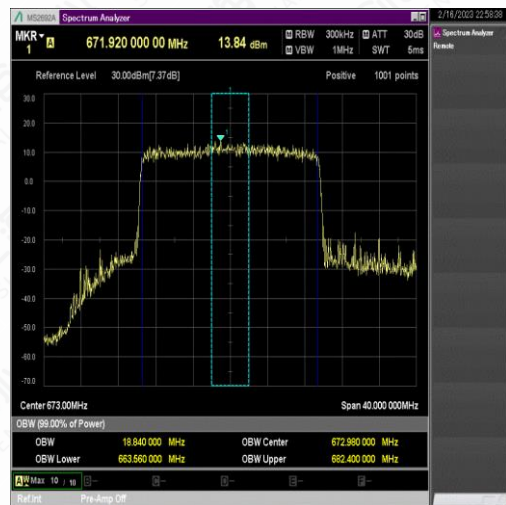
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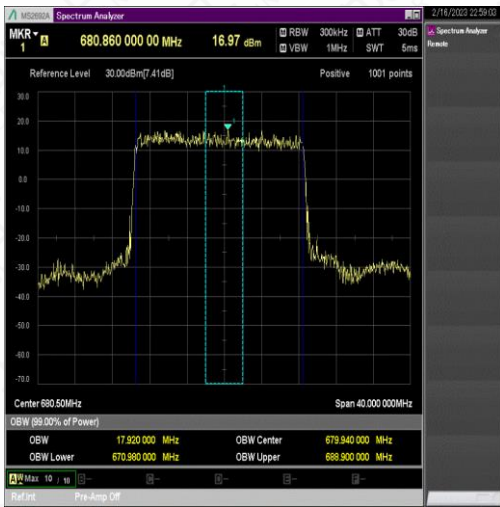
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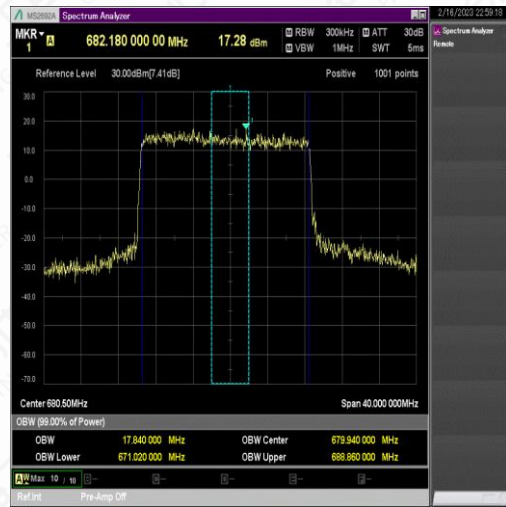
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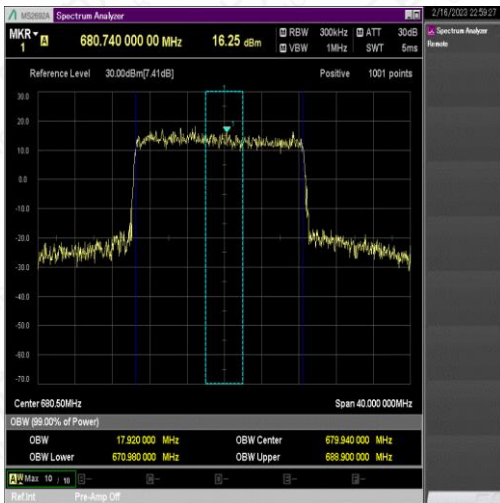
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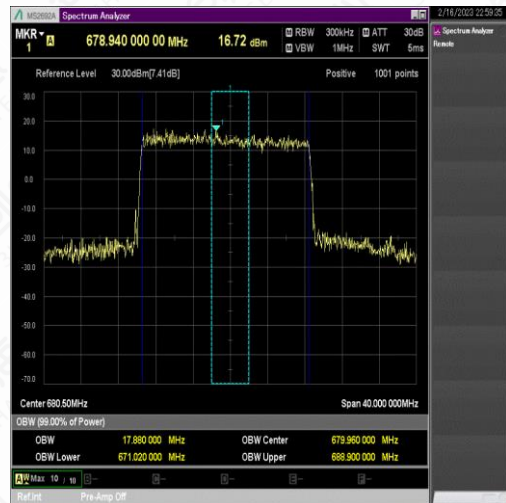
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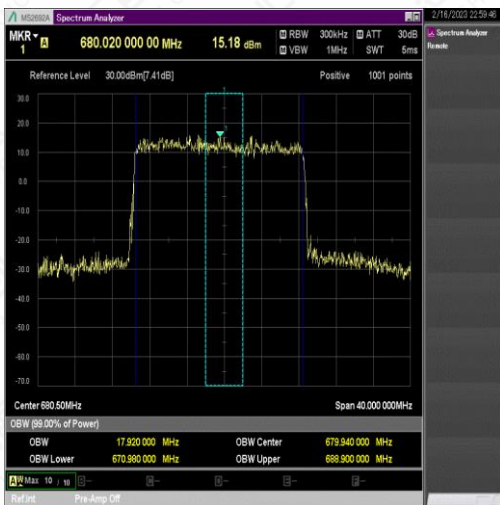
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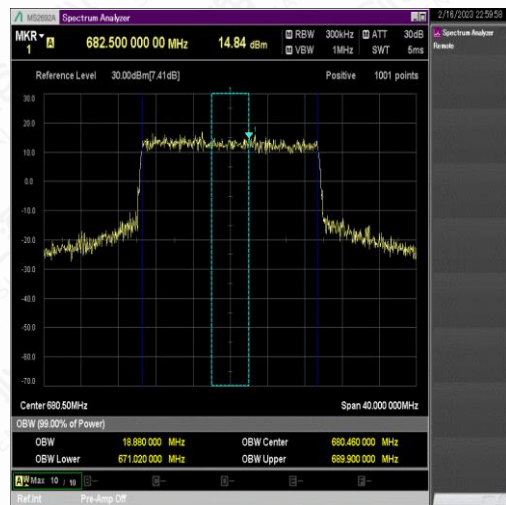
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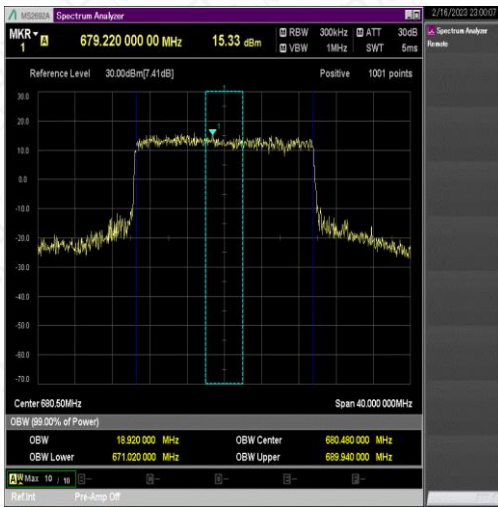
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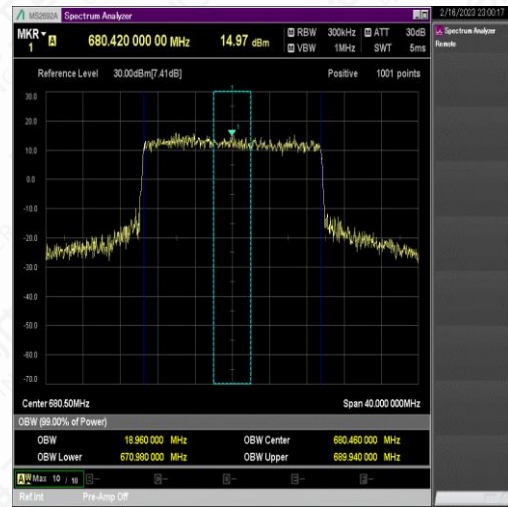
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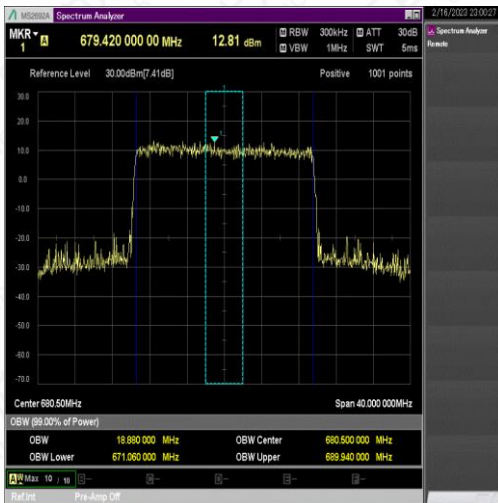
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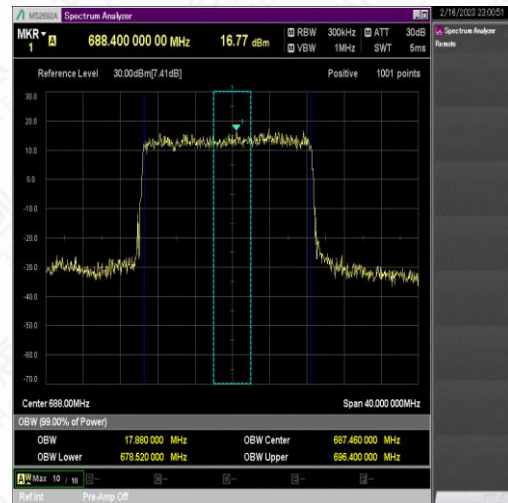
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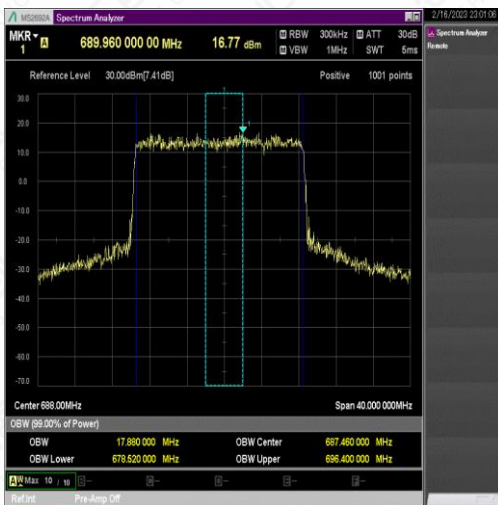
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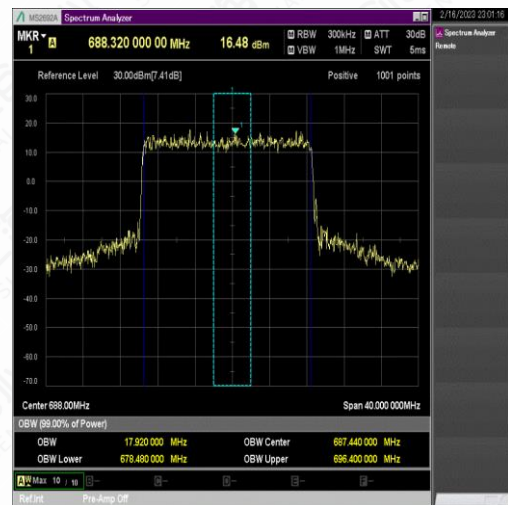
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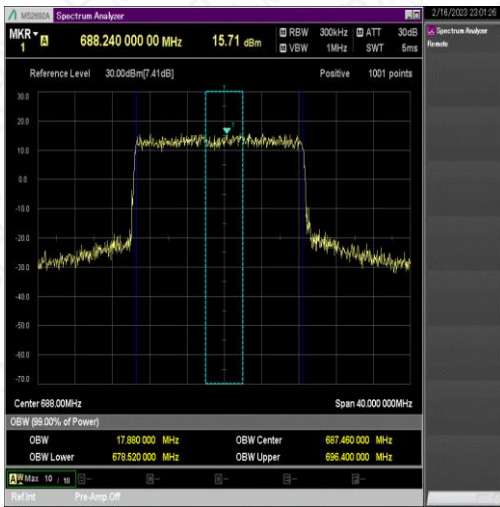
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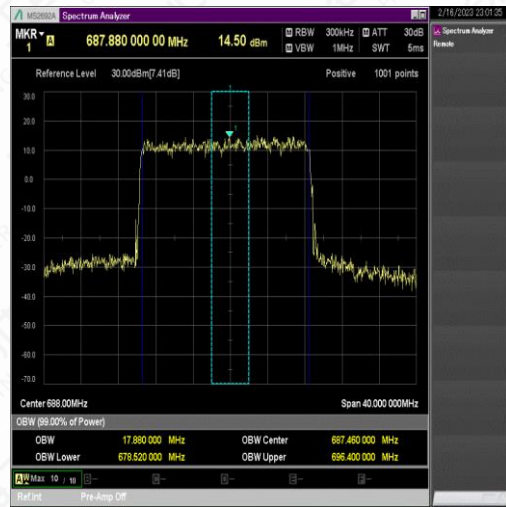
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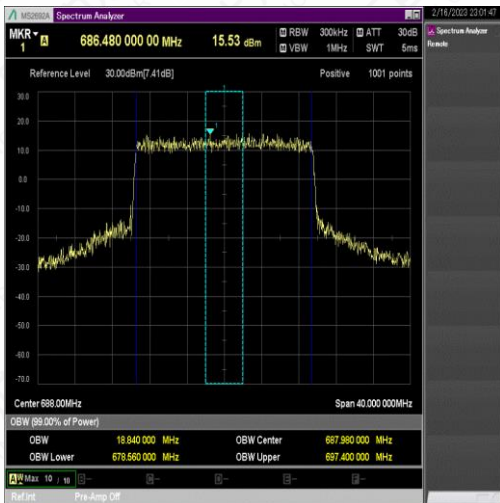
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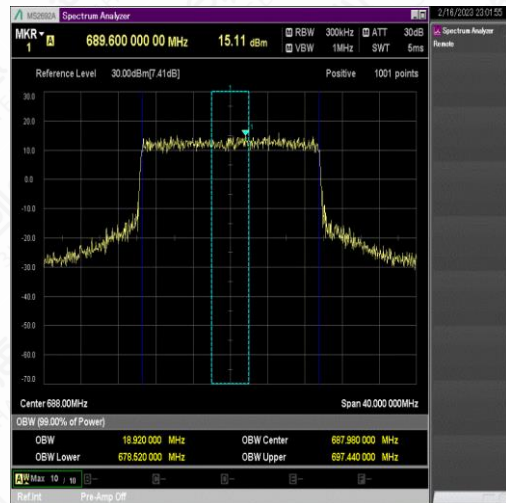
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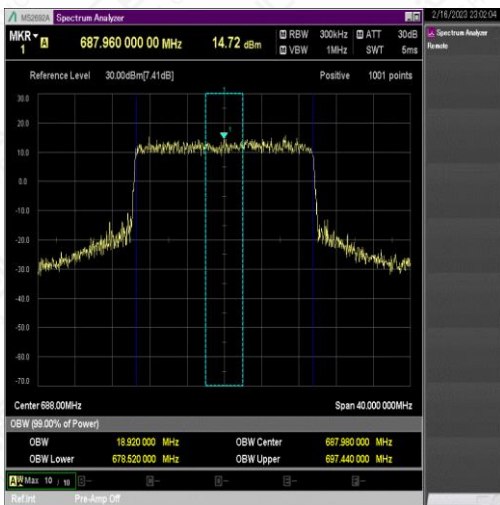
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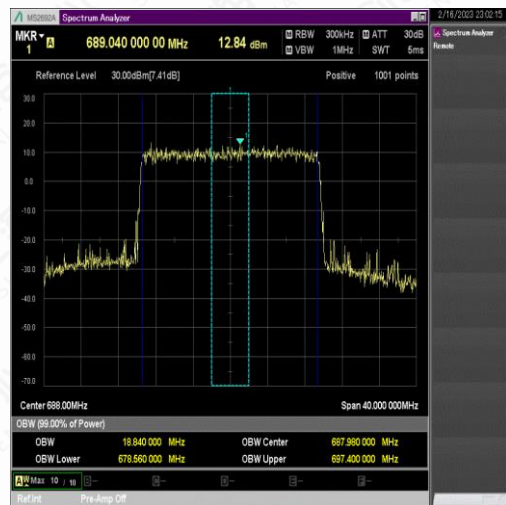
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NTNV_N71_PC3_15_20_H_TID8_Ant1#1

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