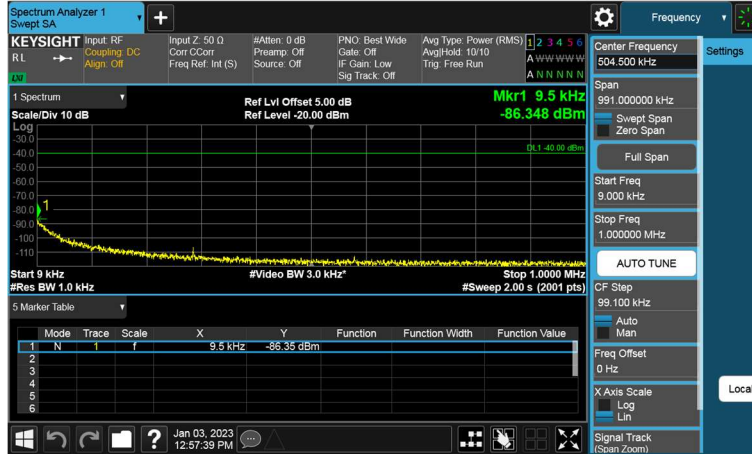




20M_20M_Q16_FULLRB_CSE0a



20M_20M_Q16_FULLRB_CSE0b



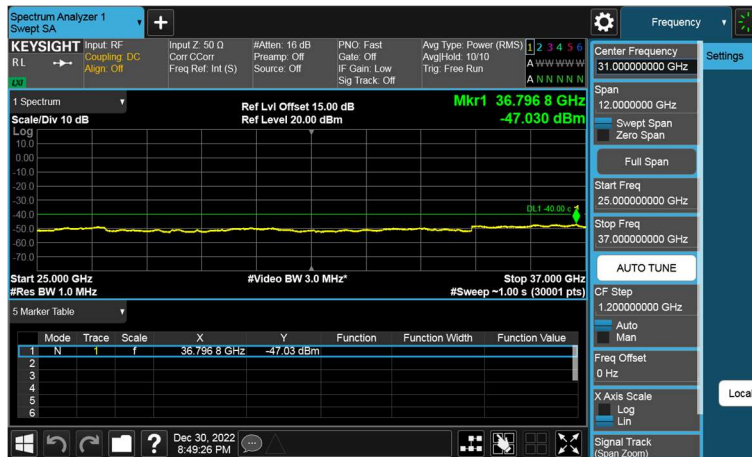
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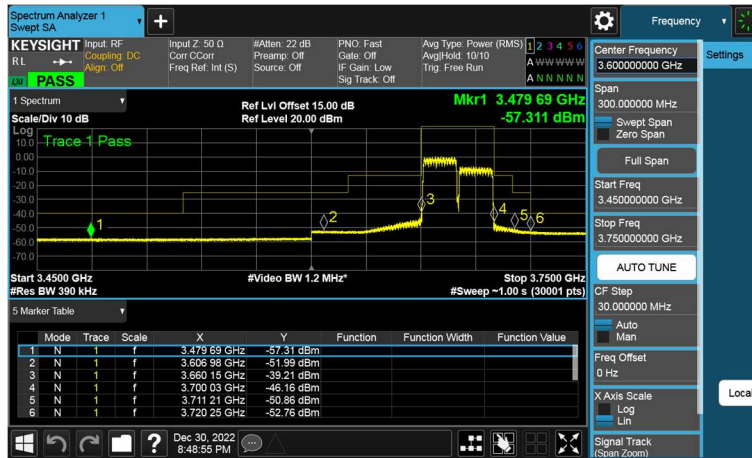
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20M_20M_Q16_FULLLRB_CSE3

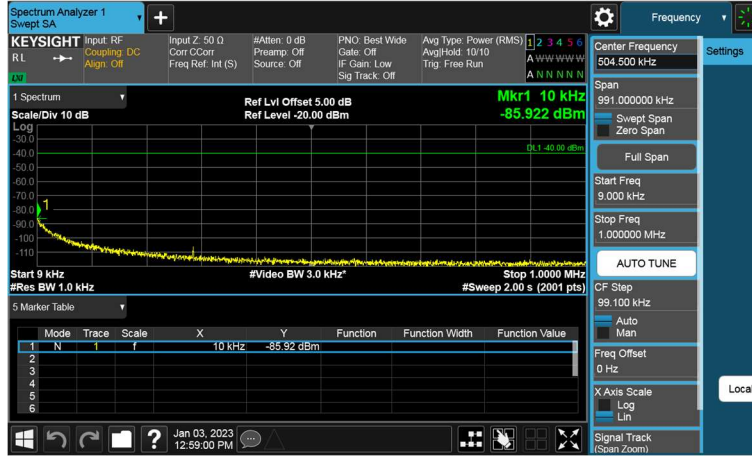


20M_20M_Q16_FULLLRB_CSEpart96

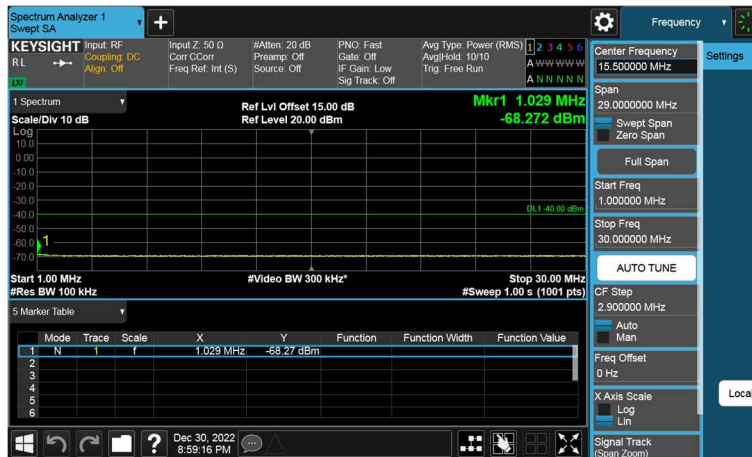




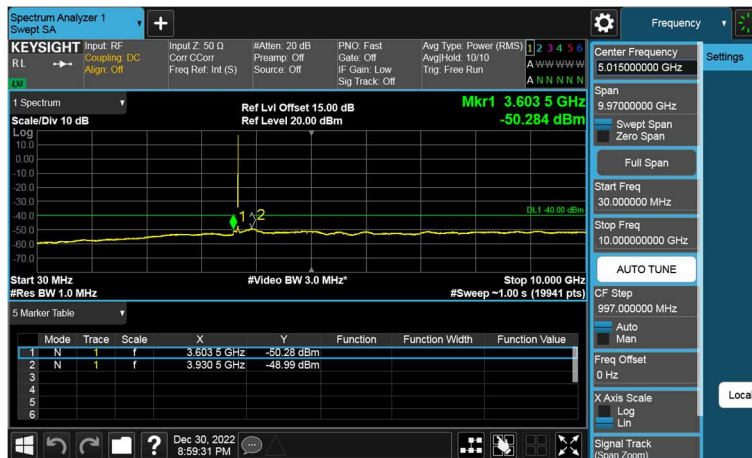
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20M_20M_Q64_1RB_CSE0b



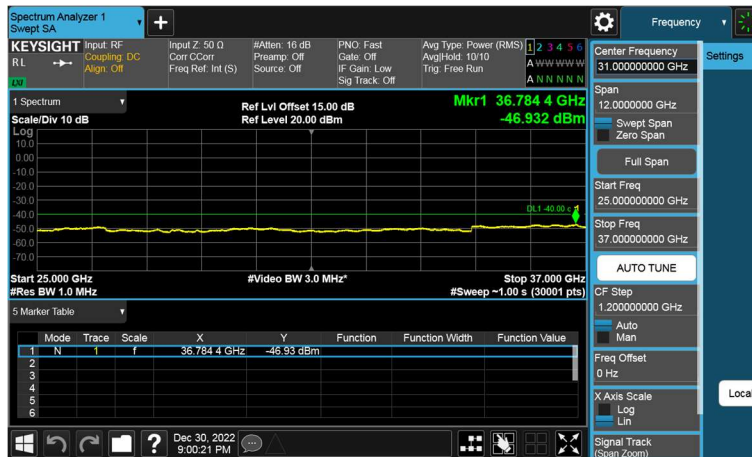
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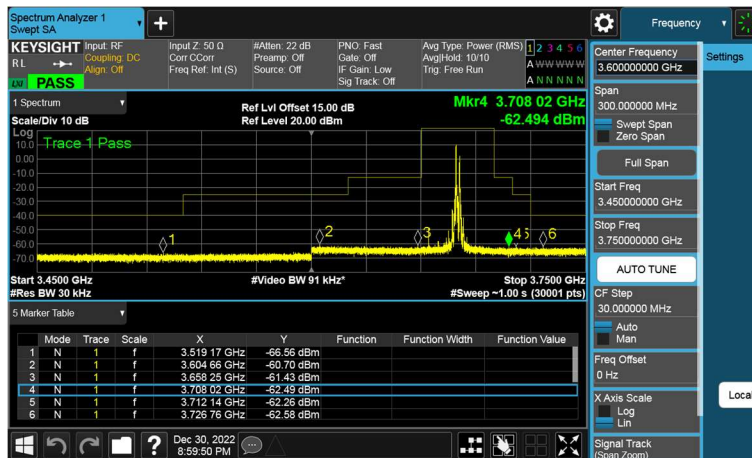
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20M_20M_Q64_1RB_CSE3

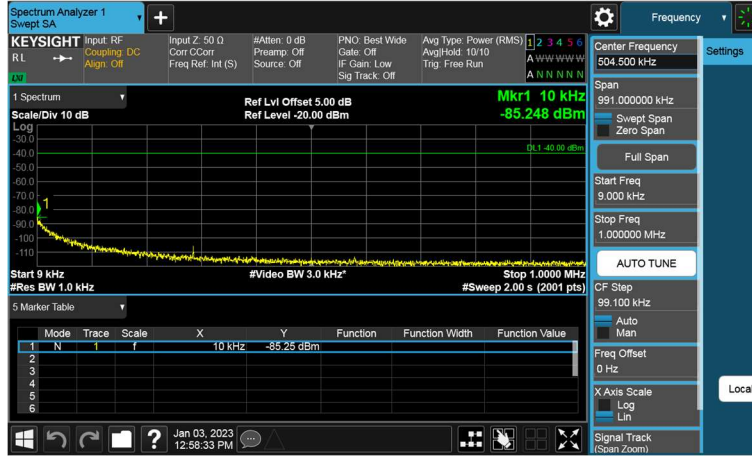


20M_20M_Q64_1RB_CSEpart96

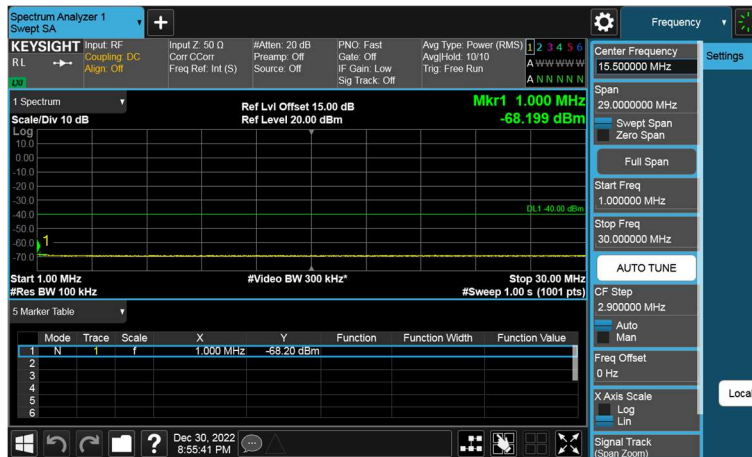




20M_20M_Q64_FULLRB_CSE0a



20M_20M_Q64_FULLRB_CSE0b



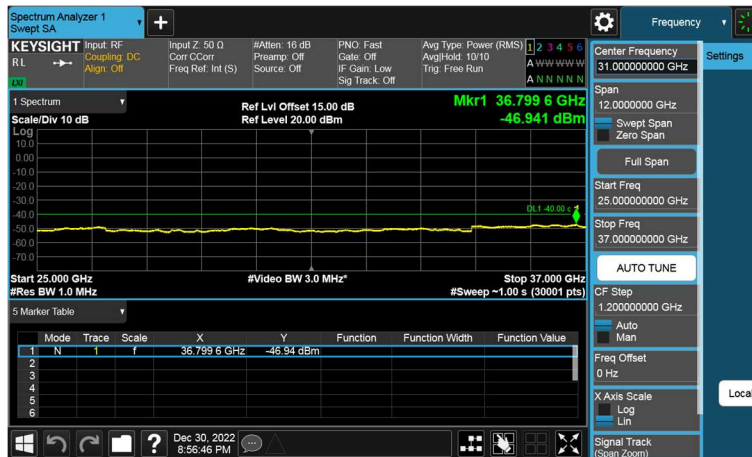
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20M_20M_Q64_FULLLRB_CSE2



20M_20M_Q64_FULLLRB_CSE3

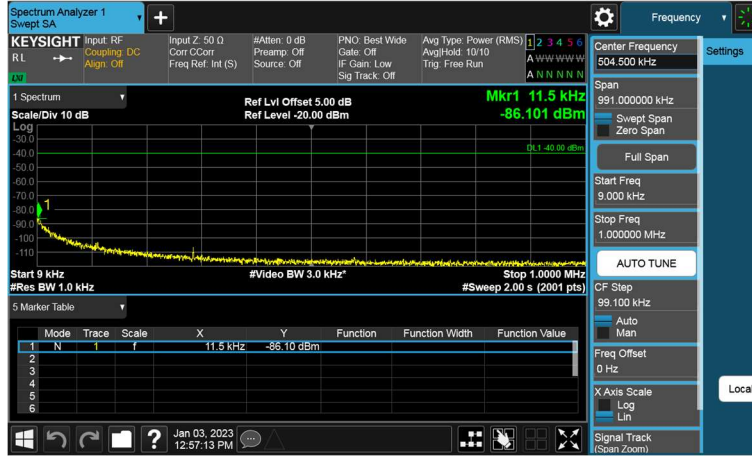


20M_20M_Q64_FULLLRB_CSEpart96

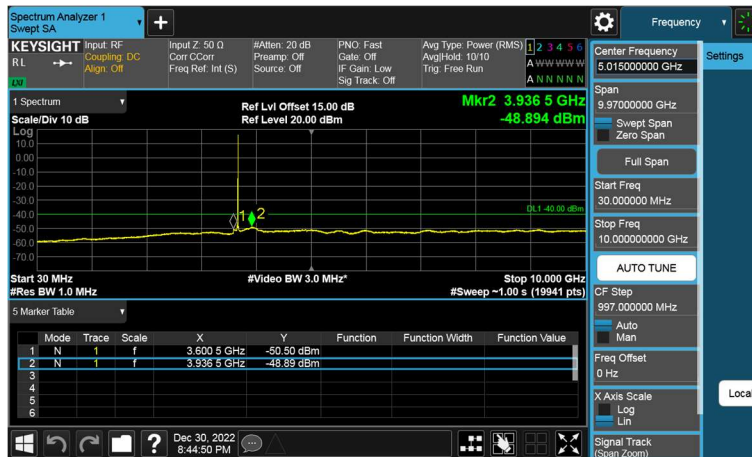




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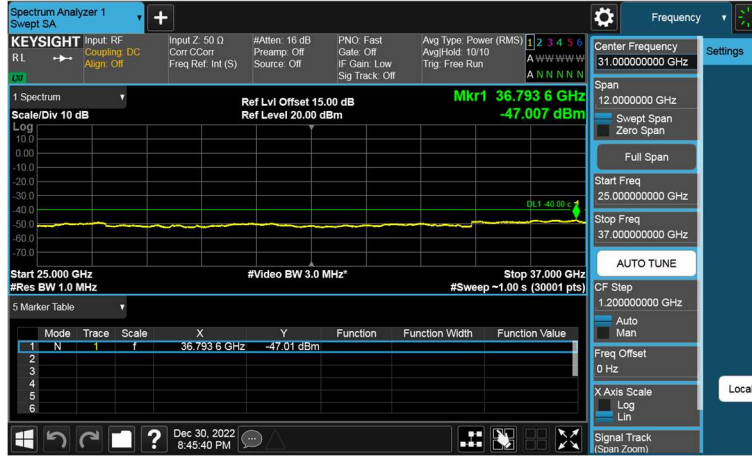
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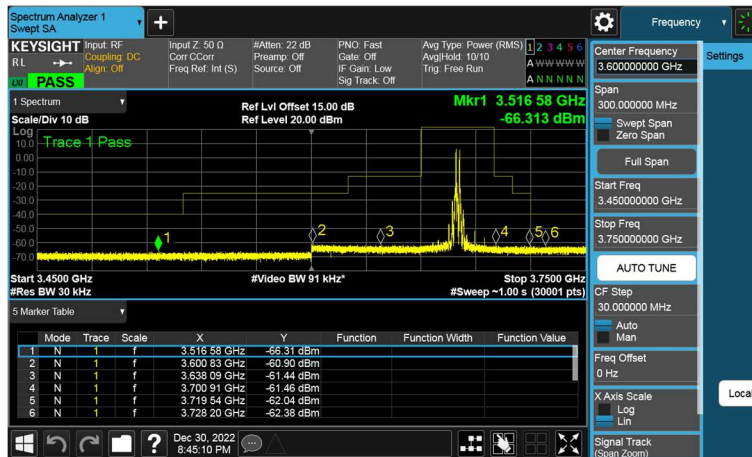
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20M_20M_QPSK_1RB_CSE3



20M_20M_QPSK_1RB_CSEpart96

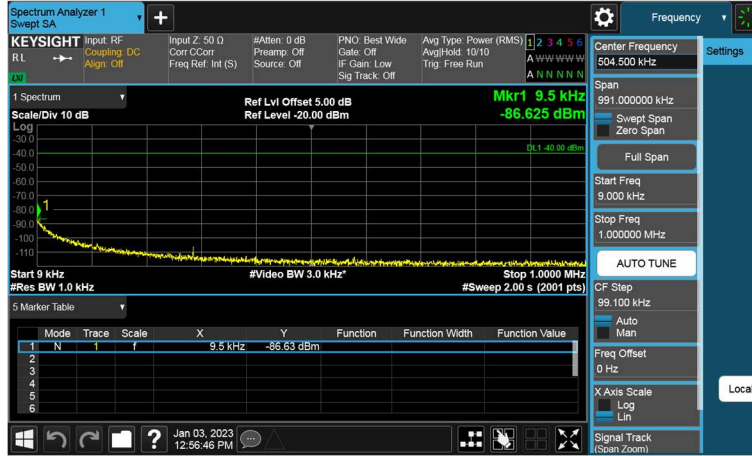


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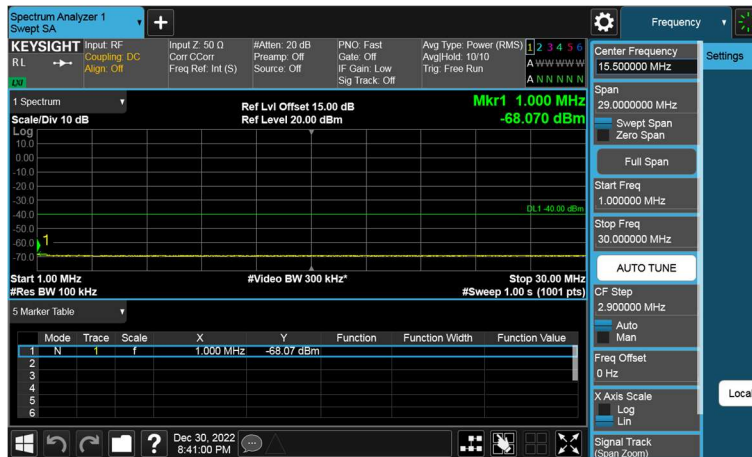




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20M_20M_QPSK_FULLRB_CSE0b



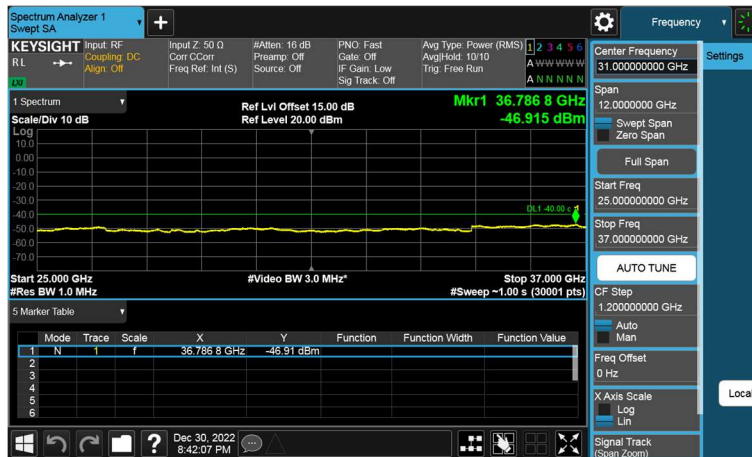
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20M_20M_QPSK_FULLRB_CSE2



20M_20M_QPSK_FULLRB_CSE3



20M_20M_QPSK_FULLRB_CSEpart6



- End of the Report -