

Operational Principle

1. MCU, high-speed CMOS technology. This chip is responsible to control RF-Baseband Transceiver.s
2. 2.4 GHz ISM Band Transceiver/Framer, SR8510 which is a low-cost,fully inegrated CMOS radio frequency(RF) transceiver block, including an on-chip MCU,RF circuit,LDO,Voltage detector, It needs an external 16MHz crystal for reference frequency..
3. ANTENNA, The antenna is typically fed from the end of the monopole section by a plated through-hole via which is in turn connected to the RF (SR8510) output on the 2.4GHz band.
4. Modulation: GFSK
5. Supported channels: 16 channels
6. Maximum E-Field Strength: 82.41dBuV/m@3m
7. Antenna type: Printed Antenna