

Operational Principle

1. CPU,SE65K have an 8-bit microprocessor designed and developed with low-power and high-speed CMOS technology. Operation frequency is 3.5 MHz with internal RC oscillator., This chip is responsible to control RF-Baseband Transceiver(SE8R01).
2. Matrix,8*18 matrix with eight input line and eighteen output line.
3. 2.4 GHz ISM Band Transceiver/Framer,SE8R01 which is a low-cost,fully integrated CMOS radio frequency(RF) transceiver block,combined with a 64-byte buffered framer block.It contains transmit,receive,VCO and PLL functions,including an on-chip channel filter and resonator. It needs an external 16MHz crystal for reference frequency;
4. ANTENNA, The antenna is typically fed from the end of the monopole section by a plated through-hole via which is in turn connected to the RF output on the 2.4GHz band.