



To: Federal Communications Commission  
7435 Oakland Mills Road  
Columbia, MD 21046  
USA

Date: 21-MAR-2024

Attention: Application Examiner / Review Engineer

**Subject: Request for a Class II Permissive Change**

FCC ID: **2AHDRTDCE210**

Grant Date(s): **03/28/2022**

Pursuant to CFR47 §2.1043, the Grantee hereby requests a Class II Permissive Change for the above identified device.

Following changes apply to the modified device:

The Variscite VAR-SOM-MX7 module is updated with an increased RAM (2 GB DDR3L instead of the previous 1 GB). In addition, the Ethernet PHY changed from AR8033-AL (Qualcomm) to ADIN1300 (Analog Devices Inc.) associated with some minor PCB board adaptations of the Variscite SOM module.

There are no changes on the main PCB or the integrated RF portion itself.

Sincerely,

RECD Assistant / Authorized Agent