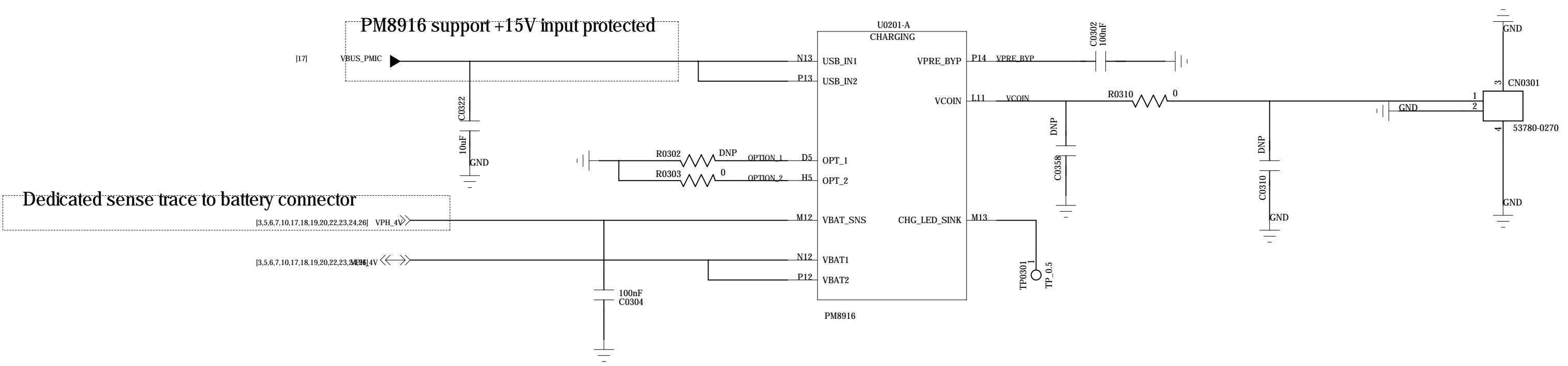


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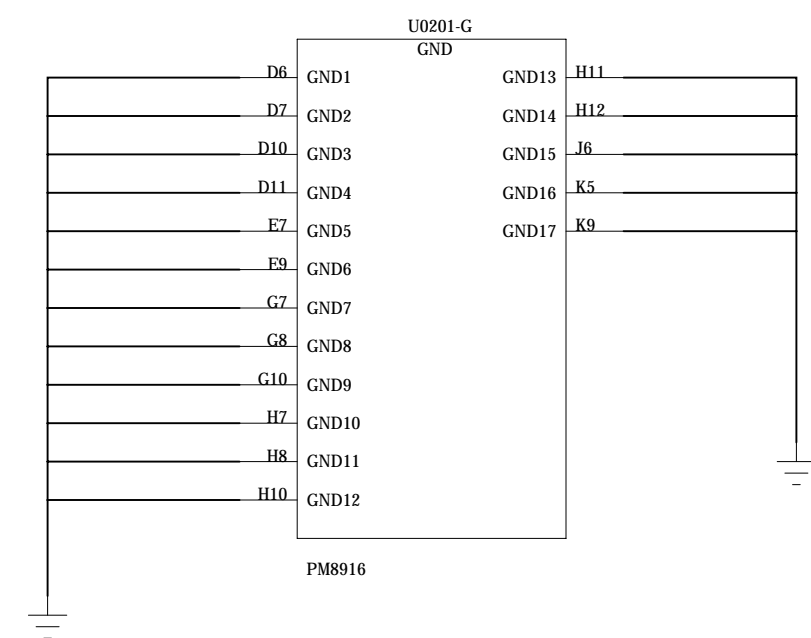
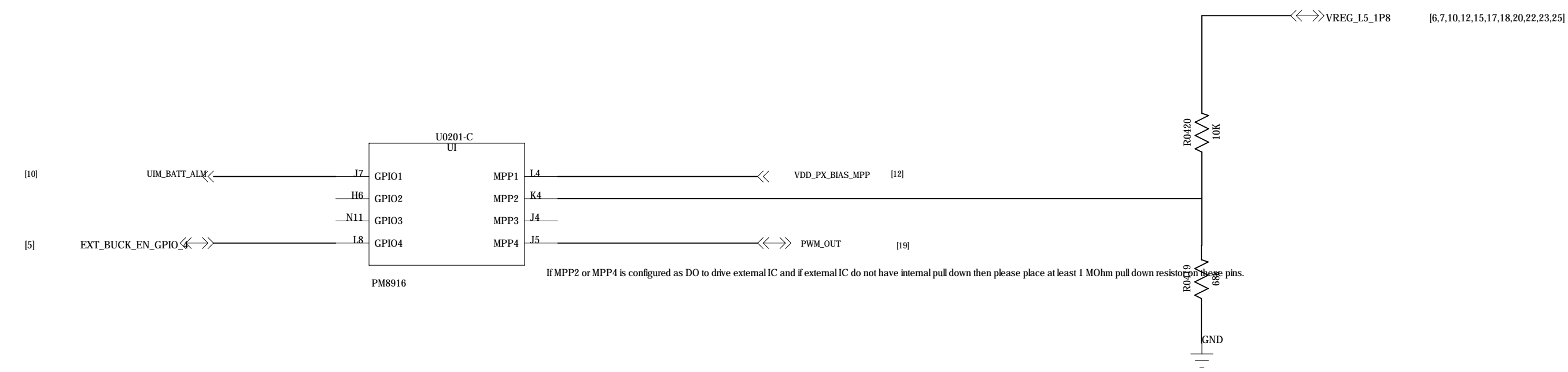
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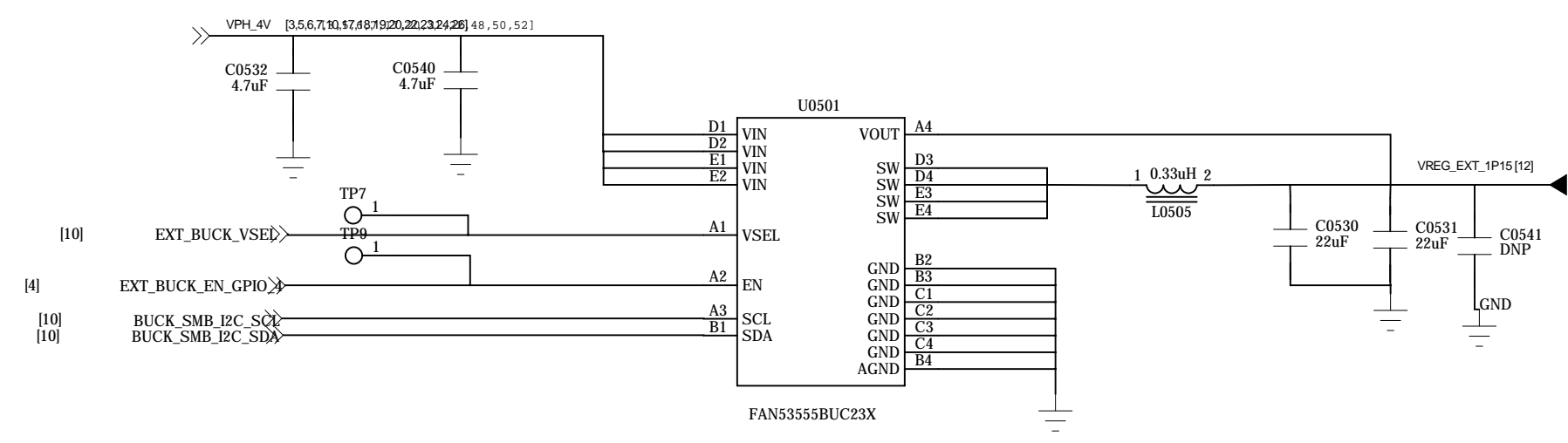
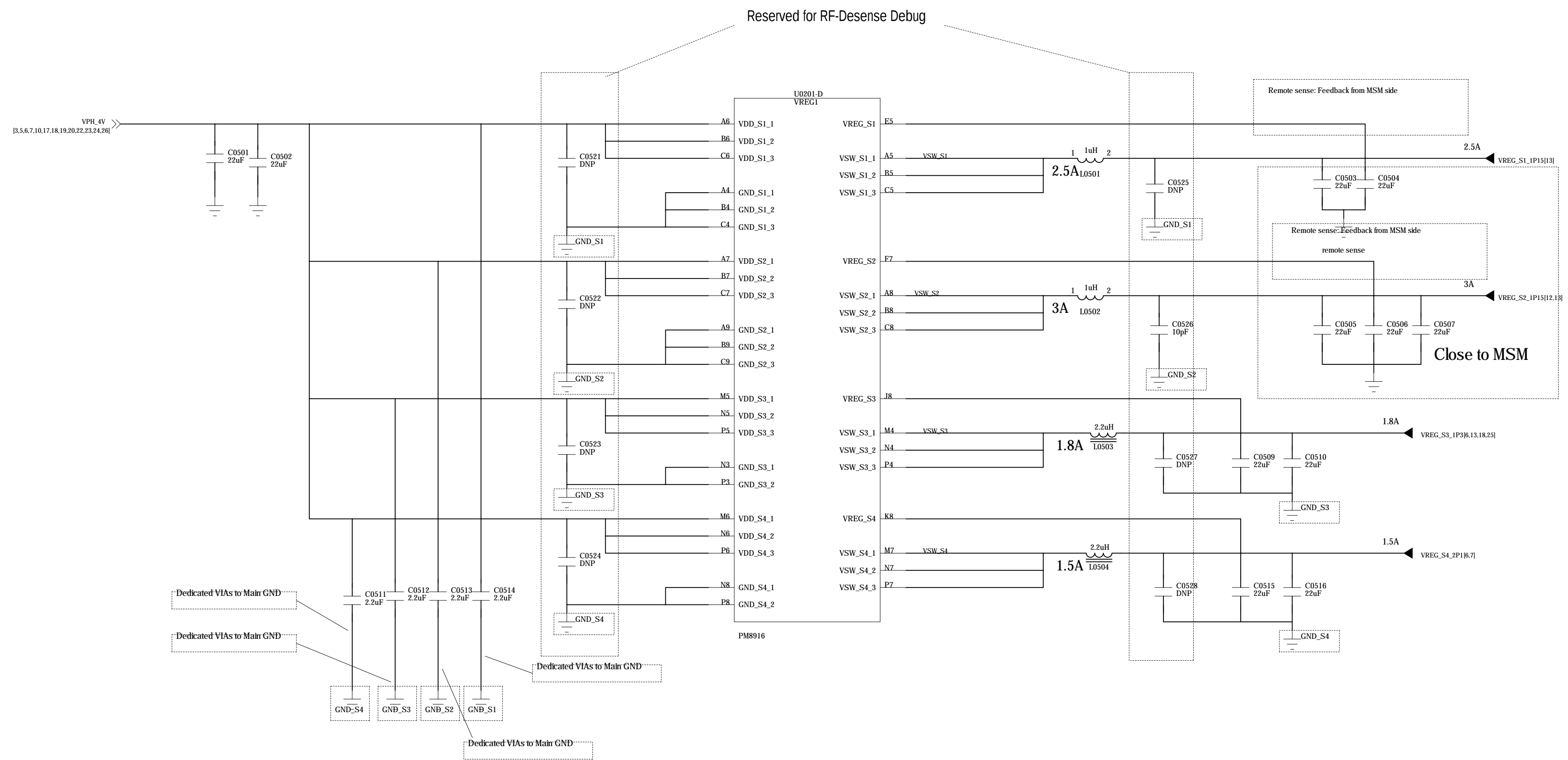
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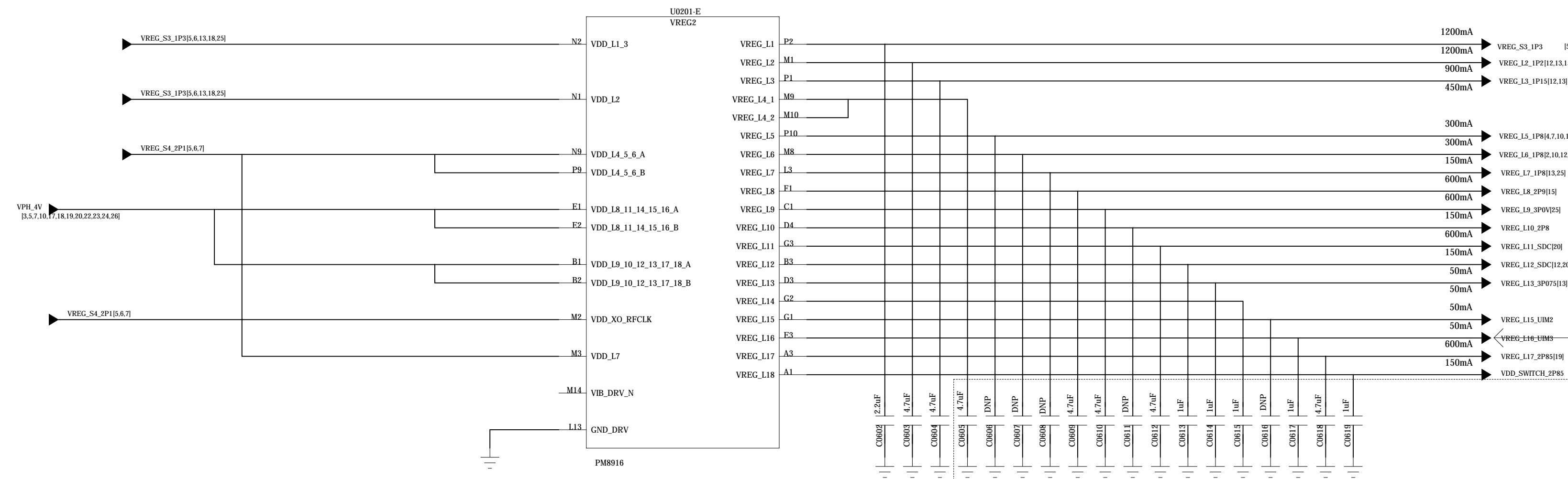
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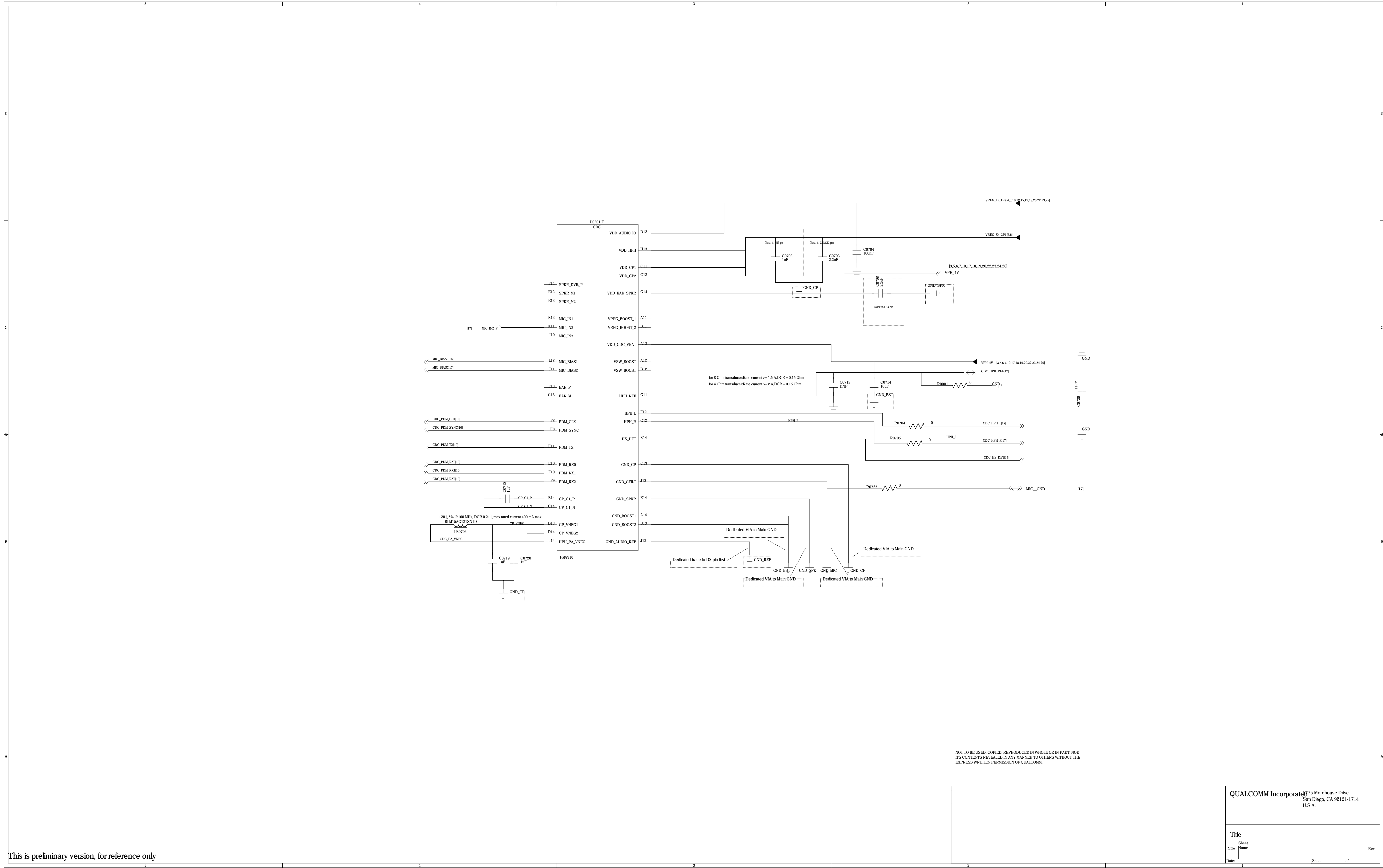


Software Caution:
L16 Need to Config to 1.8V and turn on later than L17

PSEUDO CAPLESS LDOs
L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, L16, L17, L18

Local output capacitor can NC/Remove depends on remote load capacitors and PCB layout
 LDOs L8, L9, L11, L17 each shall have total effective output capacitance of 4.7uF.
 Please refer to PM8916 training slides(80-NK808-21) for limits on LDO output trace resistance and inductance
 Rest of the LDOs shall each have total effective output capacitance of 1.0uF
 Please ensure when you DNI the output capacitor of Pseudo capless LDOs
 the effective total capacitance at load side shall match above requirement

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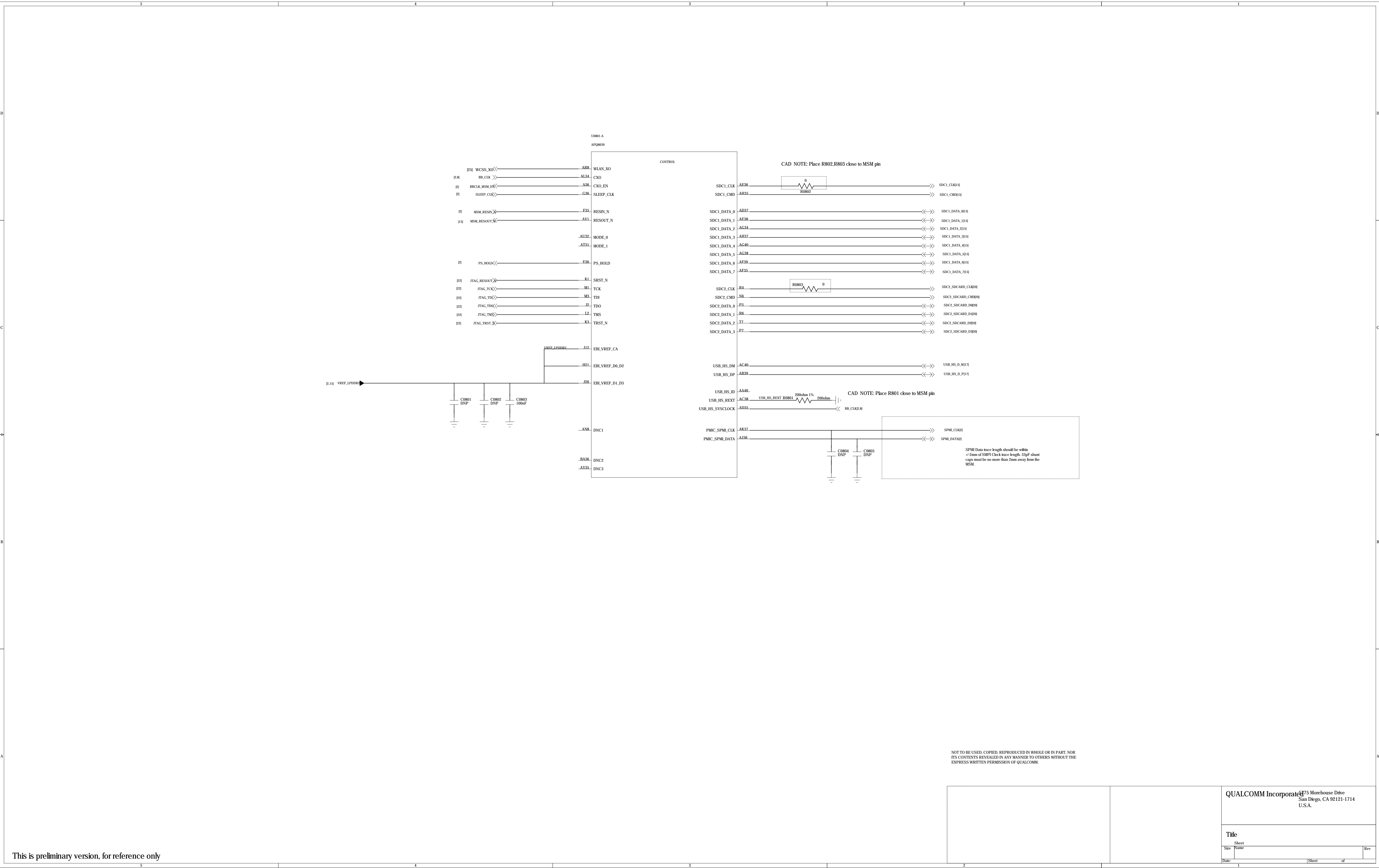


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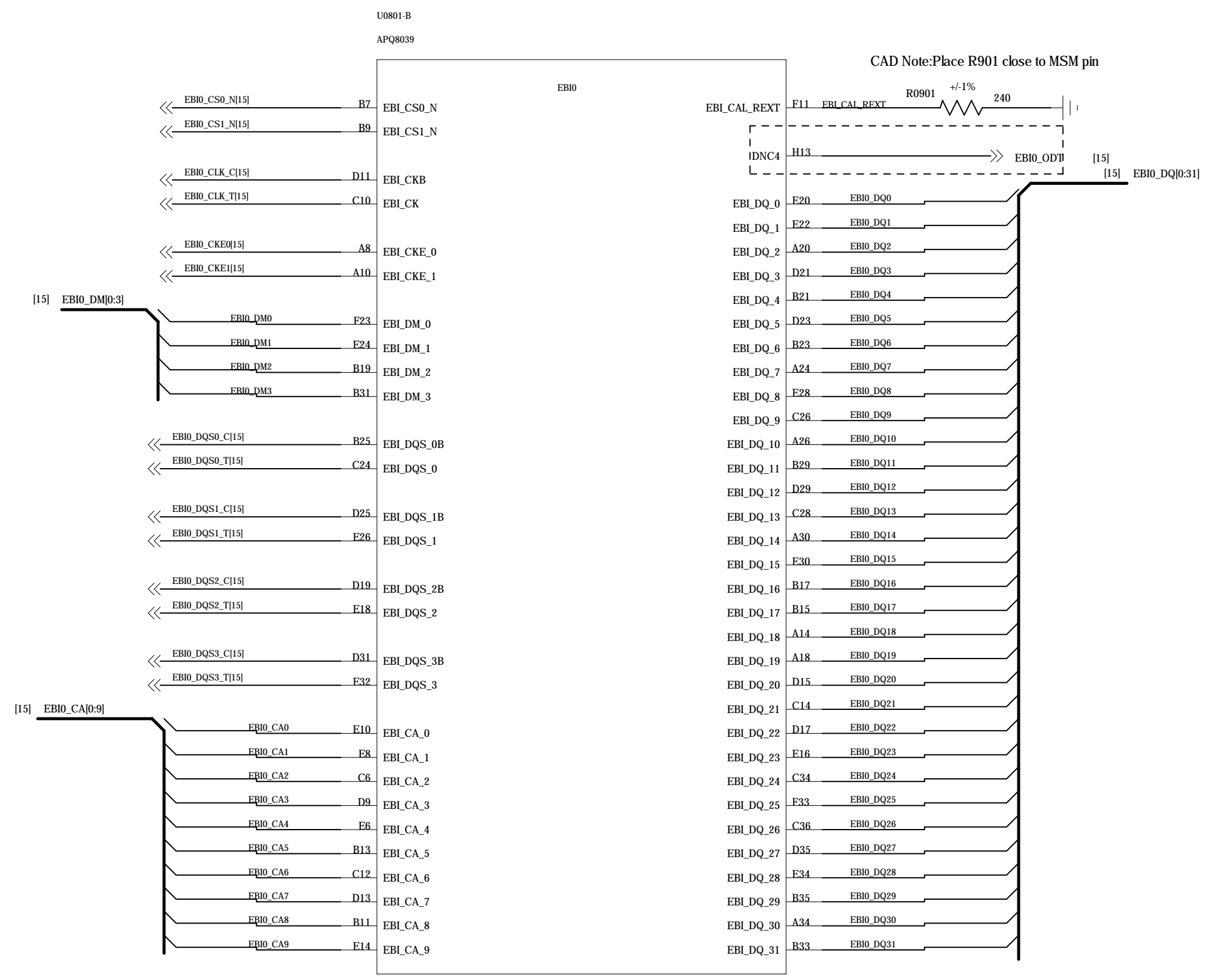
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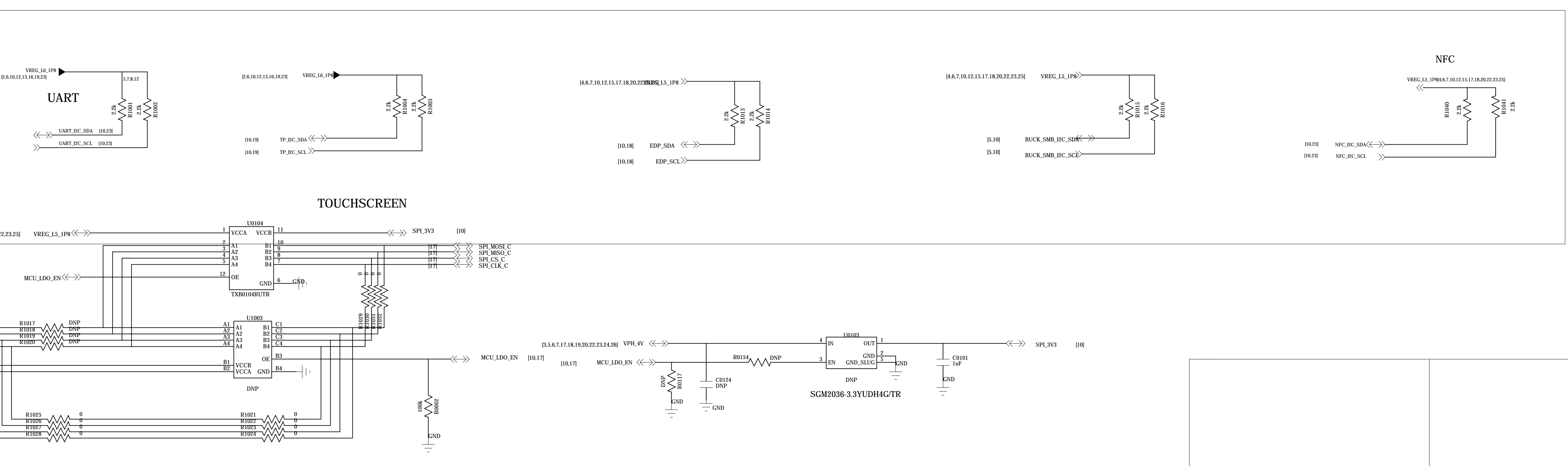
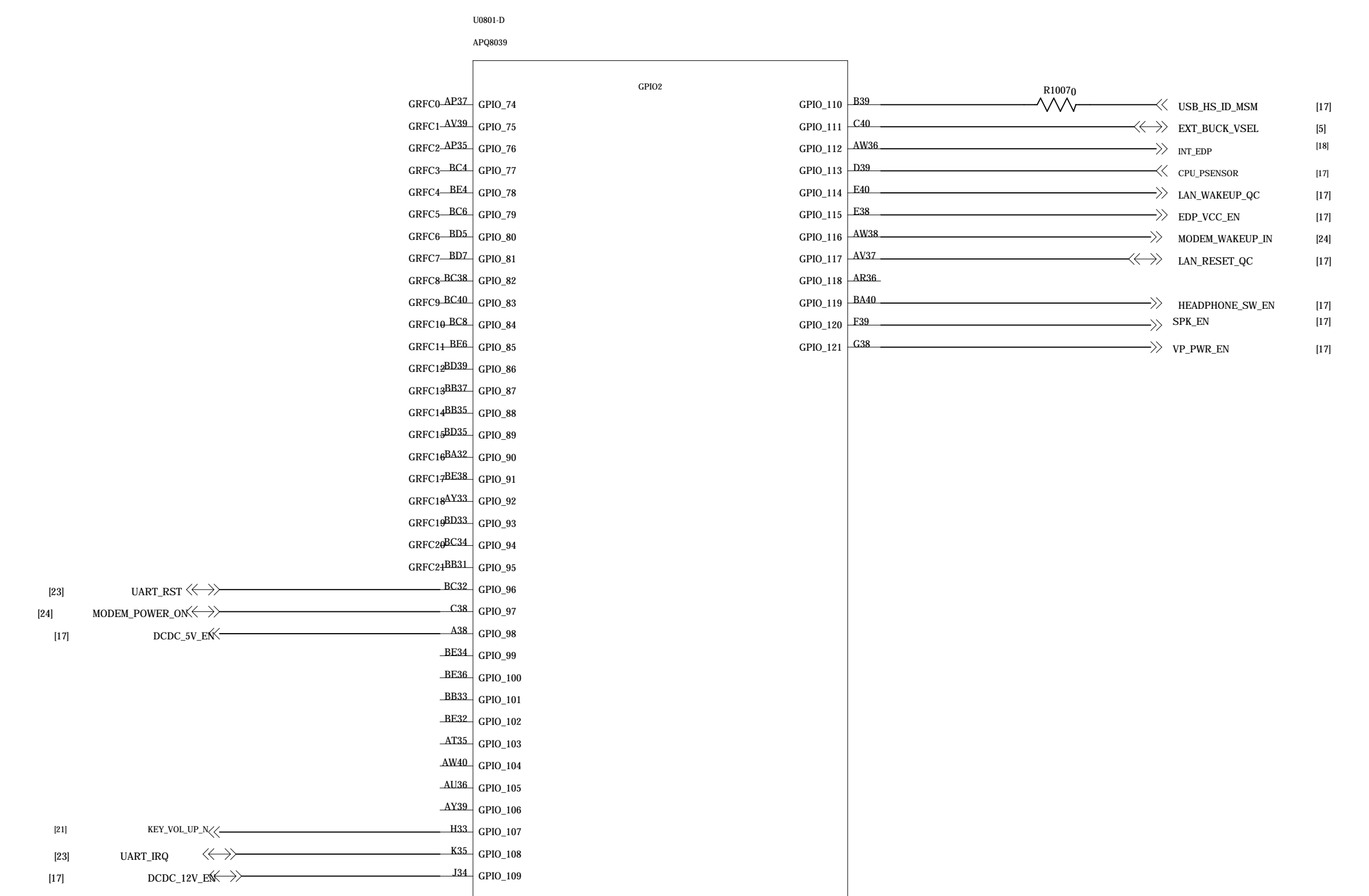
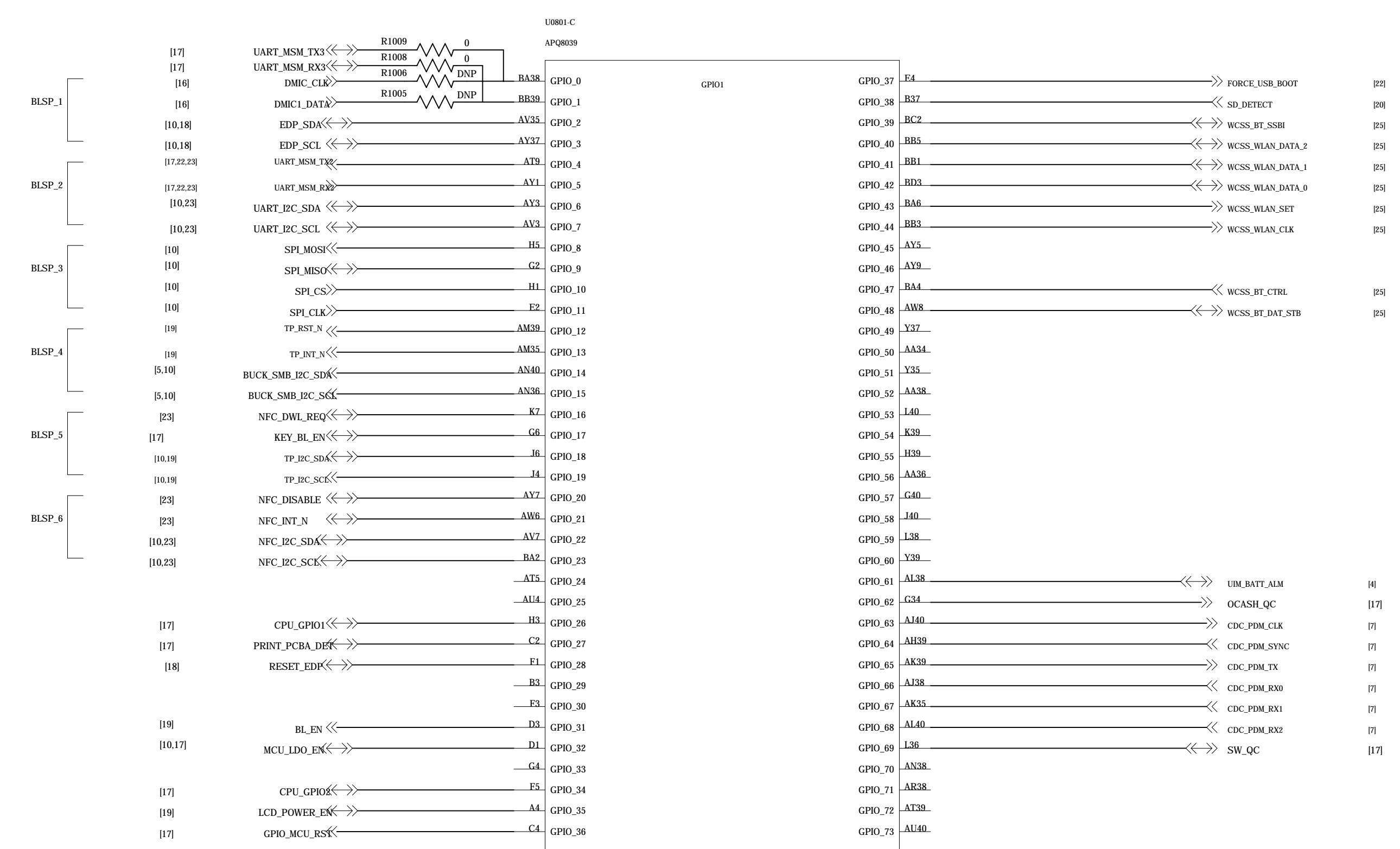
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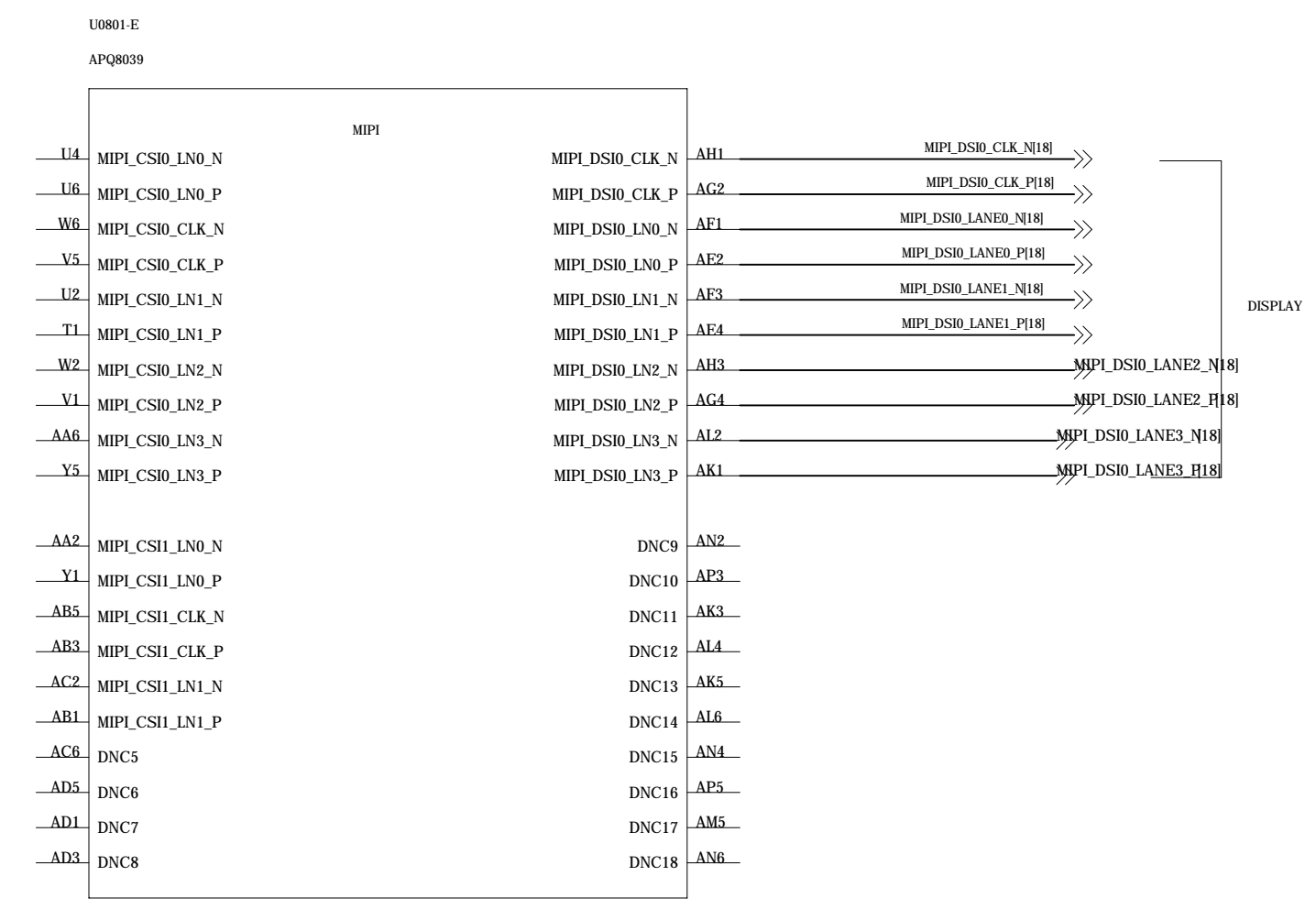
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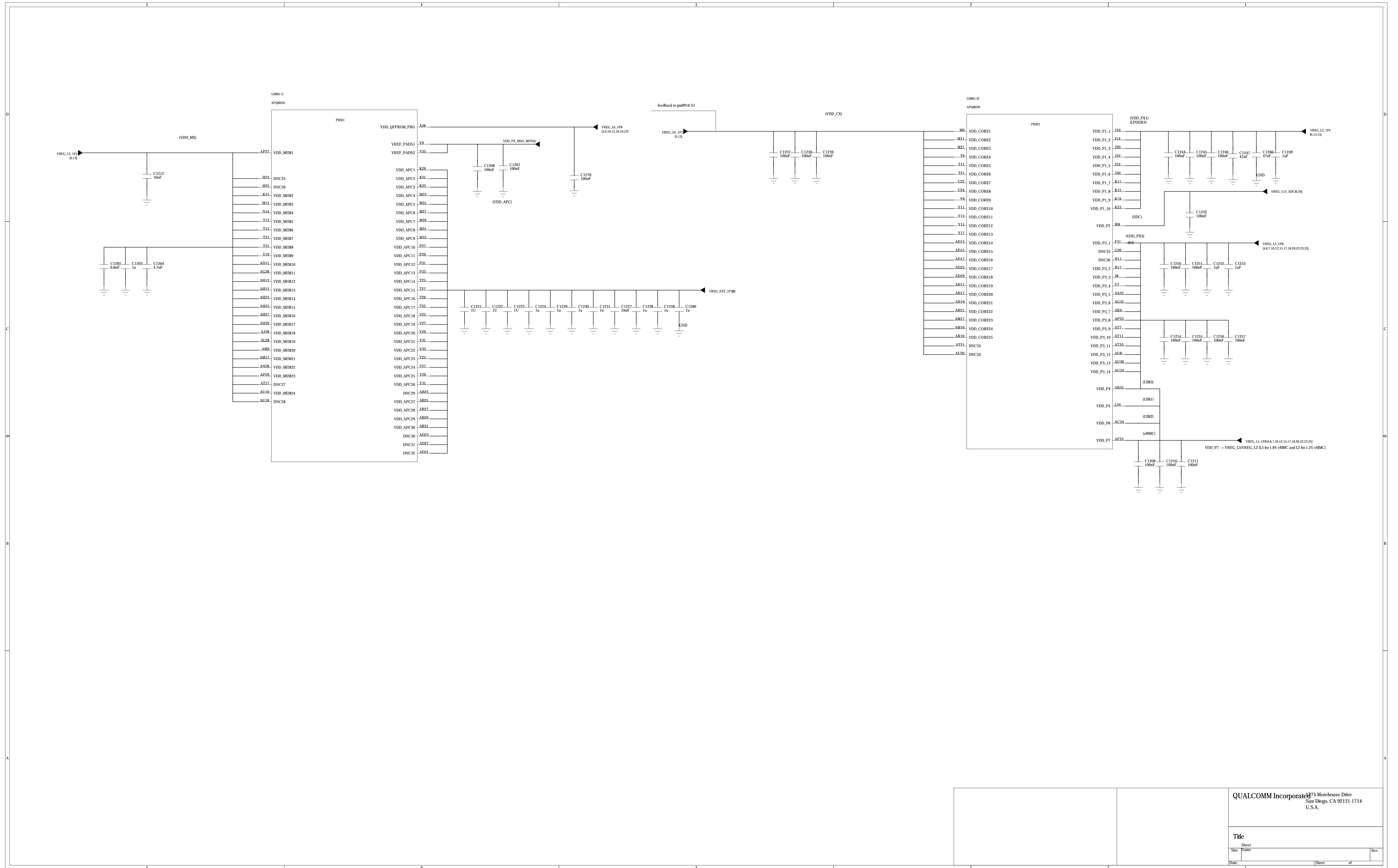


NOTE: Connect MSM Pad H13 to LPDDR3 PAD P10 if ODT is used



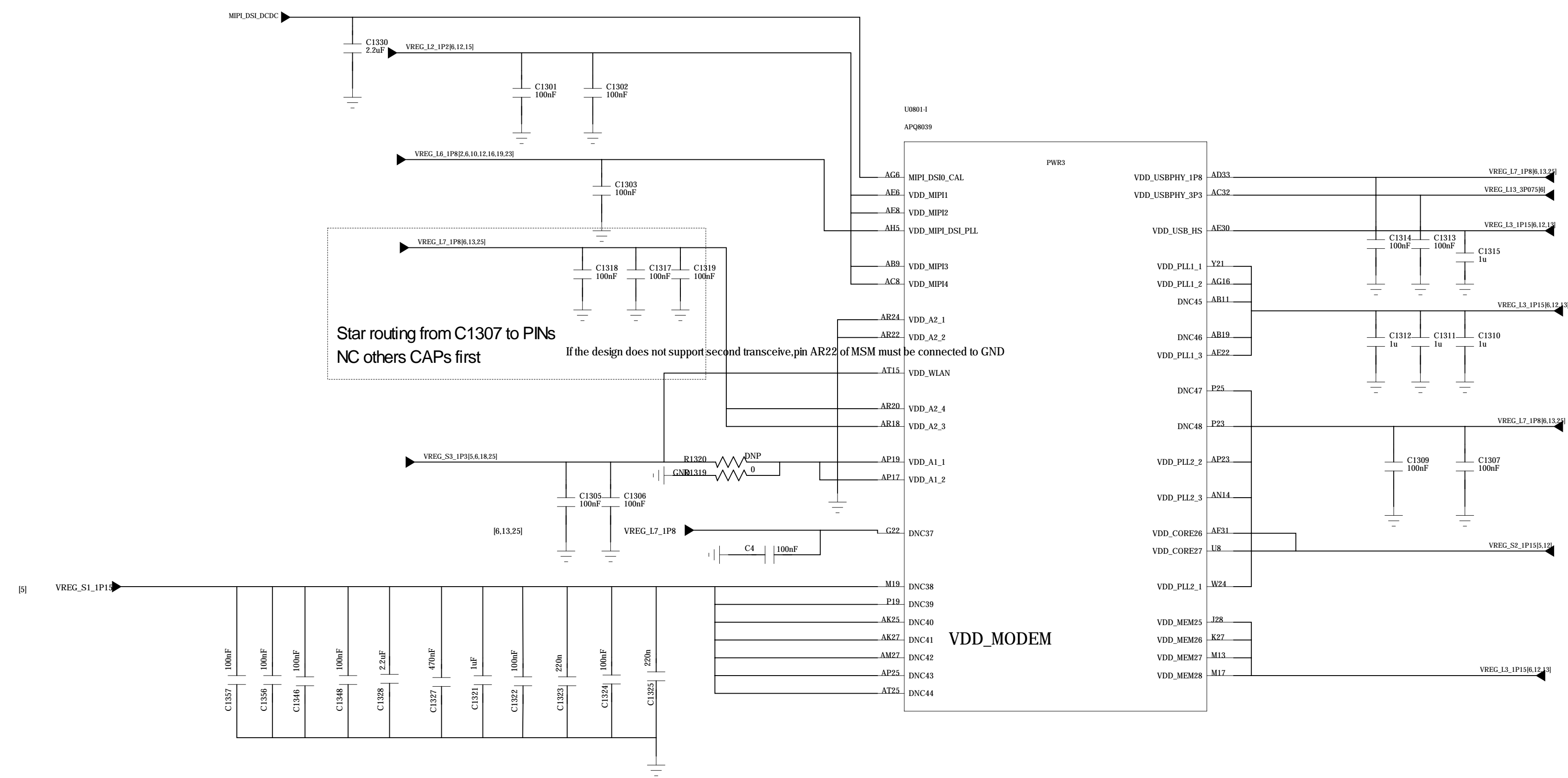


NOTE:
MSM8916 CONFIG:
REAR CAMERA: 13MP(4-LANE)
FRONET CAMERA: 5MP(2-LANE)



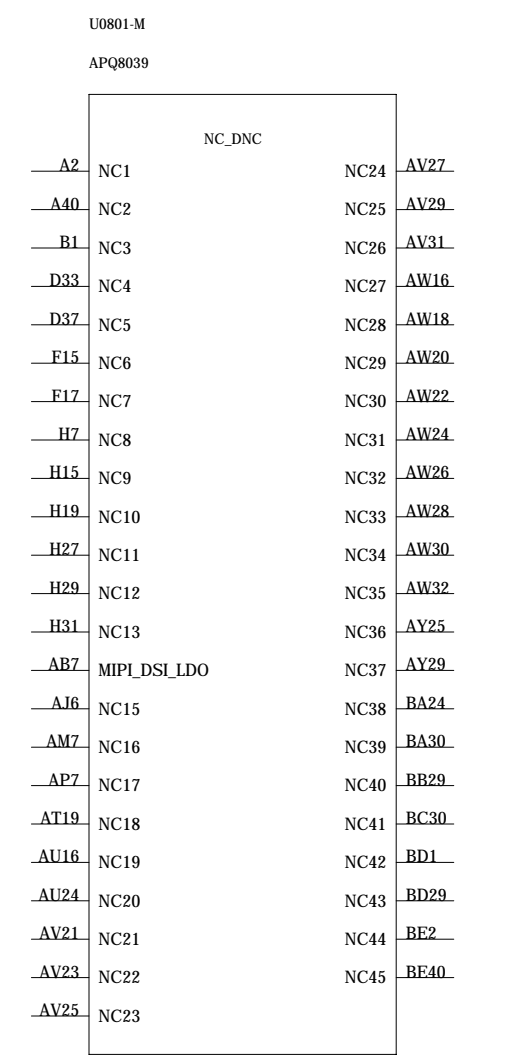
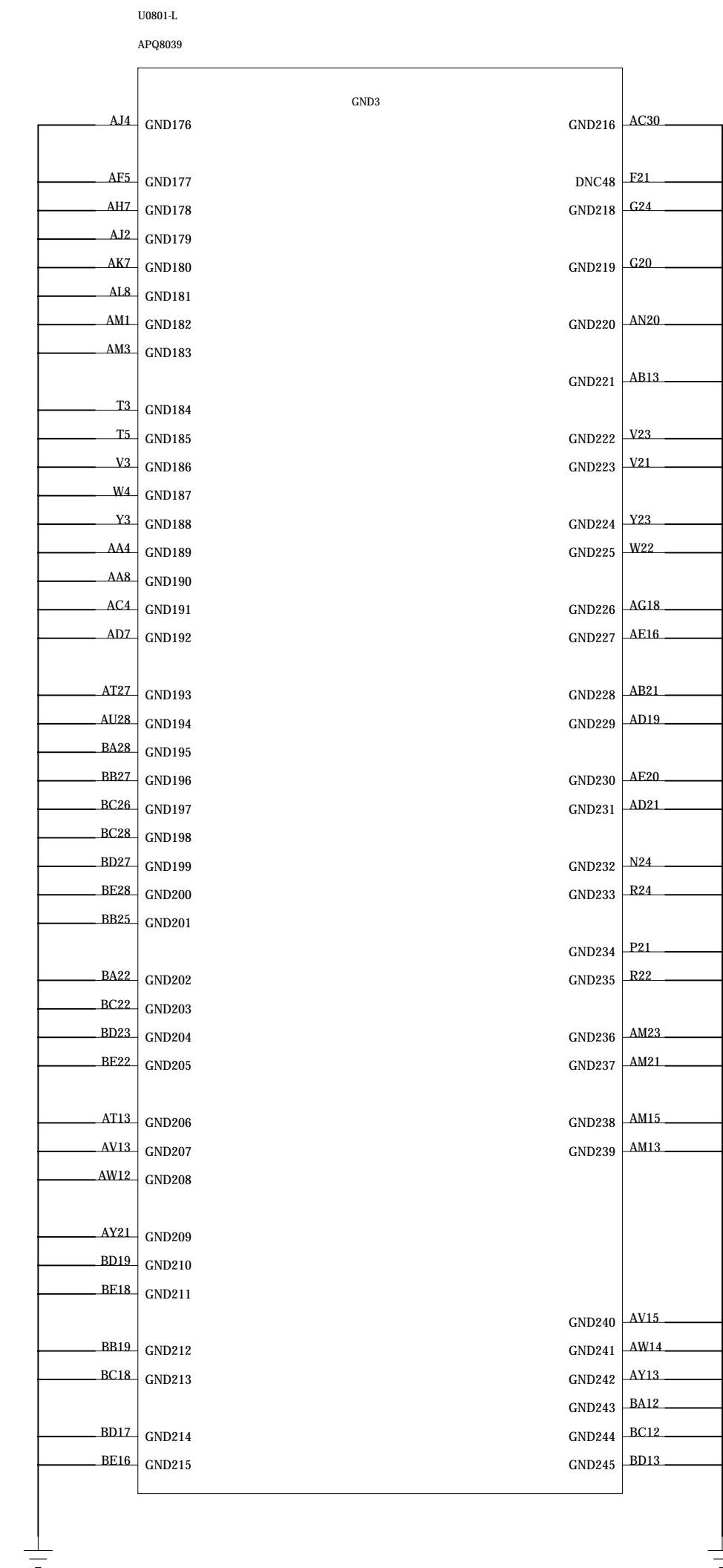
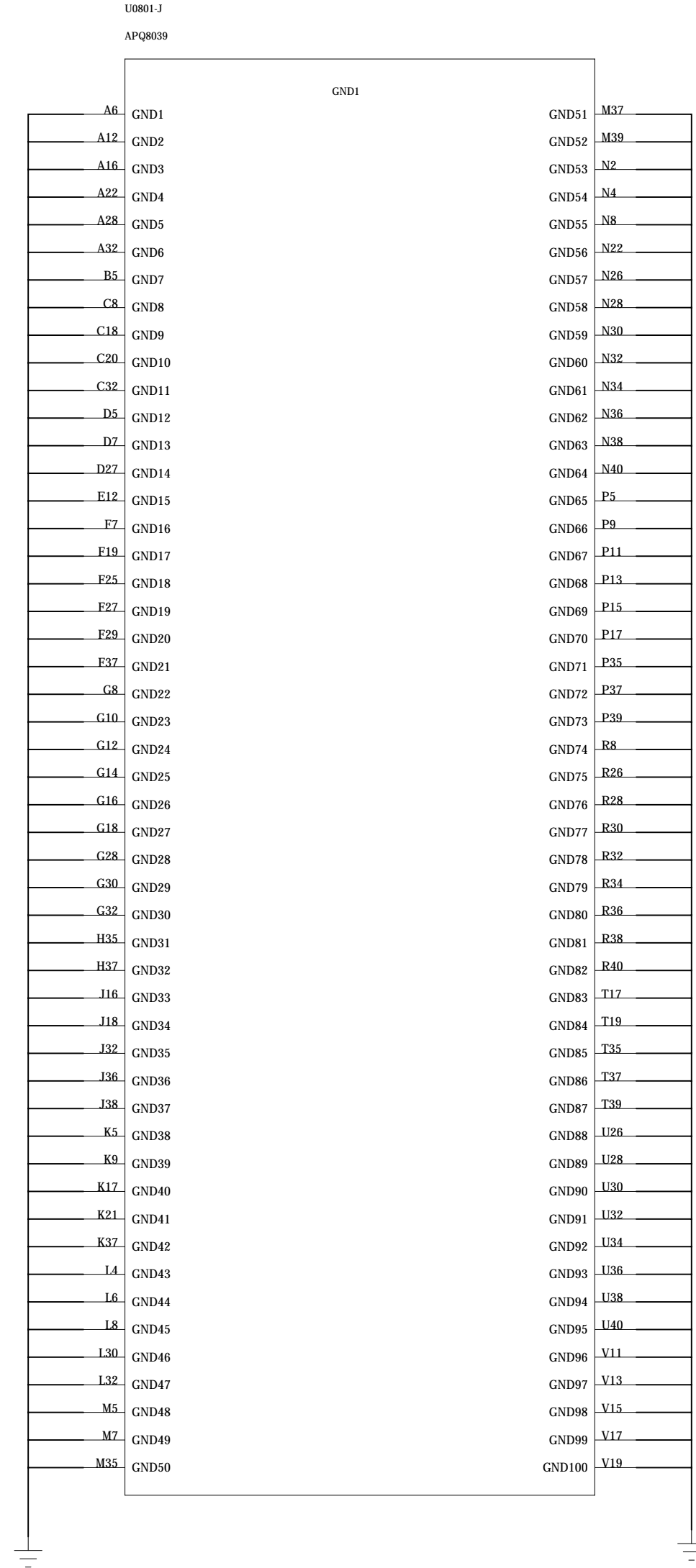
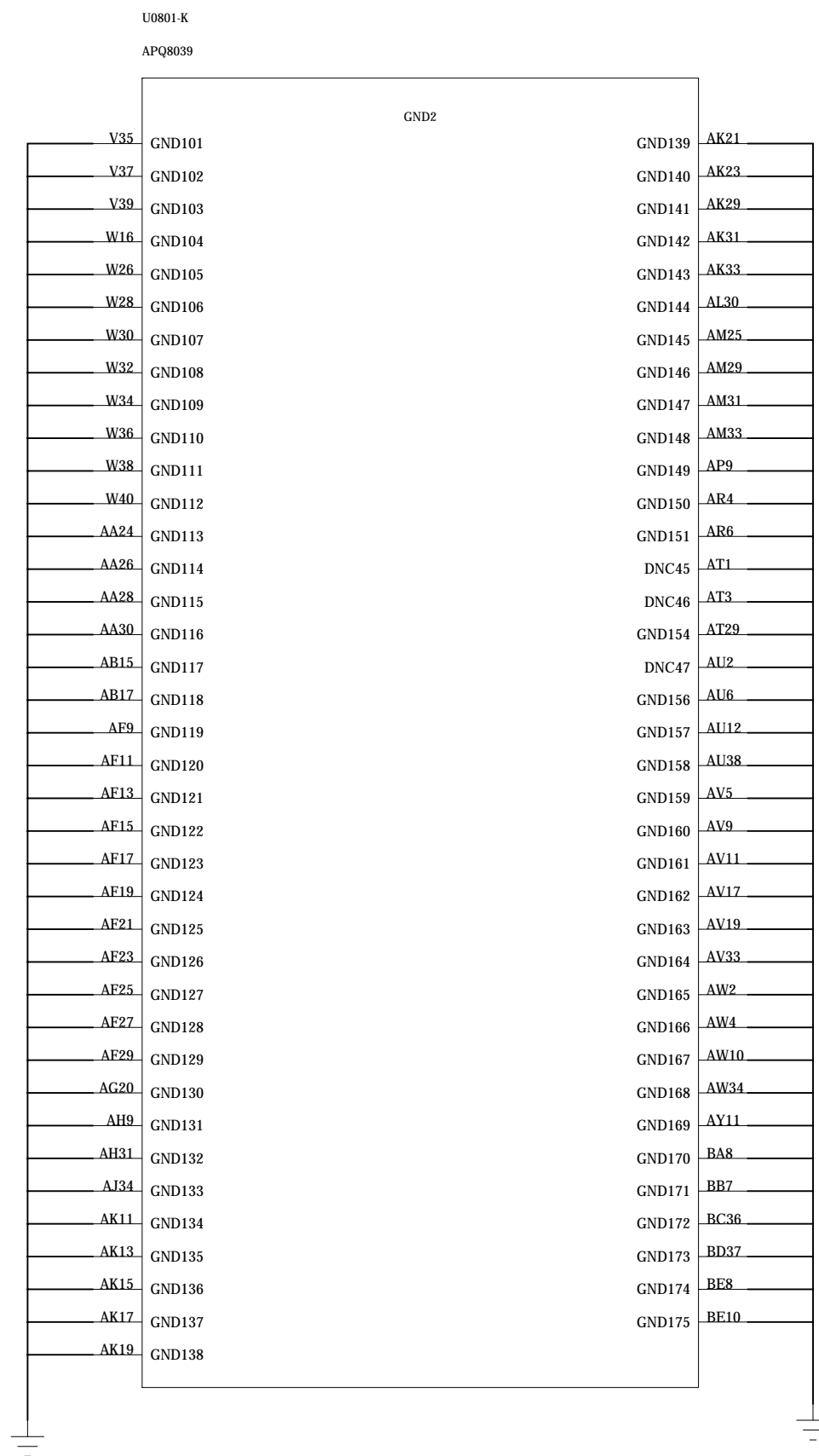
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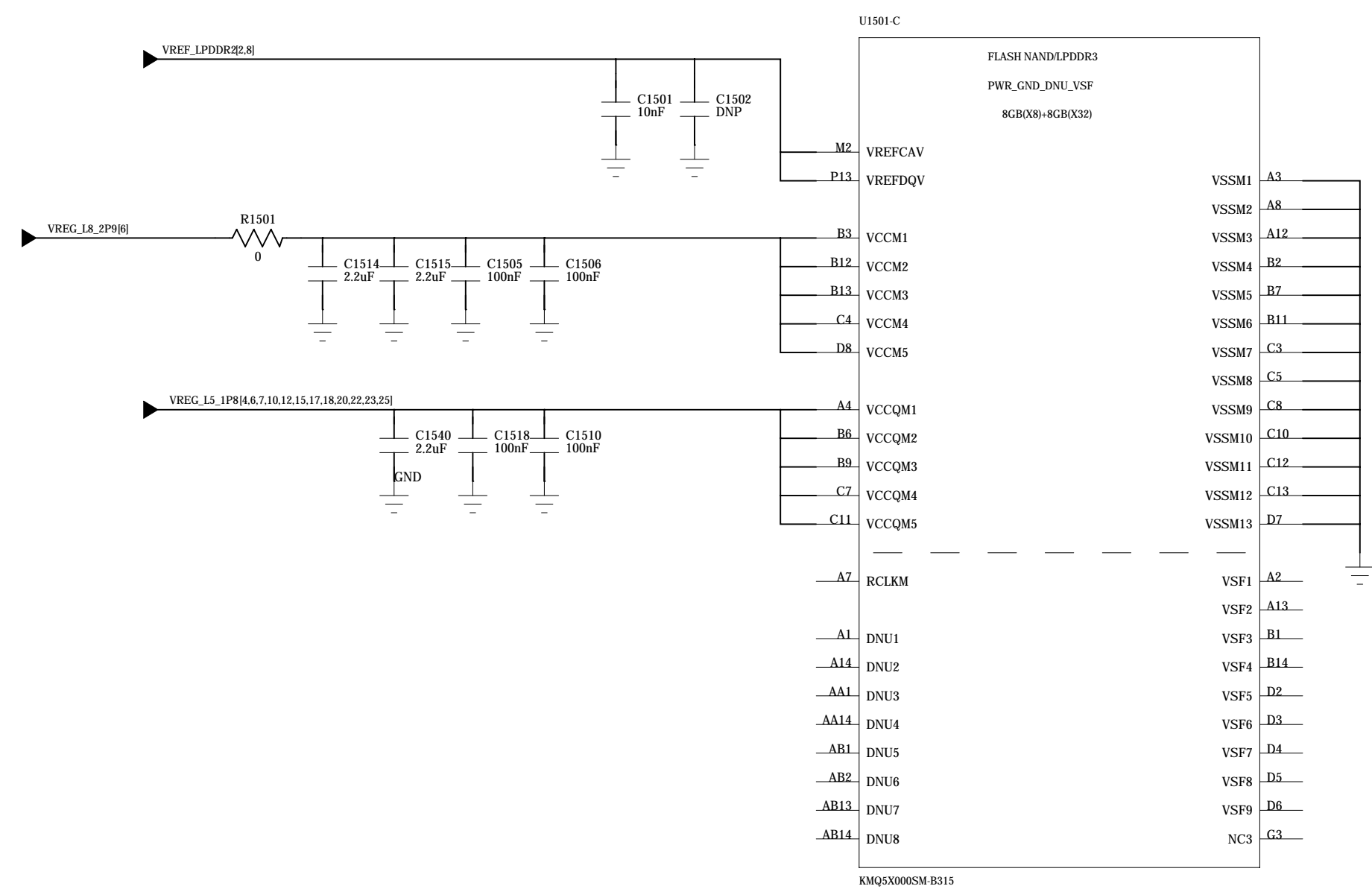
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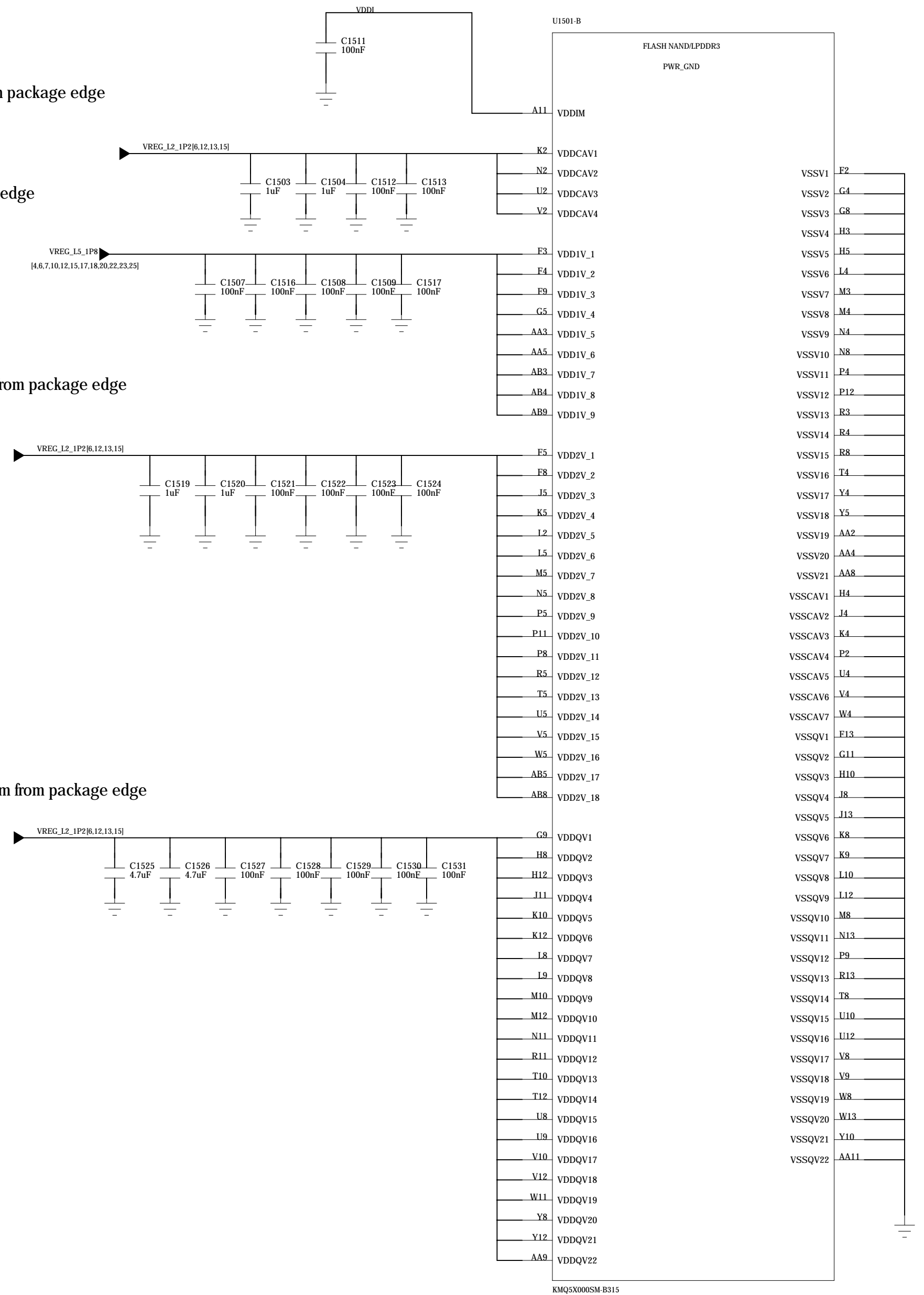
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VDDCA decaps must be < 1.5mm from package edge

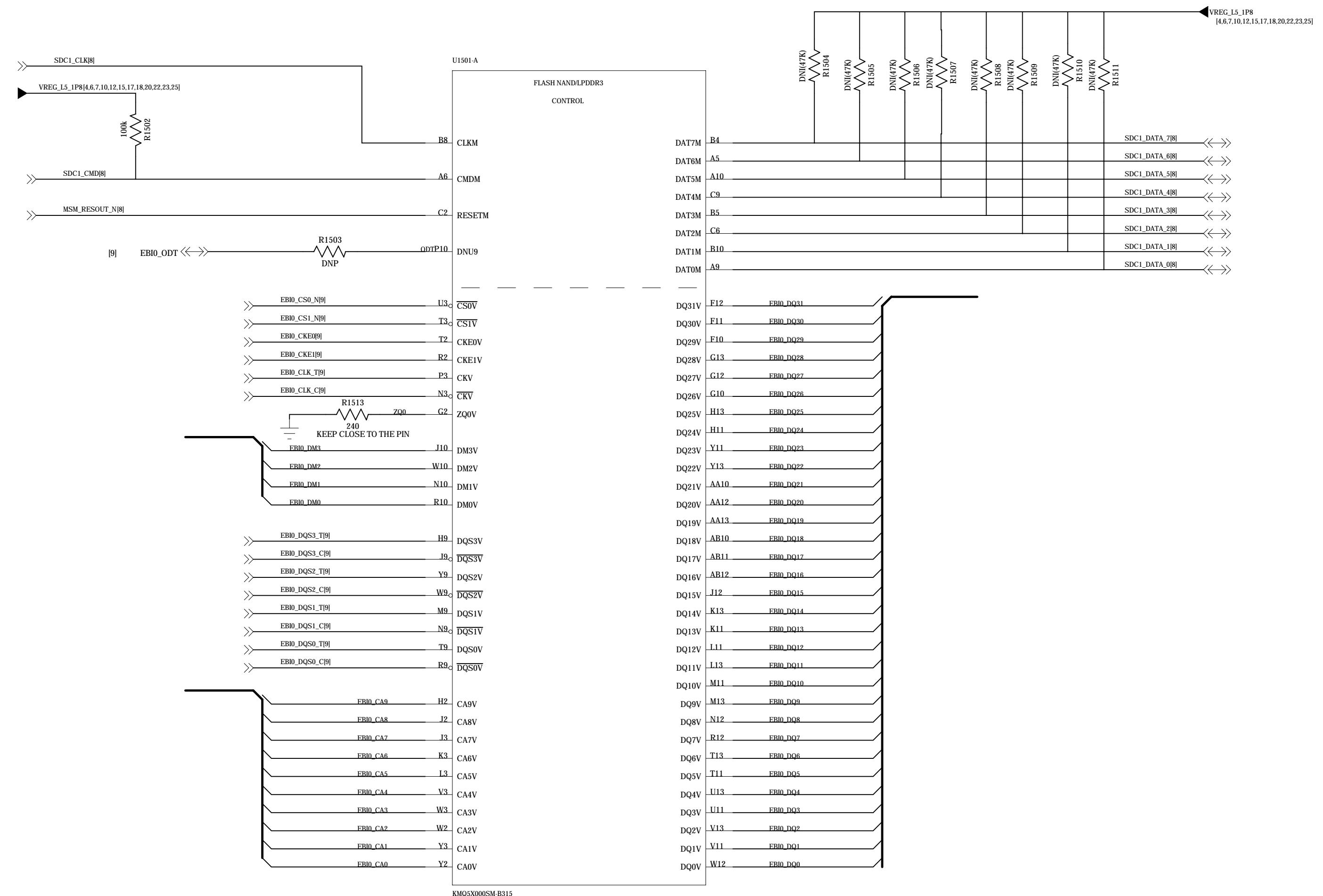
VDD1 decaps must be < 5mm from package edge

VDD2 decaps must be < 1.5mm from package edge



Use solid Plane for L2
VDD2 decaps must be < 1.5mm from package edge

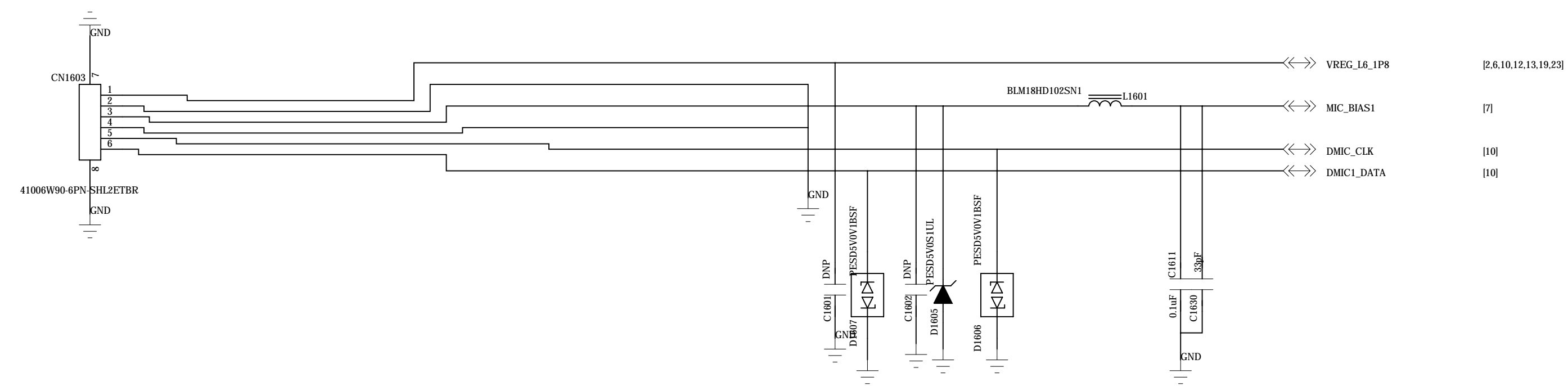
Note: Pull-up resistors on SDC1_DATA are PCB and eMMC vender dependent



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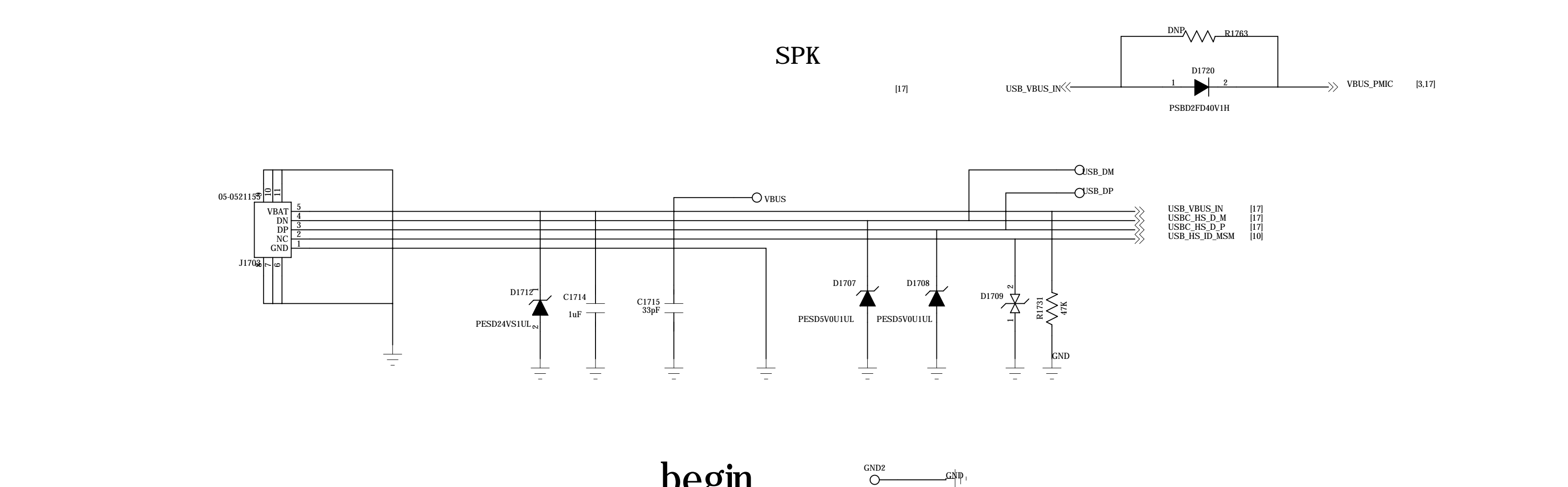
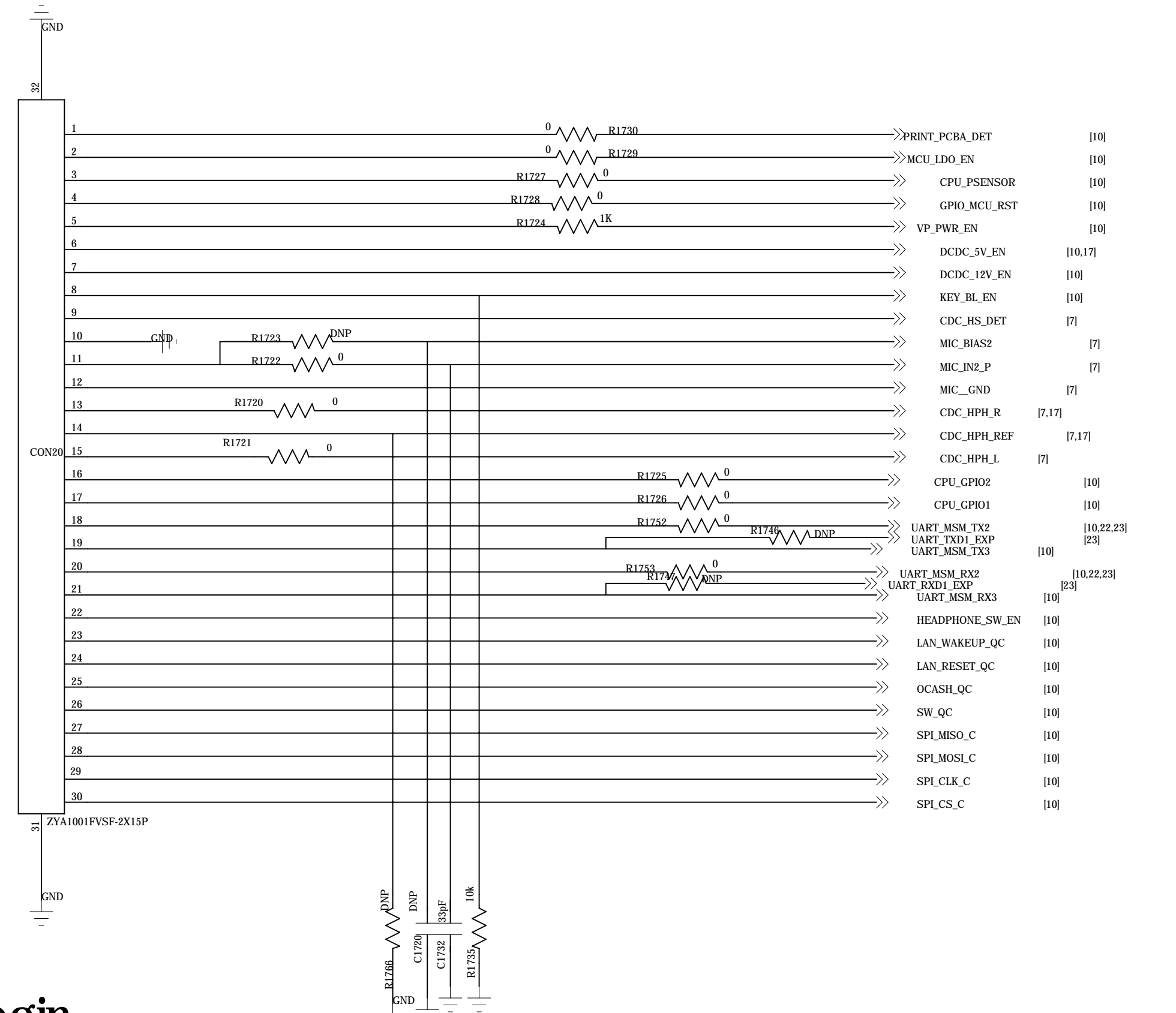
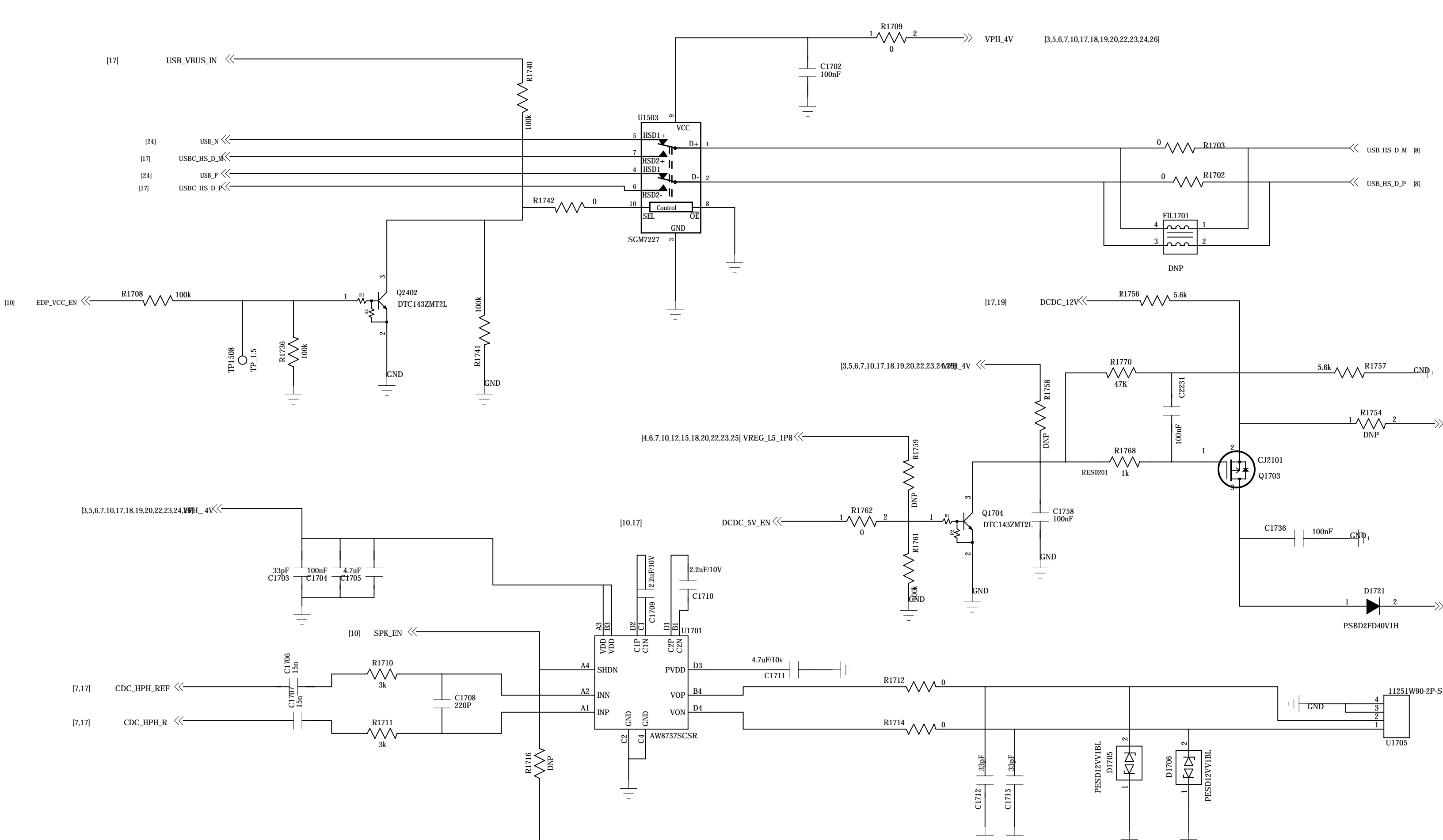
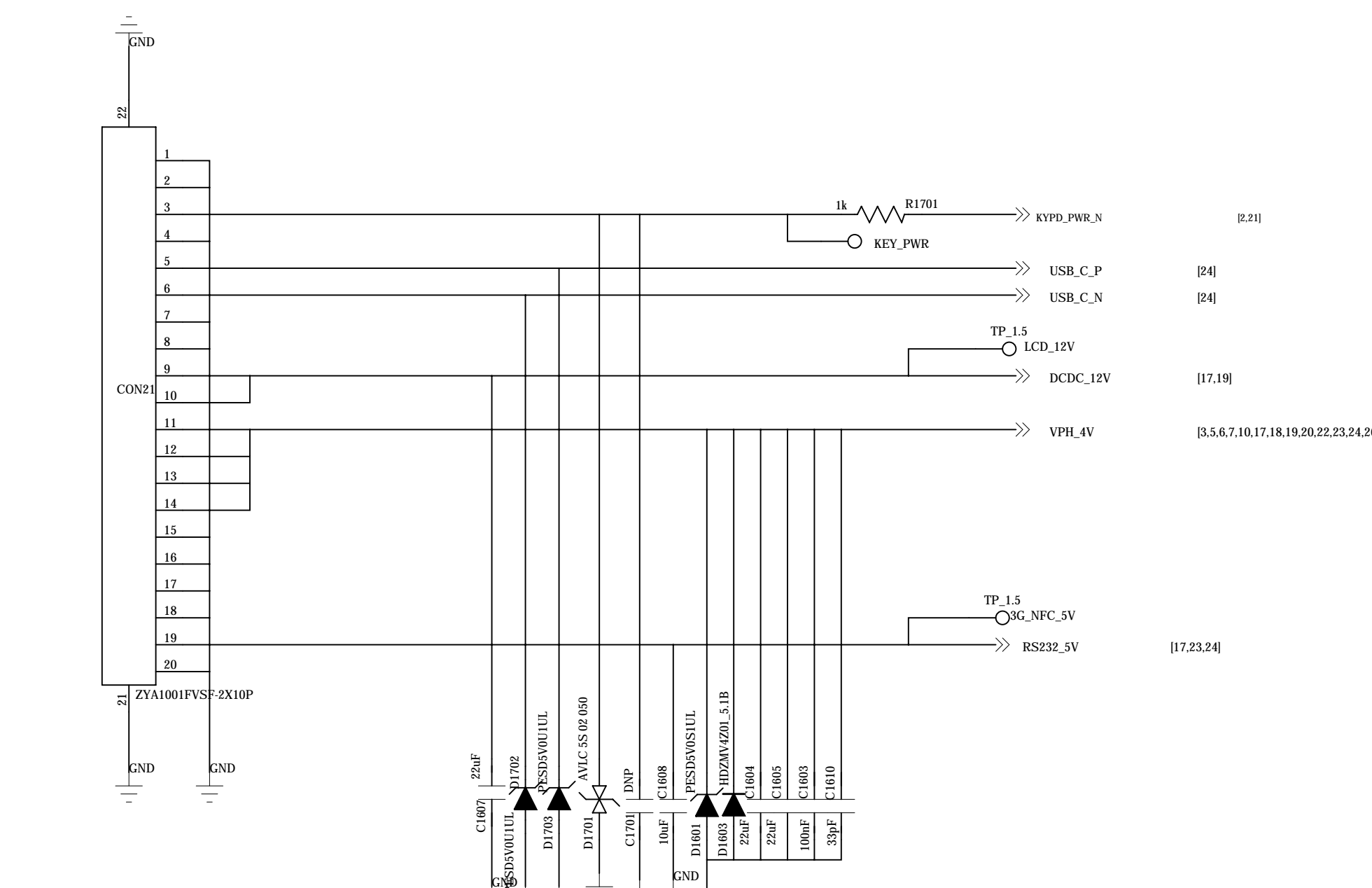
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to MIC Board

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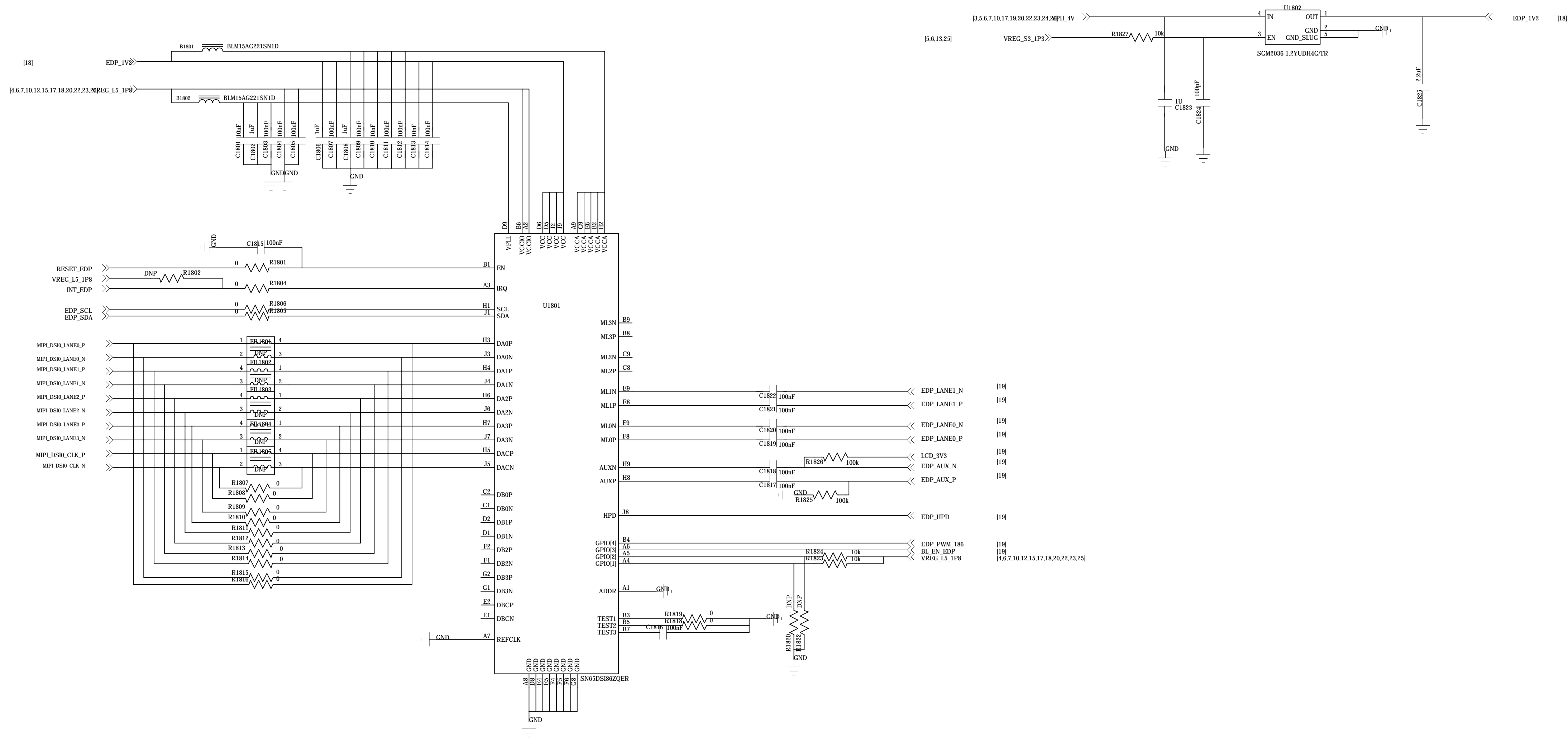
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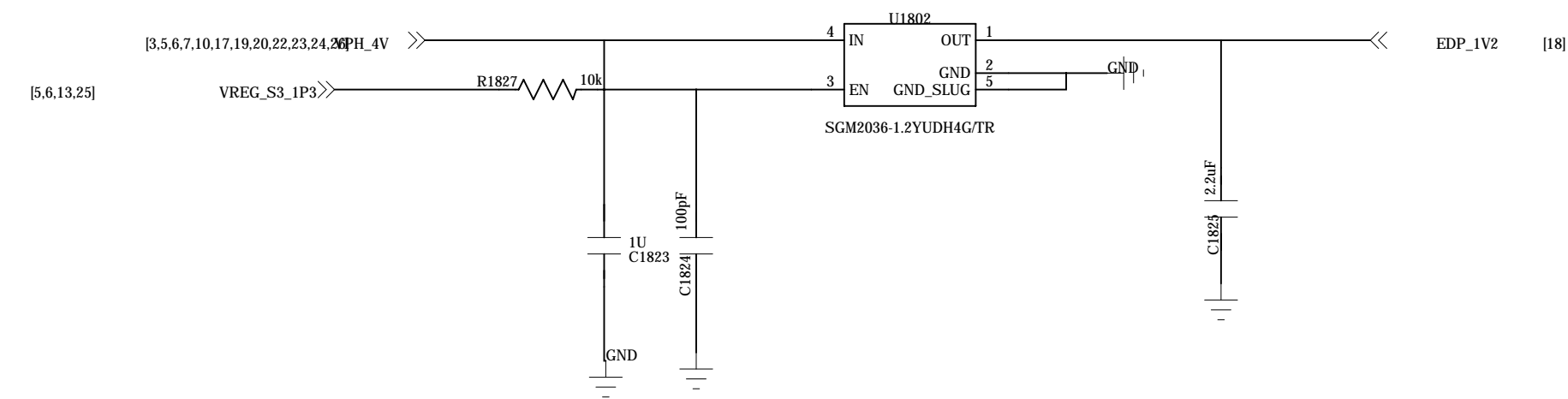
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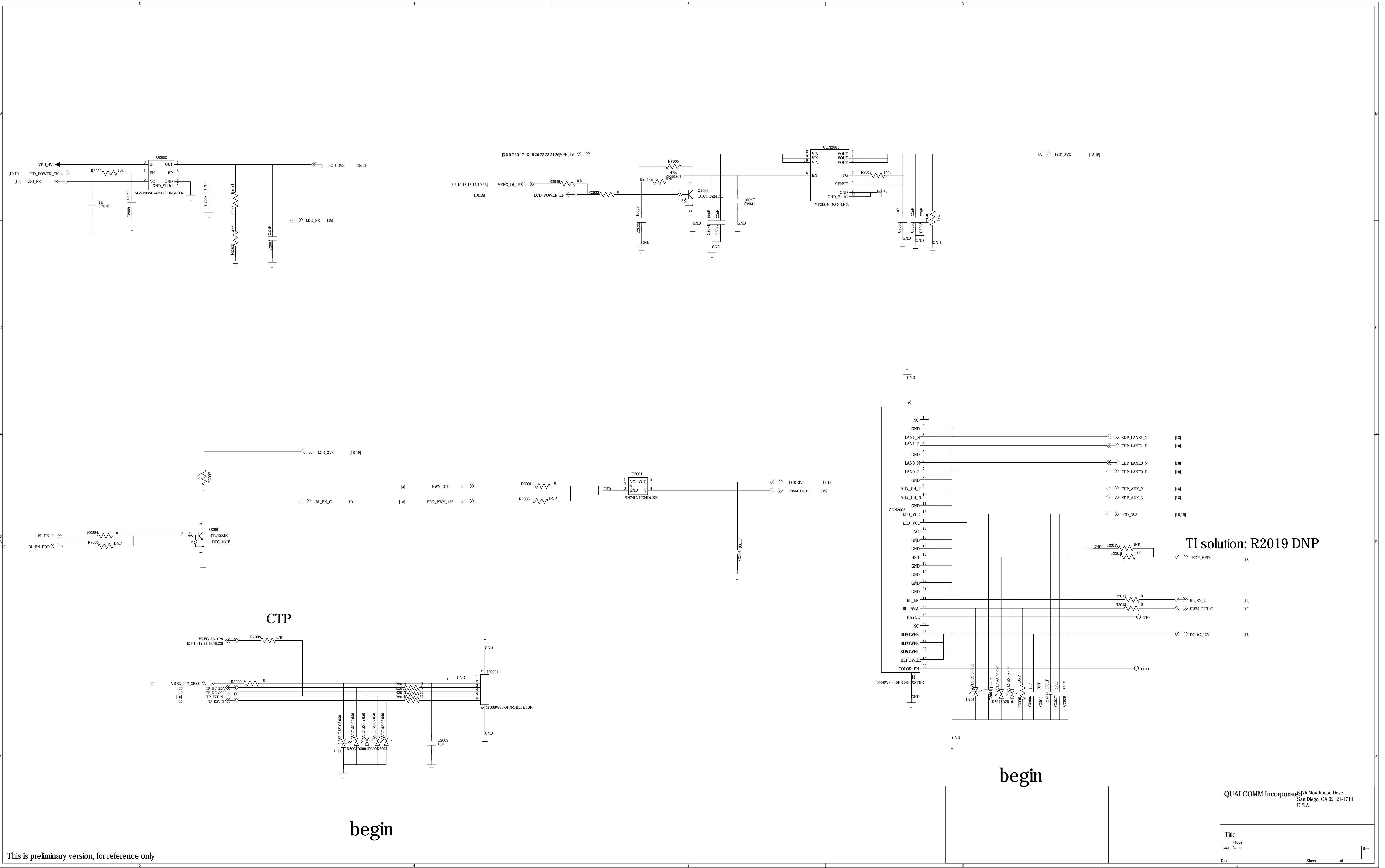
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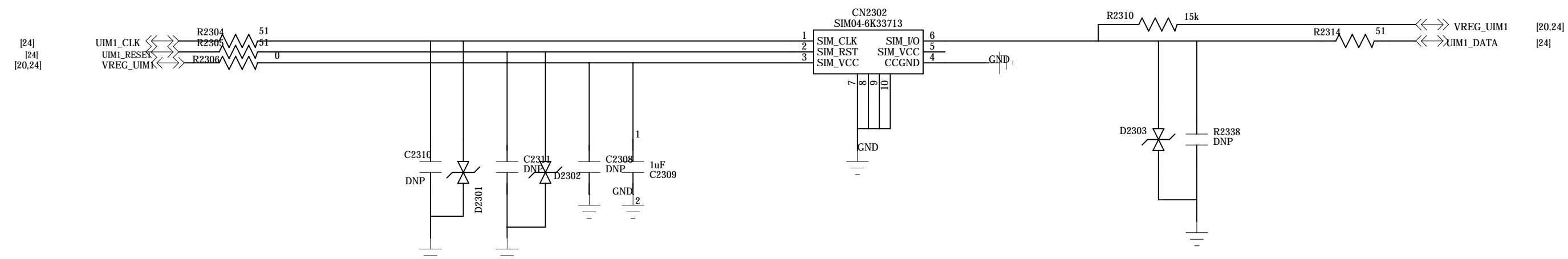
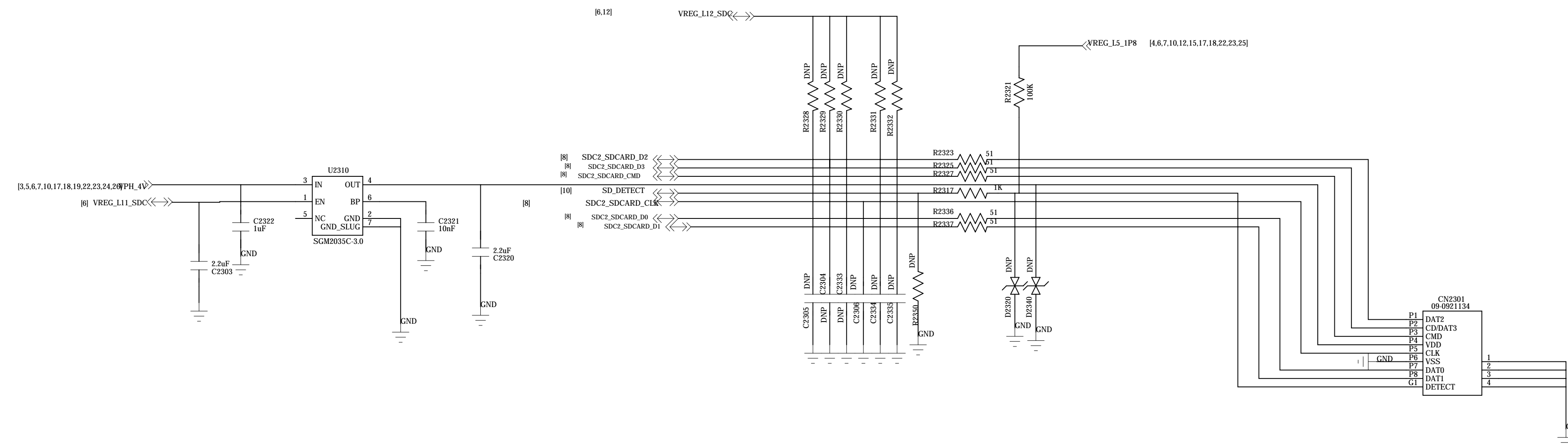
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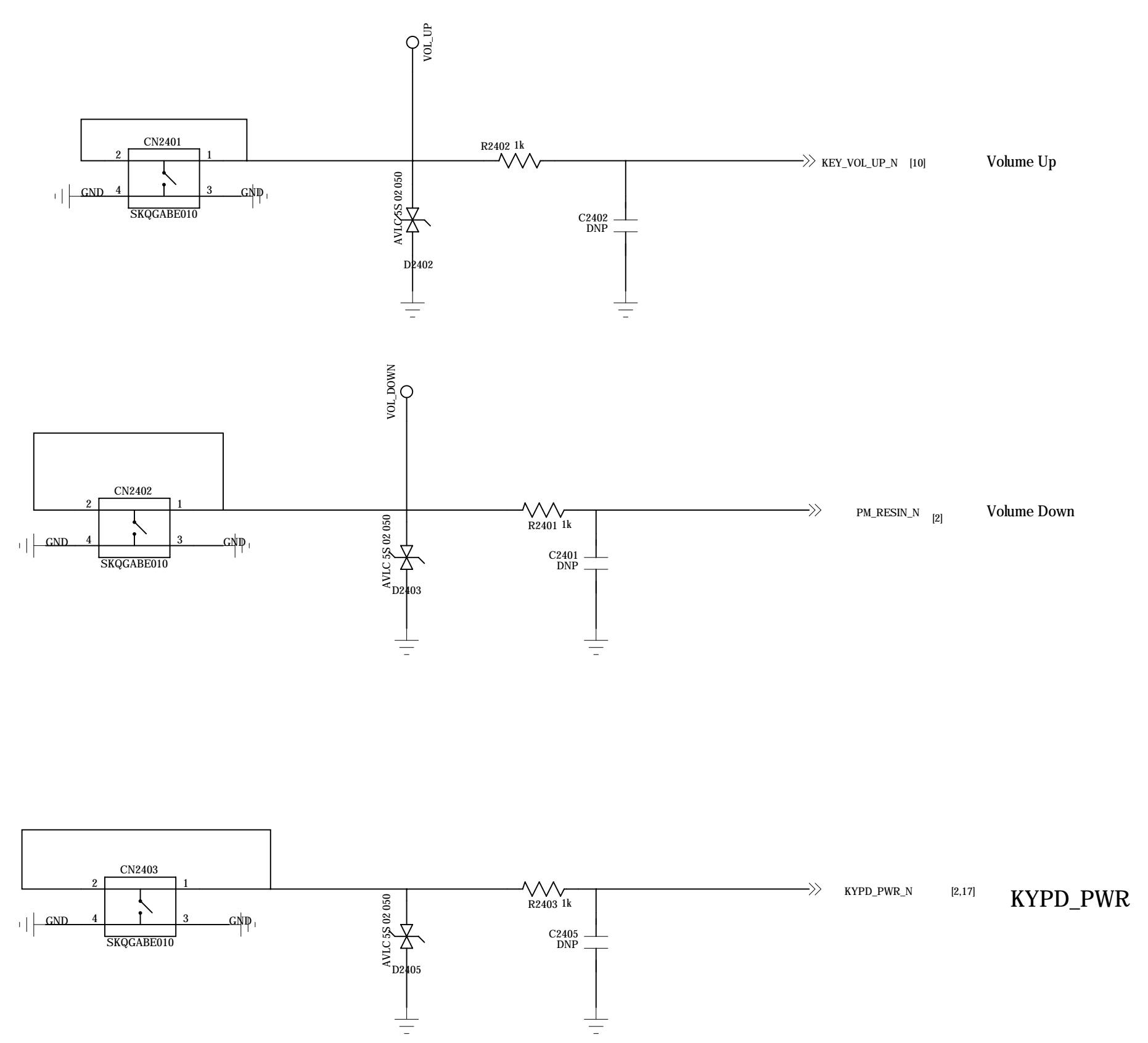
TI solution: R2019 DNP

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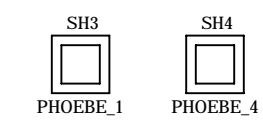
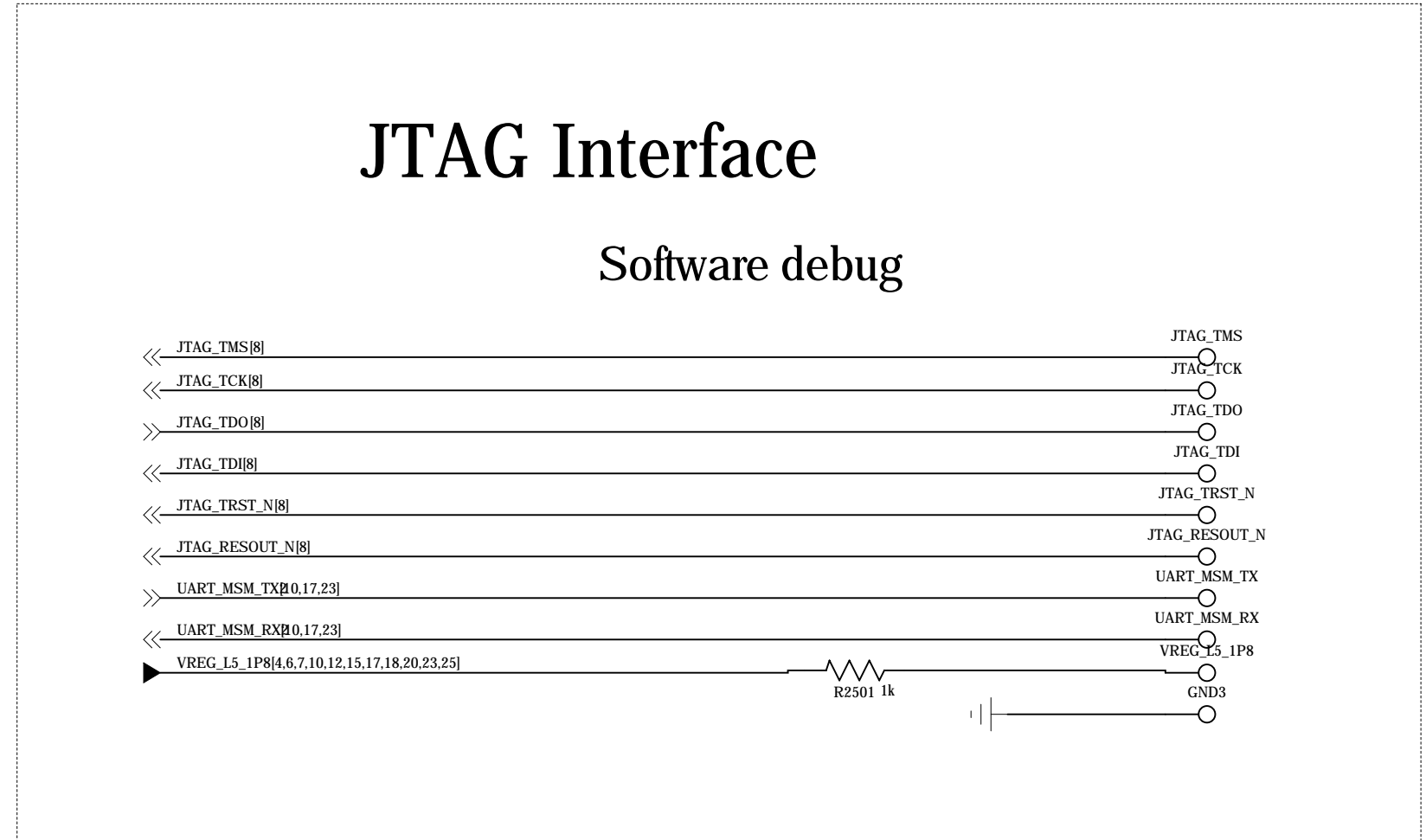
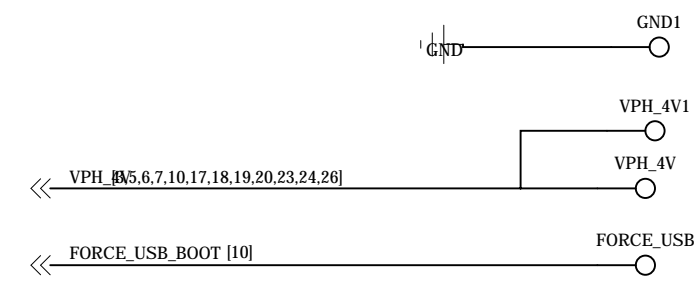
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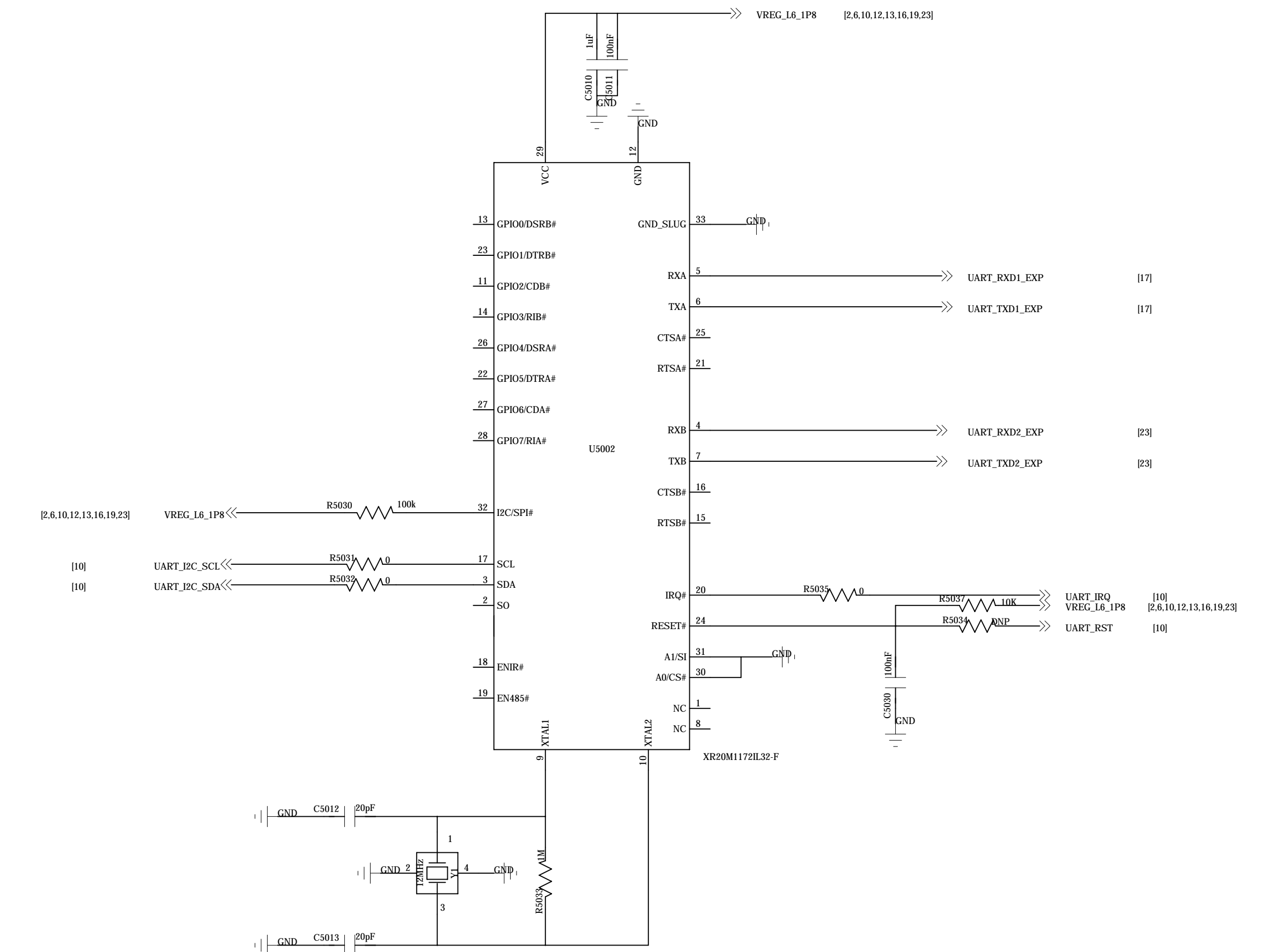
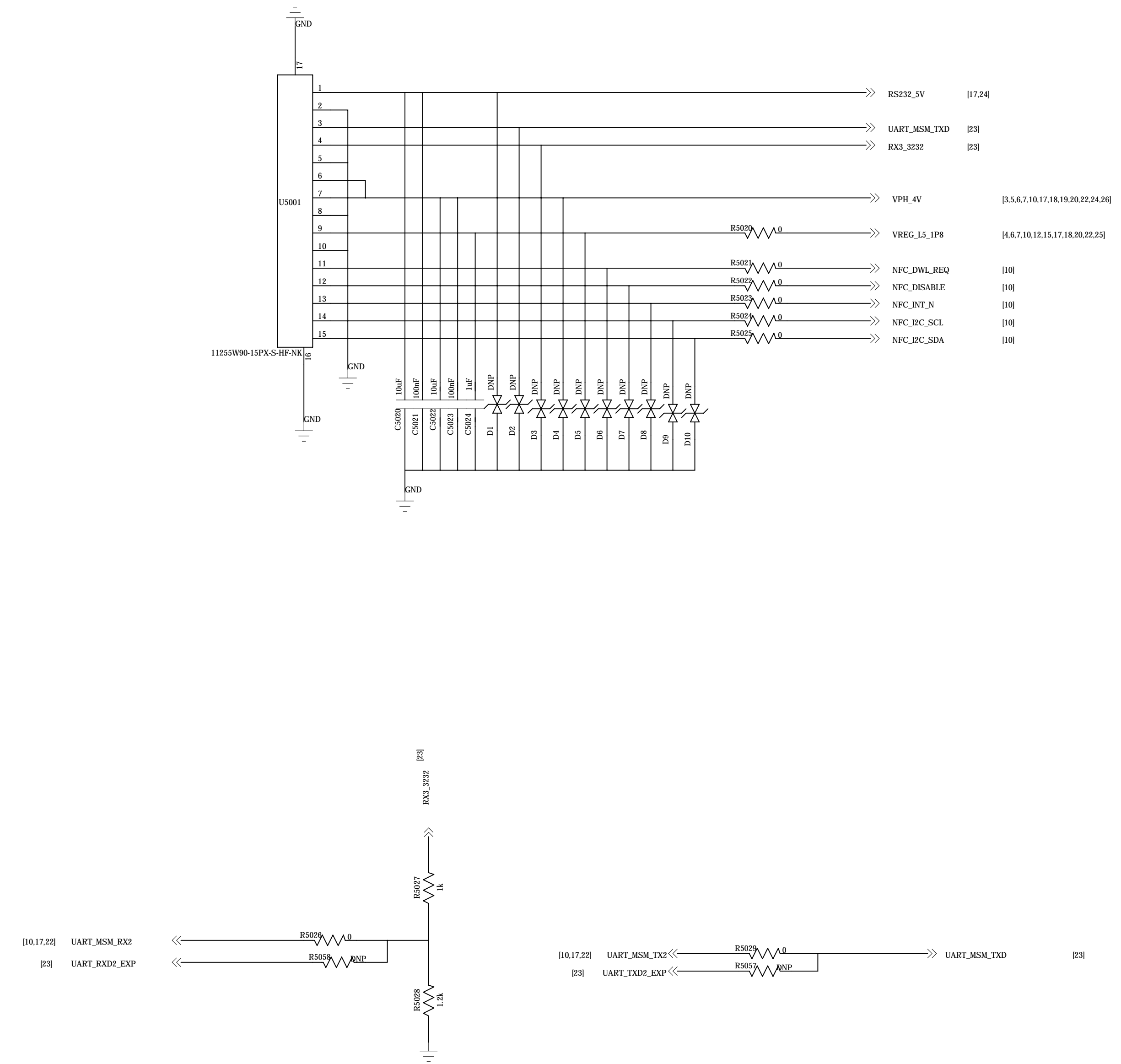
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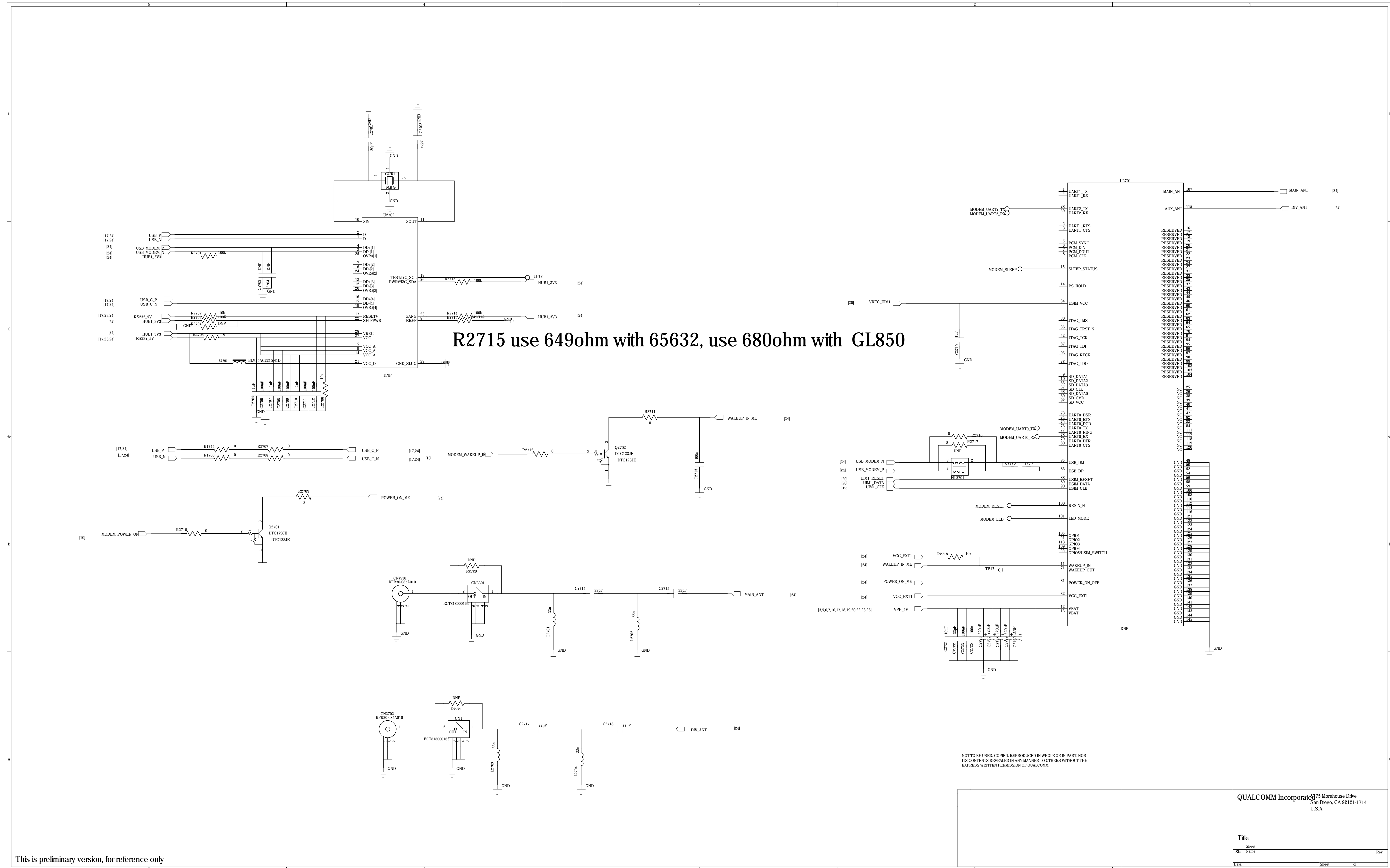
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R2715 use 649ohm with 65632, use 680ohm with GL850

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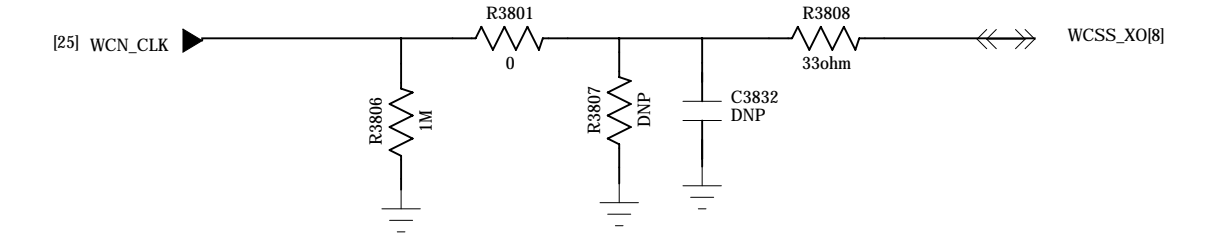
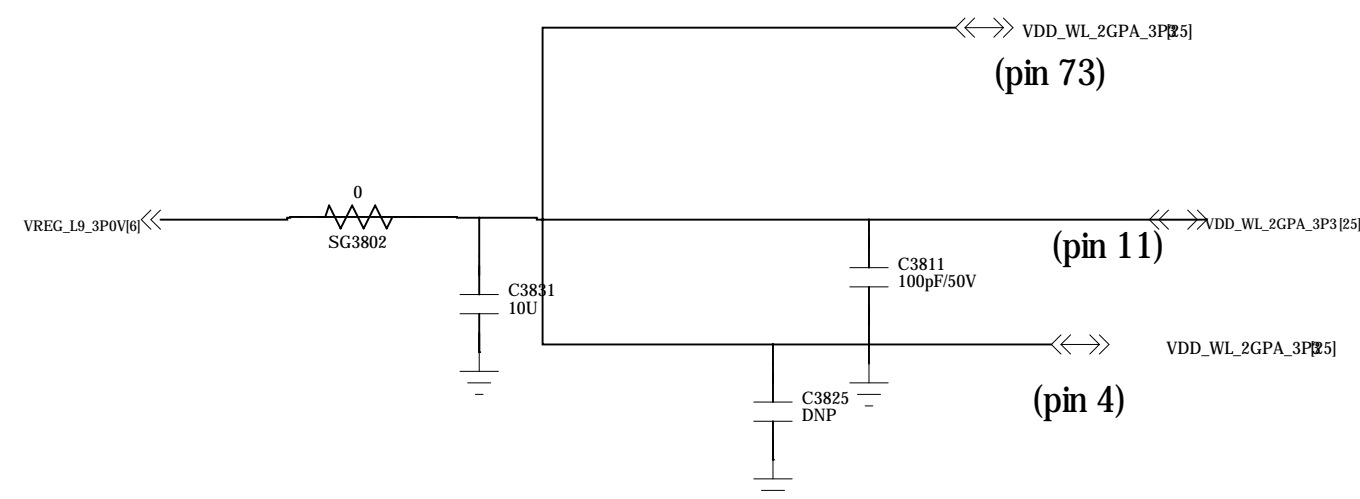
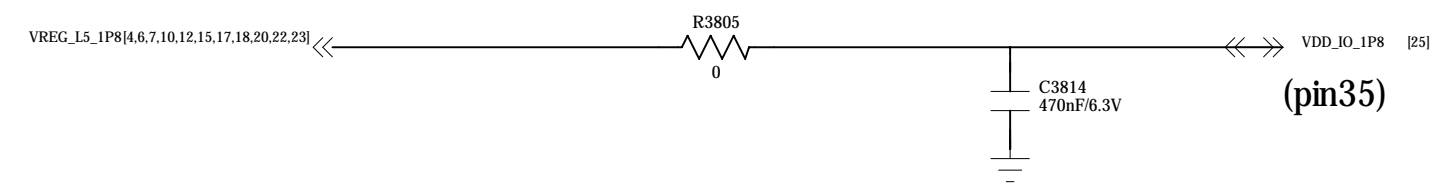
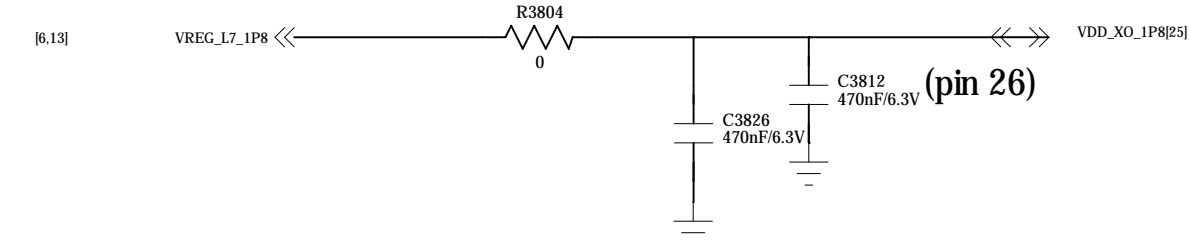
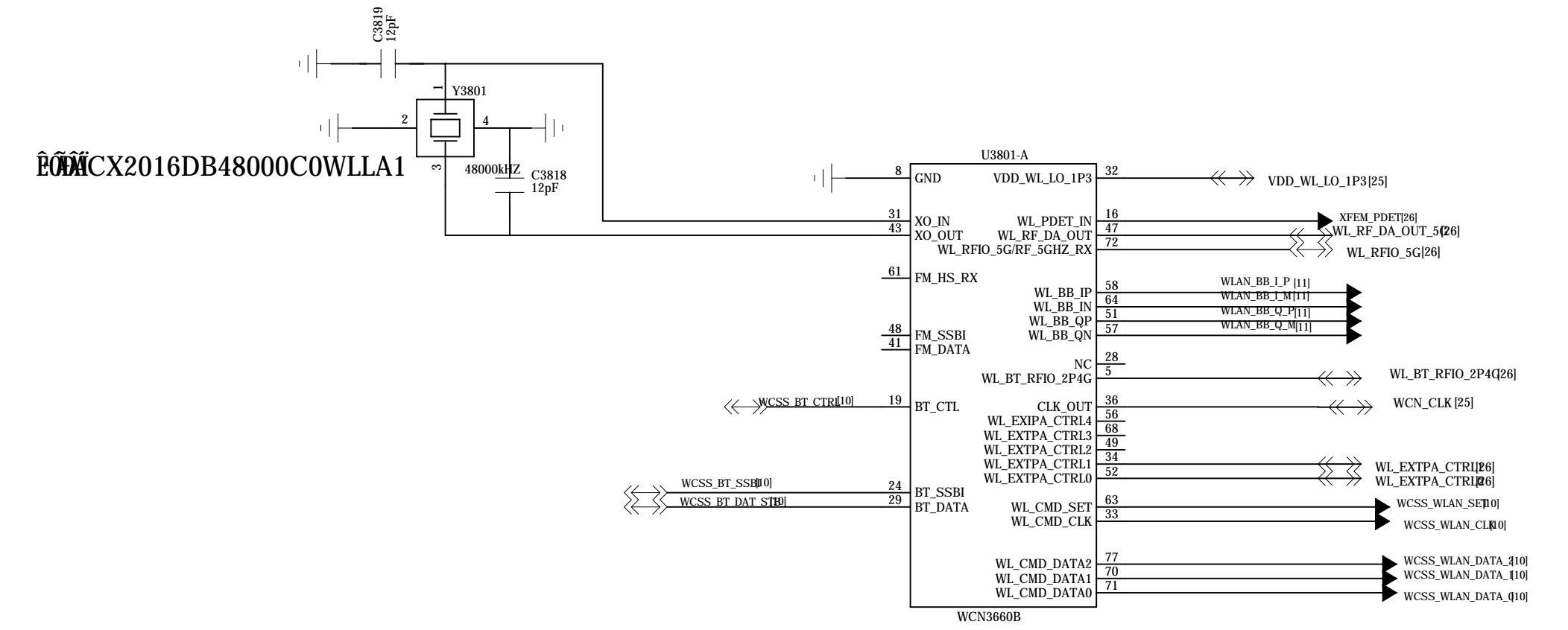
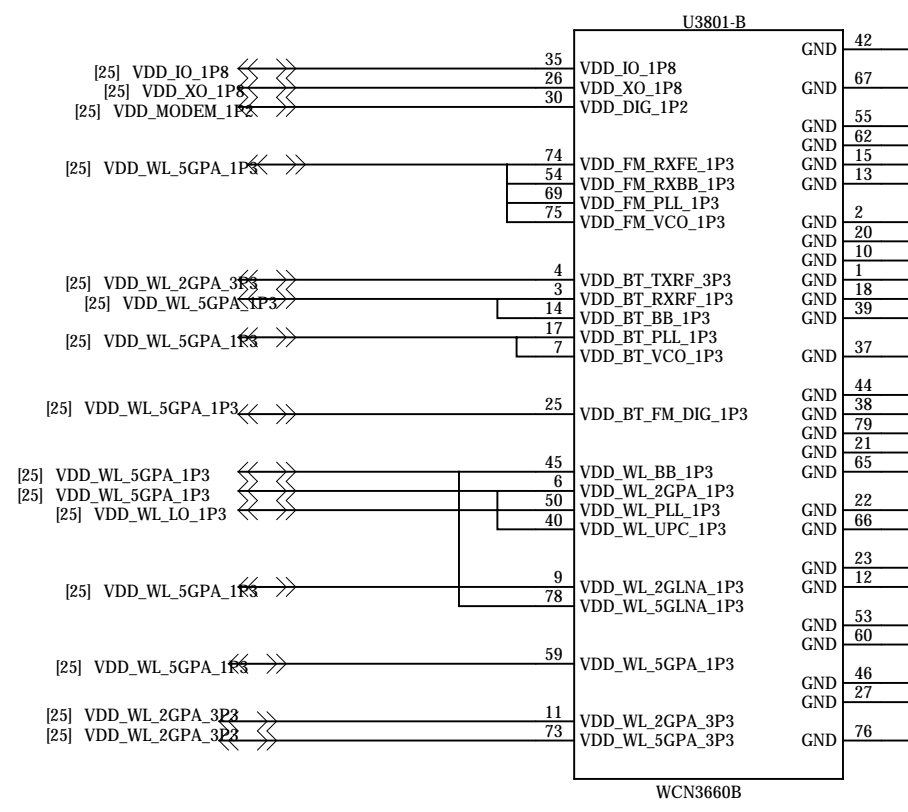
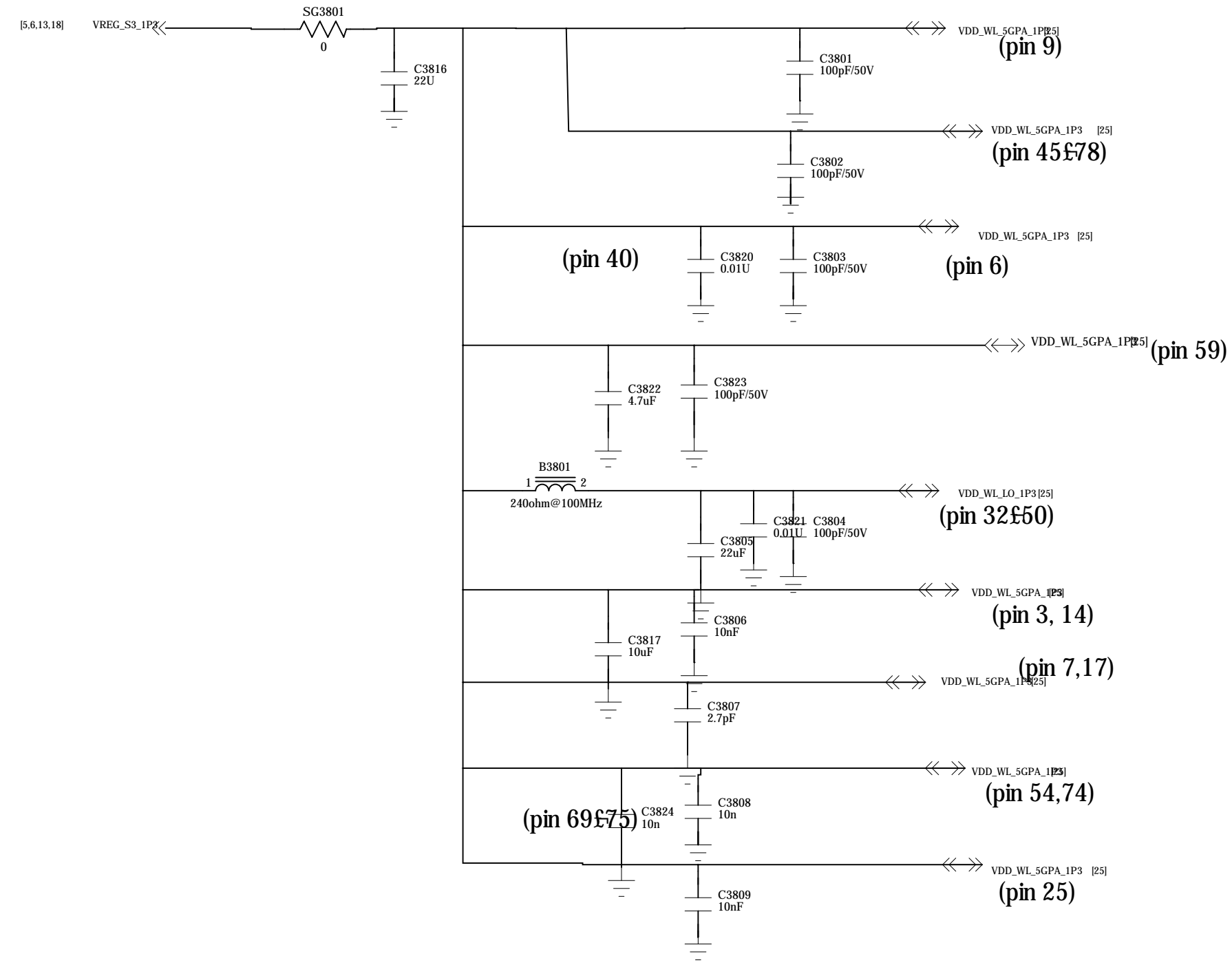
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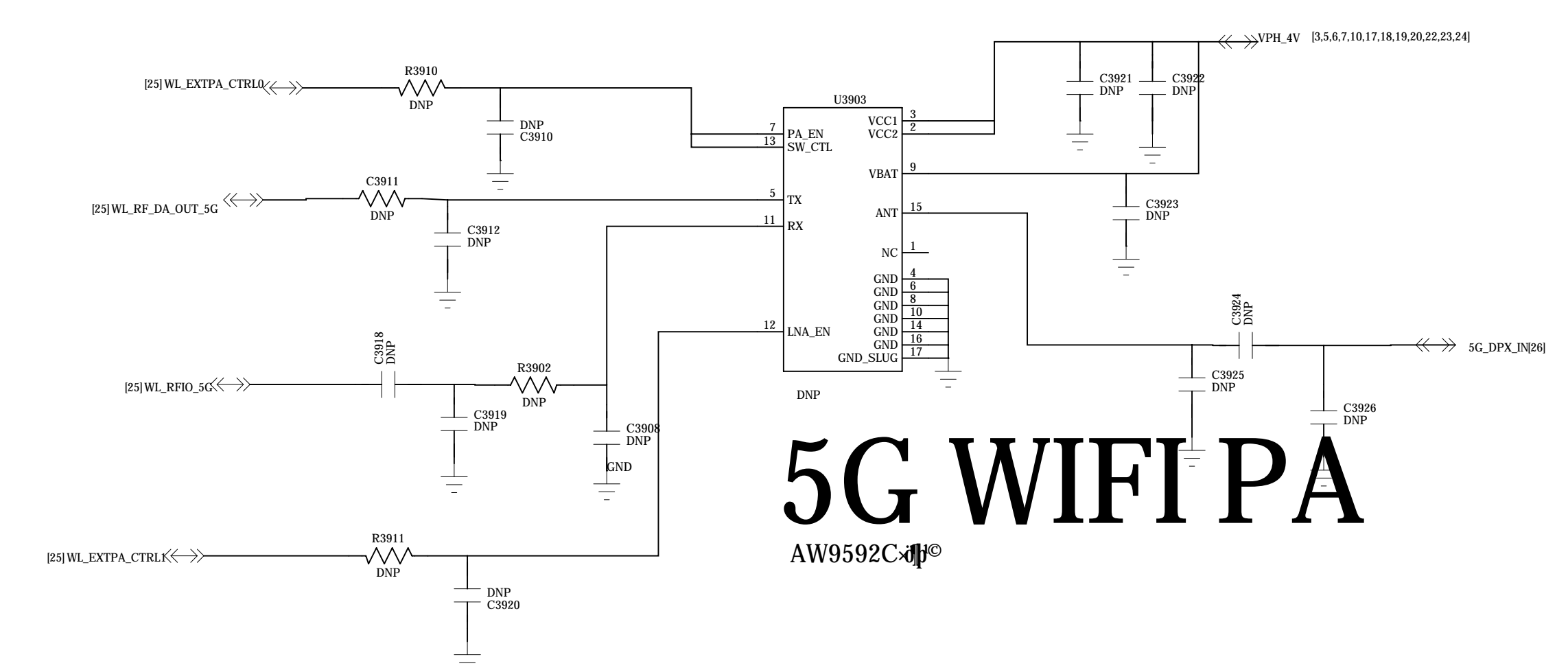
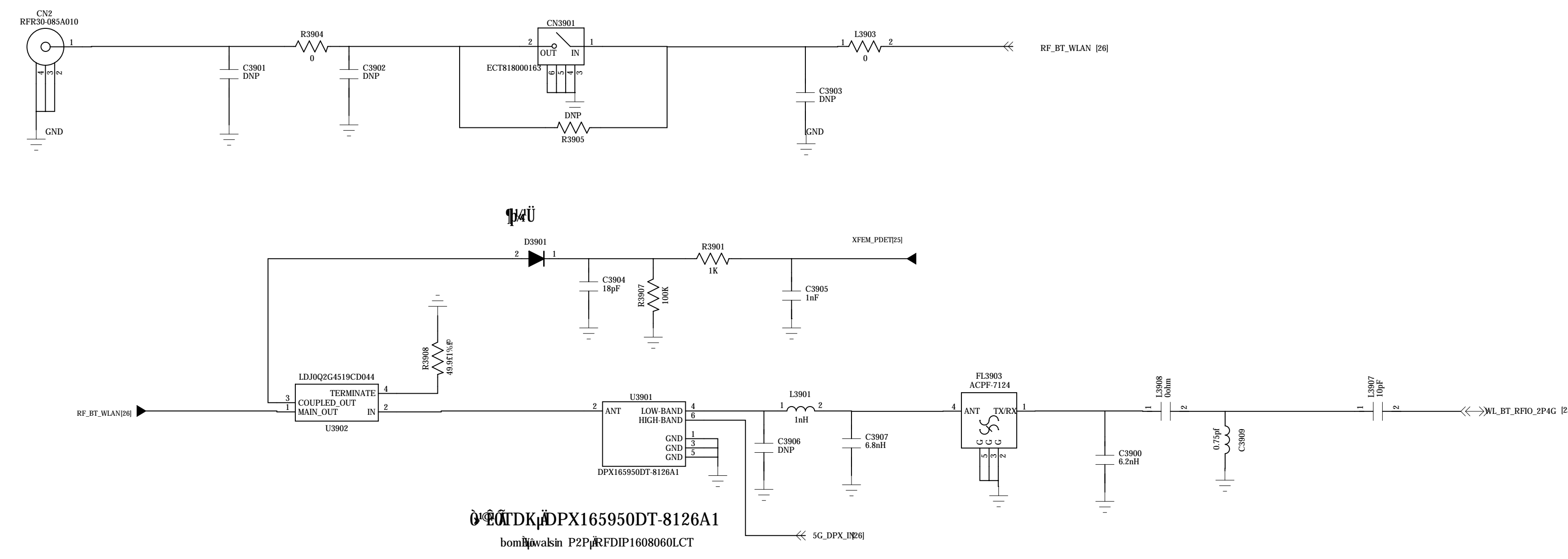


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