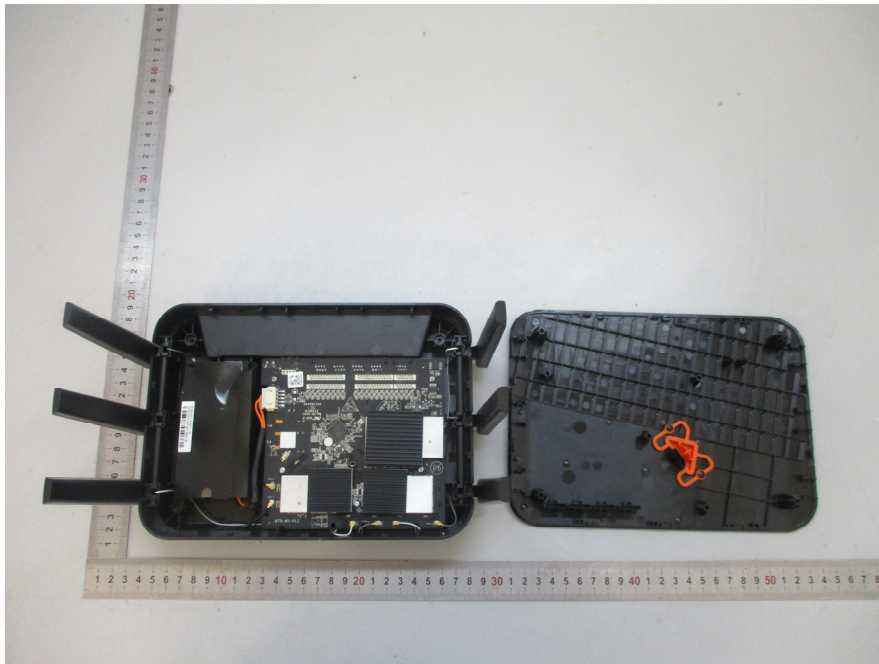


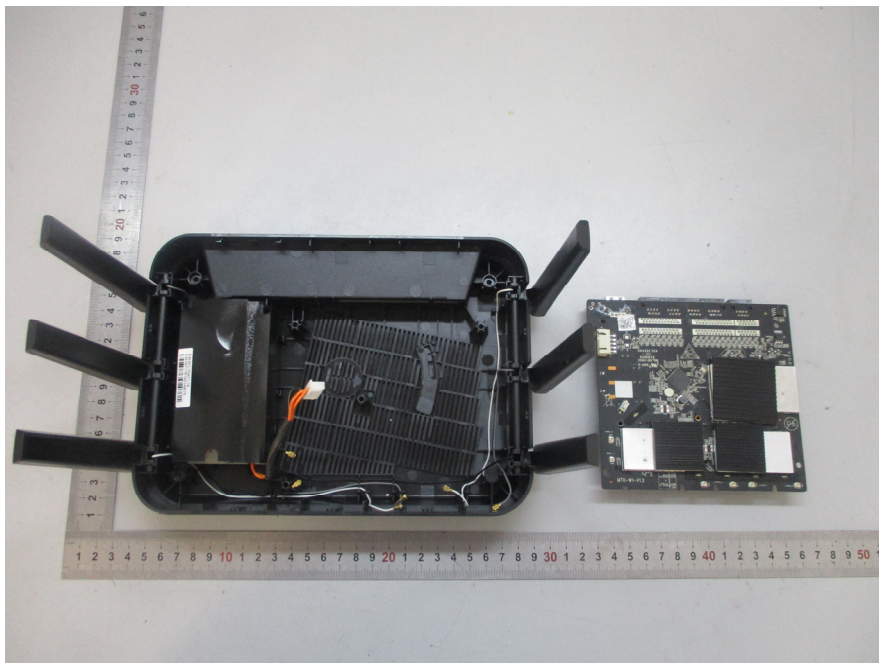
## **EXHIBIT B - EUT INTERNAL PHOTOGRAPHS**

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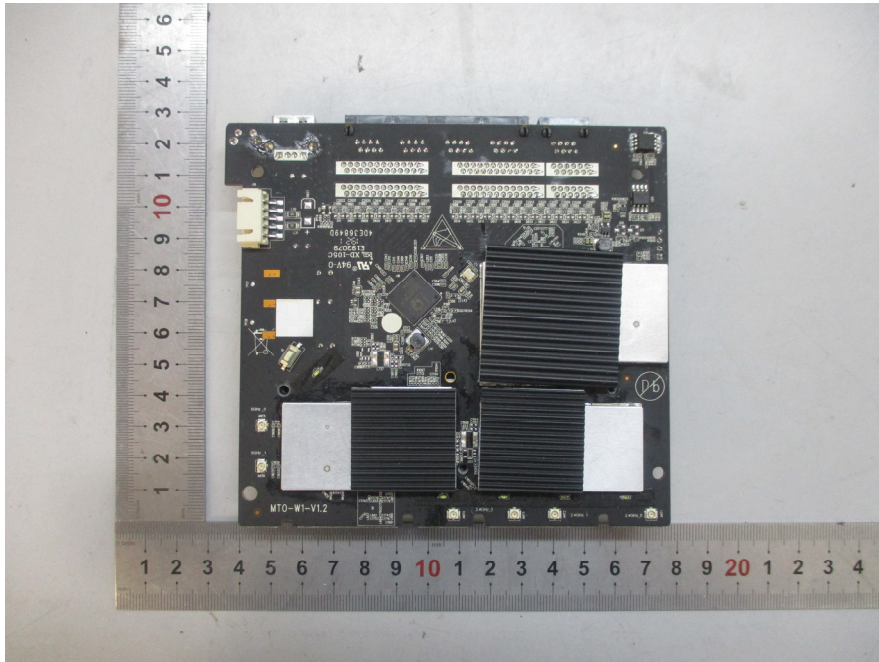
**EUT – Cover off View-1**



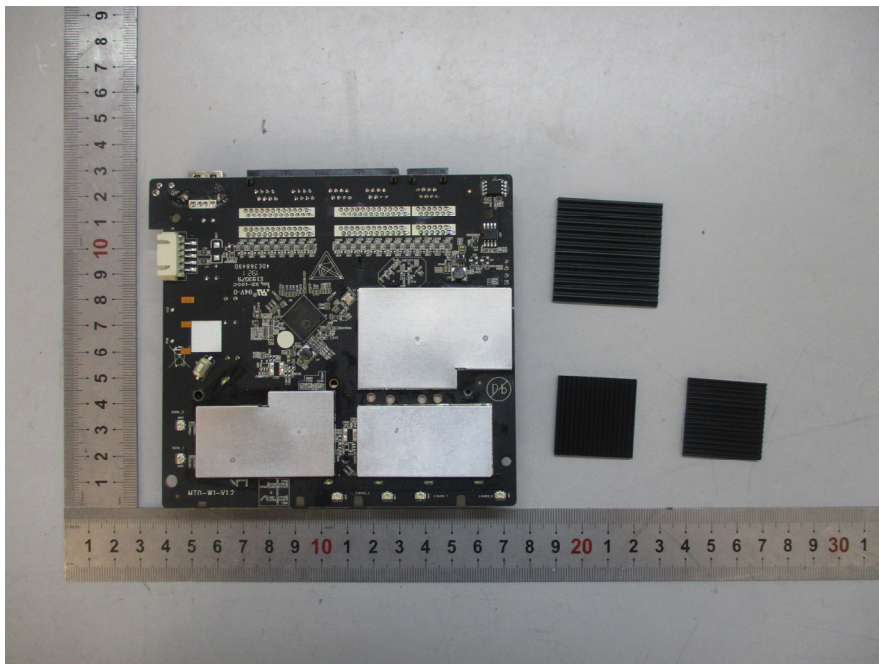
**EUT – Cover off View-2**



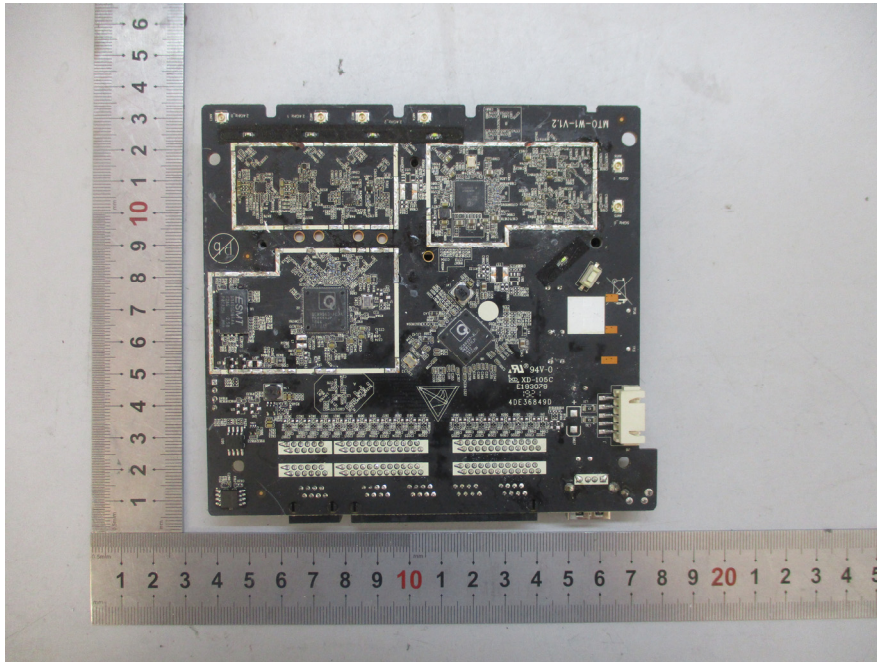
**EUT -PCB1 Top View-1**



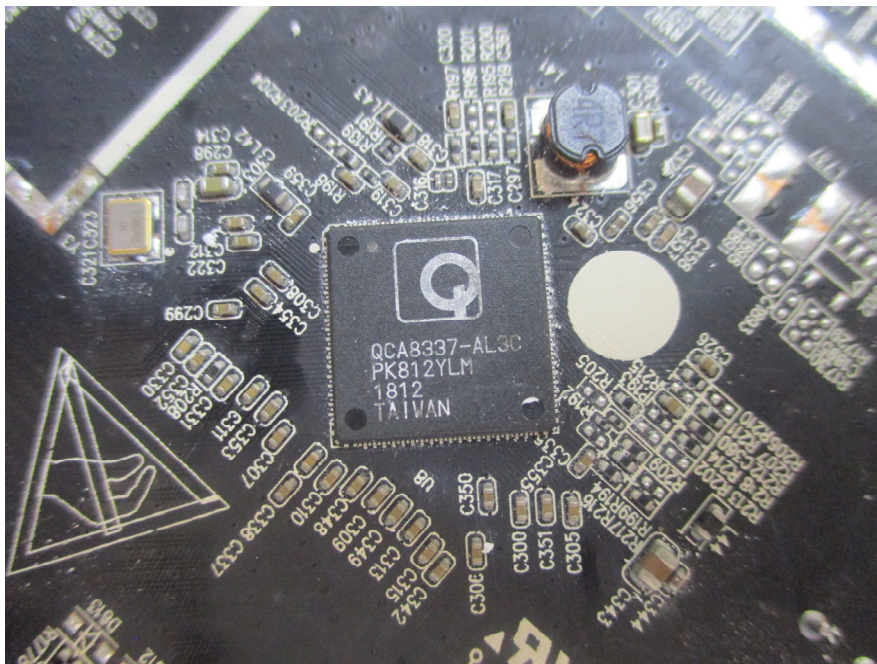
**EUT -PCB1 Top Shielding off View**



**EUT -PCB1 Top View-2**

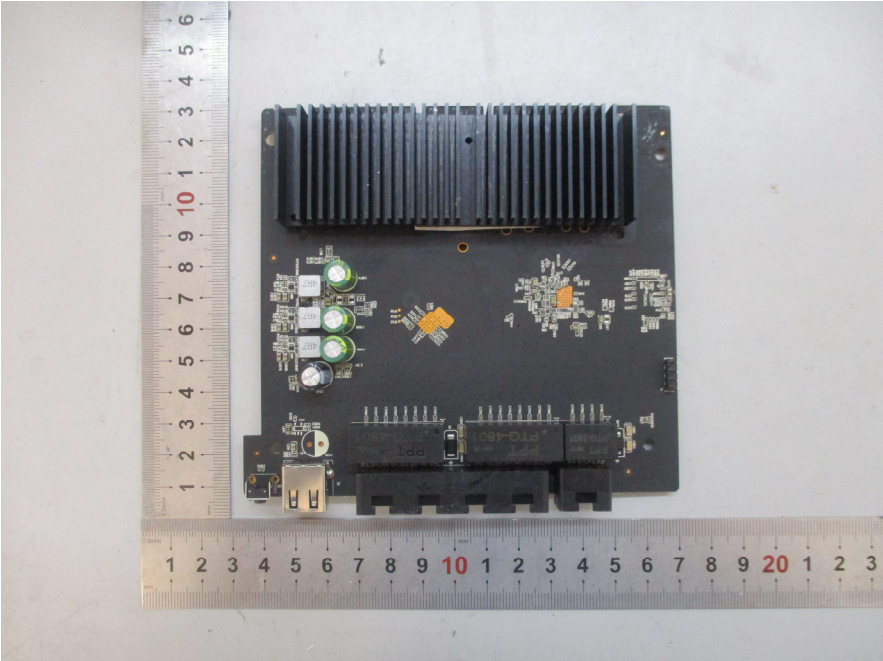


**EUT -PCB1 Chip View-1**

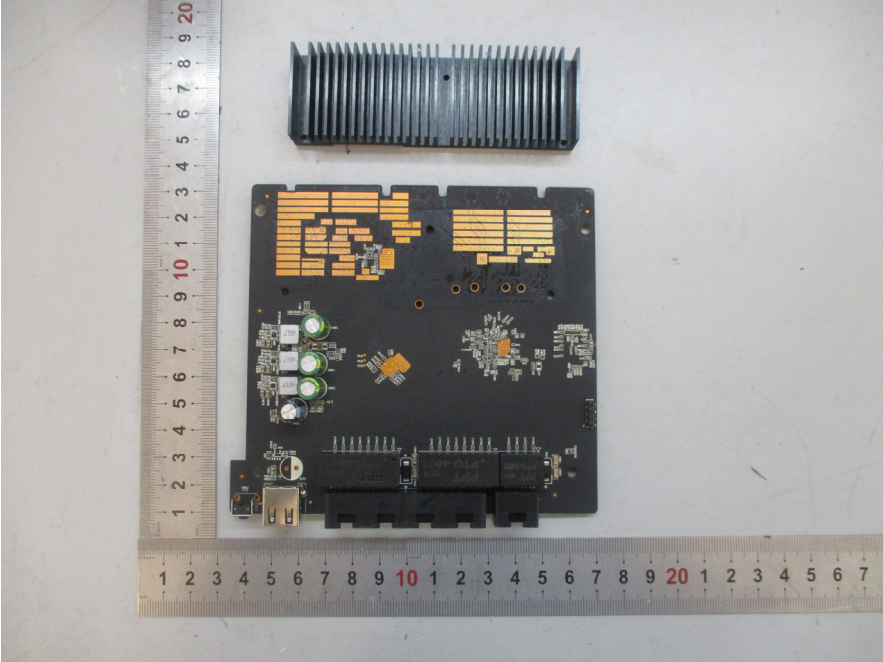




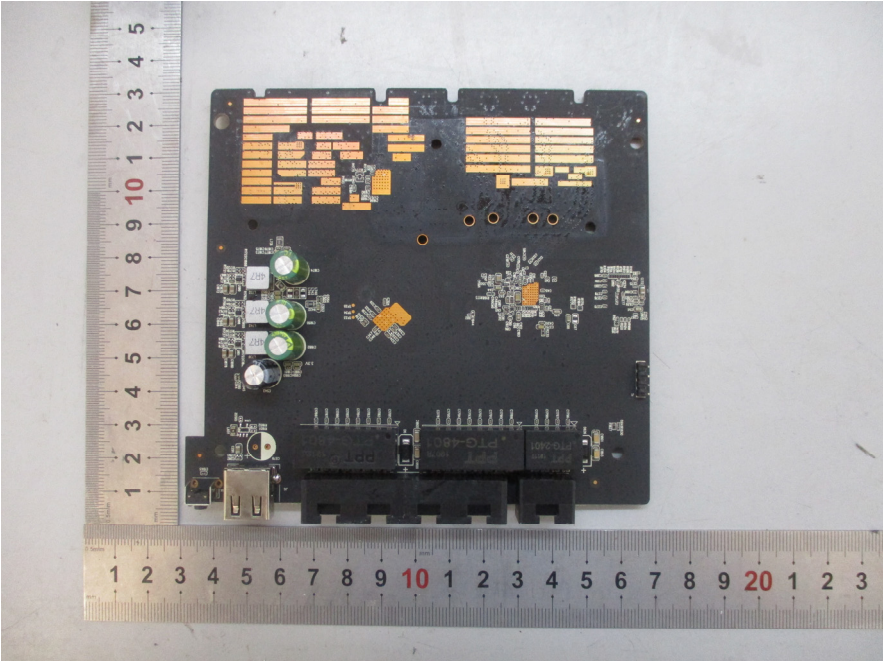
**EUT -PCB1 Bottom View-1**



**EUT -PCB1 Bottom Shielding off View**



**EUT –PCB1 Bottom View-2**



**EUT – Cover off View-3**

