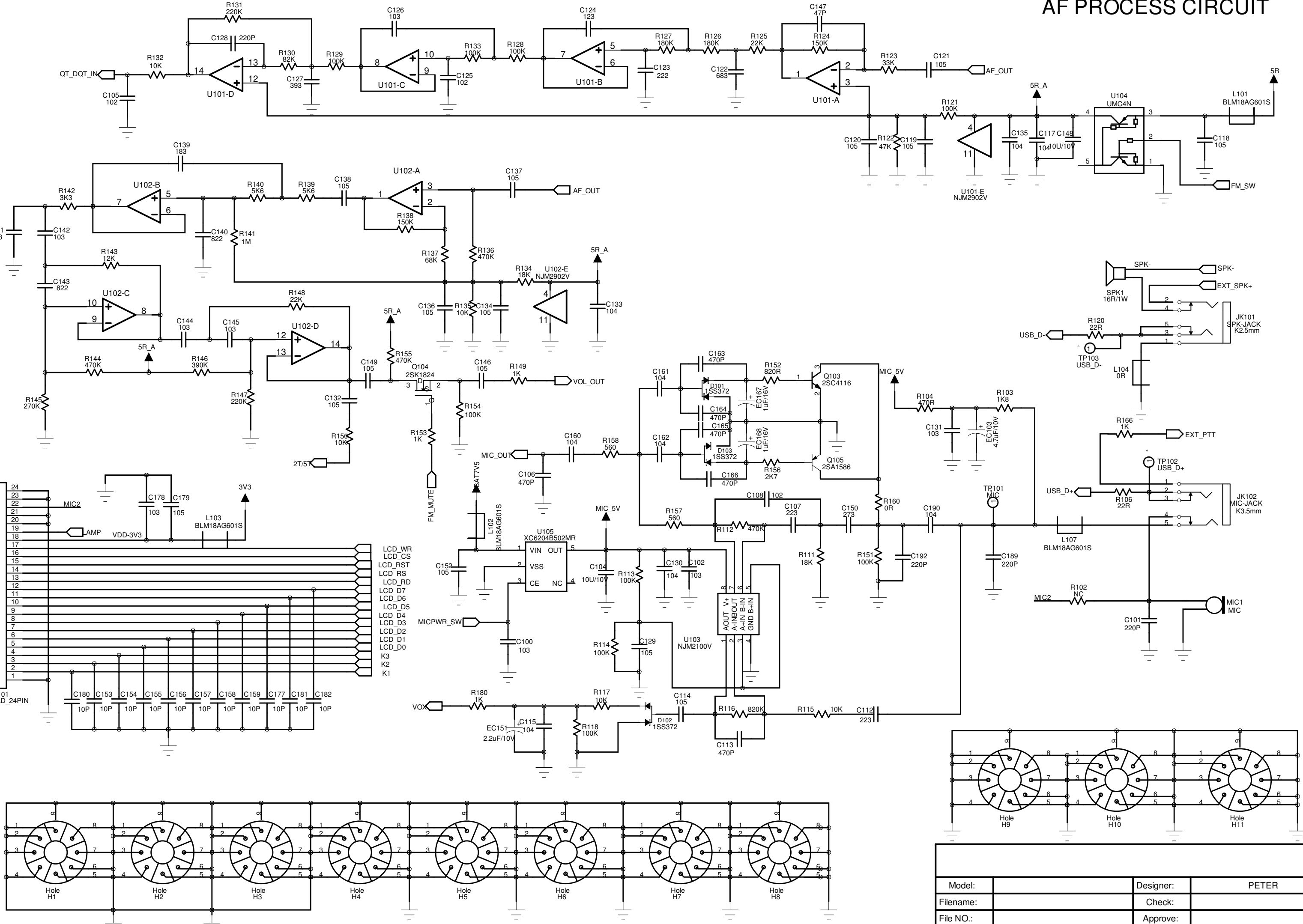
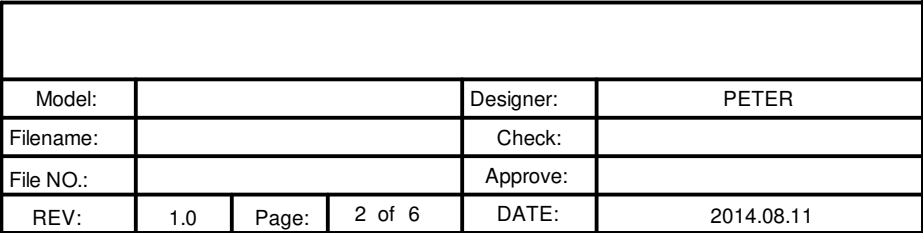


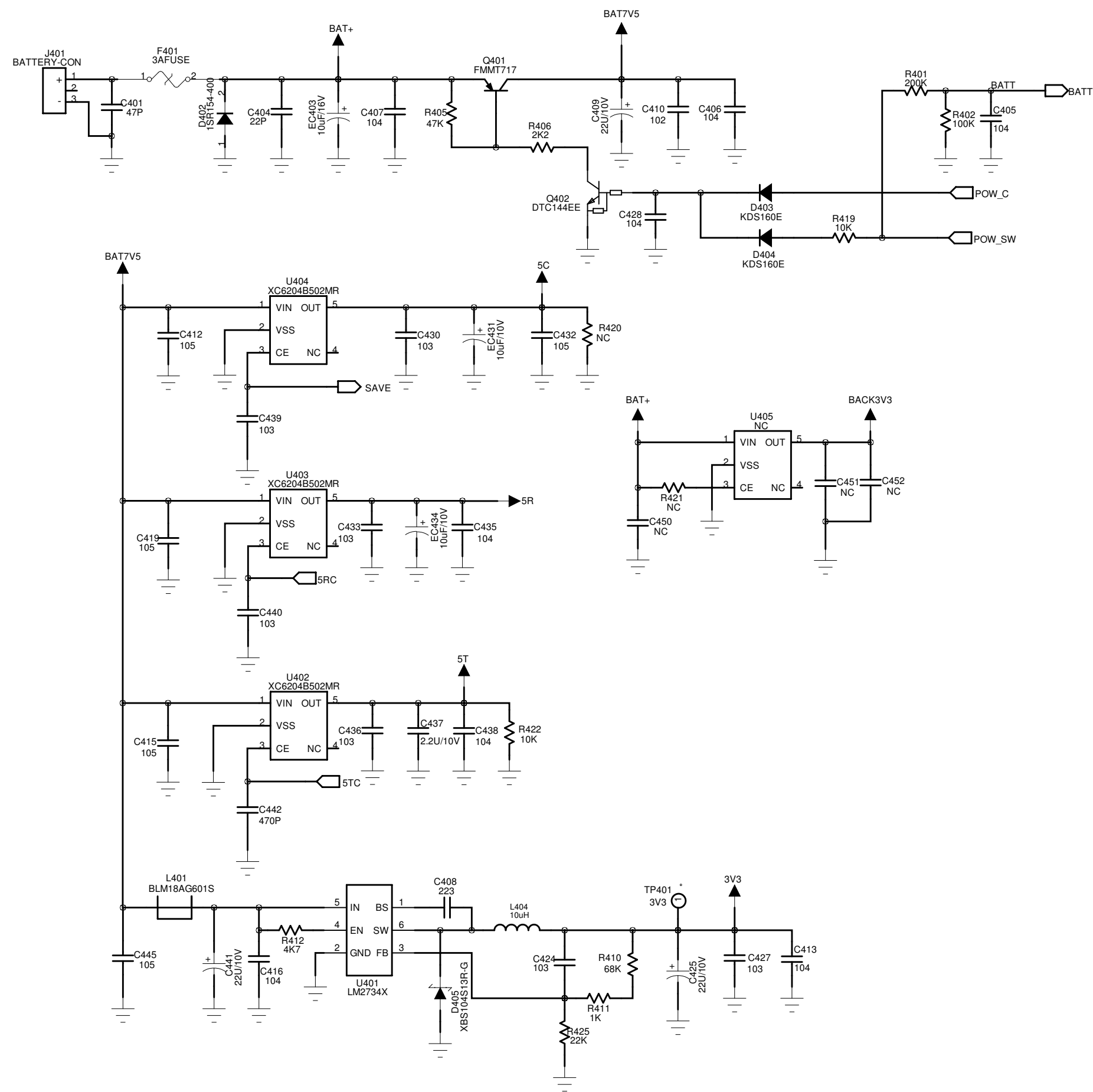
AF PROCESS CIRCUIT



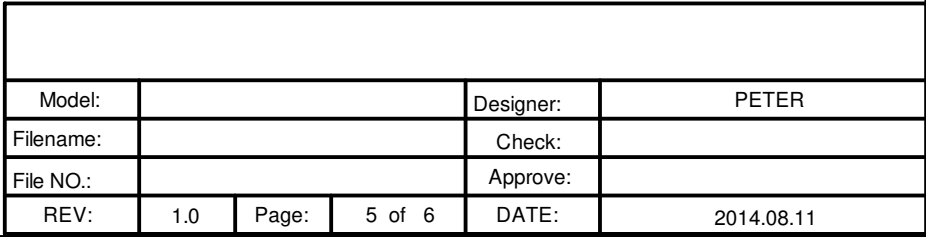
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Filename:				Check:	
File NO.:				Approve:	
REV:	1.0	Page:	1 of 6	DATE:	2014.08.11



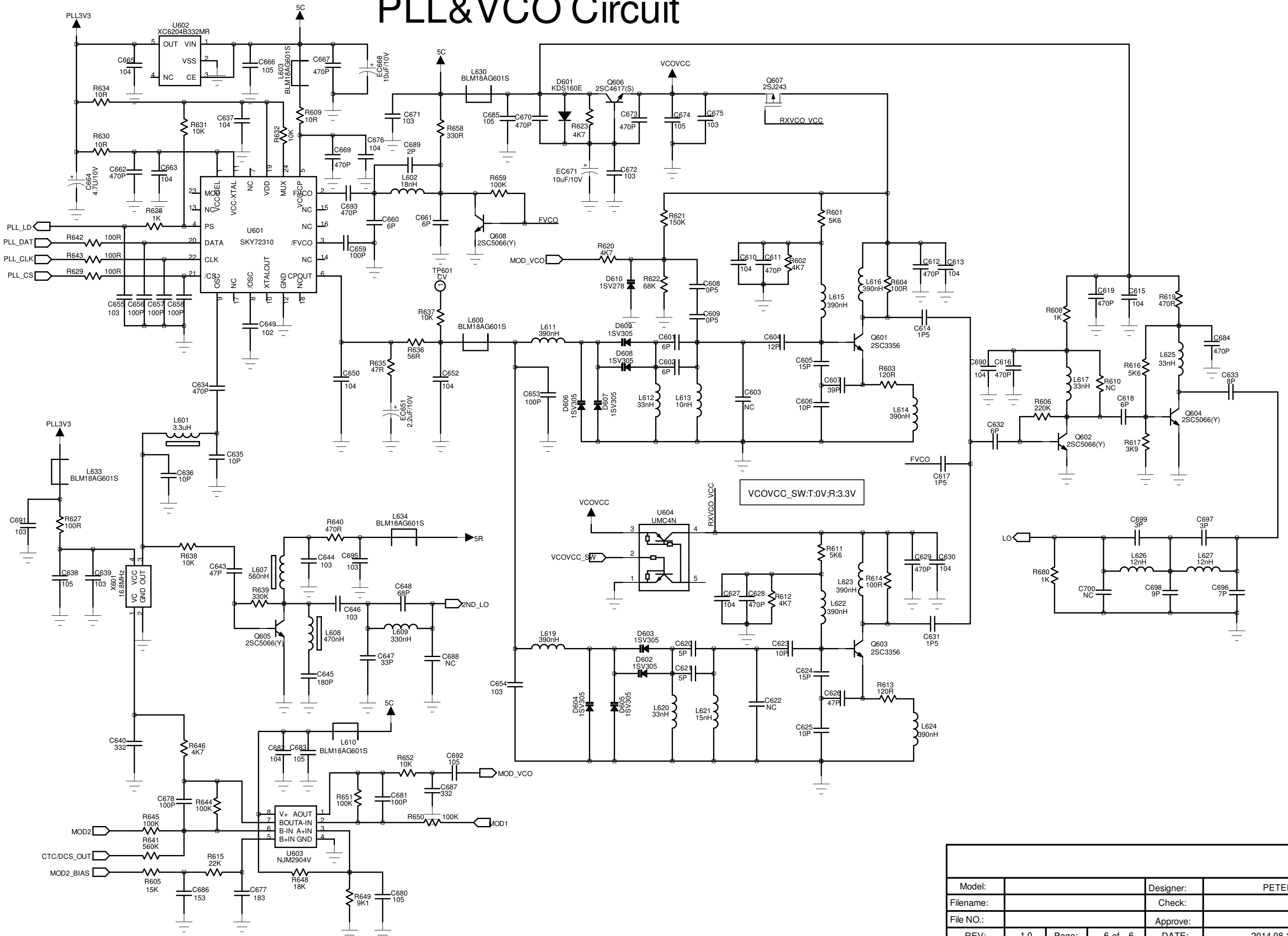
POWER CIRCUIT



Model:		Designer:	PETER
Filename:		Check:	
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PLL&VCO Circuit



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File NO.:		Approve:	
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