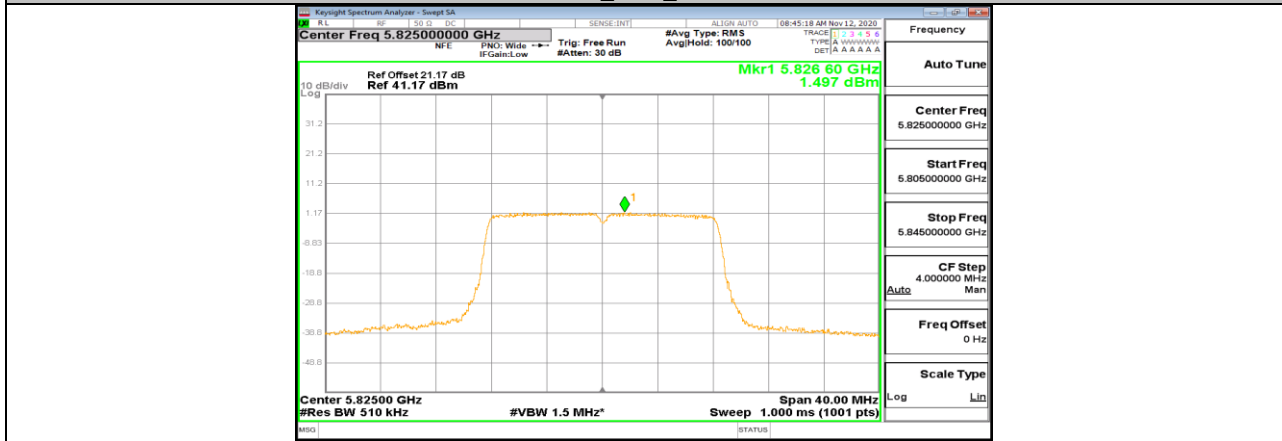
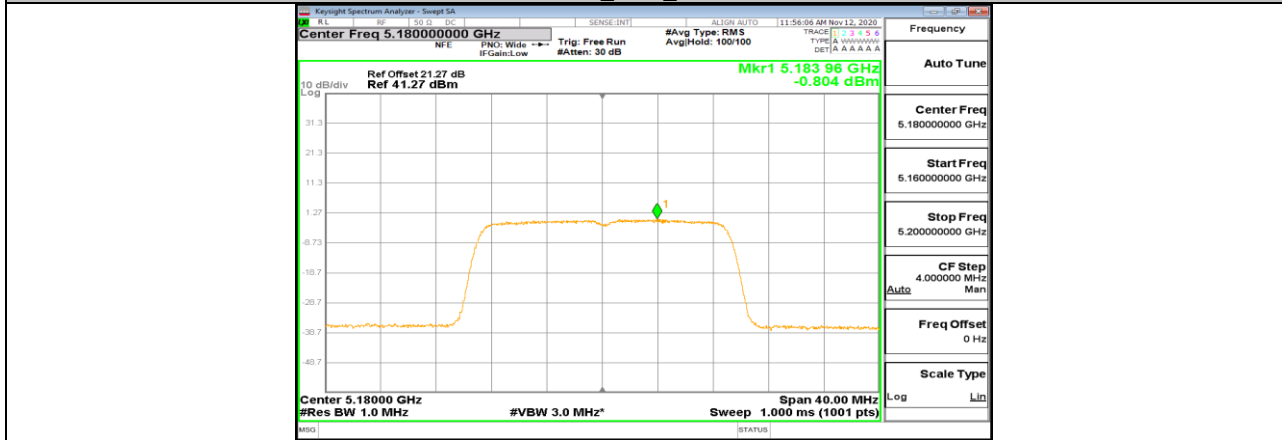


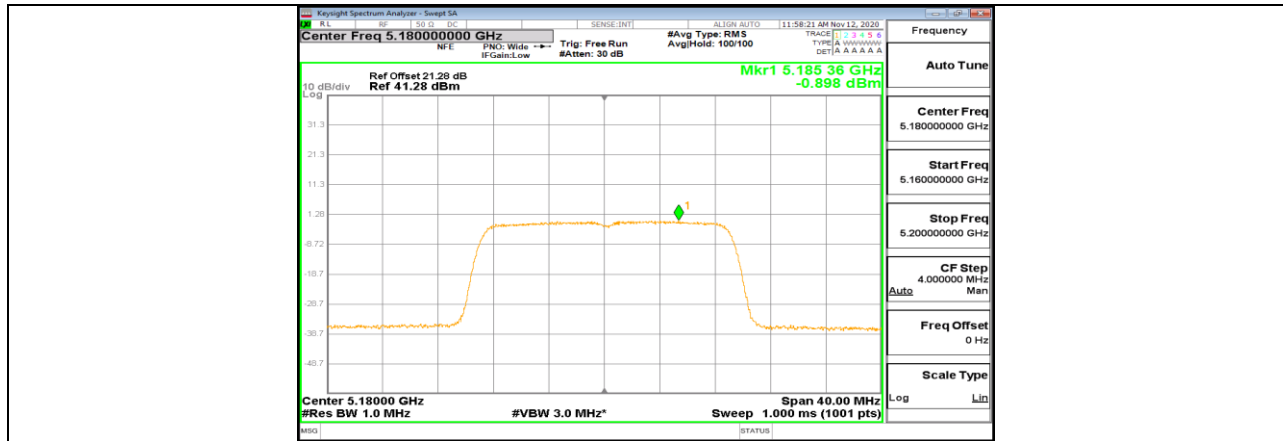
11A_Ant2_5785



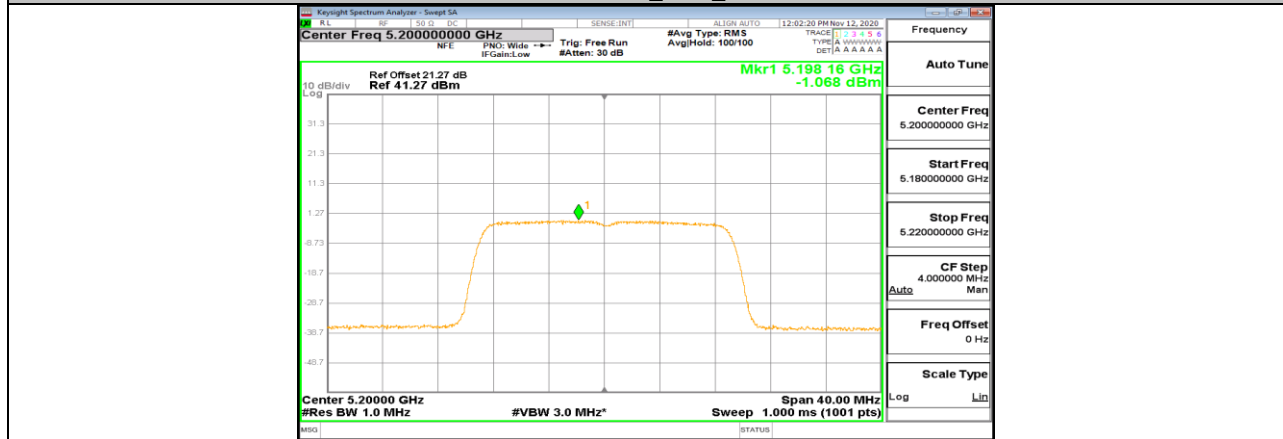
11A_Ant2_5825



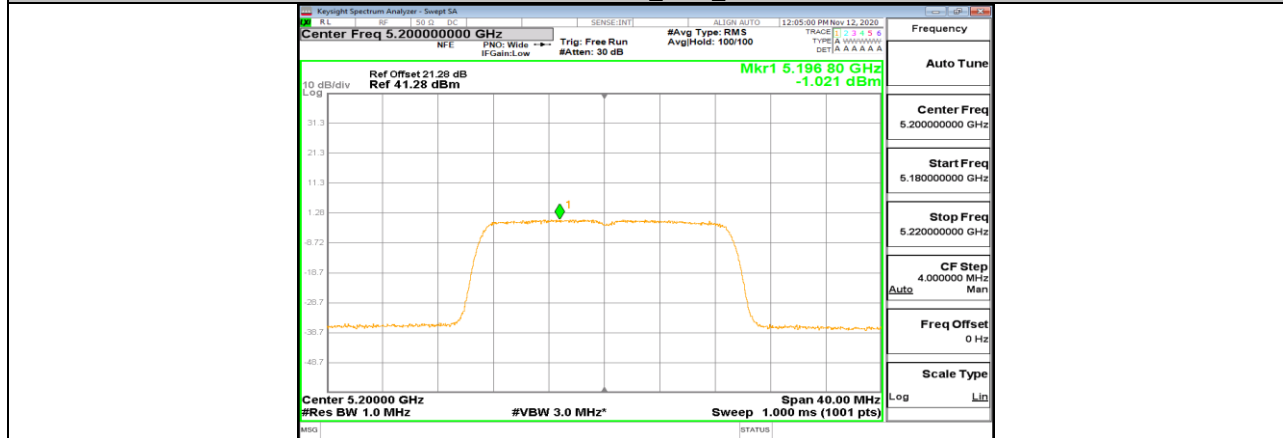
11N20MIMO_Ant1_5180



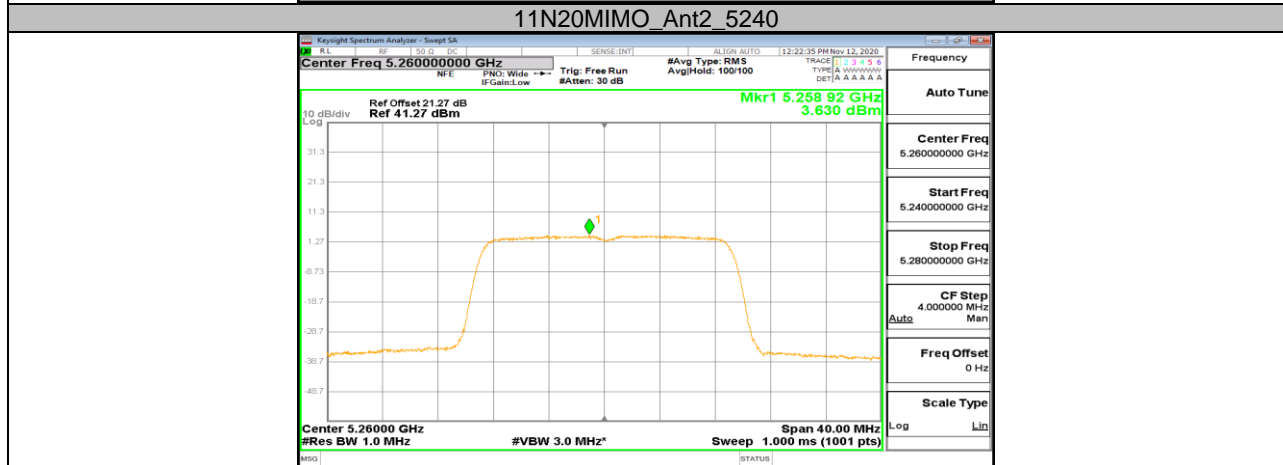
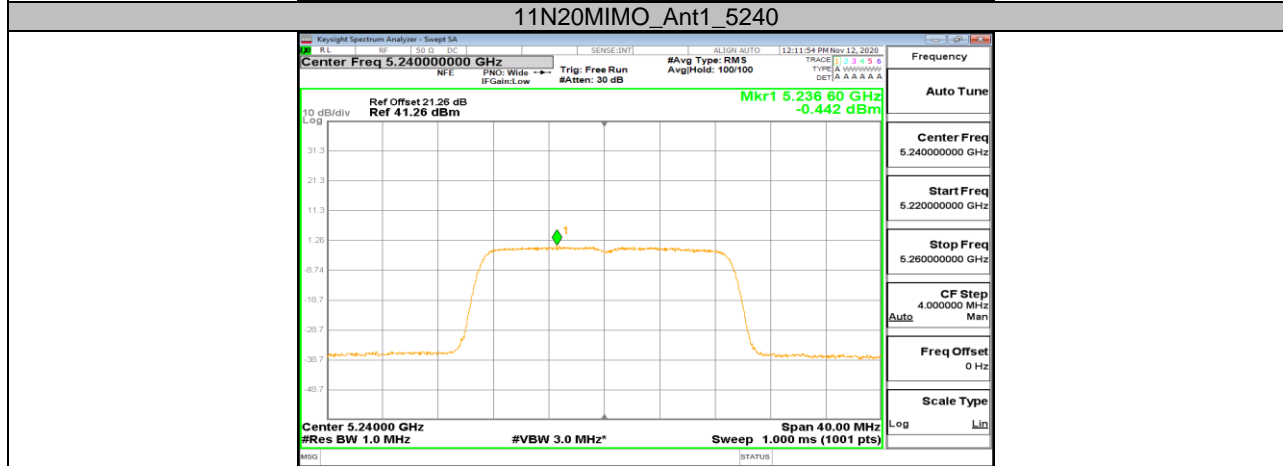
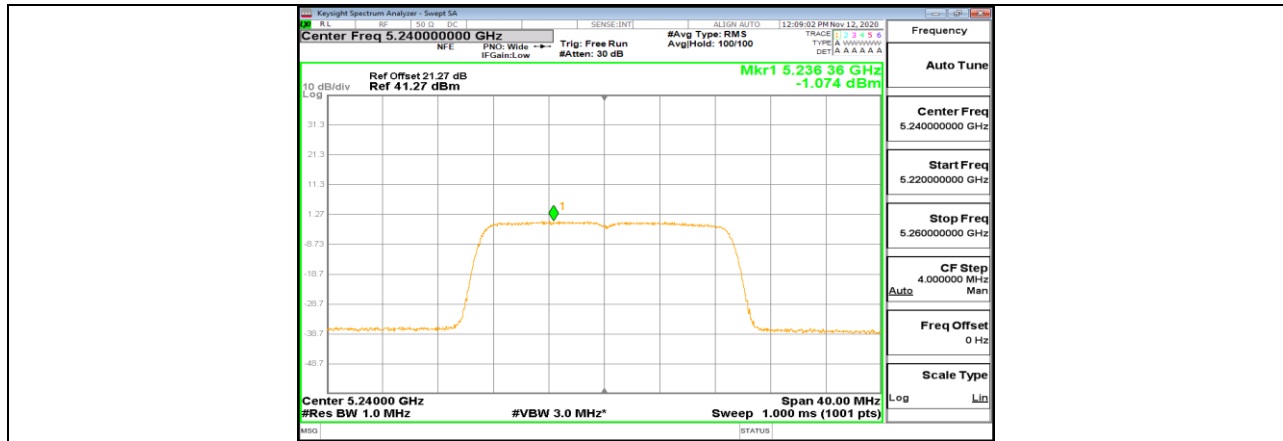
11N20MIMO_Ant2_5180

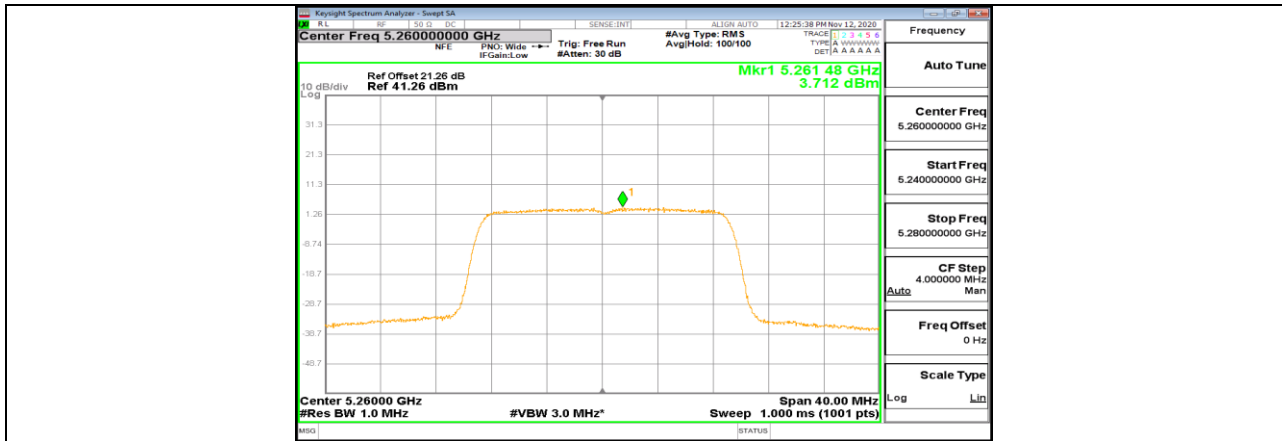


11N20MIMO_Ant1_5200

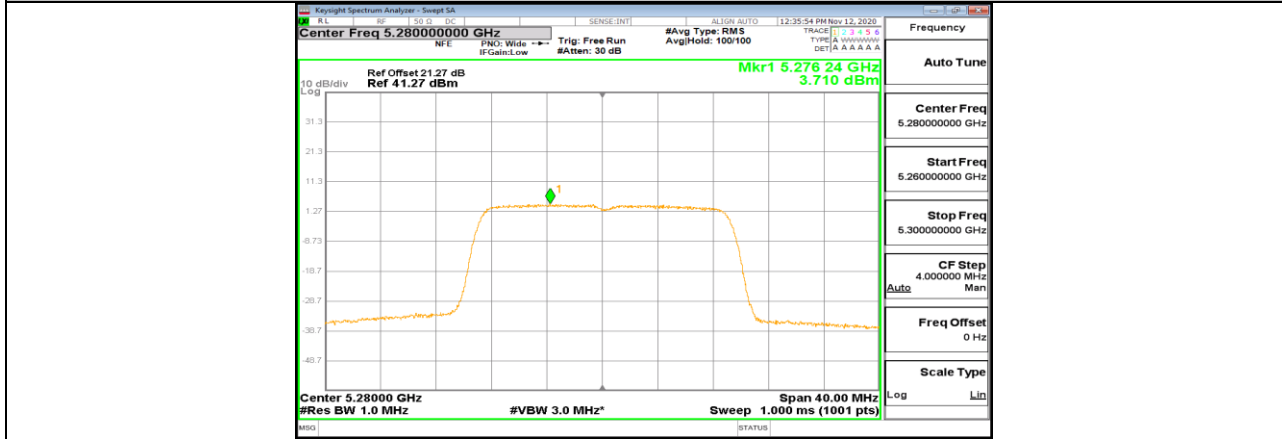


11N20MIMO_Ant2_5200

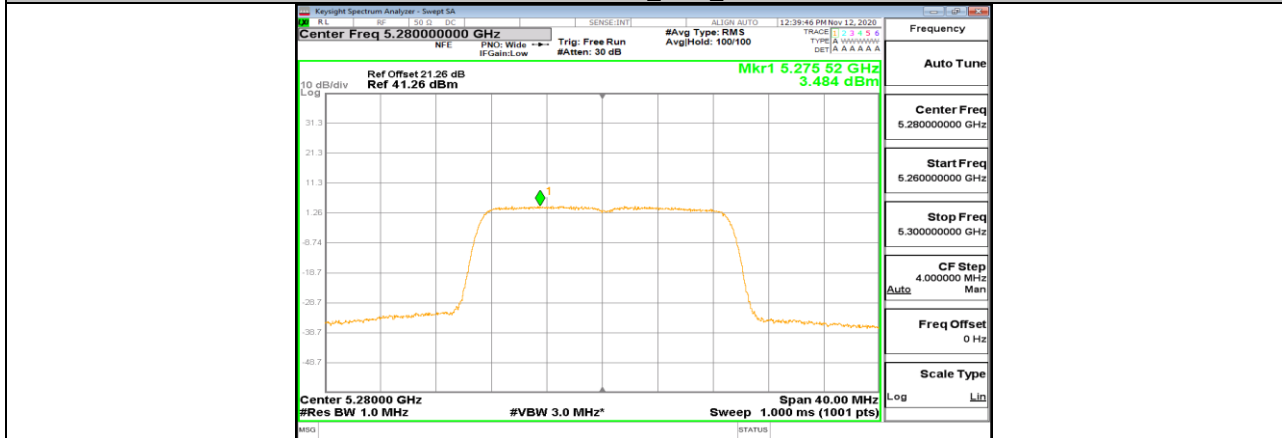




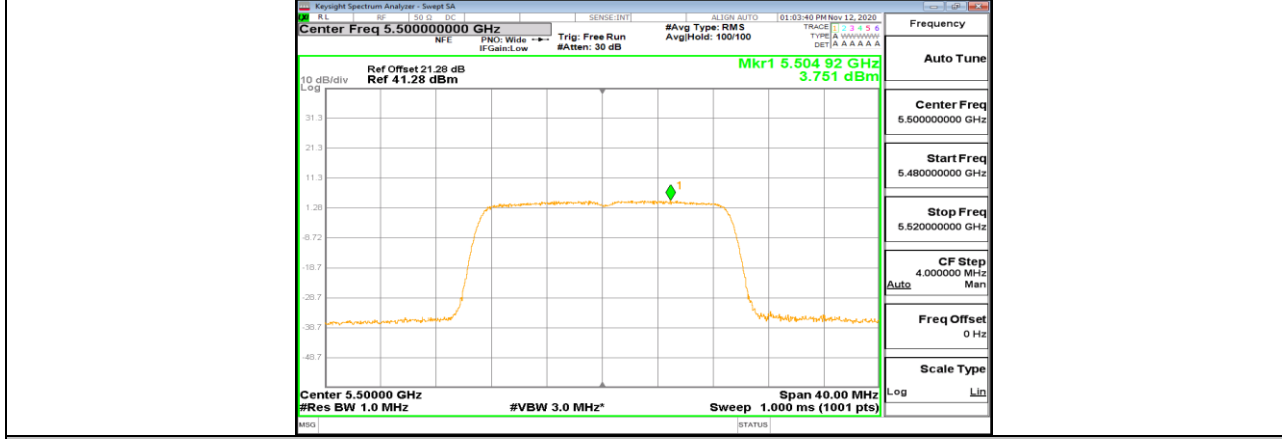
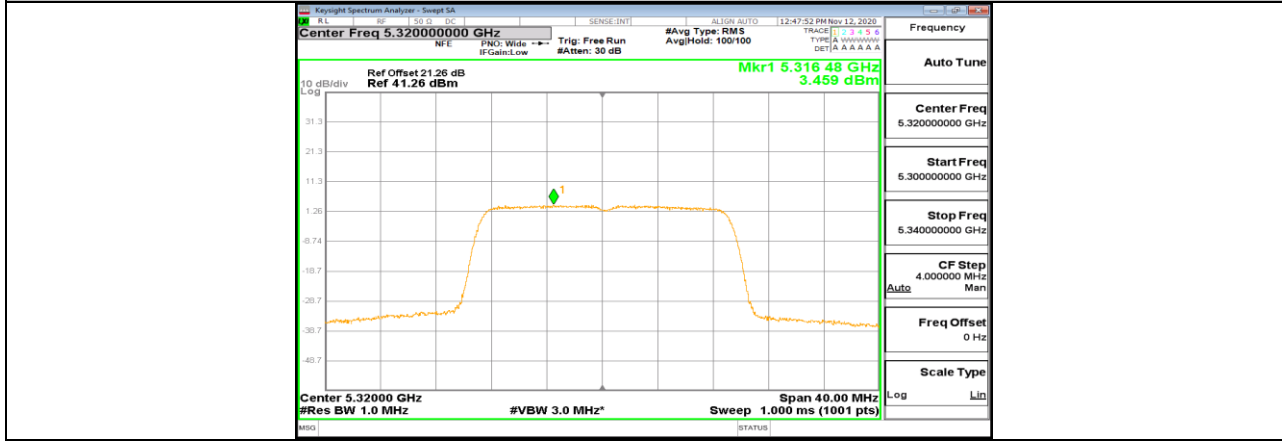
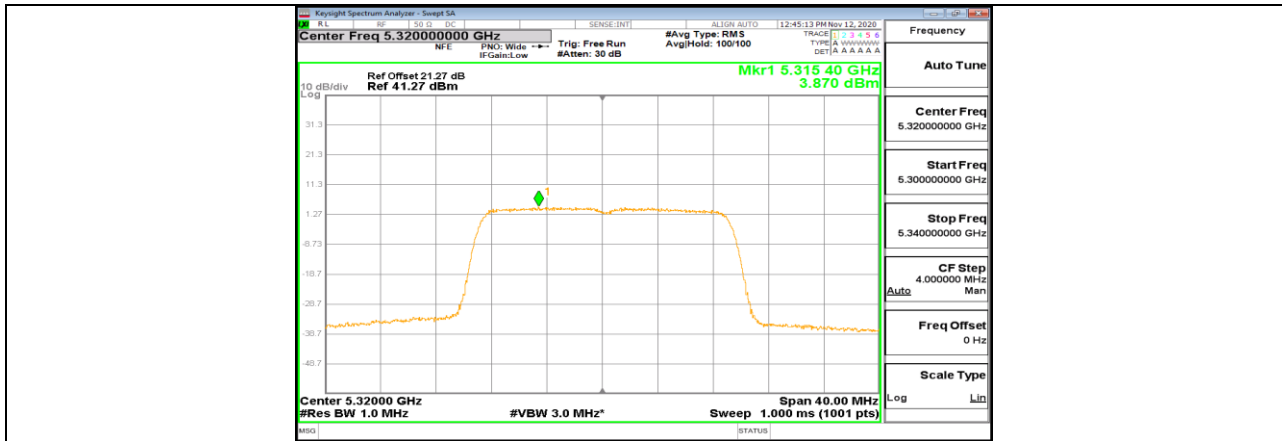
11N20MIMO_Ant2_5260



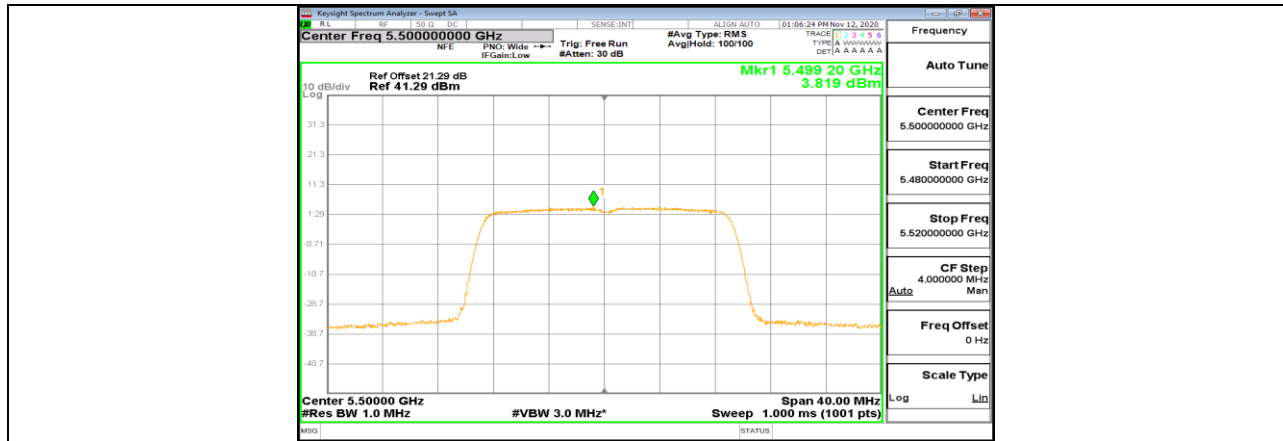
11N20MIMO_Ant1_5280



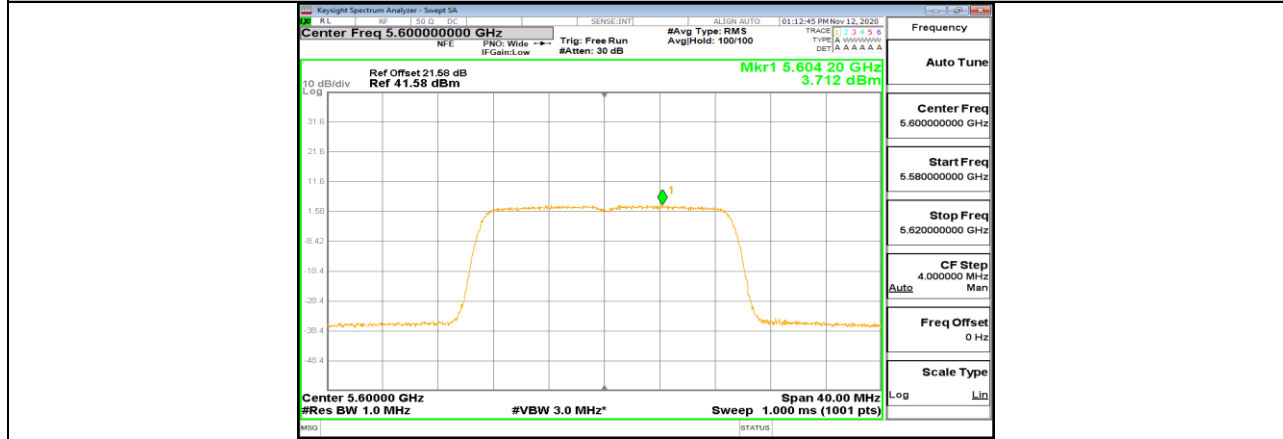
11N20MIMO_Ant2_5280



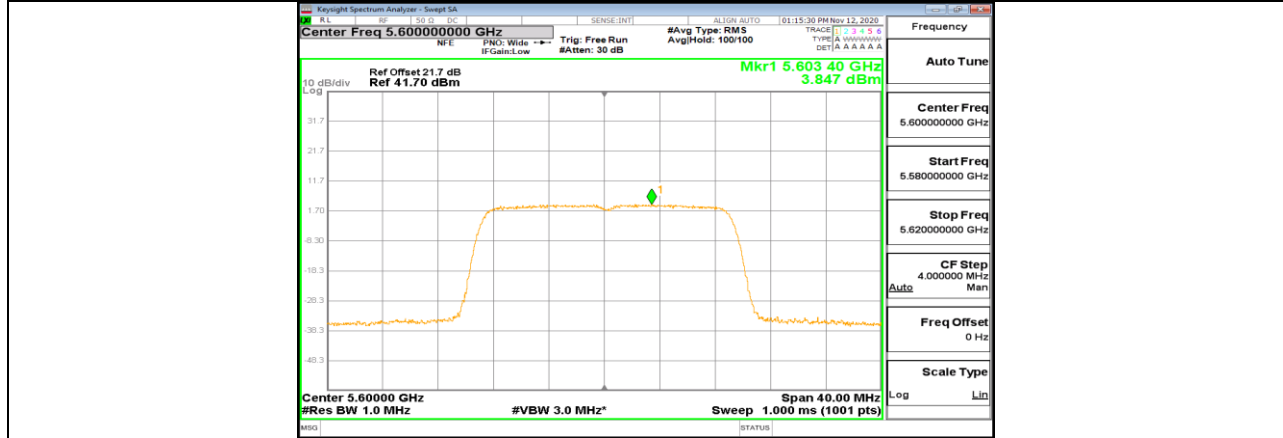
11N20MIMO_Ant1_5500



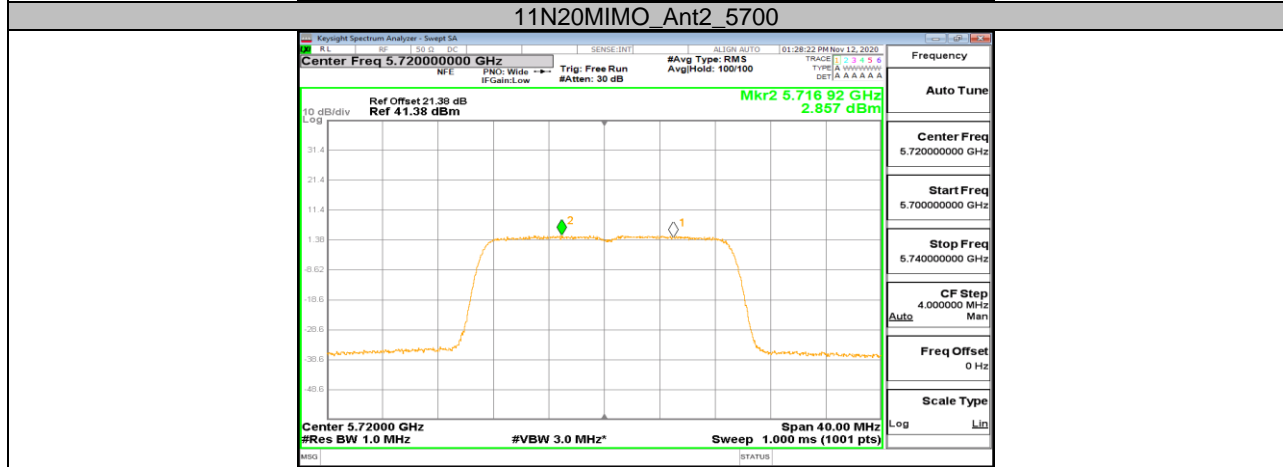
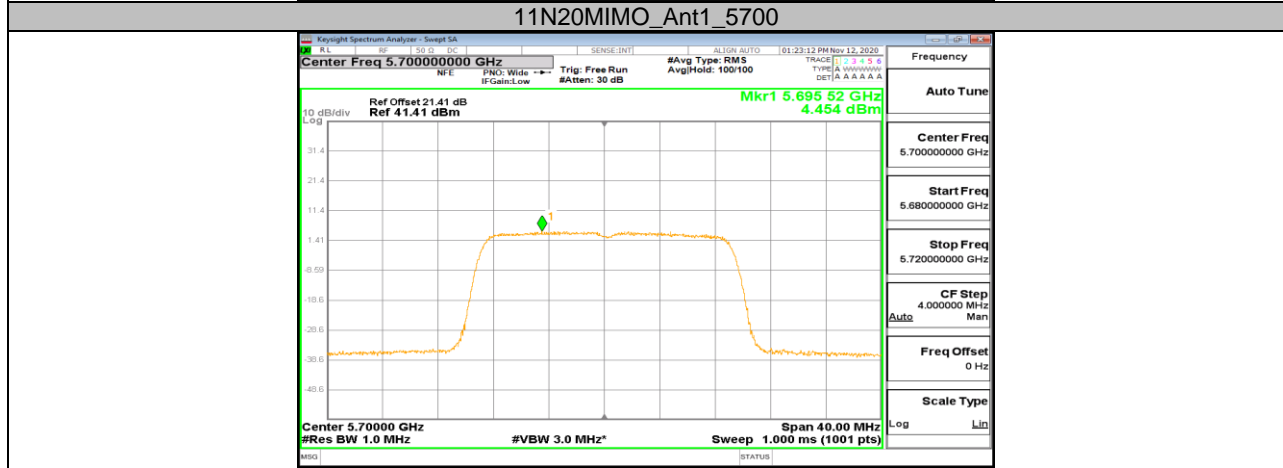
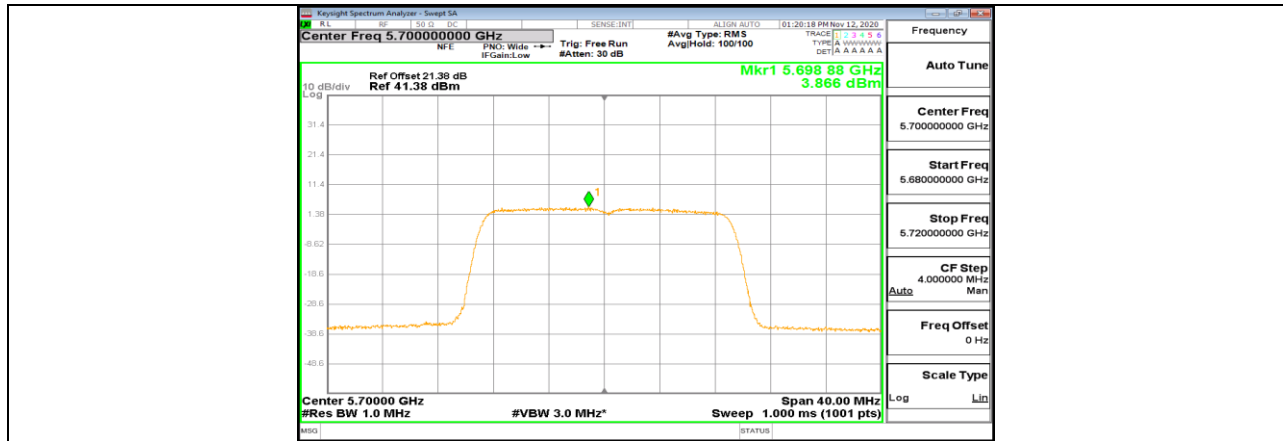
11N20MIMO_Ant2_5500

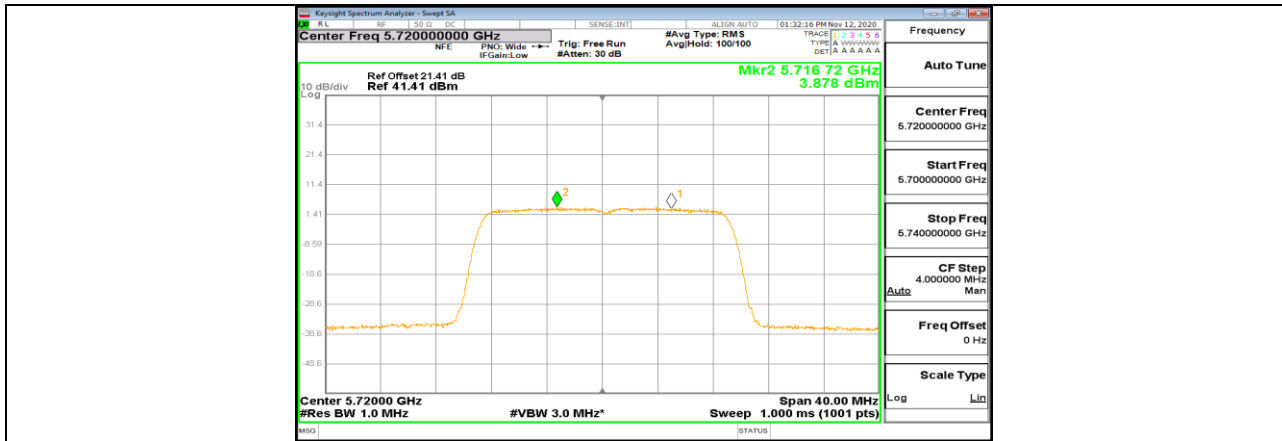


11N20MIMO_Ant1_5600

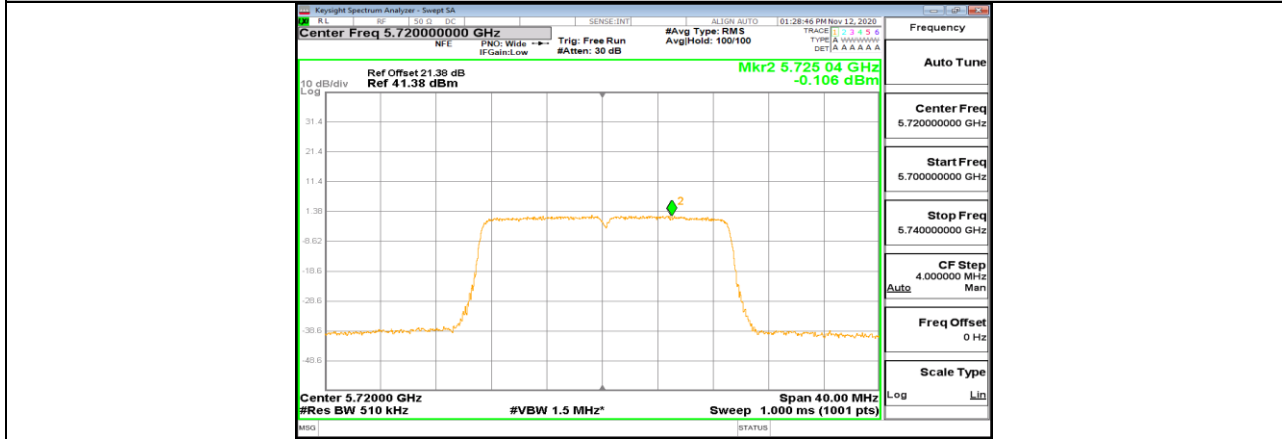


11N20MIMO_Ant2_5600

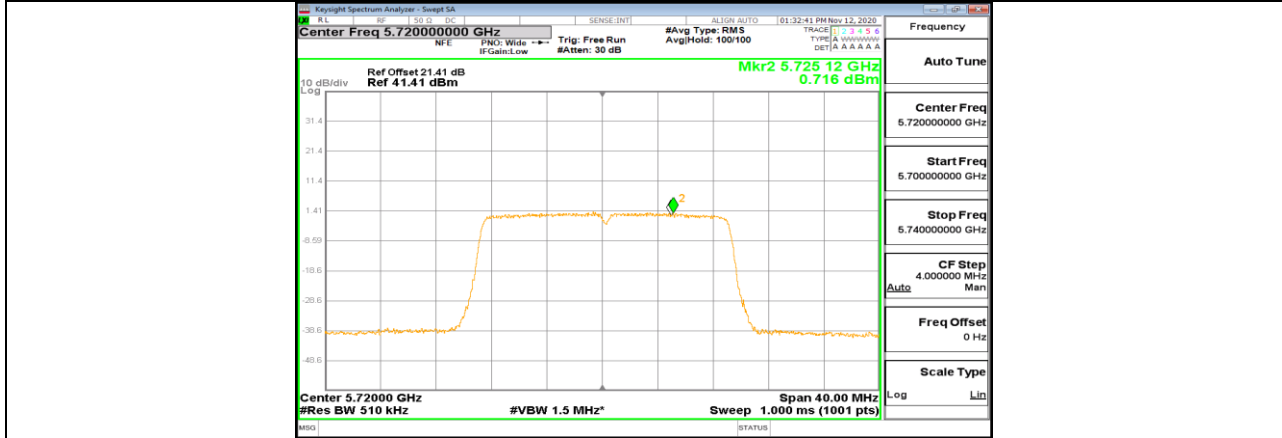




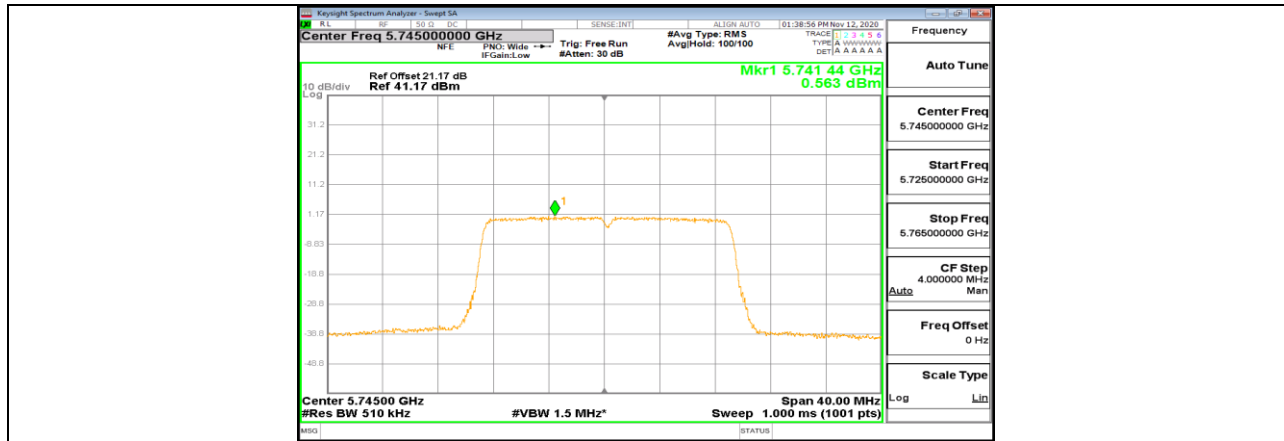
11N20MIMO_Ant2_5720_UNII-2C



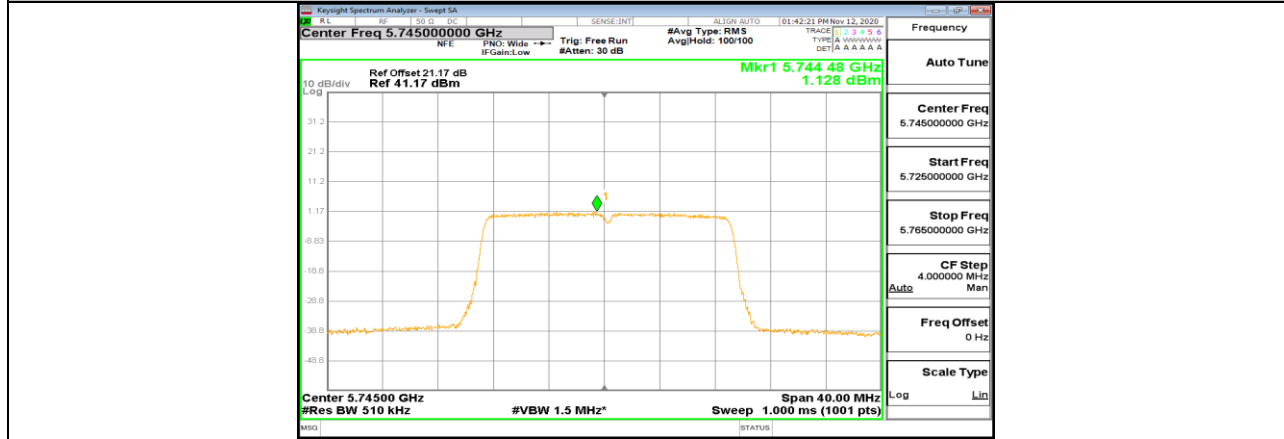
11N20MIMO_Ant1_5720_UNII-3



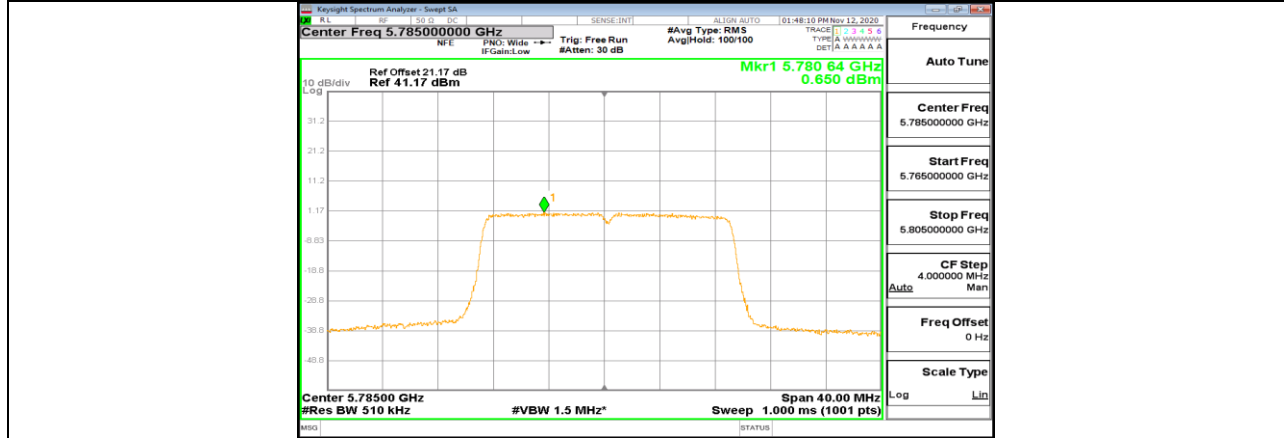
11N20MIMO_Ant2_5720_UNII-3



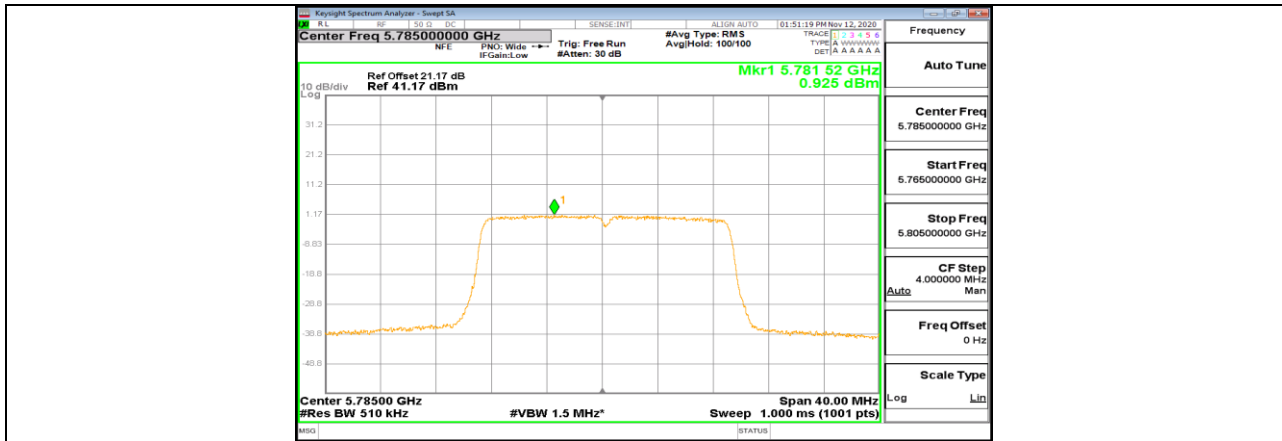
11N20MIMO_Ant1_5745



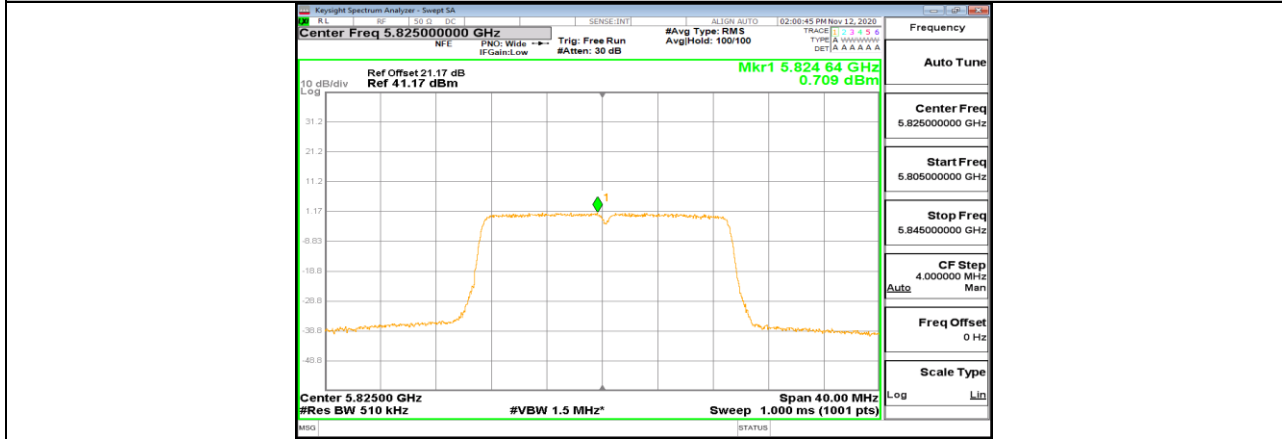
11N20MIMO_Ant2_5745



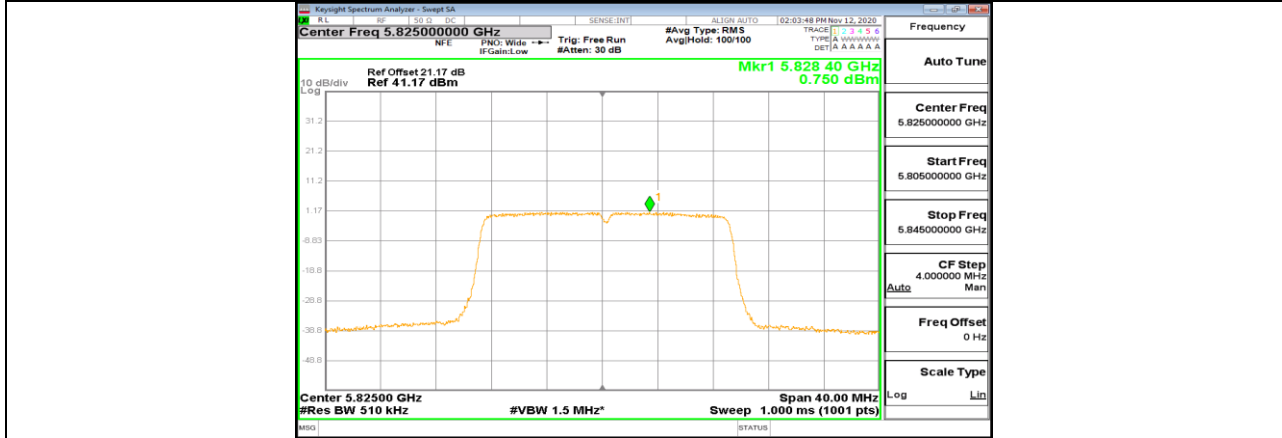
11N20MIMO_Ant1_5785



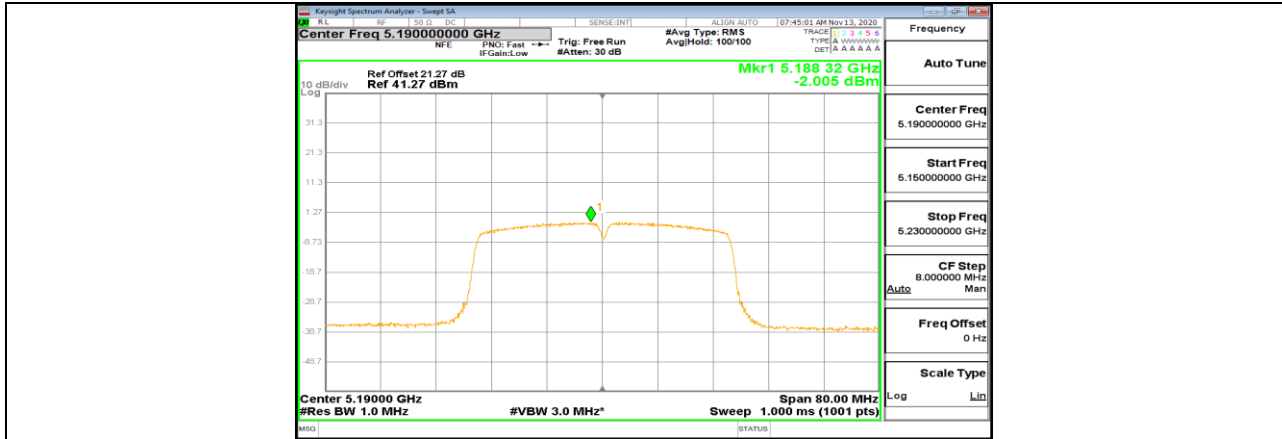
11N20MIMO_Ant2_5785



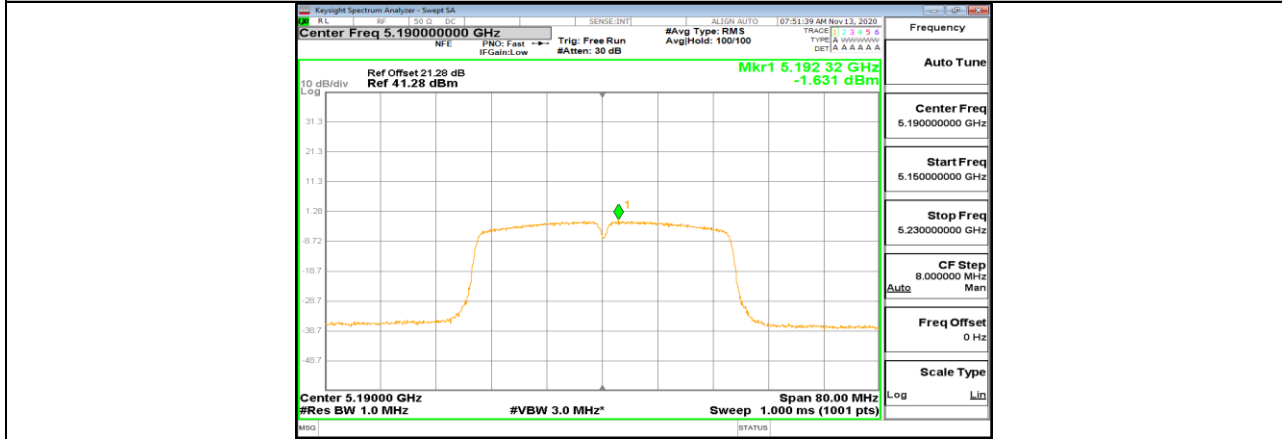
11N20MIMO_Ant1_5825



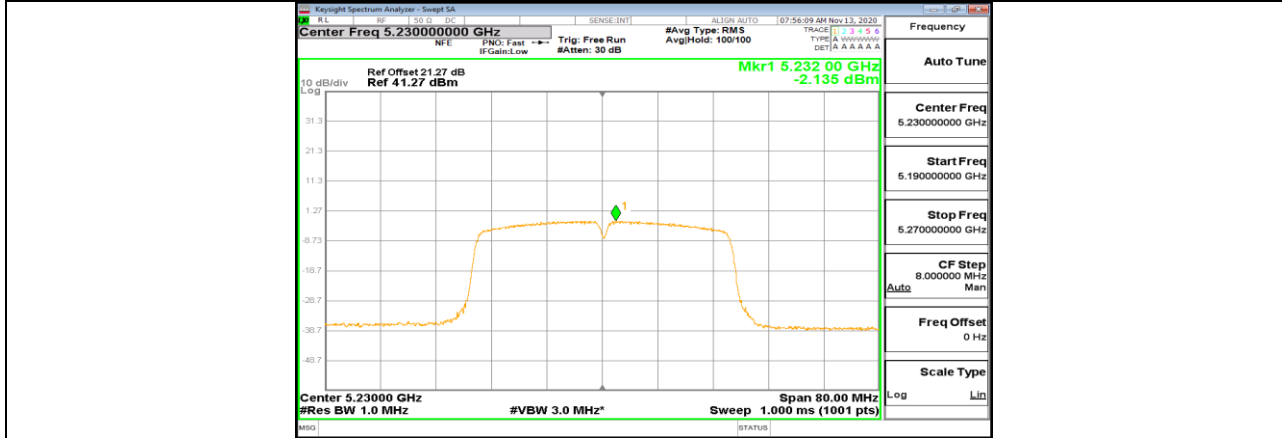
11N20MIMO_Ant2_5825



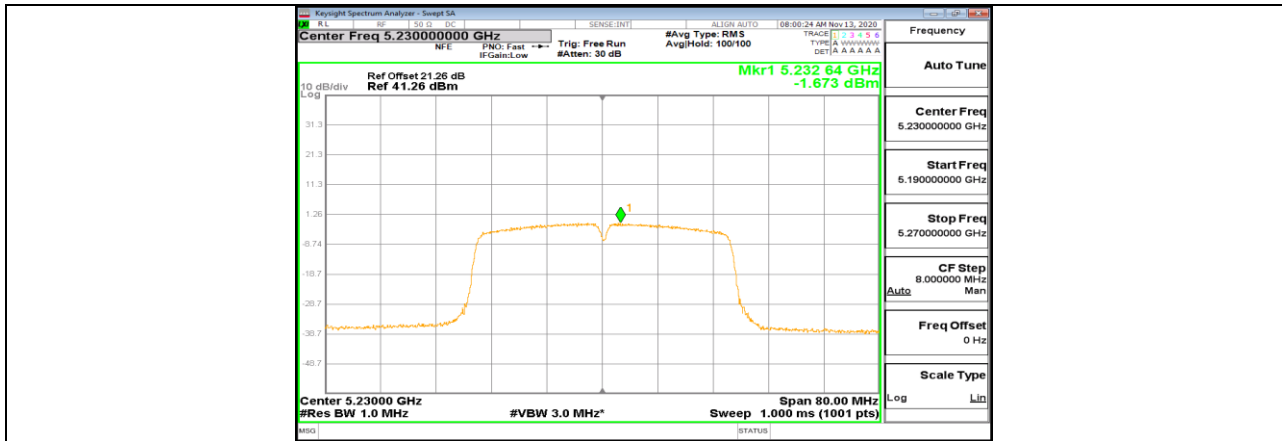
11N40MIMO_Ant1_5190



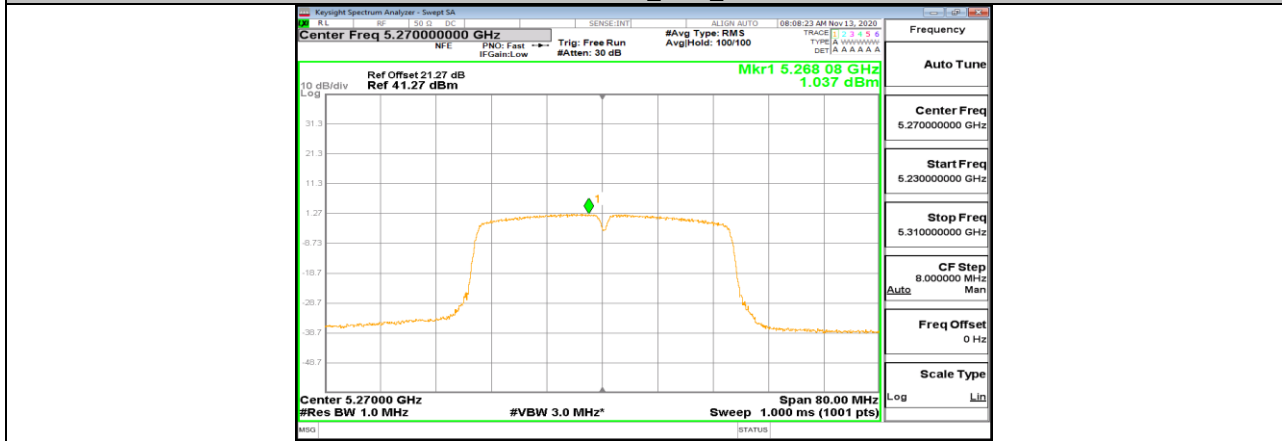
11N40MIMO_Ant2_5190



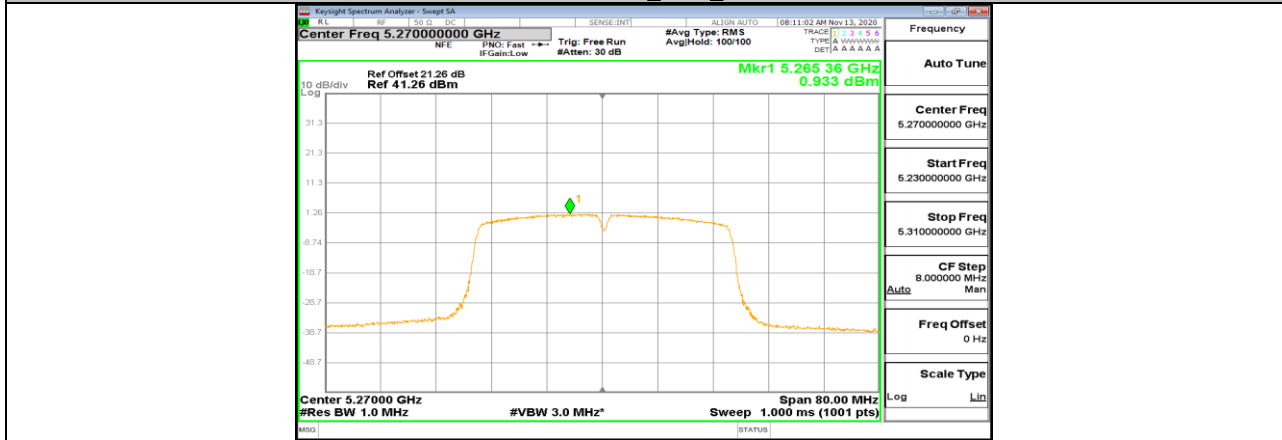
11N40MIMO_Ant1_5230



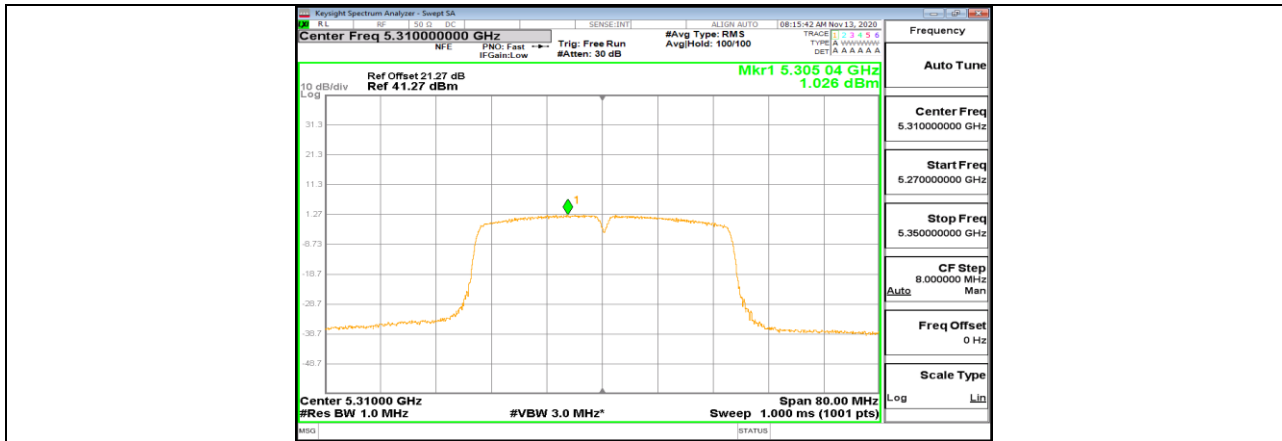
11N40MIMO_Ant2_5230



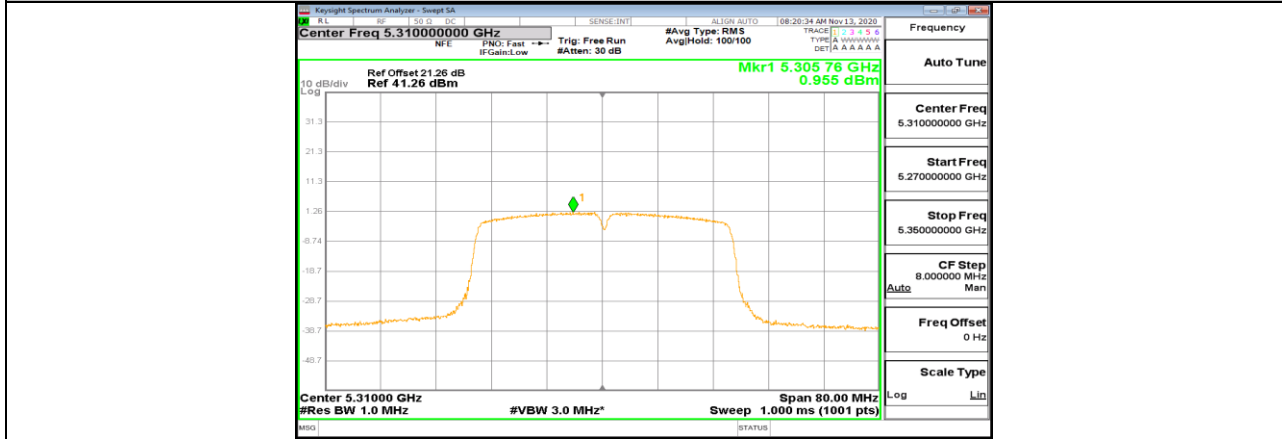
11N40MIMO_Ant1_5270



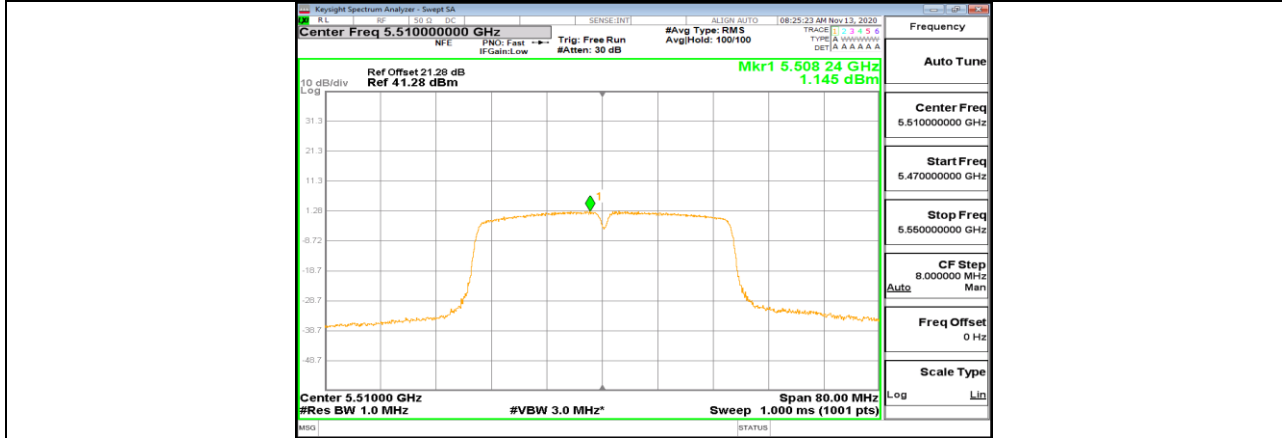
11N40MIMO_Ant2_5270



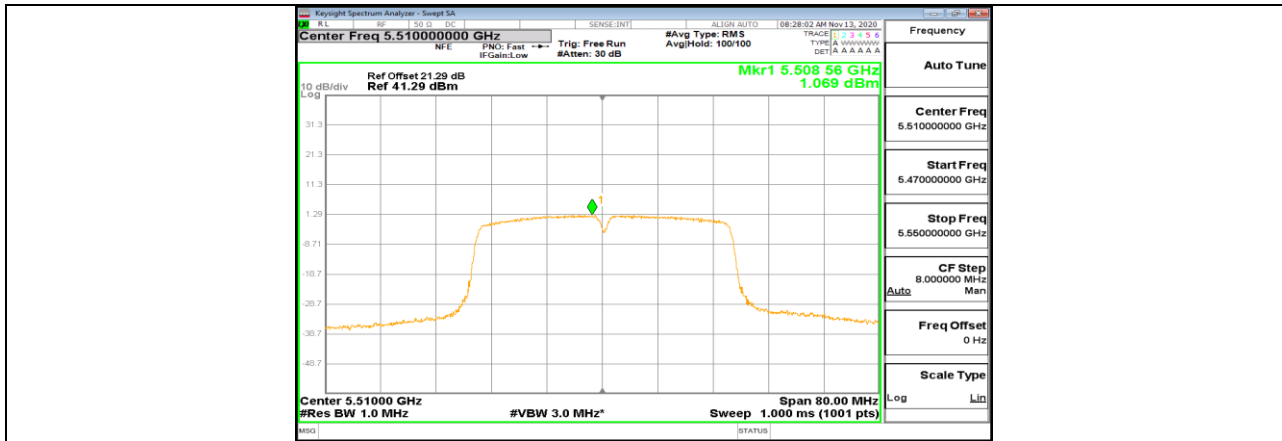
11N40MIMO_Ant1_5310



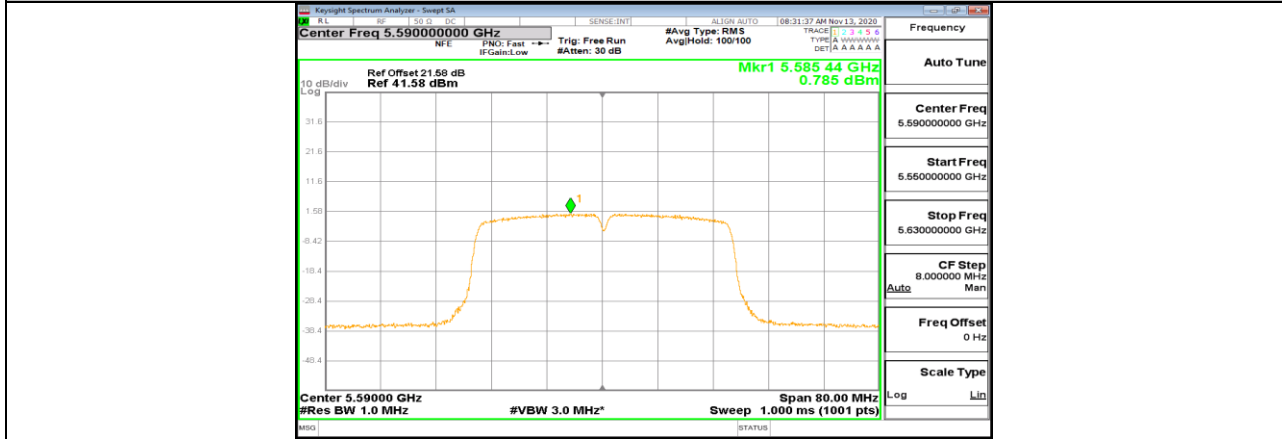
11N40MIMO_Ant2_5310



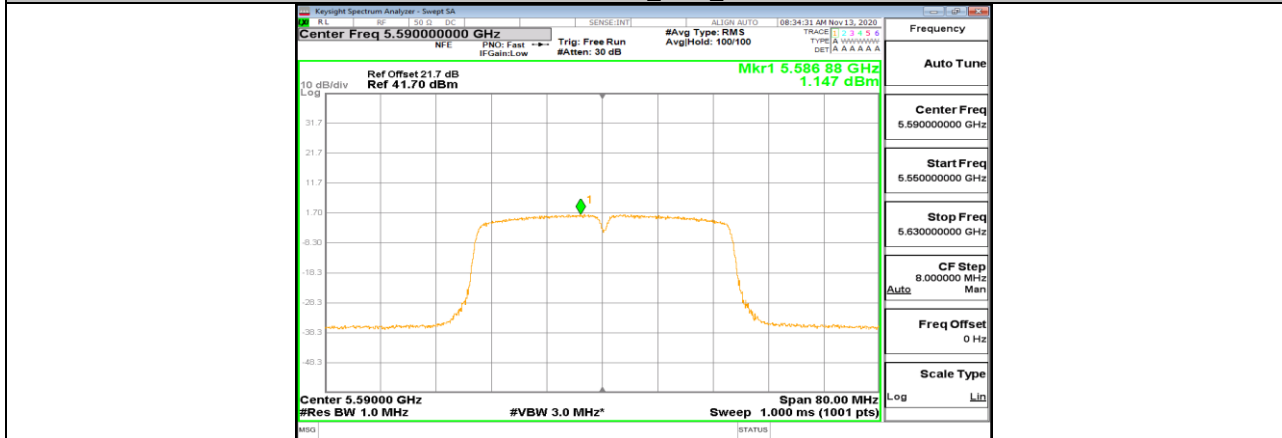
11N40MIMO_Ant1_5510



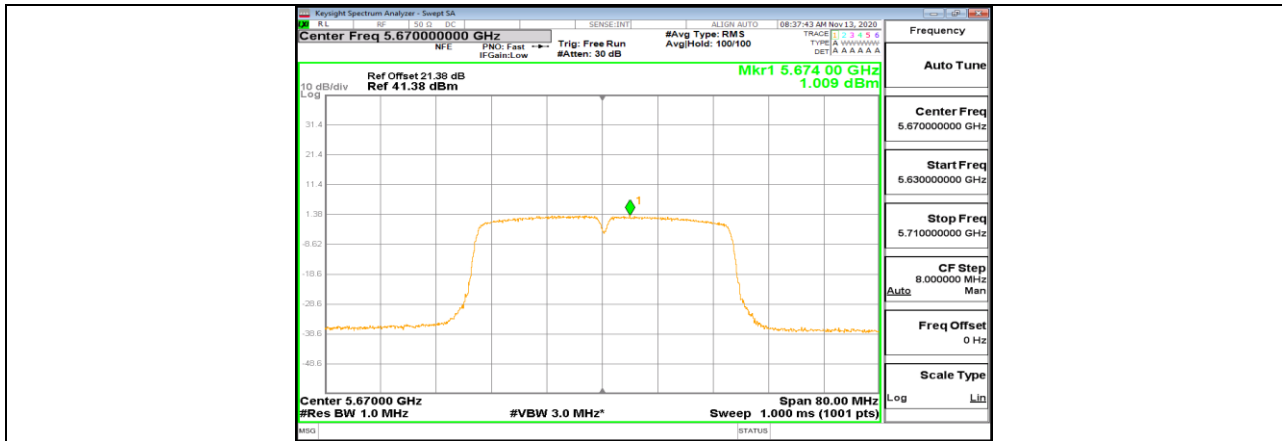
11N40MIMO_Ant2_5510



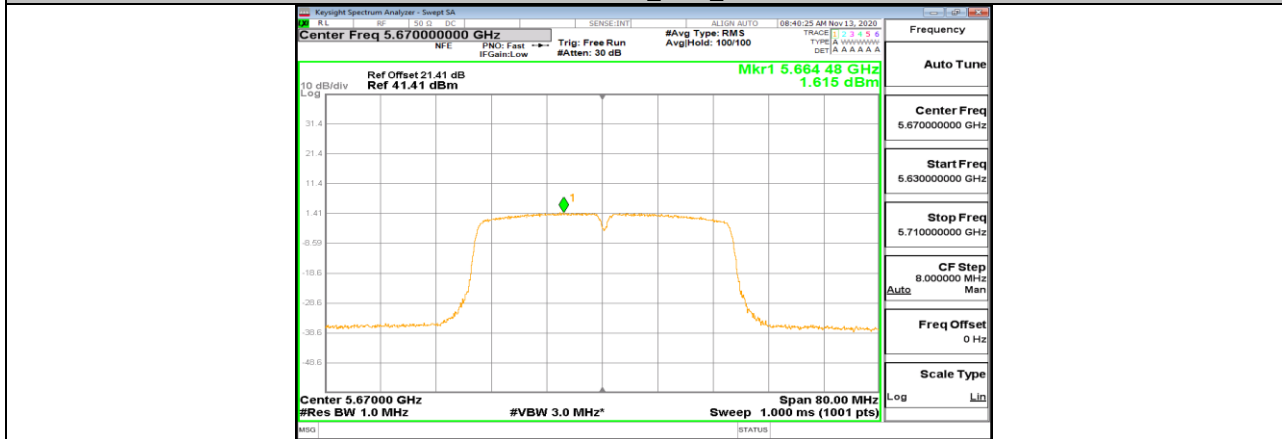
11N40MIMO_Ant1_5590



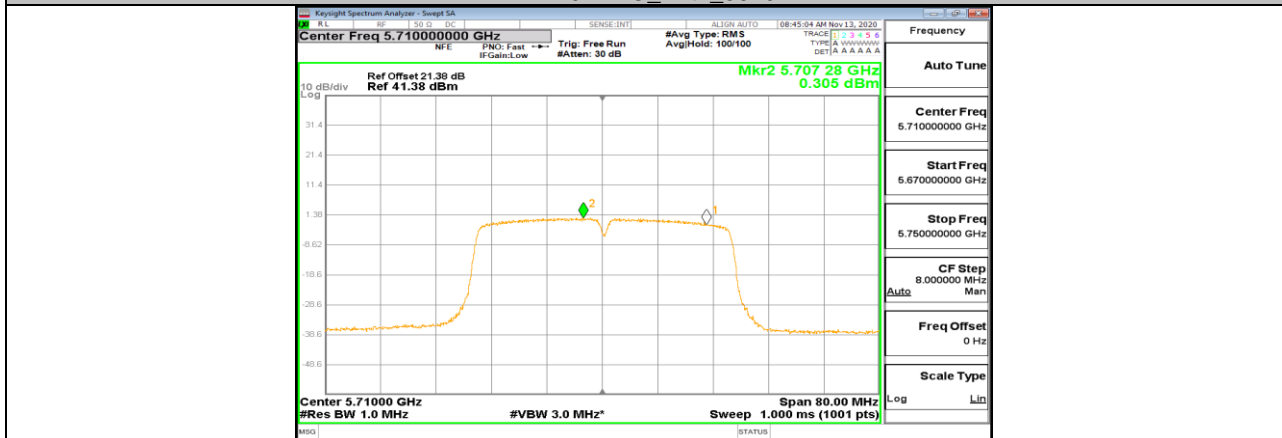
11N40MIMO_Ant2_5590



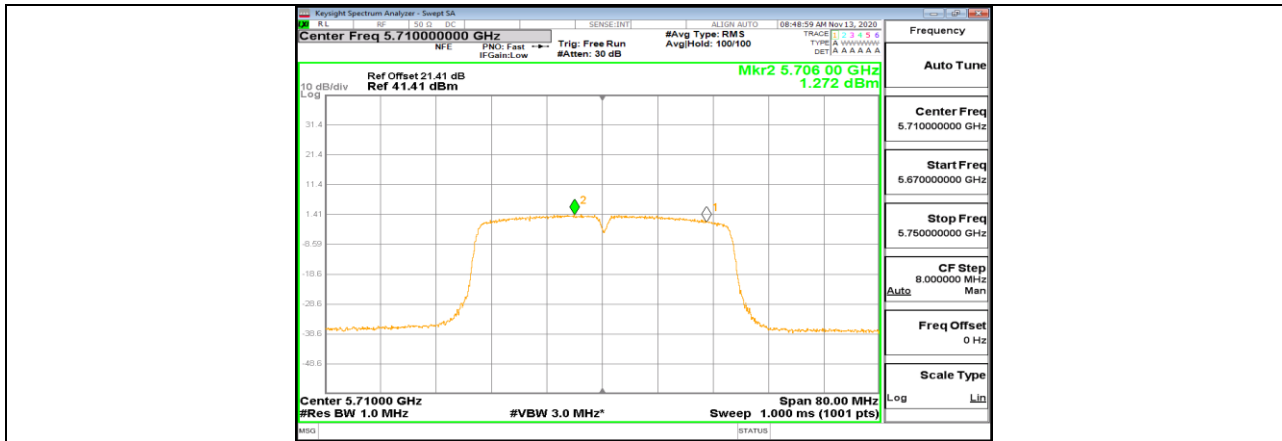
11N40MIMO_Ant1_5670



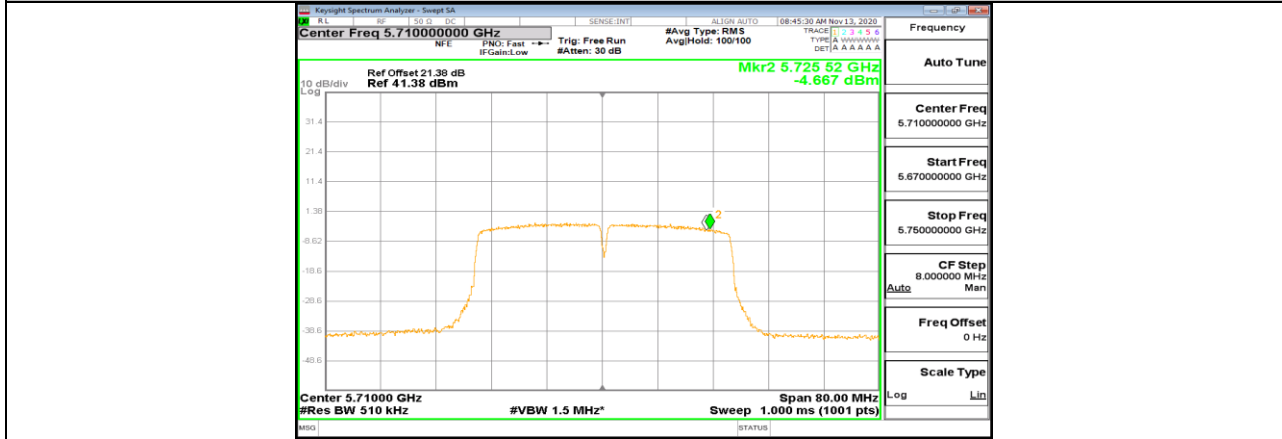
11N40MIMO_Ant2_5670



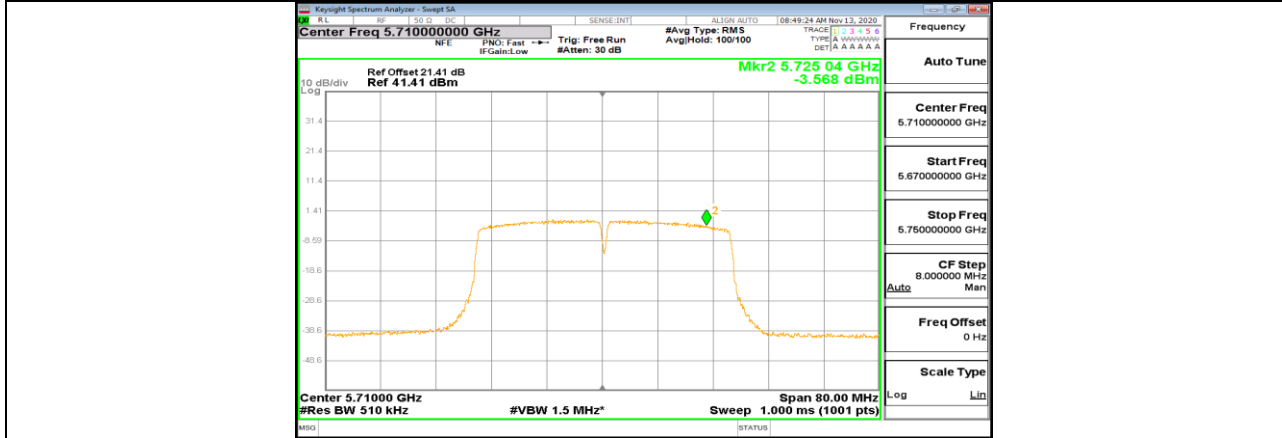
11N40MIMO_Ant1_5710_UNII-2C



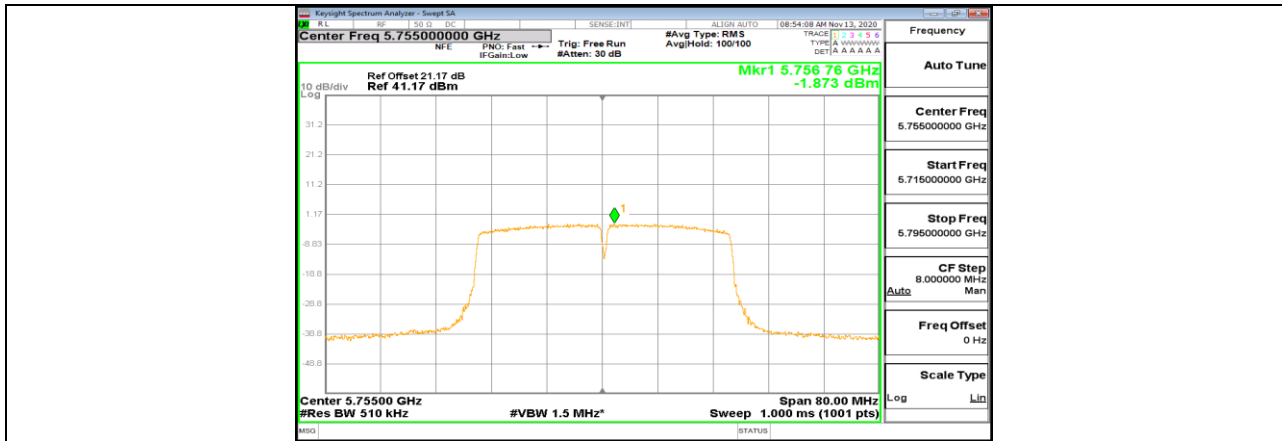
11N40MIMO_Ant2_5710_UNII-2C



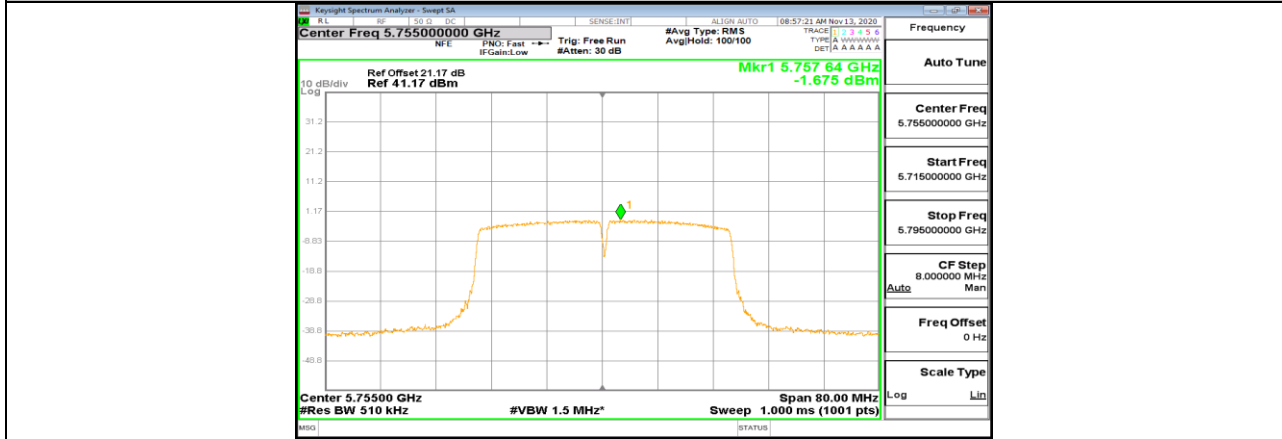
11N40MIMO_Ant1_5710_UNII-3



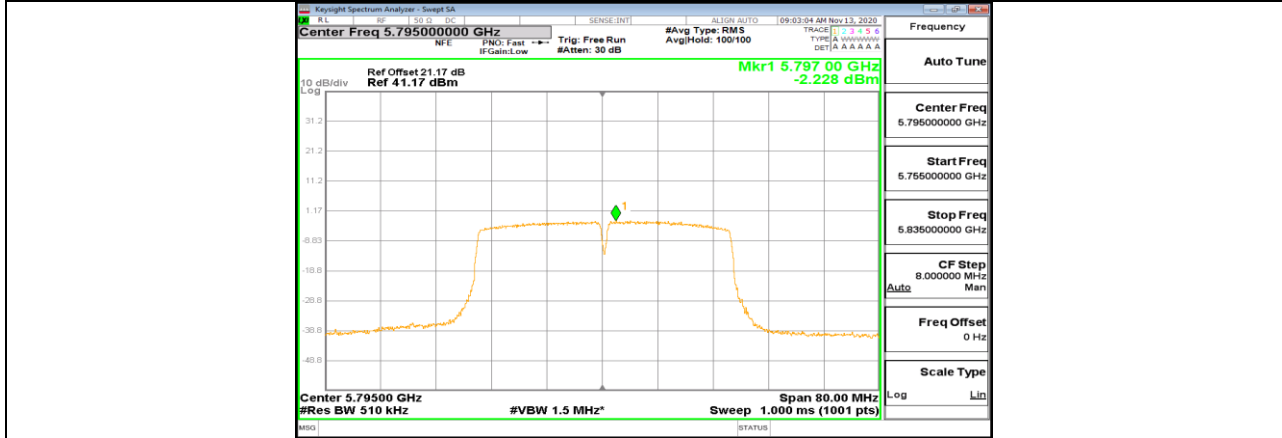
11N40MIMO_Ant2_5710_UNII-3



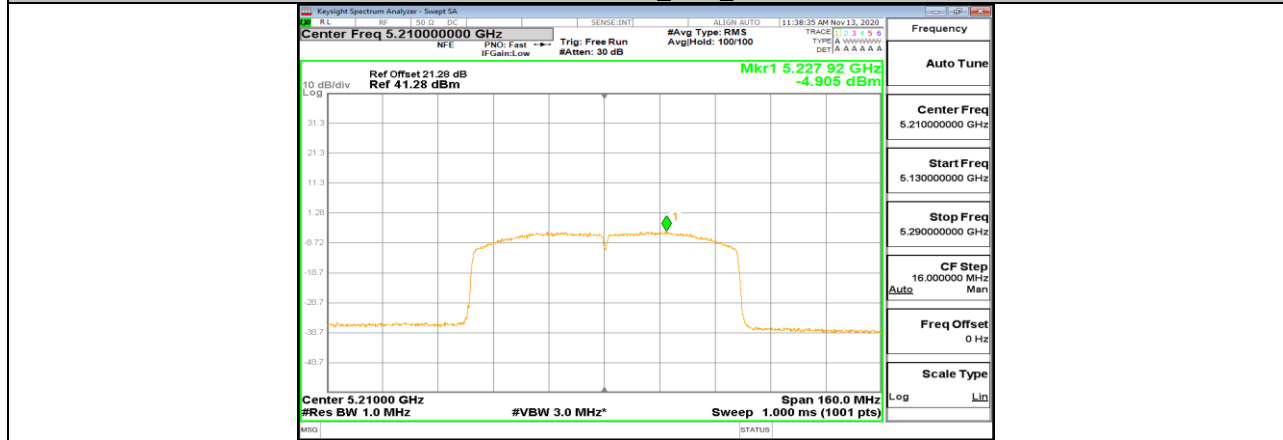
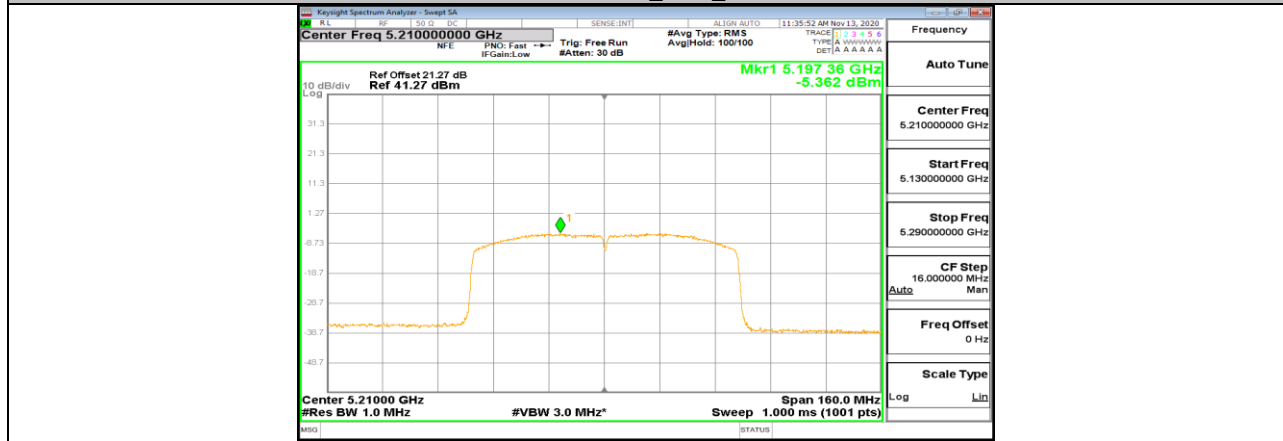
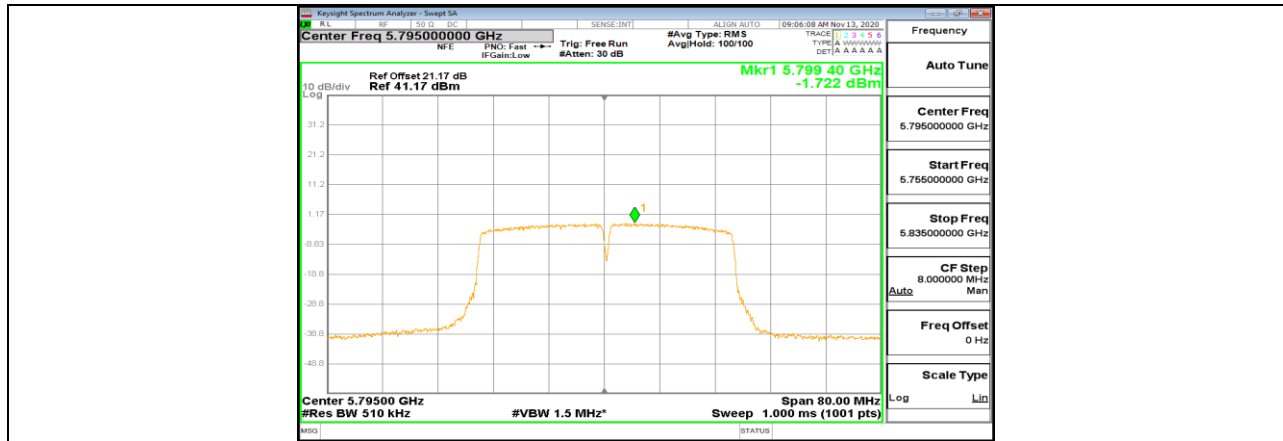
11N40MIMO_Ant1_5755

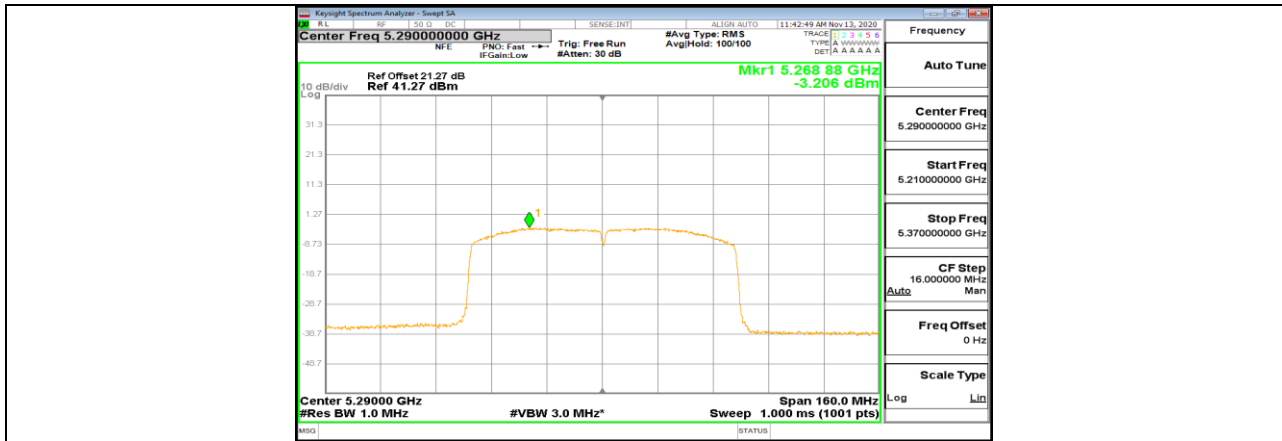


11N40MIMO_Ant2_5755

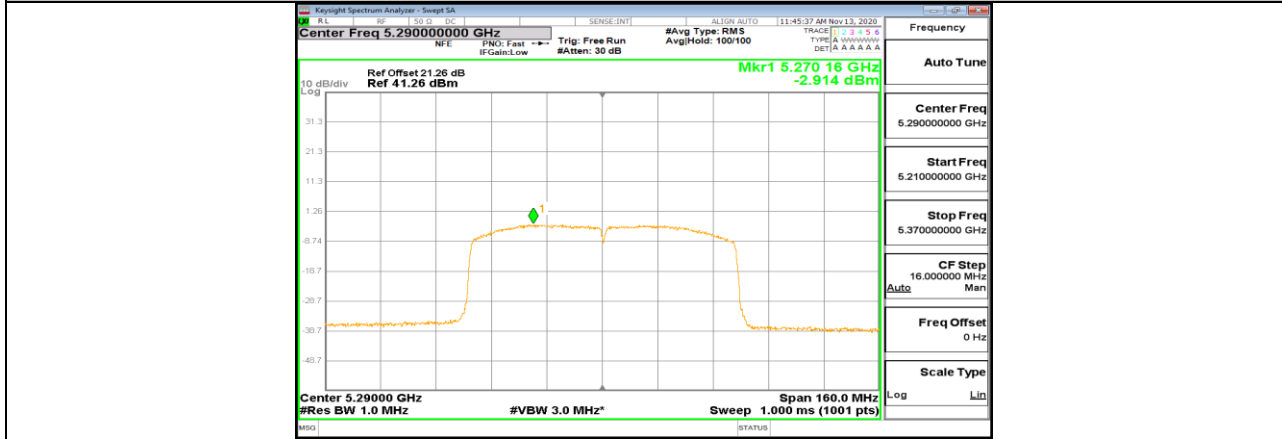


11N40MIMO_Ant1_5795

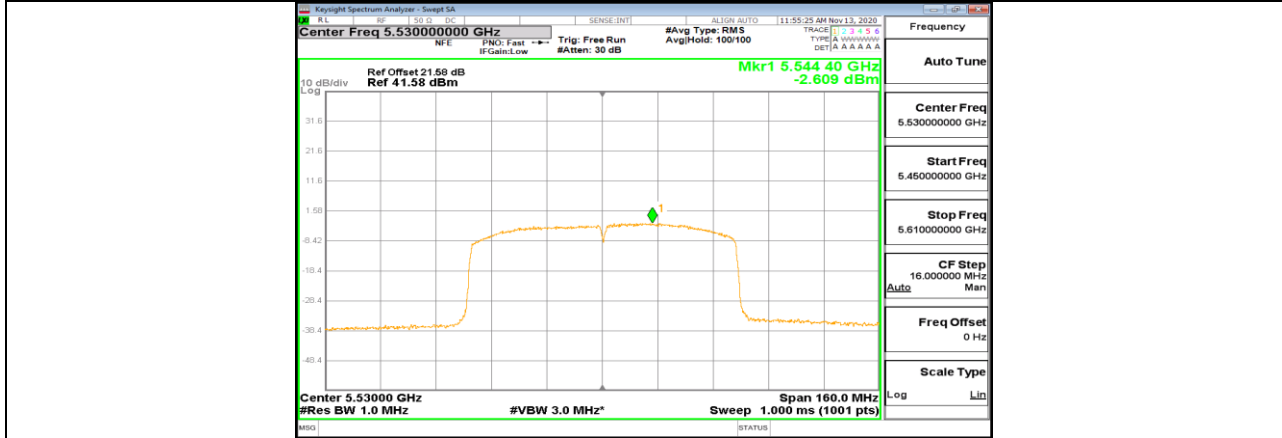




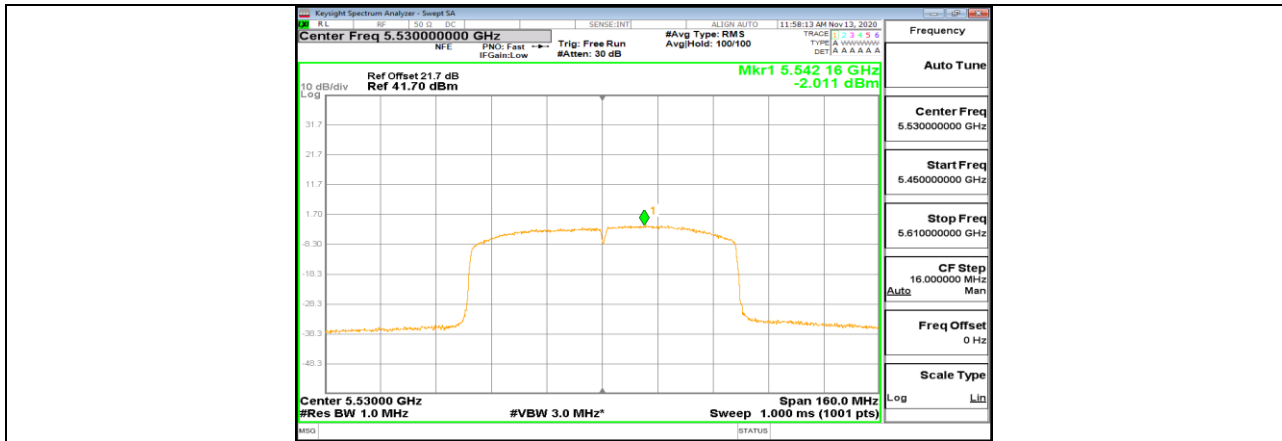
11AC80MIMO_Ant1_5290



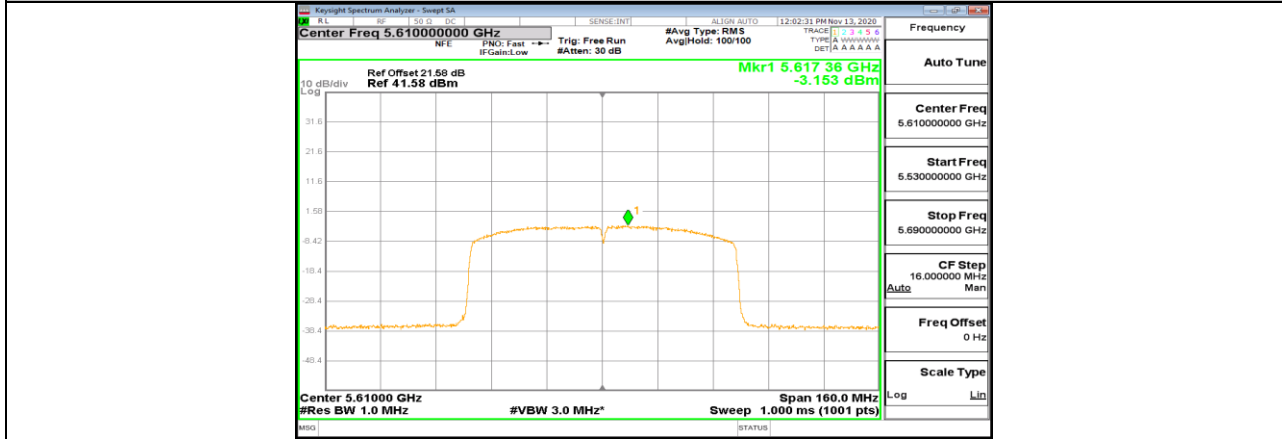
11AC80MIMO_Ant2_5290



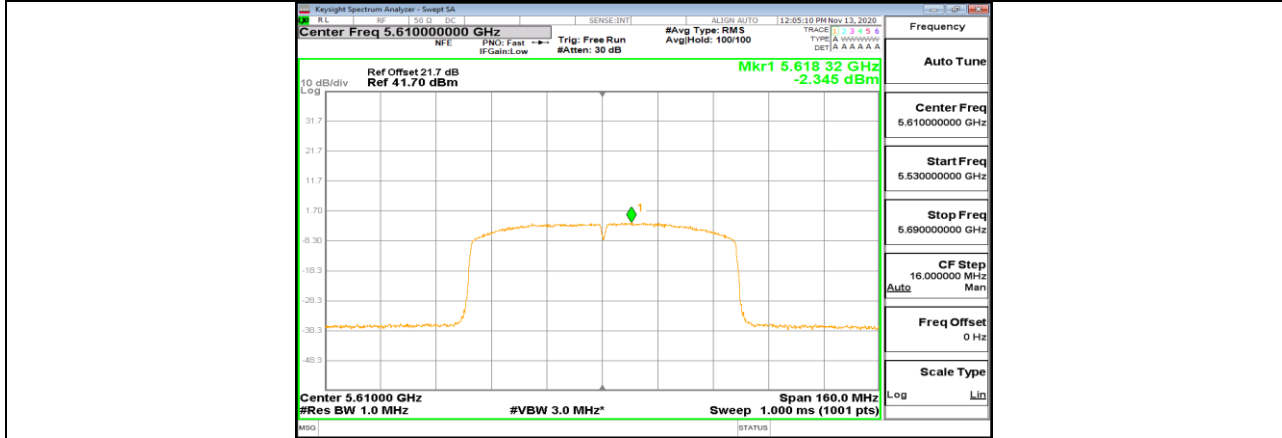
11AC80MIMO_Ant1_5530



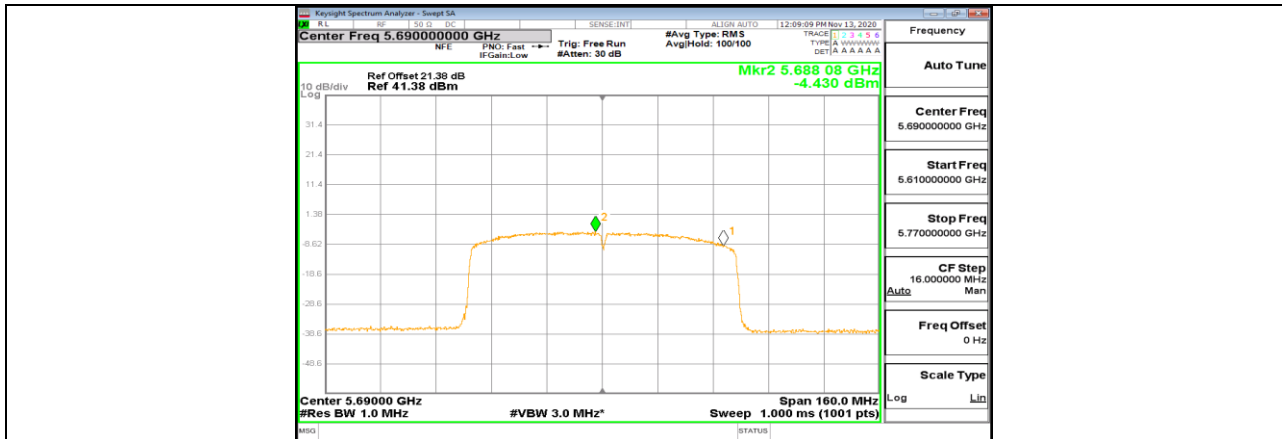
11AC80MIMO_Ant2_5530



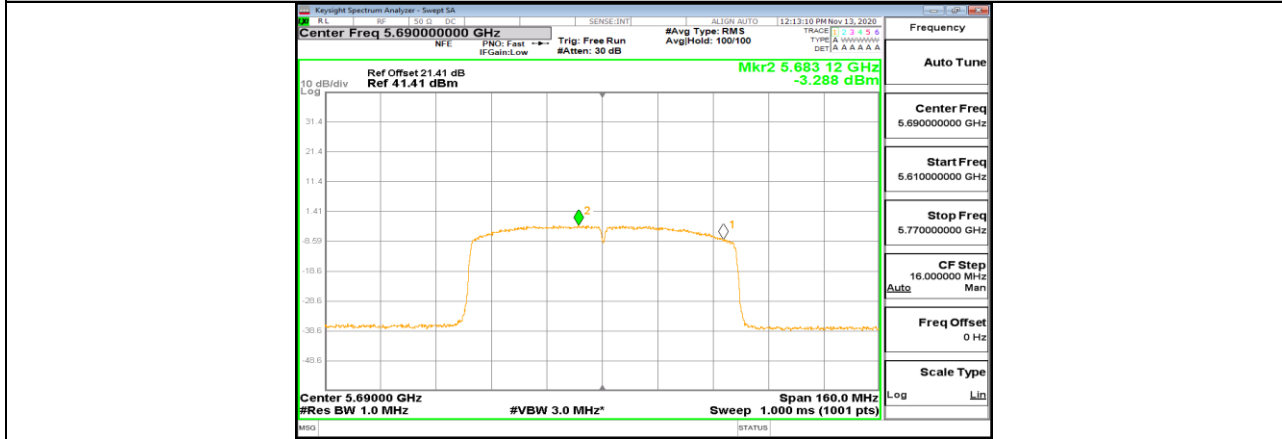
11AC80MIMO_Ant1_5610



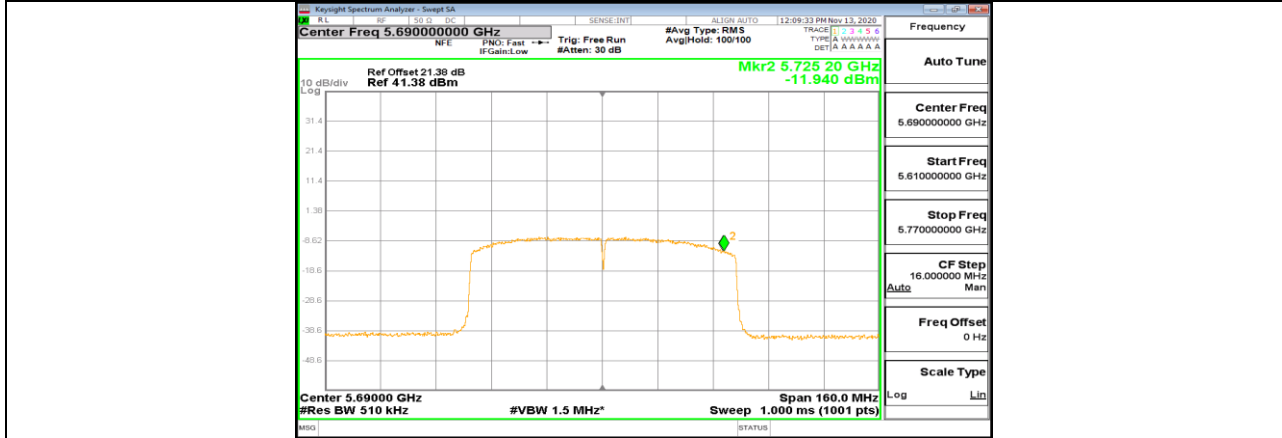
11AC80MIMO_Ant2_5610



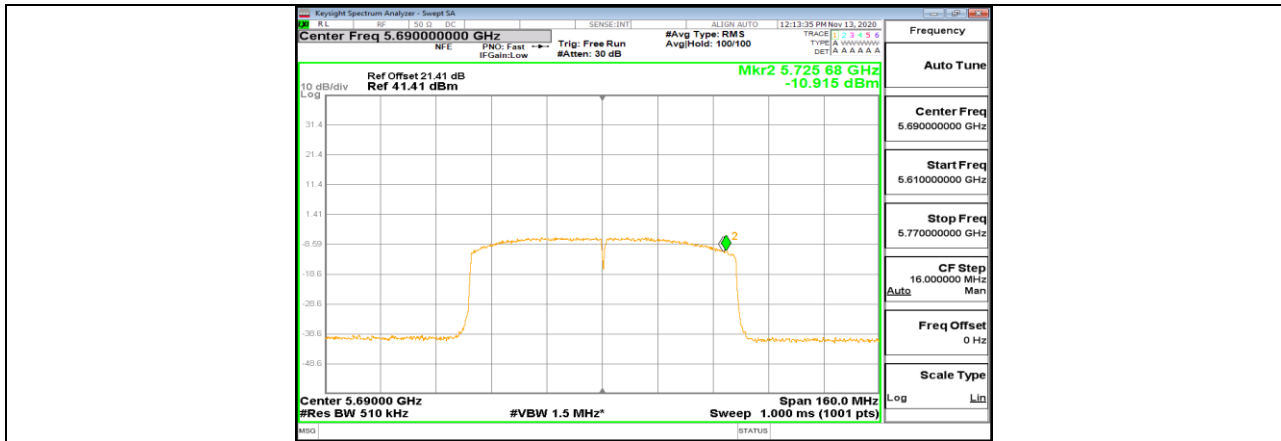
11AC80MIMO_Ant1_5690_UNII-2C



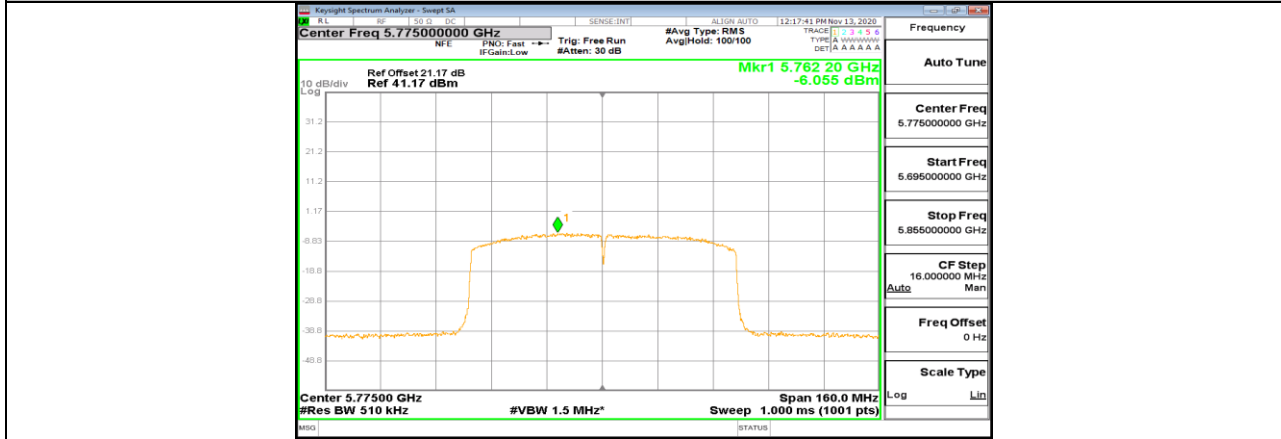
11AC80MIMO_Ant2_5690_UNII-2C



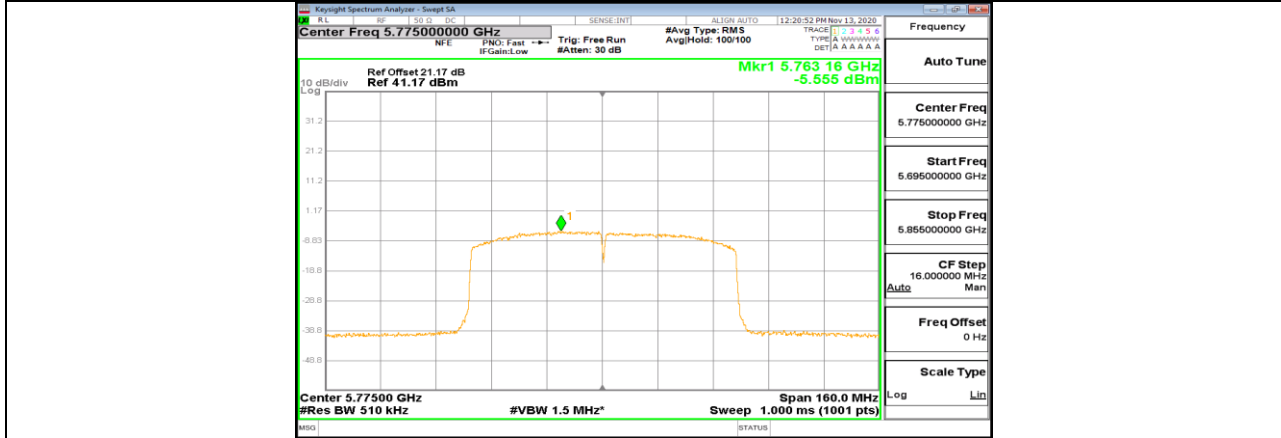
11AC80MIMO_Ant1_5690_UNII-3



11AC80MIMO_Ant2_5690_UNII-3



11AC80MIMO_Ant1_5775



11AC80MIMO_Ant2_5775



Appendix D: Duty Cycle Test Result

Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A	100.1	100.1	1	100	0	0.01	0.01
11N20MIMO	100.1	100.1	1	100	0	0.01	0.01
11N40MIMO	100.1	100.1	1	100	0	0.01	0.01
11AC80MIMO	100.1	100.1	1	100	0	0.01	0.01

Note:

Duty Cycle Correction Factor=10log (1/x).

Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.