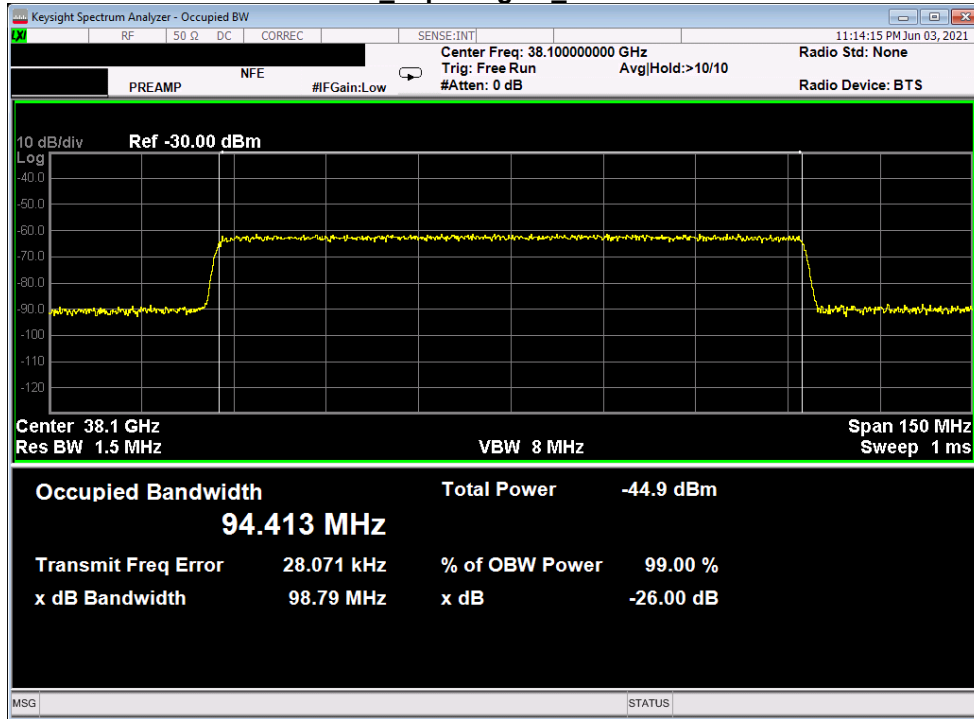


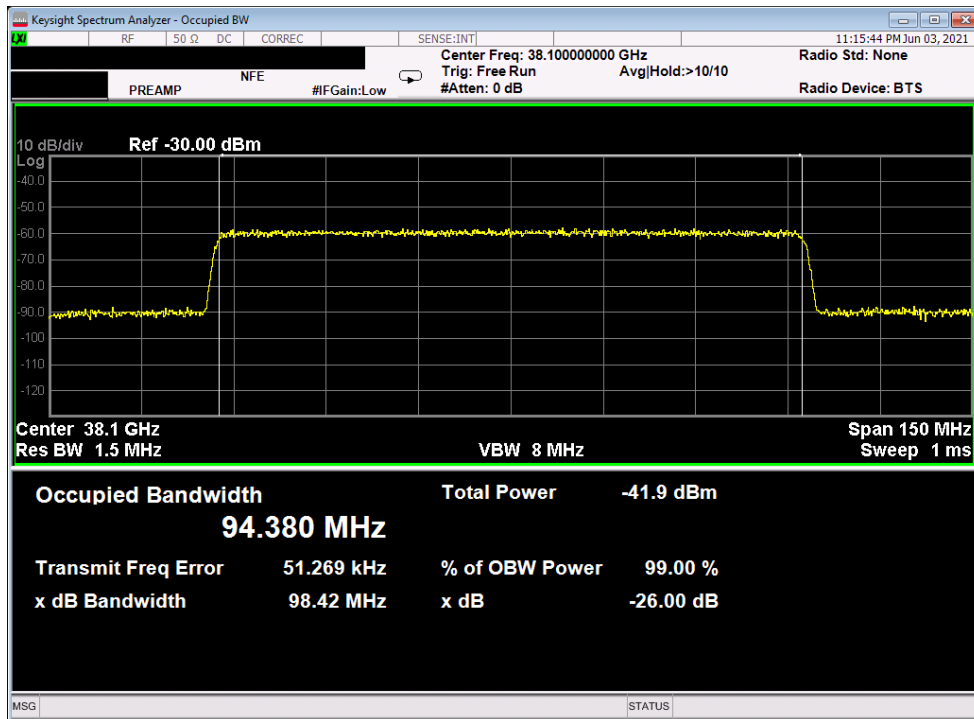
## **Annex B**

### **Input vs Output**

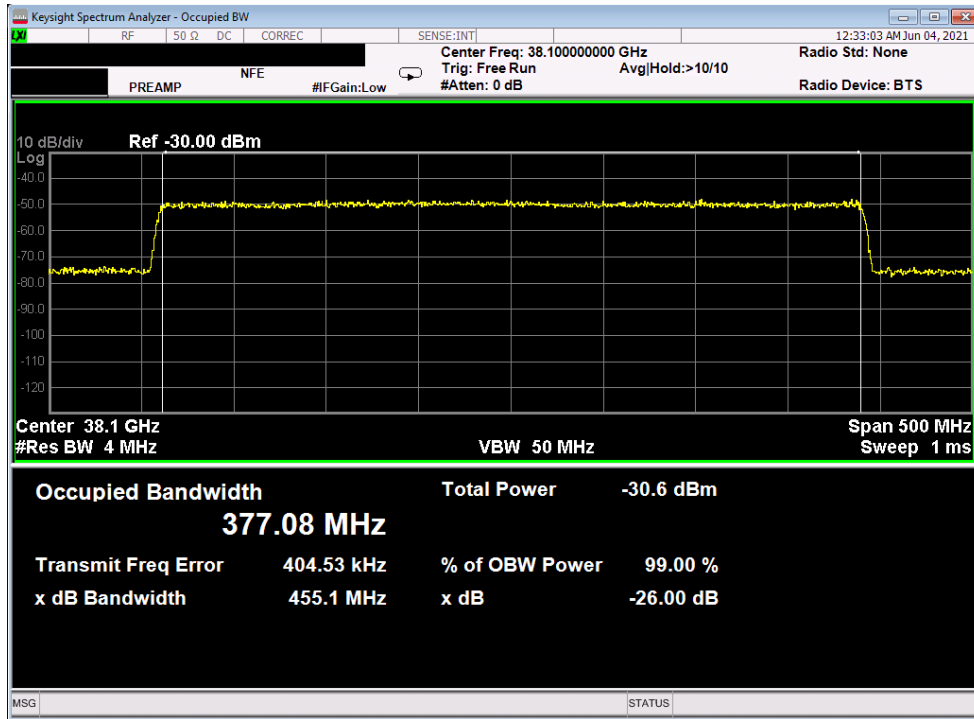
### OCC BW\_ Input signal\_100 MHz BW



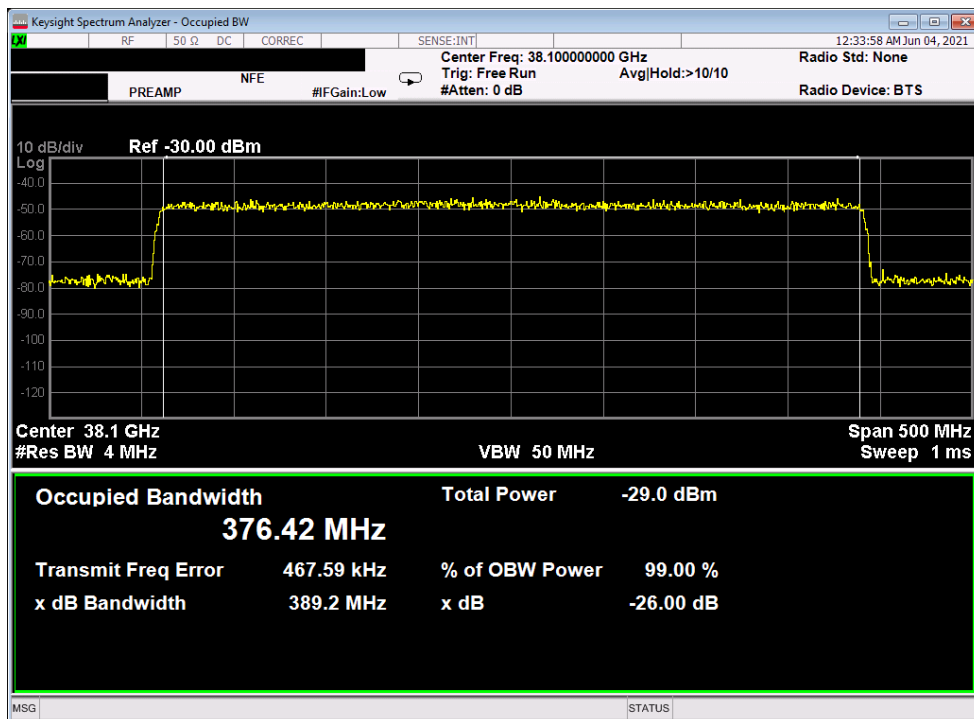
### OCC BW\_ Input signal\_100 MHz BW\_ Pin + 3 dB



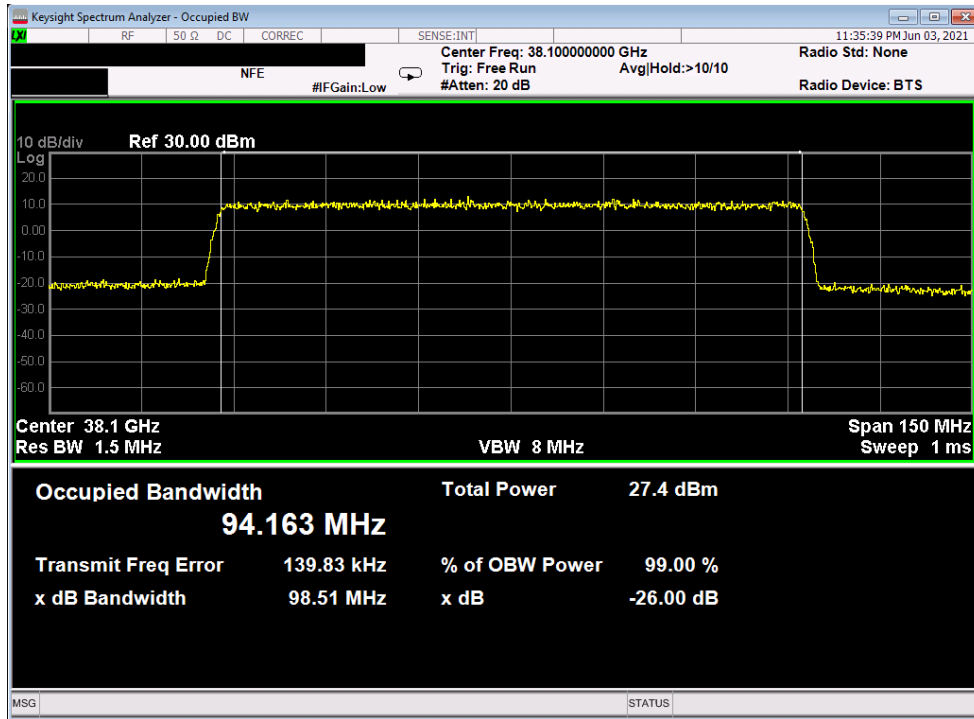
### OCC BW\_ Input signal\_400 MHz BW



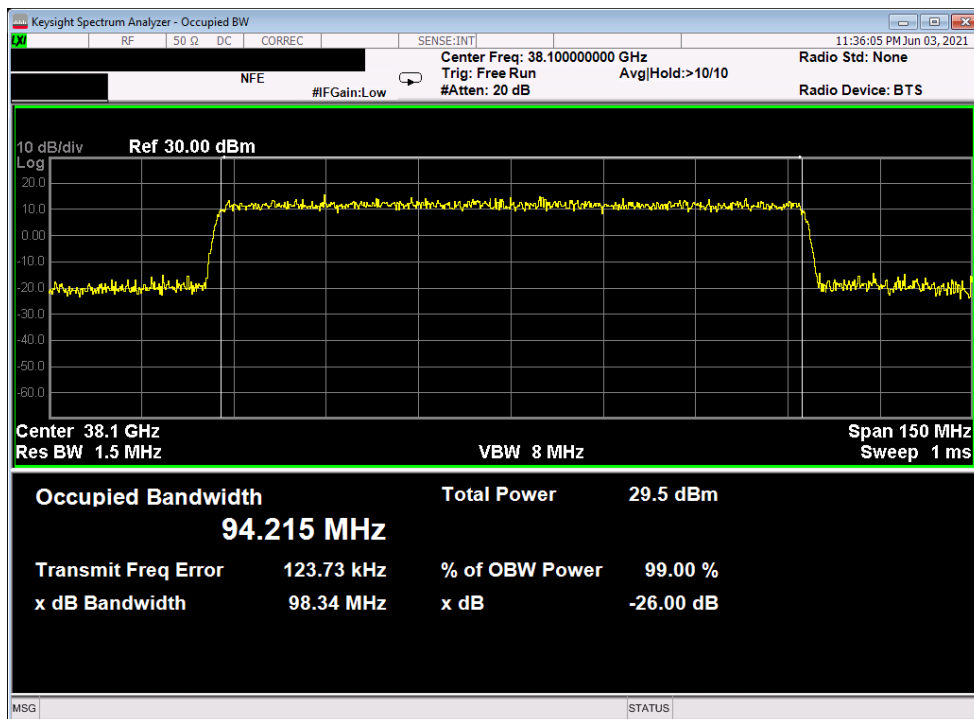
### OCC BW\_ Input signal\_400 MHz BW\_ Pin + 3 dB



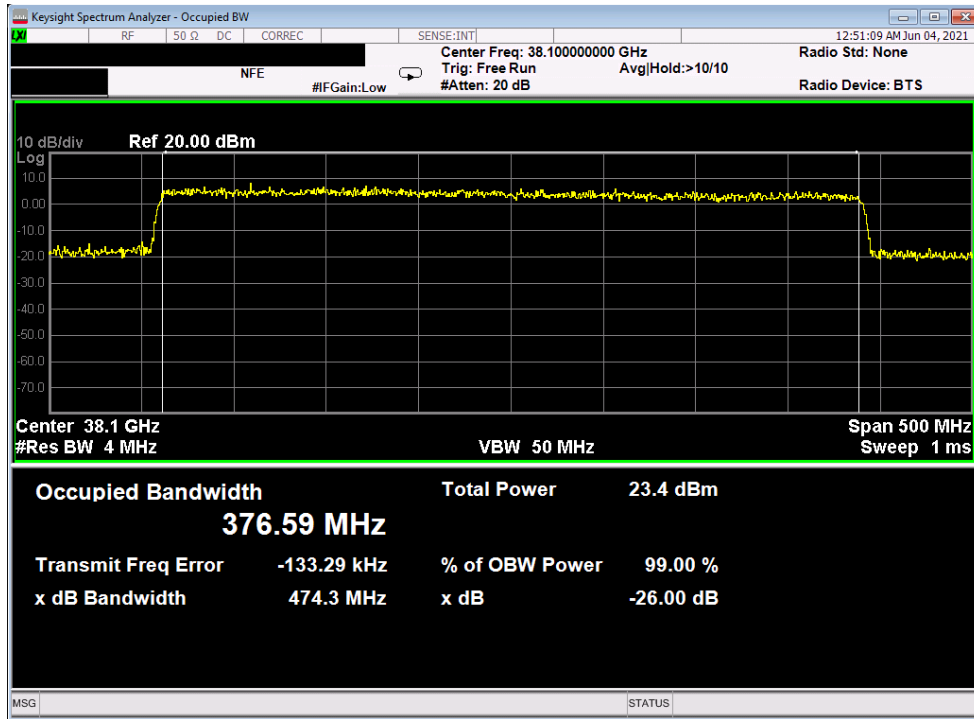
### OCC BW\_MIMO DL Input - MIMO DL Output\_100 MHz BW\_p2130011



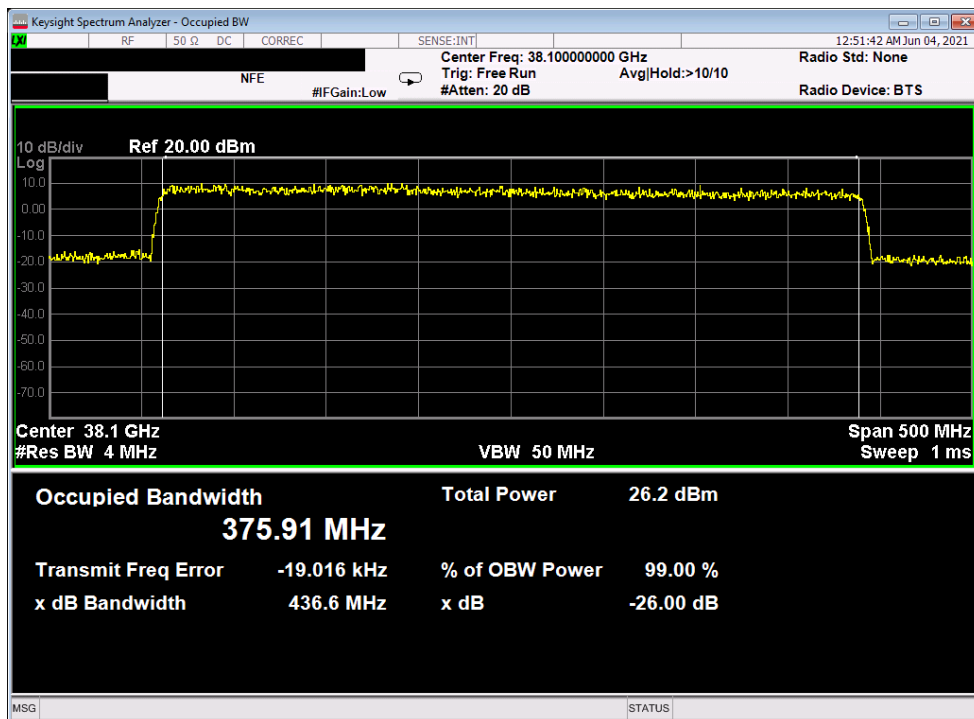
### OCC BW\_MIMO DL Input - MIMO DL Output\_100 MHz BW\_p2130011\_Pin + 3 dB



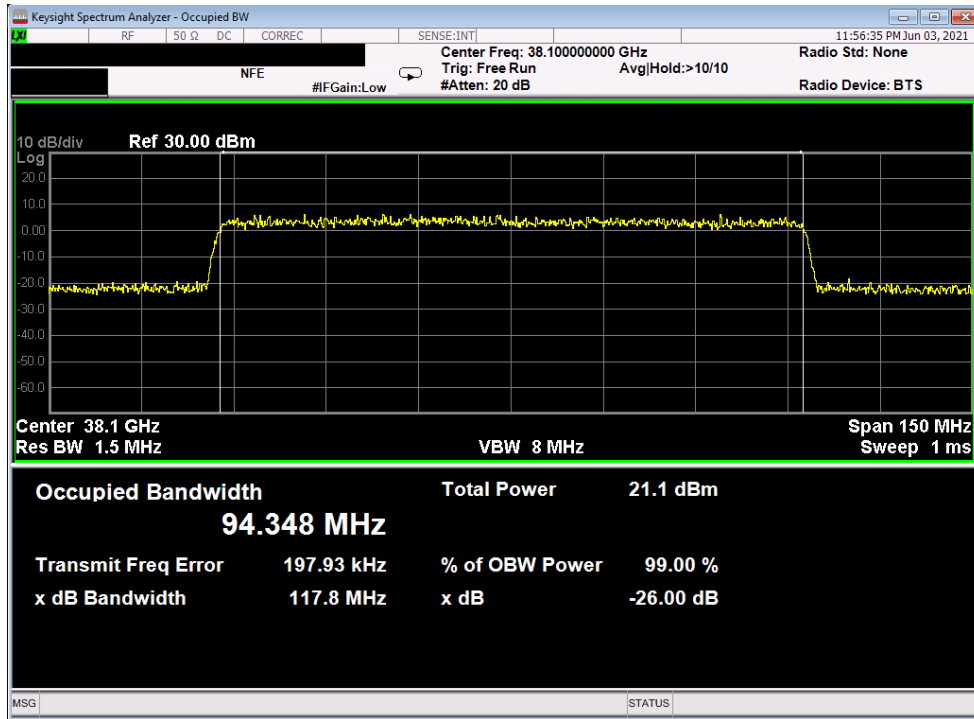
### OCC BW\_MIMO DL Input - MIMO DL Output\_400 MHz BW\_p2130011



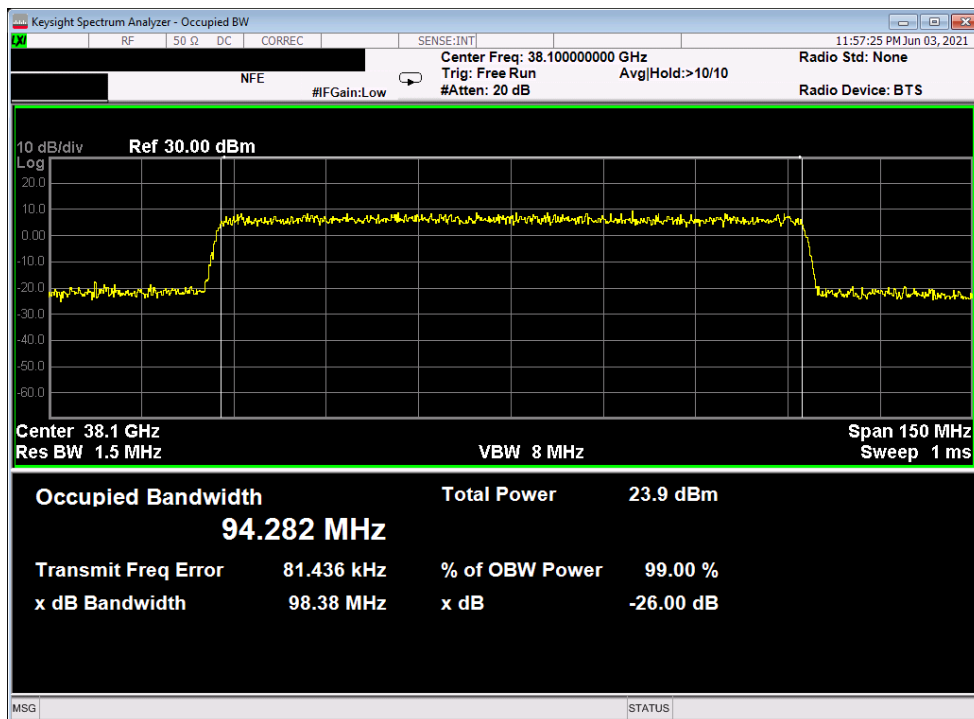
### OCC BW\_MIMO DL Input - MIMO DL Output\_400 MHz BW\_p2130011\_Pin + 3 dB



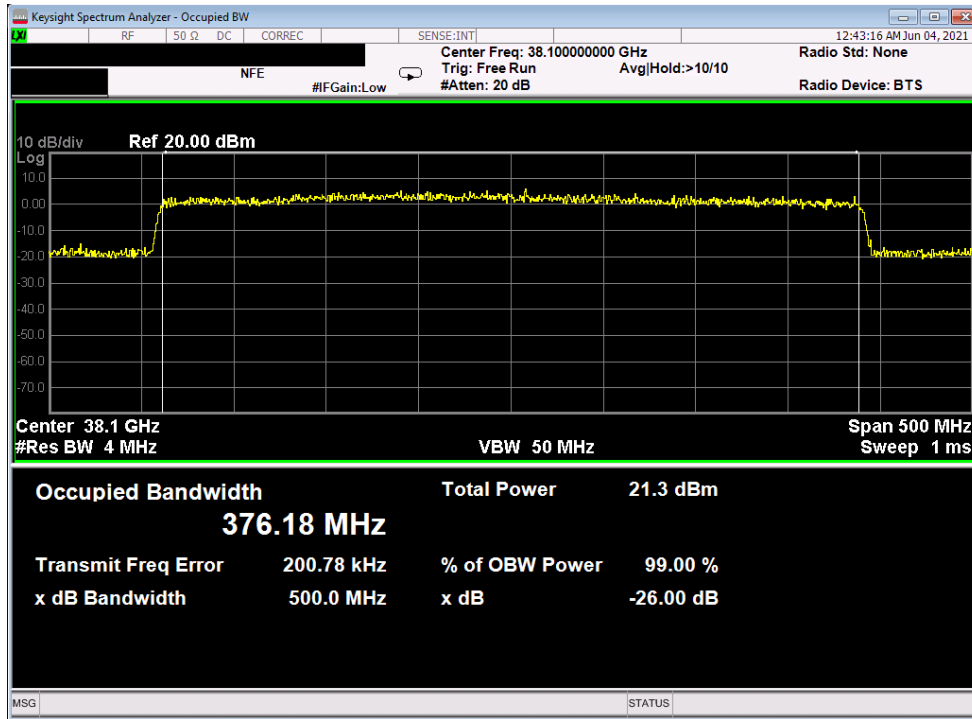
### OCC BW\_MIMO UL Input - MIMO UL Output\_100 MHz BW\_p2130011



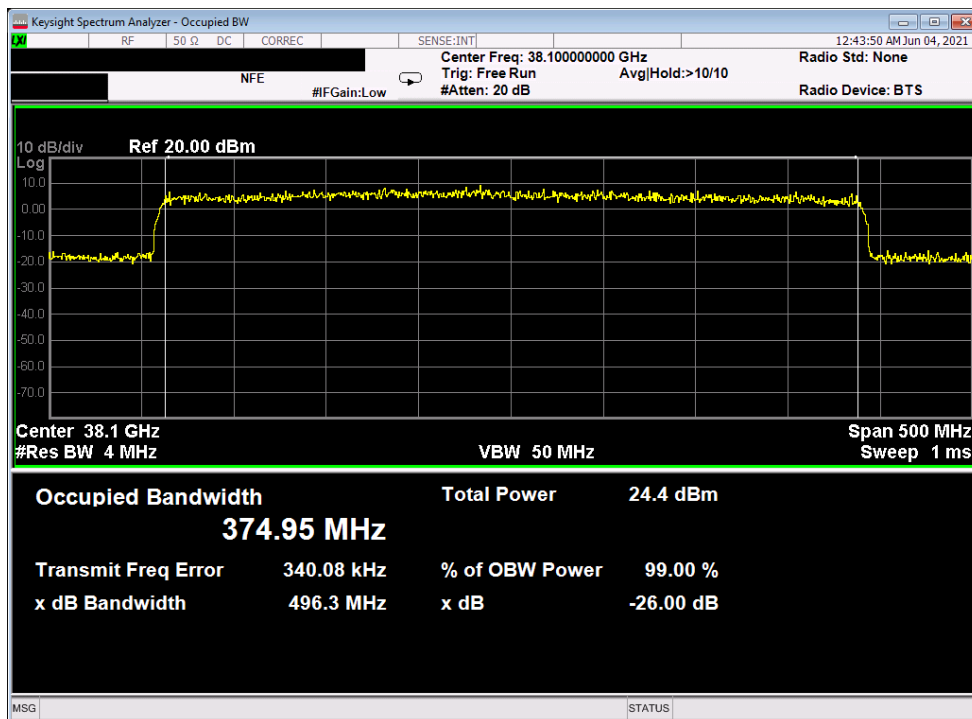
### OCC BW\_MIMO UL Input - MIMO UL Output\_100 MHz BW\_p2130011\_Pin + 3 dB



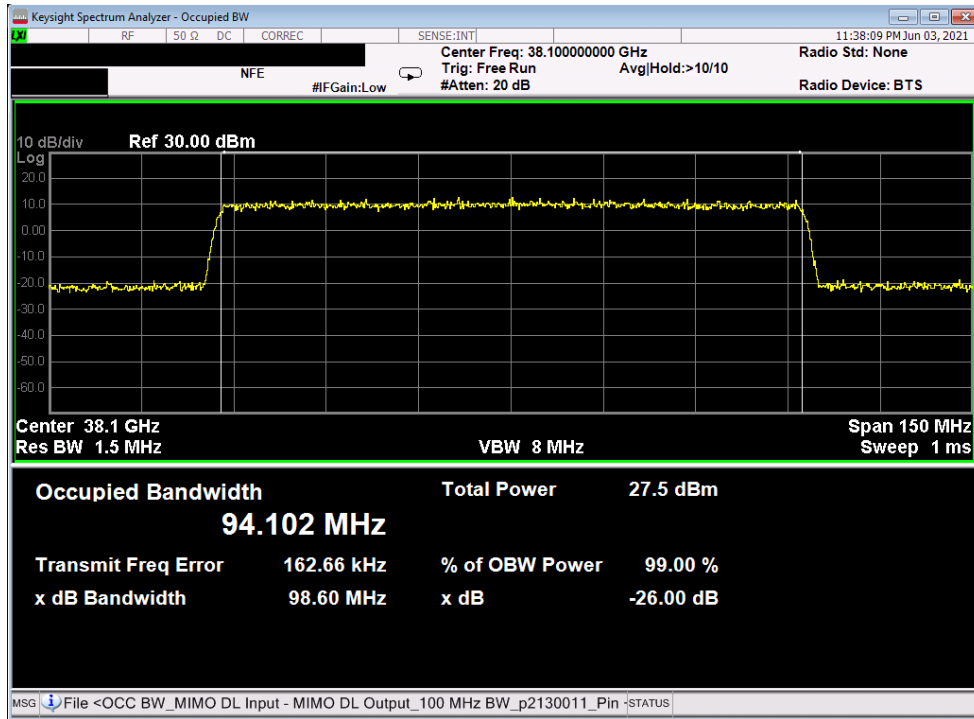
### OCC BW\_MIMO UL Input - MIMO UL Output\_400 MHz BW\_p2130011



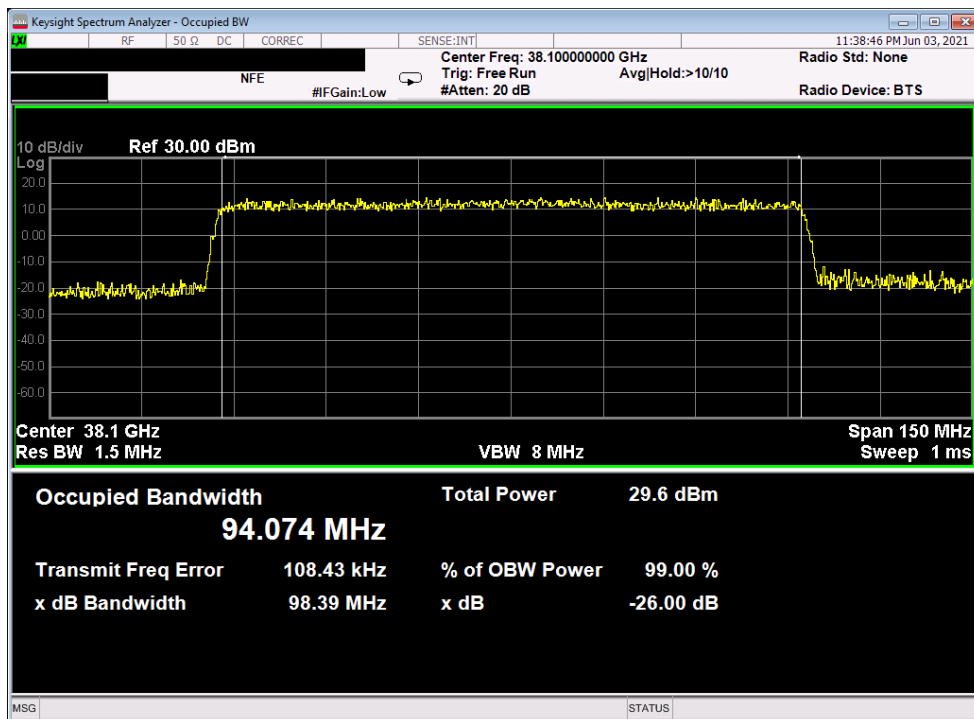
### OCC BW\_MIMO UL Input - MIMO UL Output\_400 MHz BW\_p2130011\_Pin + 3 dB



### OCC BW\_SISO DL Input - SISO DL Output\_100 MHz BW\_p2130011

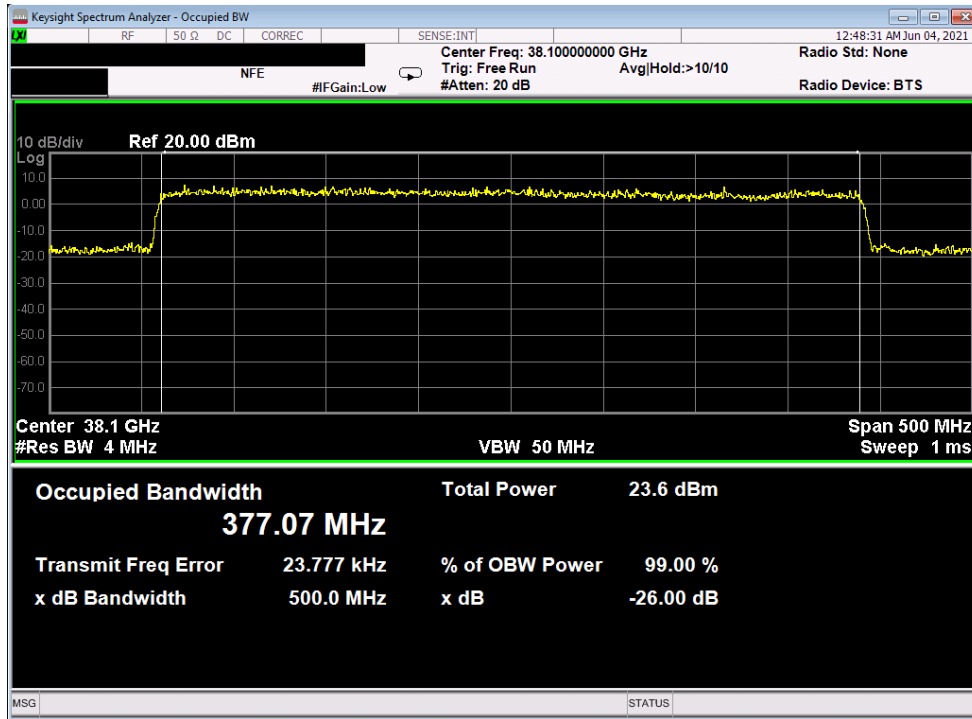


### OCC BW\_SISO DL Input - SISO DL Output\_100 MHz BW\_p2130011\_Pin + 3 dB

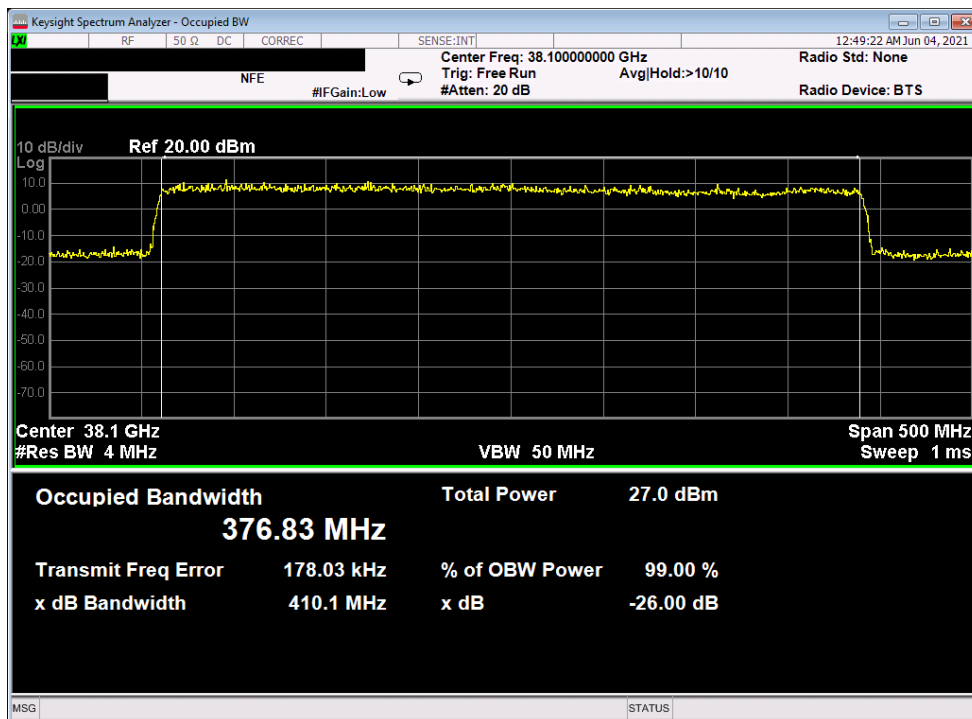




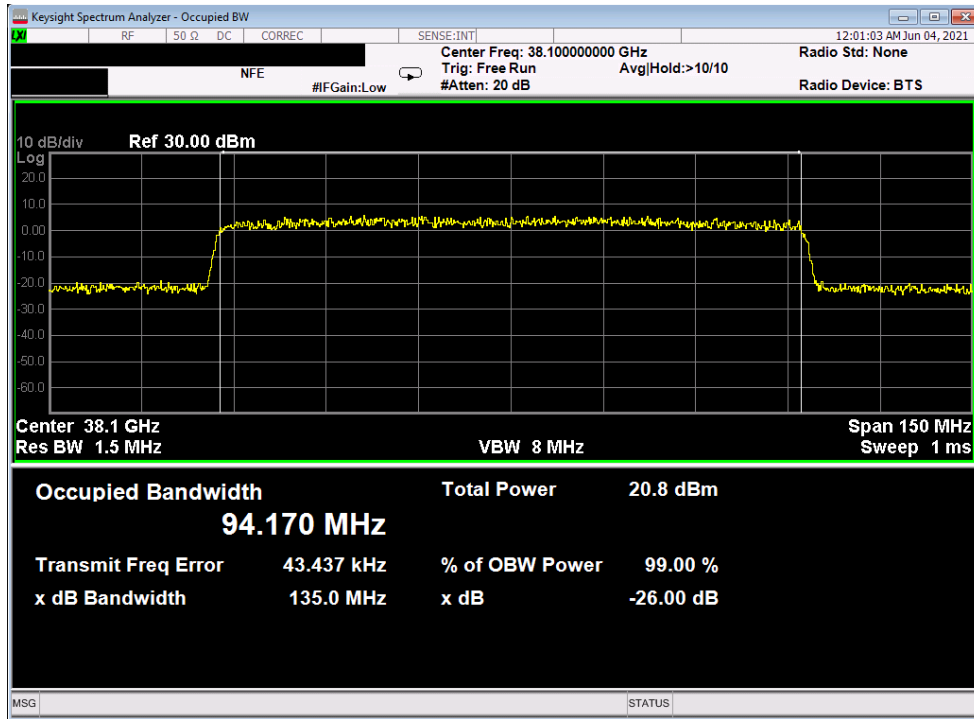
### OCC BW\_SISO DL Input - SISO DL Output\_400 MHz BW\_p2130011



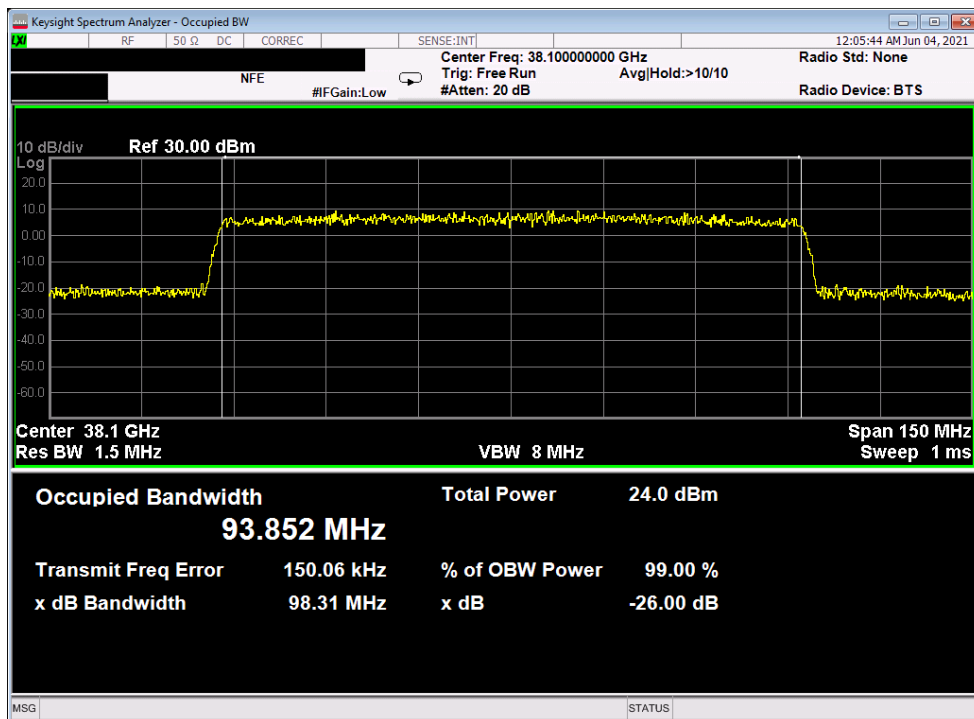
### OCC BW\_SISO DL Input - SISO DL Output\_400 MHz BW\_p2130011\_Pin + 3 dB



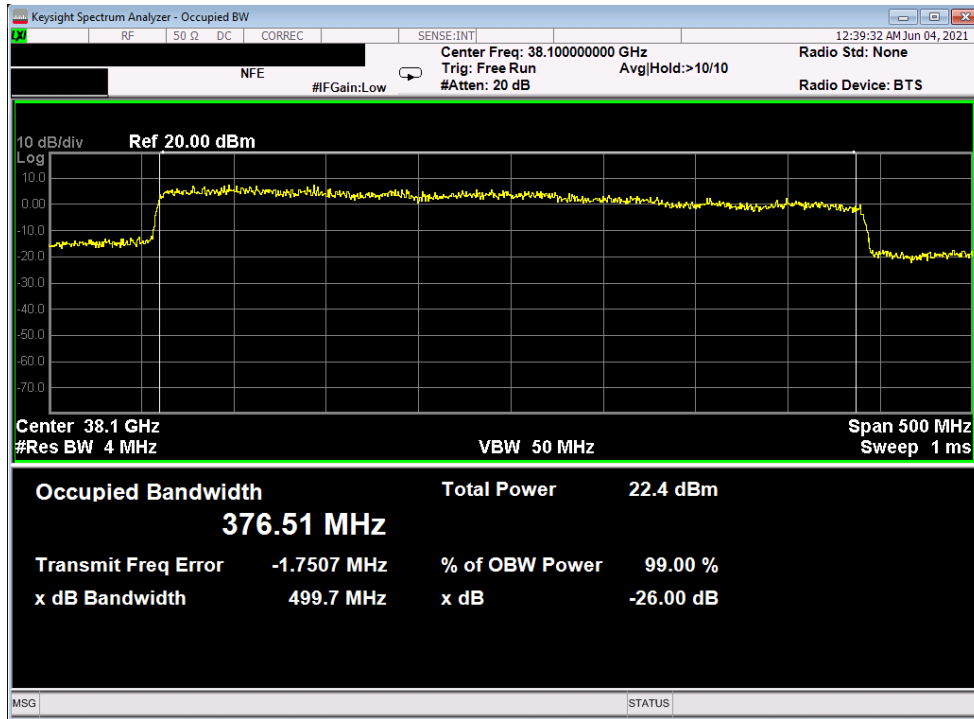
### OCC BW\_SISO UL Input - SISO UL Output\_100 MHz BW\_p2130011



### OCC BW\_SISO UL Input - SISO UL Output\_100 MHz BW\_p2130011\_Pin + 3 dB



### OCC BW\_SISO UL Input - SISO UL Output\_400 MHz BW\_p2130011



### OCC BW\_SISO UL Input - SISO UL Output\_400 MHz BW\_p2130011\_Pin + 3 dB

