

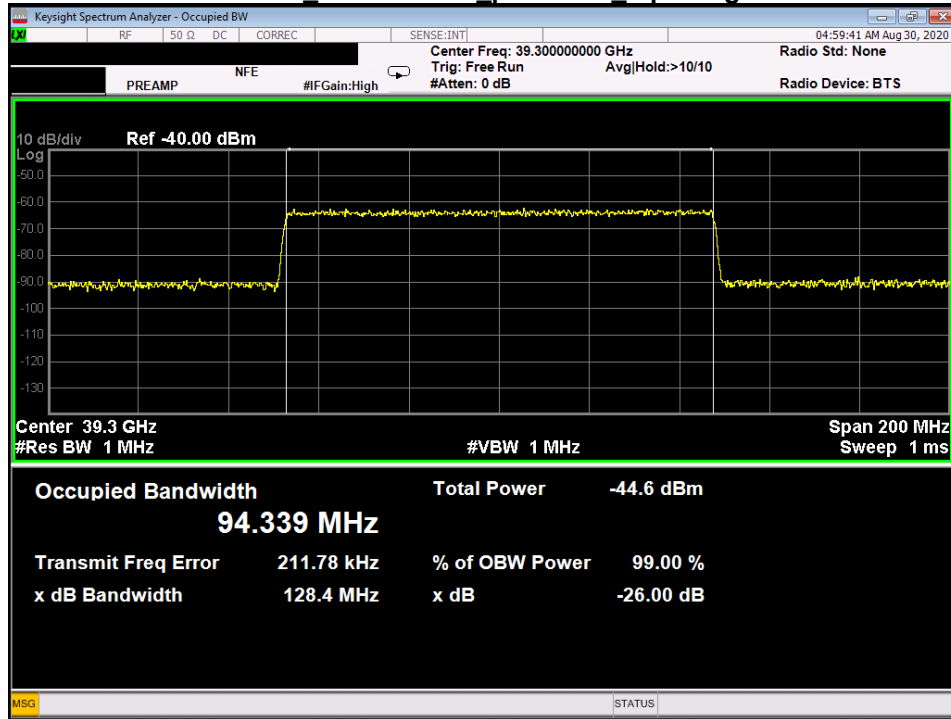


## **Annex B**

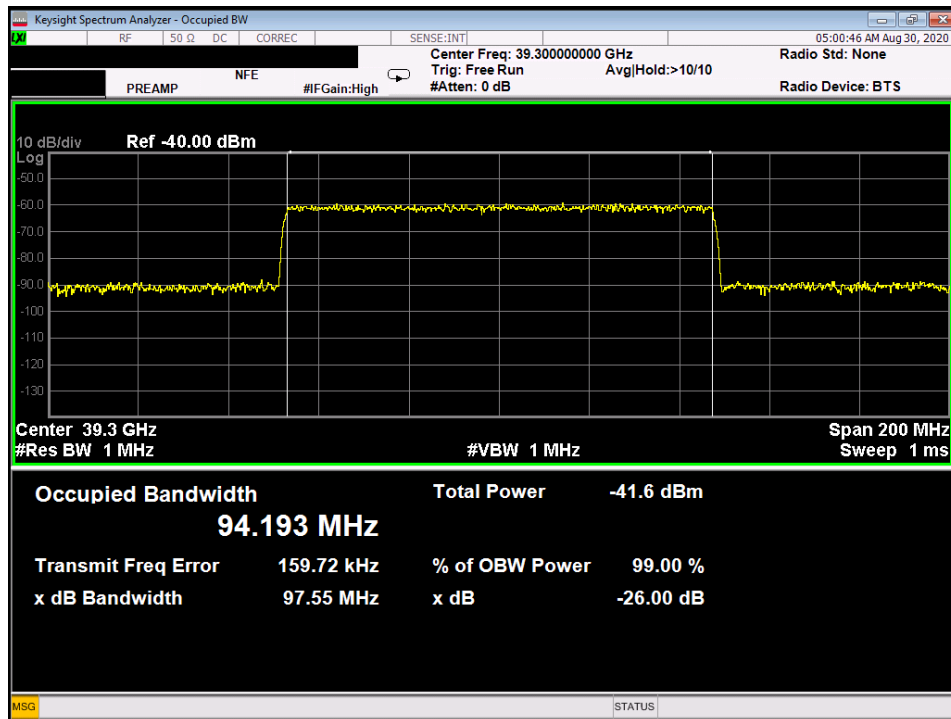
### **Input vs Output Signal Comparison**



### OCC BW\_100 MHz BW\_p2070006\_Input Signal

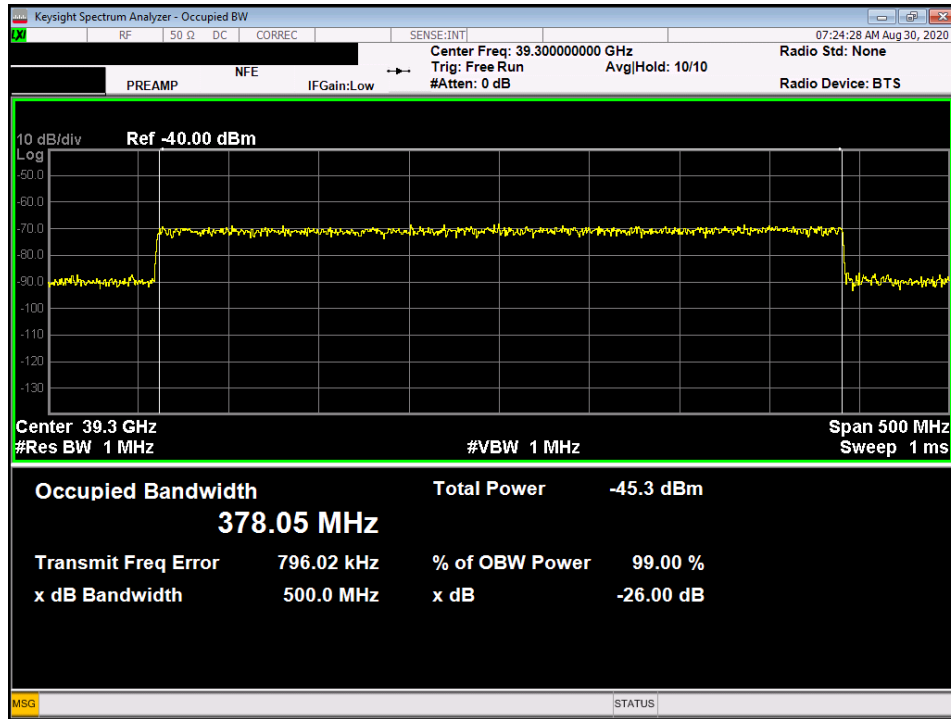


### OCC BW\_100 MHz BW\_p2070006\_Input Signal\_Pin + 3 dB

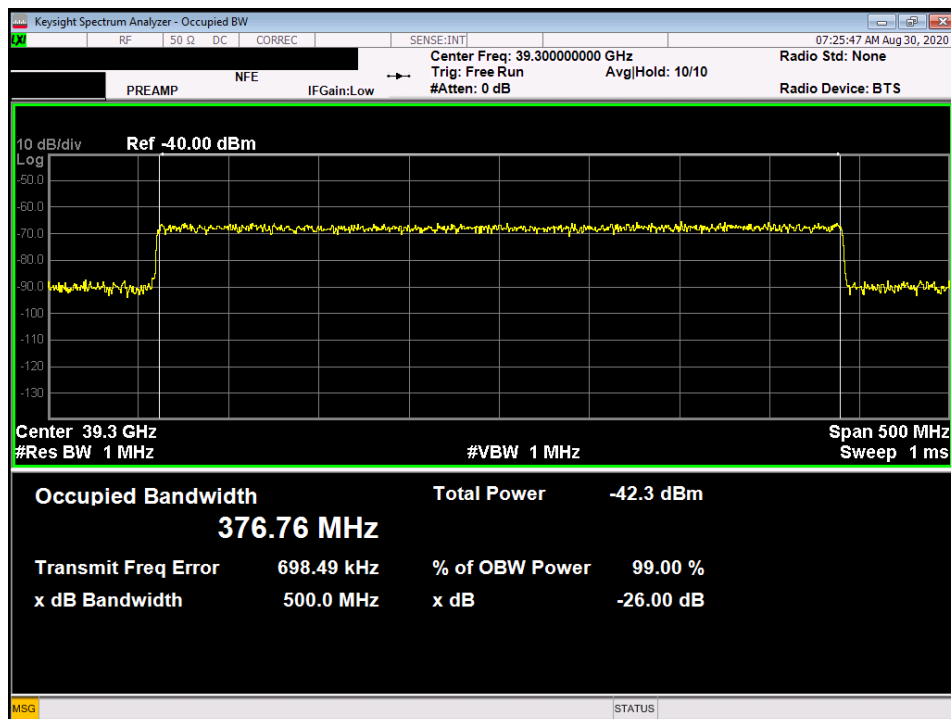




### OCC BW\_400 MHz BW\_p2070006\_Input Signal

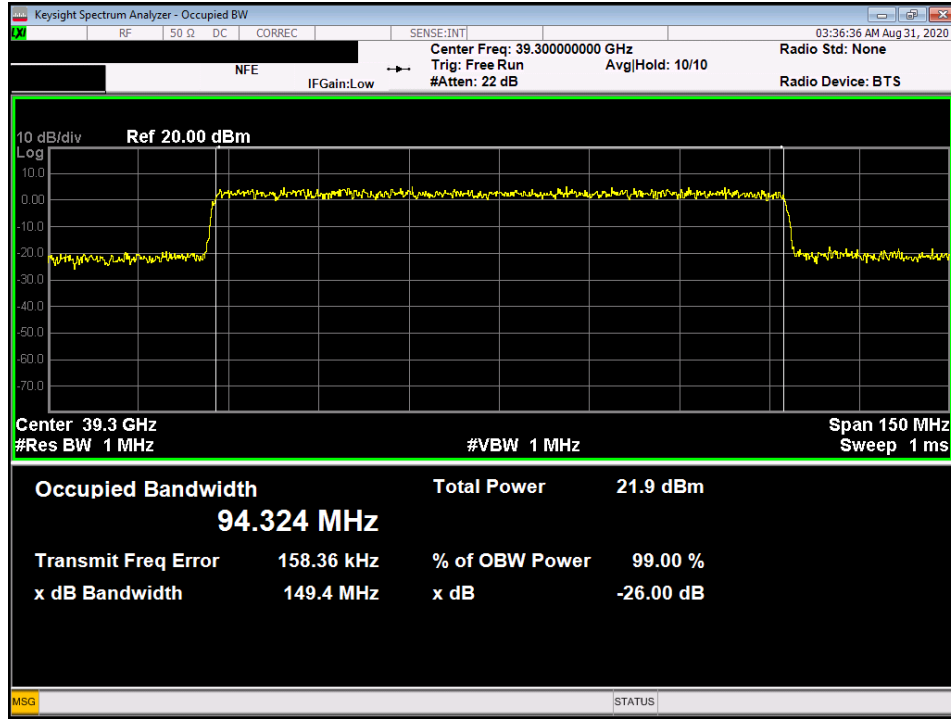


### OCC BW\_400 MHz BW\_p2070006\_Input Signal\_Pin + 3 dB

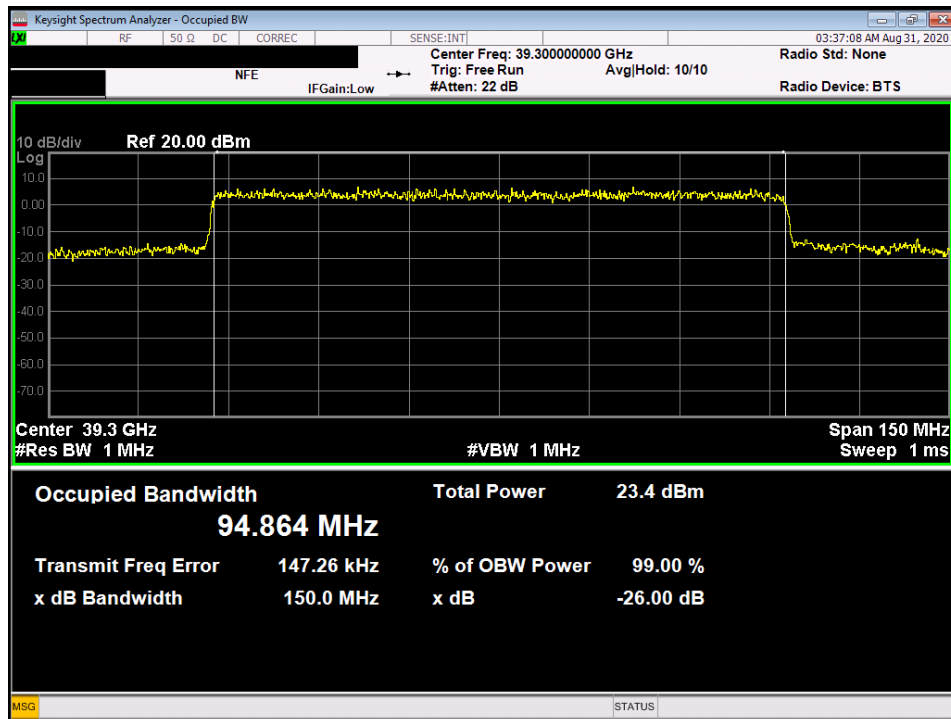




### OCC BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p2070006\_Output Signal

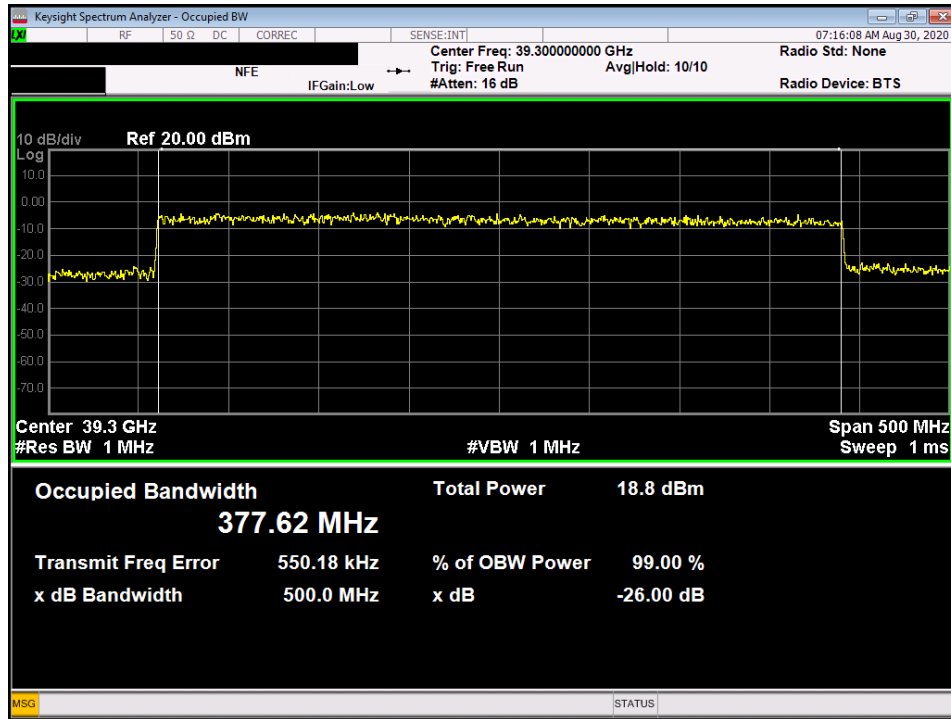


### OCC BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB

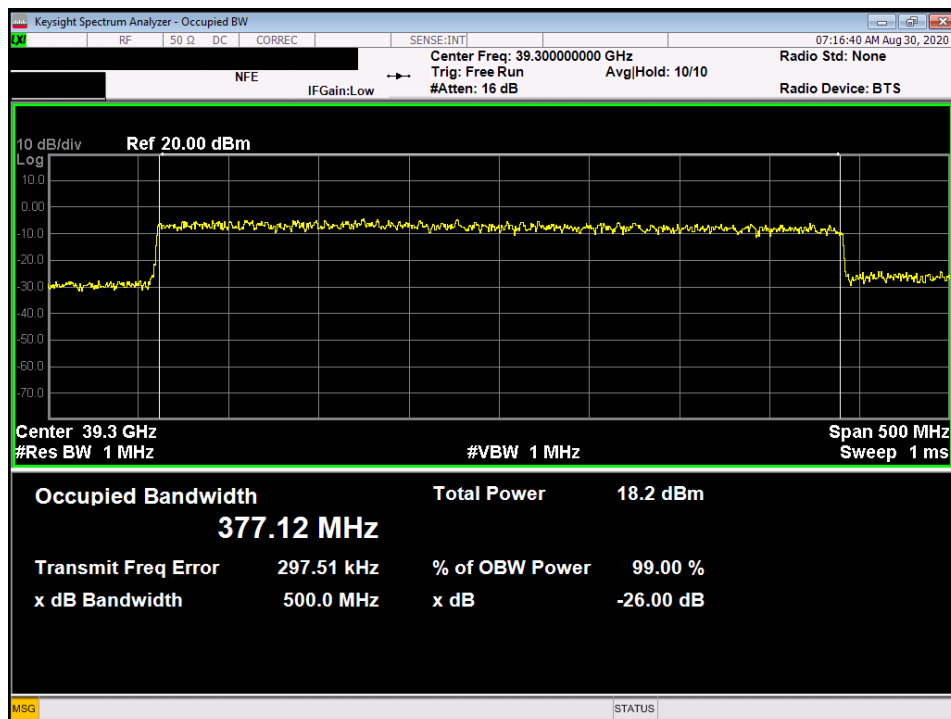




**OCC BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p2070006\_Output Signal**

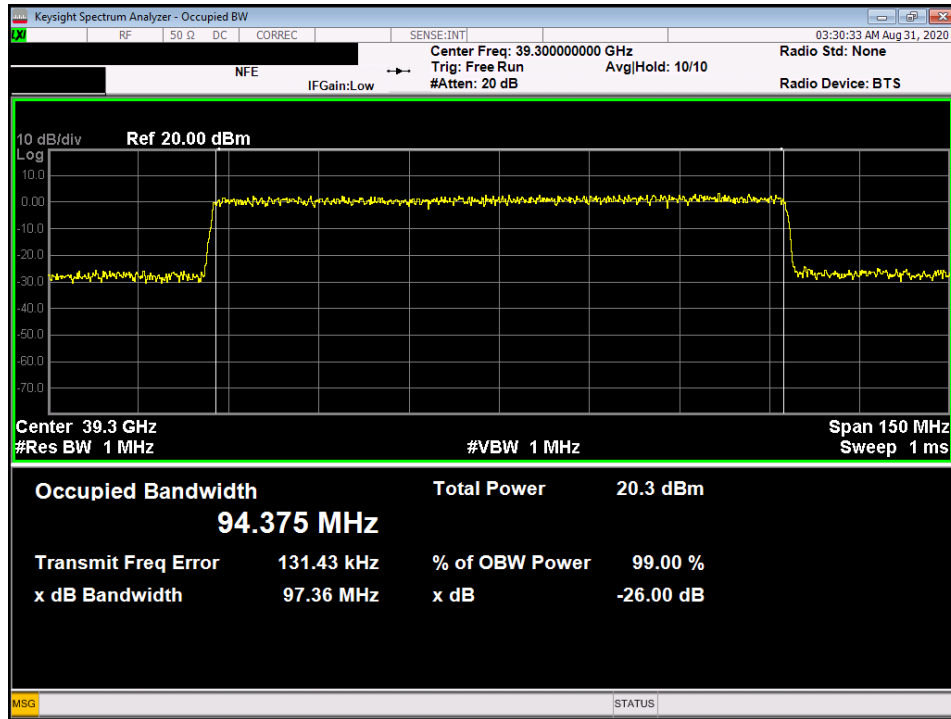


**OCC BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB**

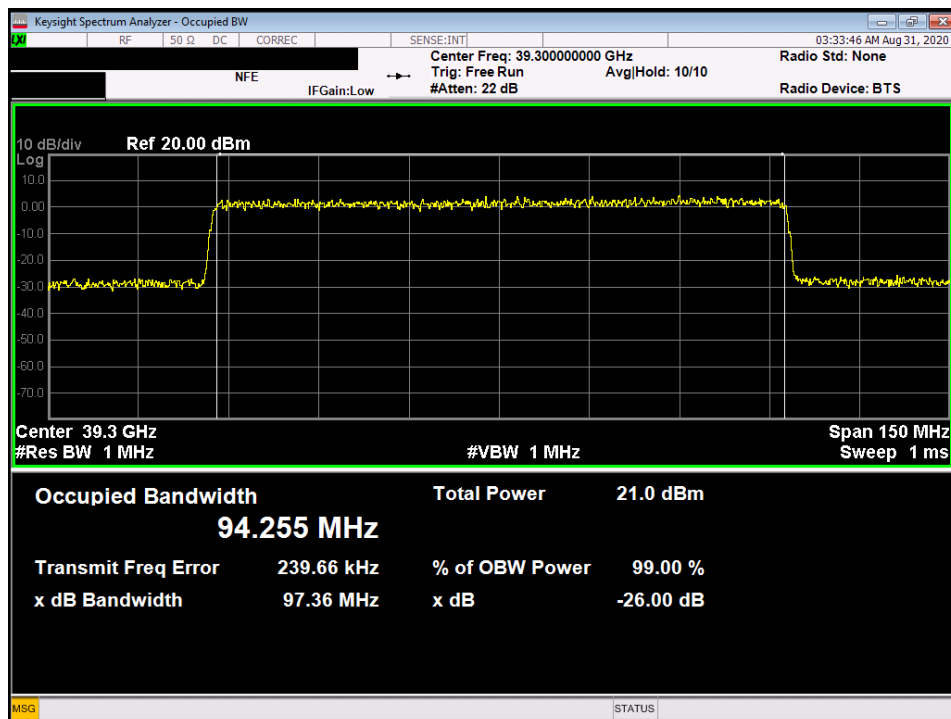




### OCC BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p2070006\_Output Signal

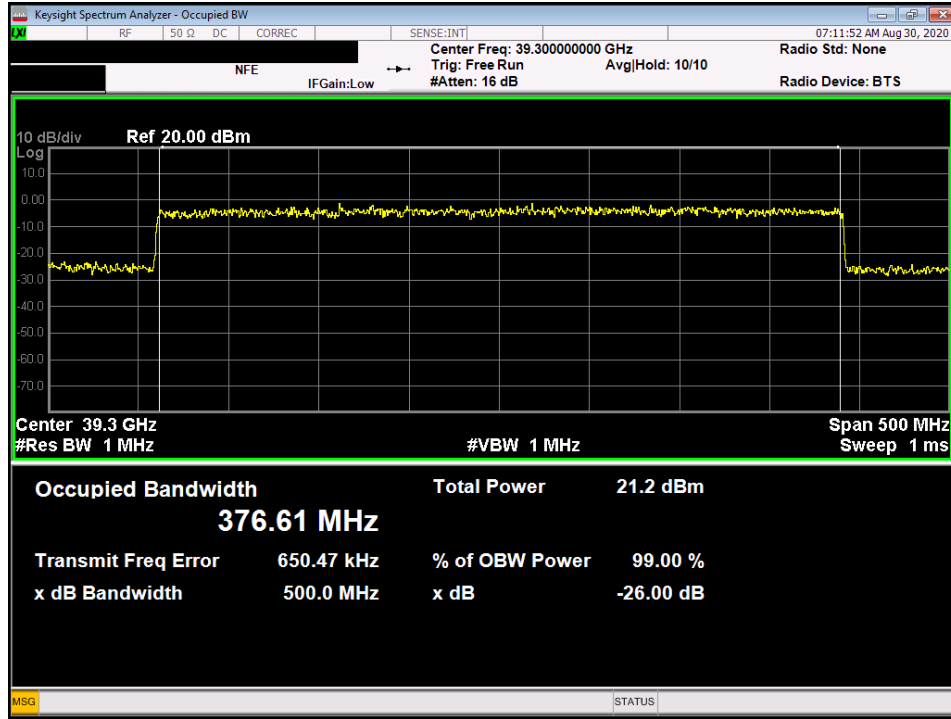


### OCC BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB

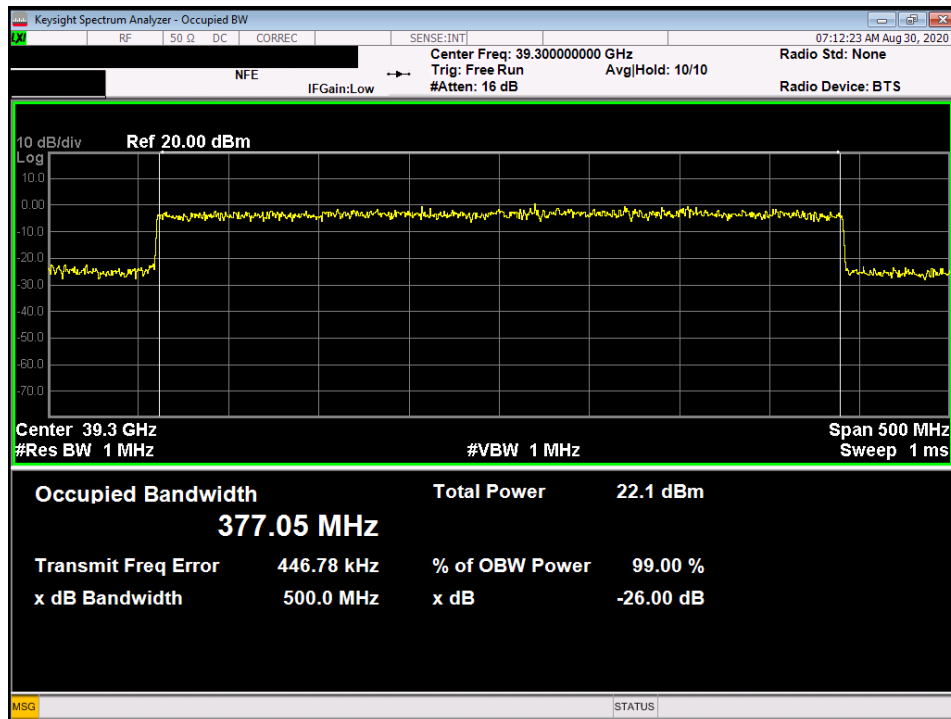




**OCC BW\_MIMO UL In - MIMO UL Out\_400 MHz BW\_p2070006\_Output Signal**

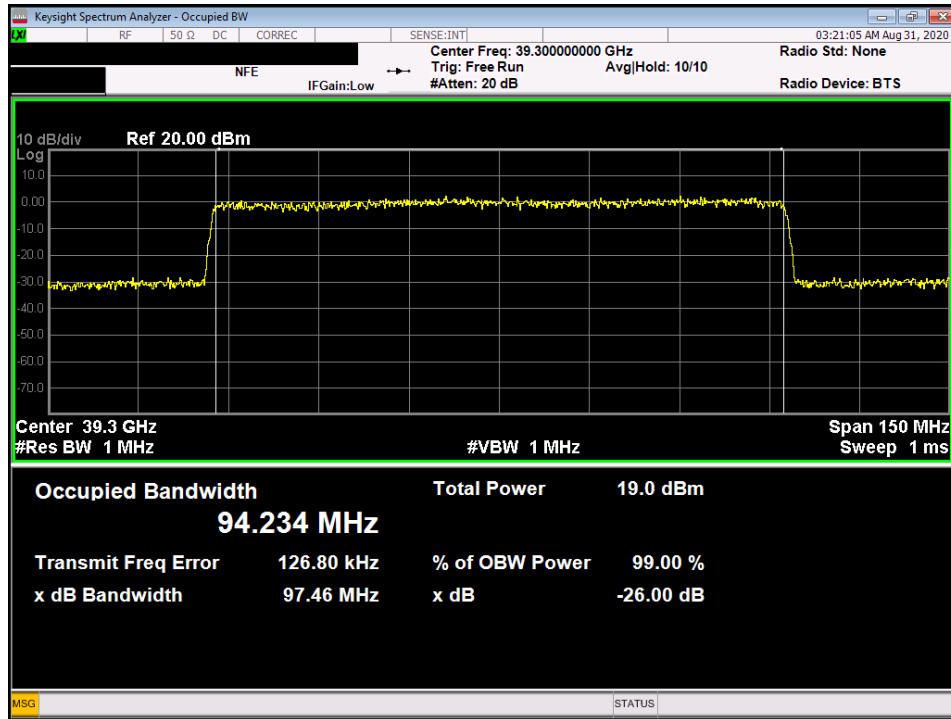


**OCC BW\_MIMO UL In - MIMO UL Out\_400 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB**

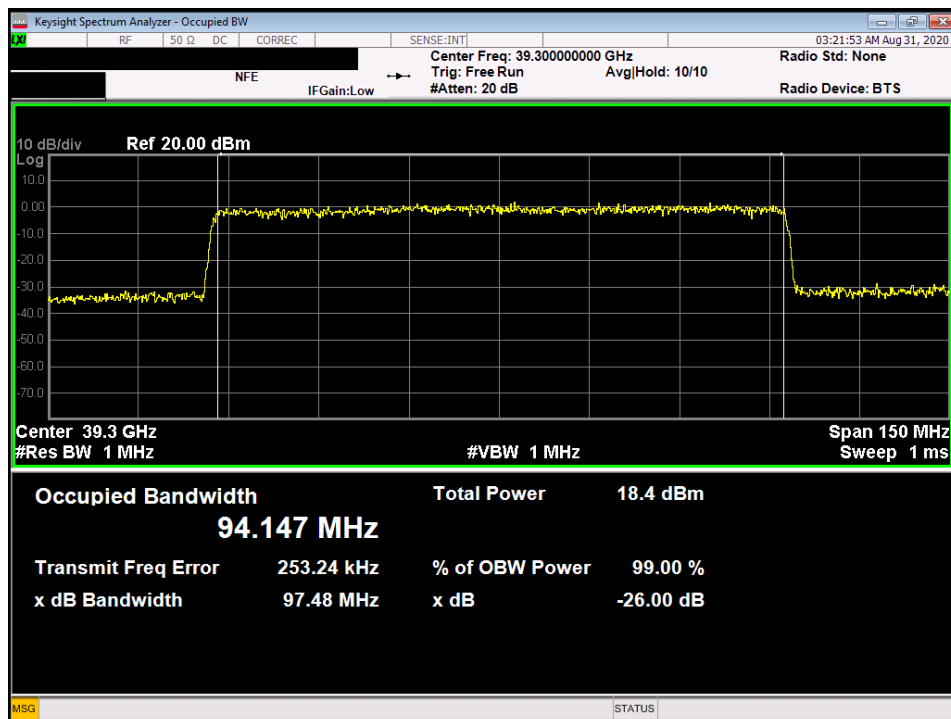




### OCC BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p2070006\_Output Signal



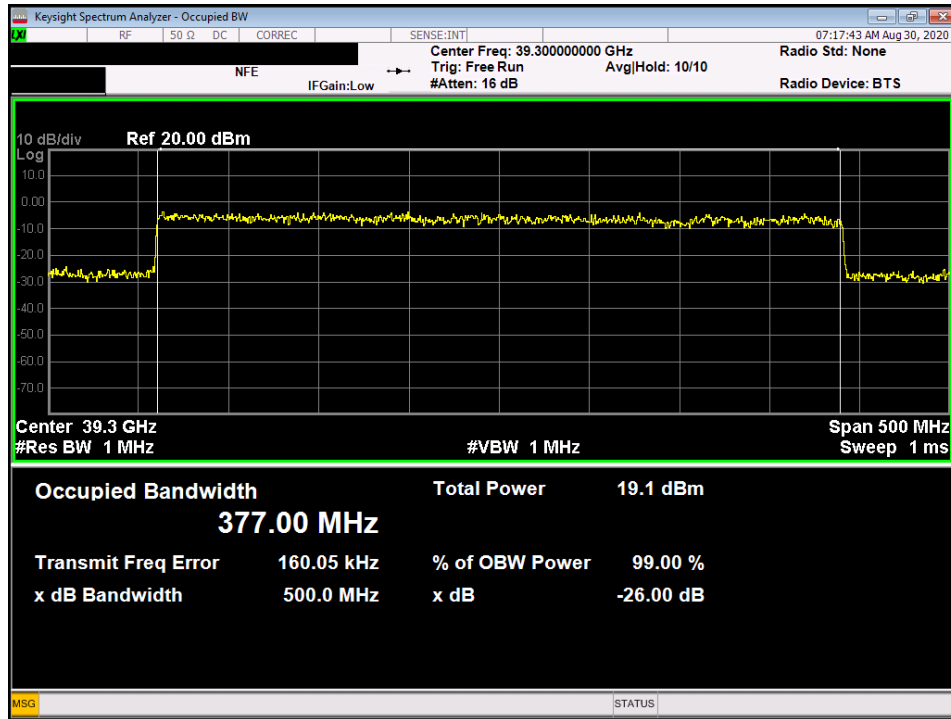
### OCC BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB



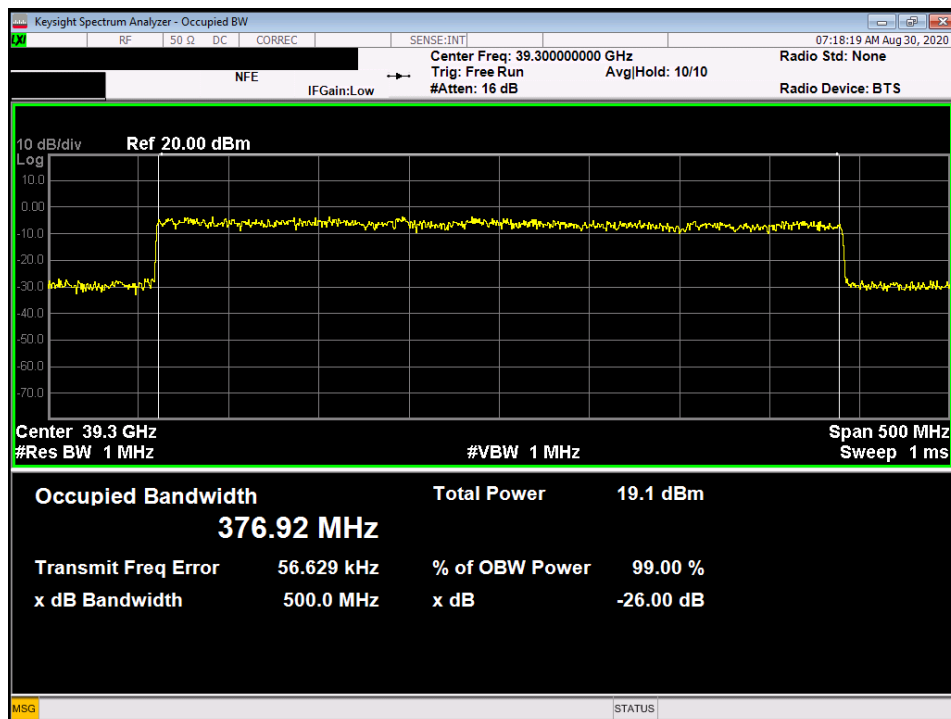




### OCC BW\_SISO DL In - SISO DL Out\_400 MHz BW\_p2070006\_Output Signal

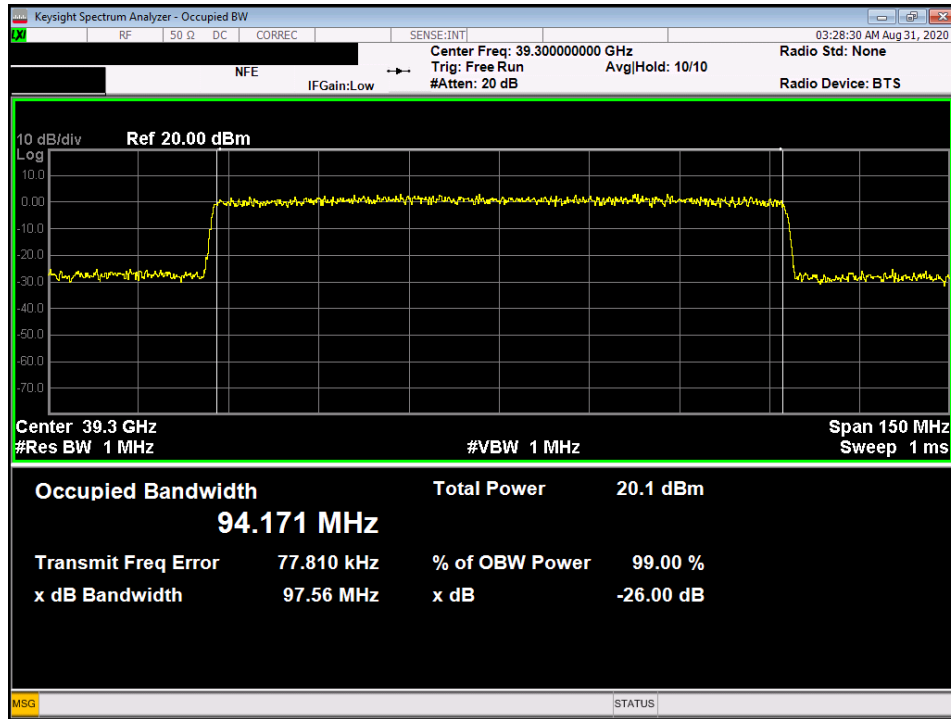


### OCC BW\_SISO DL In - SISO DL Out\_400 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB

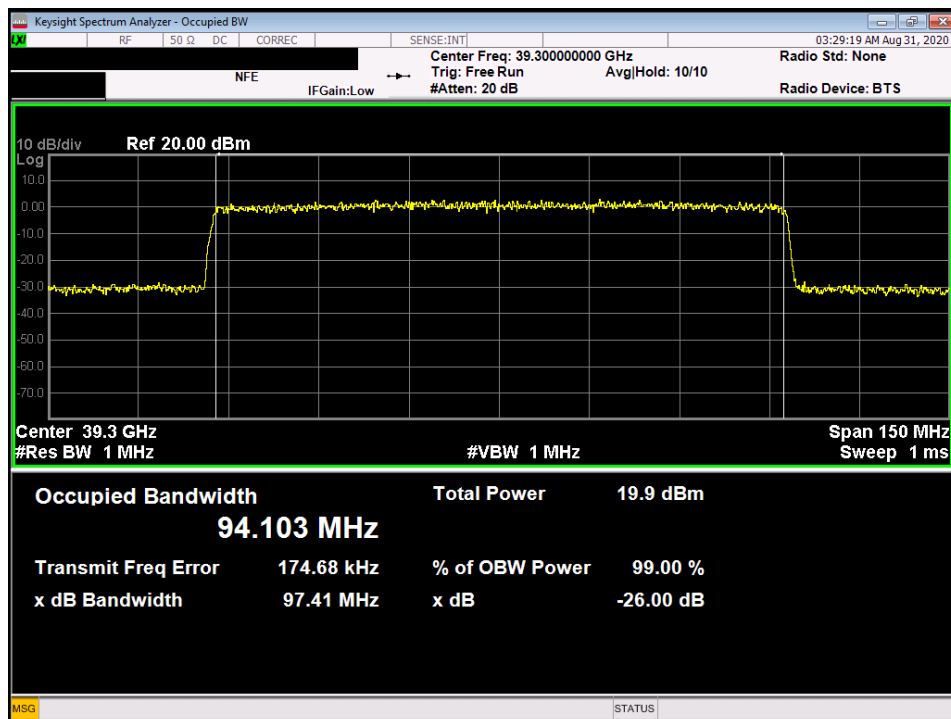




### OCC BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p2070006\_Output Signal

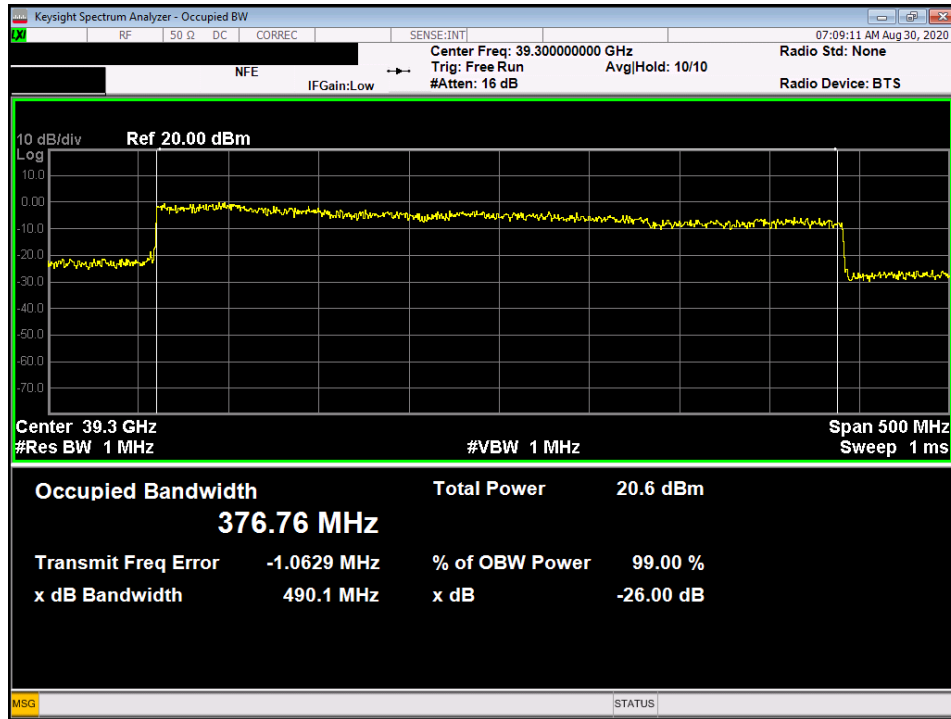


### OCC BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB





### OCC BW\_SISO UL In - SISO UL Out\_400 MHz BW\_p2070006\_Output Signal



### OCC BW\_SISO UL In - SISO UL Out\_400 MHz BW\_p2070006\_Output Signal\_Pin + 3 dB

