



Annex B

Input vs Output Signal Comparison

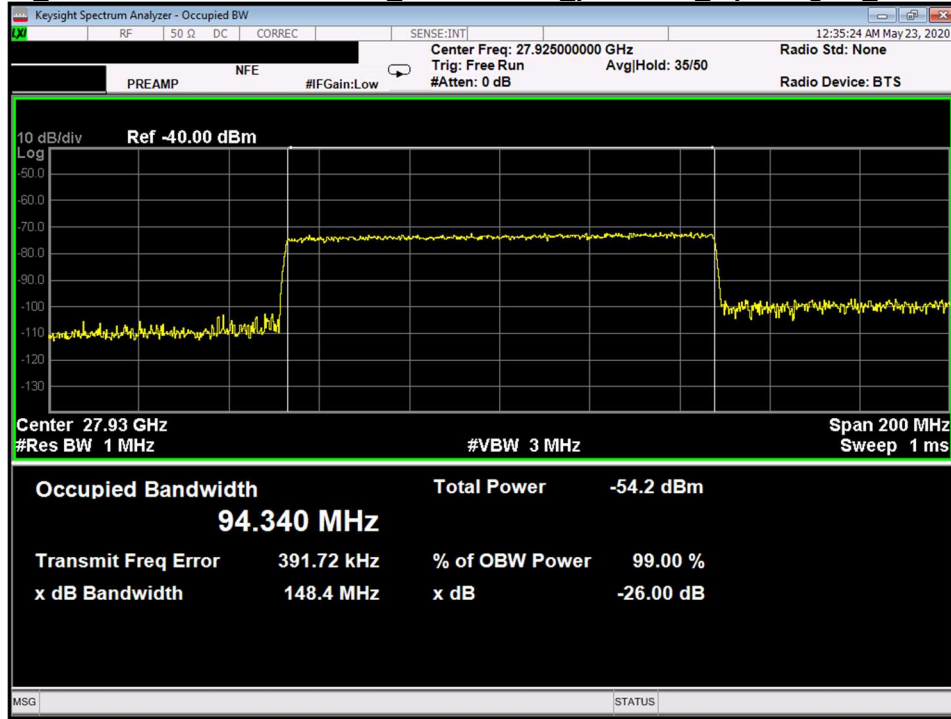


Compliance Testing, LLC

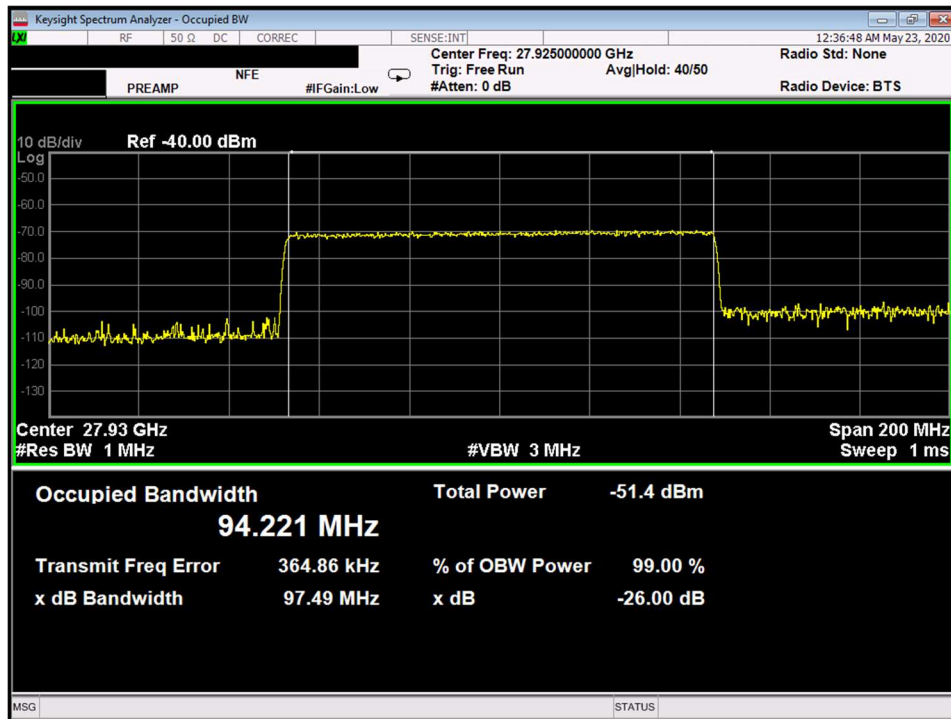
Testing since 1963



Occ BW_MIMO DL In - MIMO DL Out_100 MHz BW_p19c0009_Input Signal_Pin - 52 dBm

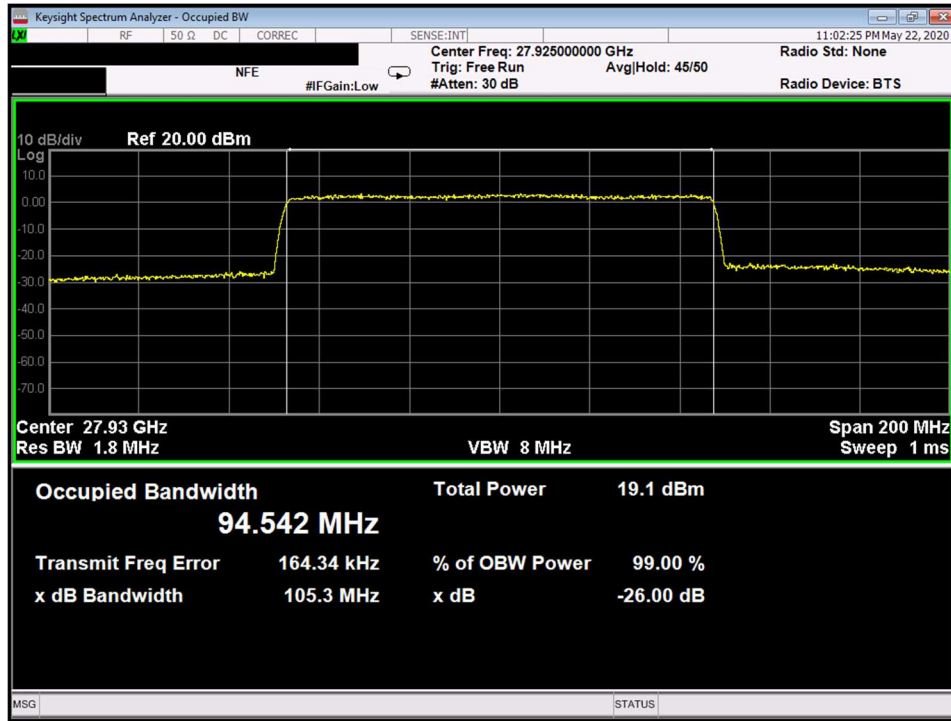


Occ BW_MIMO DL In - MIMO DL Out_100 MHz BW_p19c0009_Input Signal_Pin - 52 dBm_+ 3 dB

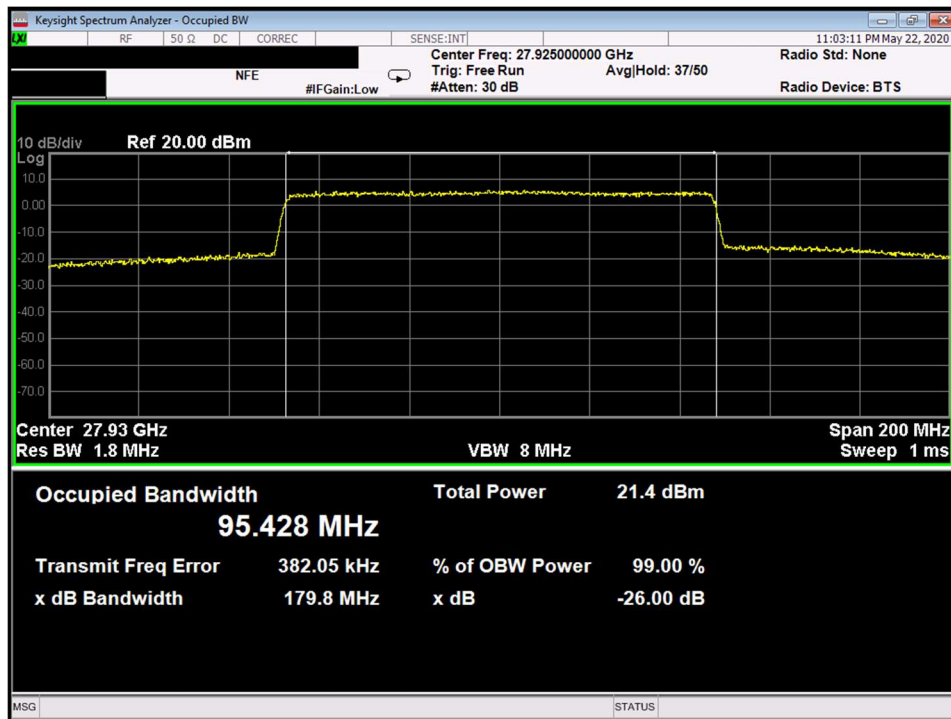




Occ BW_MIMO DL In - MIMO DL Out_100 MHz BW_p19c0009_Output Signal

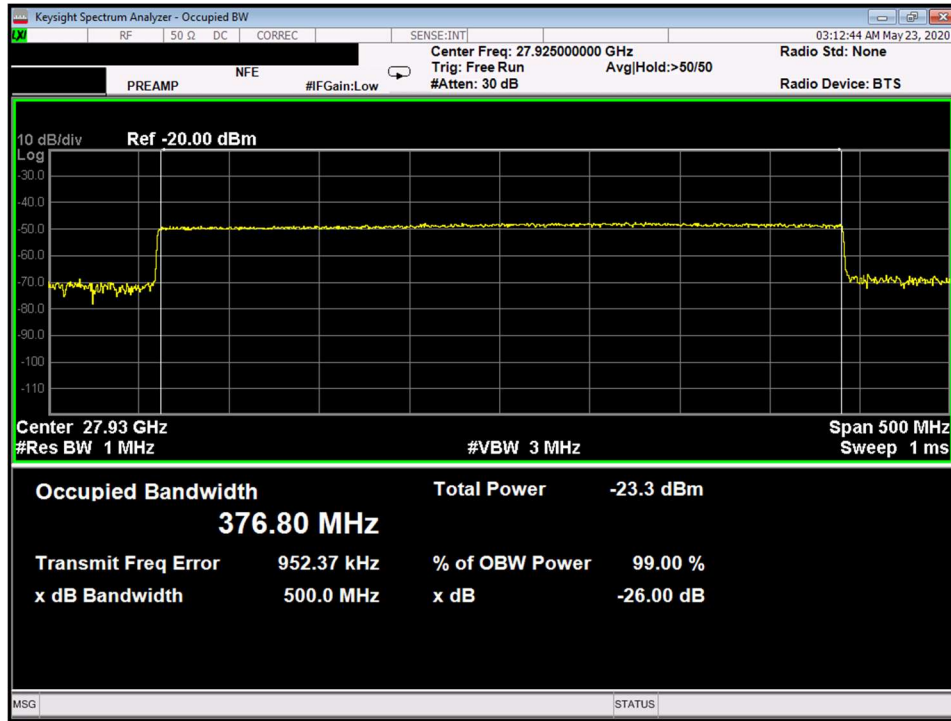


Occ BW_MIMO DL In - MIMO DL Out_100 MHz BW_p19c0009_Output Signal_Pin + 3 dB

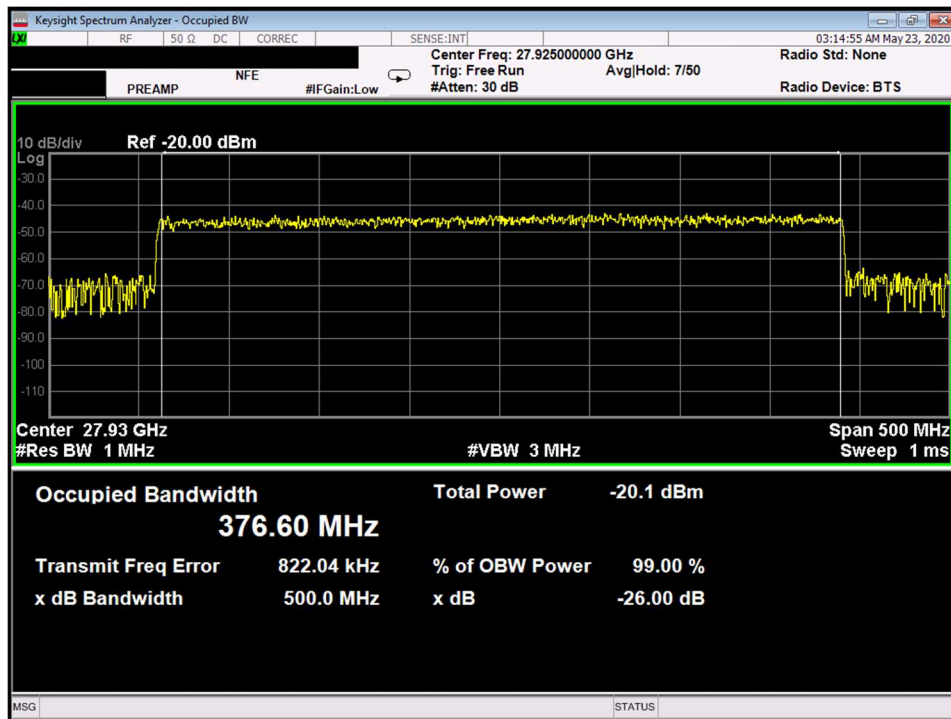




Occ BW_MIMO DL In - MIMO DL Out_400 MHz BW_p19c0009_Input Signal_-52 dBm

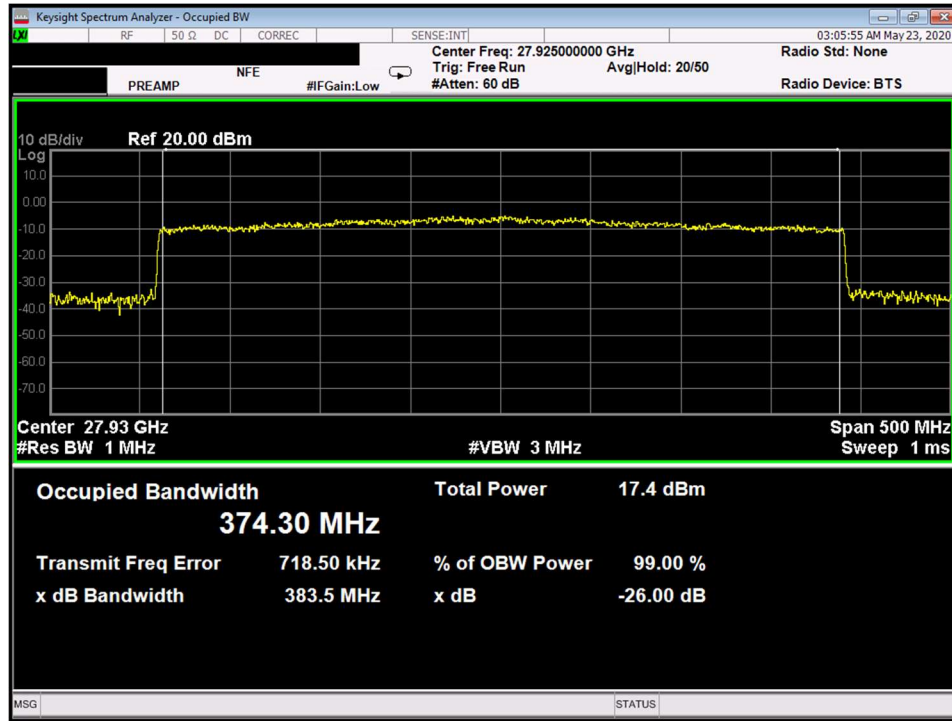


Occ BW_MIMO DL In - MIMO DL Out_400 MHz BW_p19c0009_Input Signal_-52 dBm_Pin + 3 dB

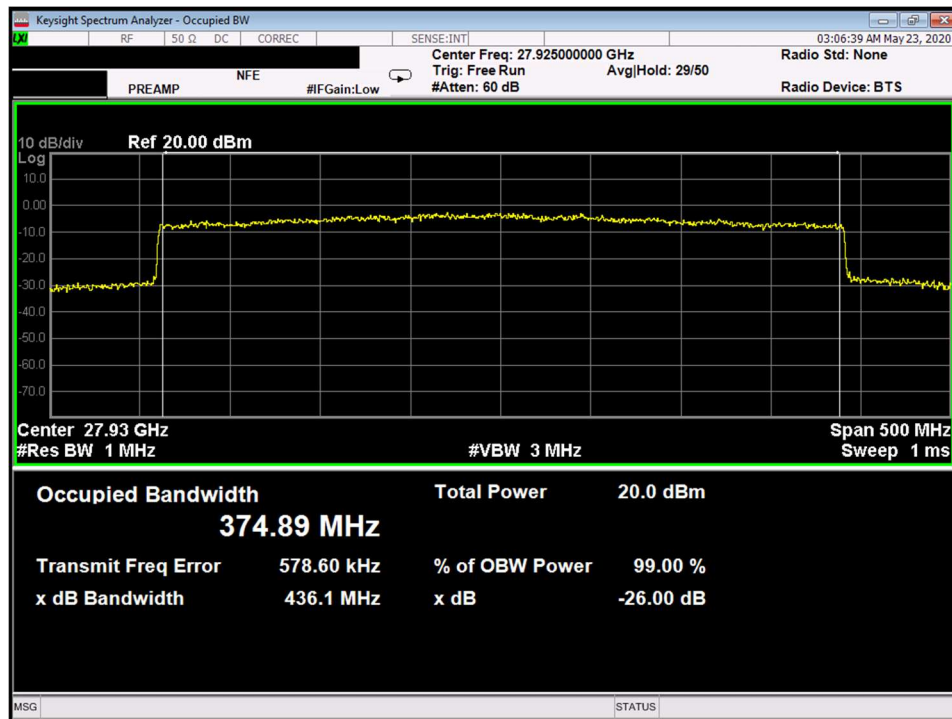




Occ BW_MIMO DL In - MIMO DL Out_400 MHz BW_p19c0009_Output Signal

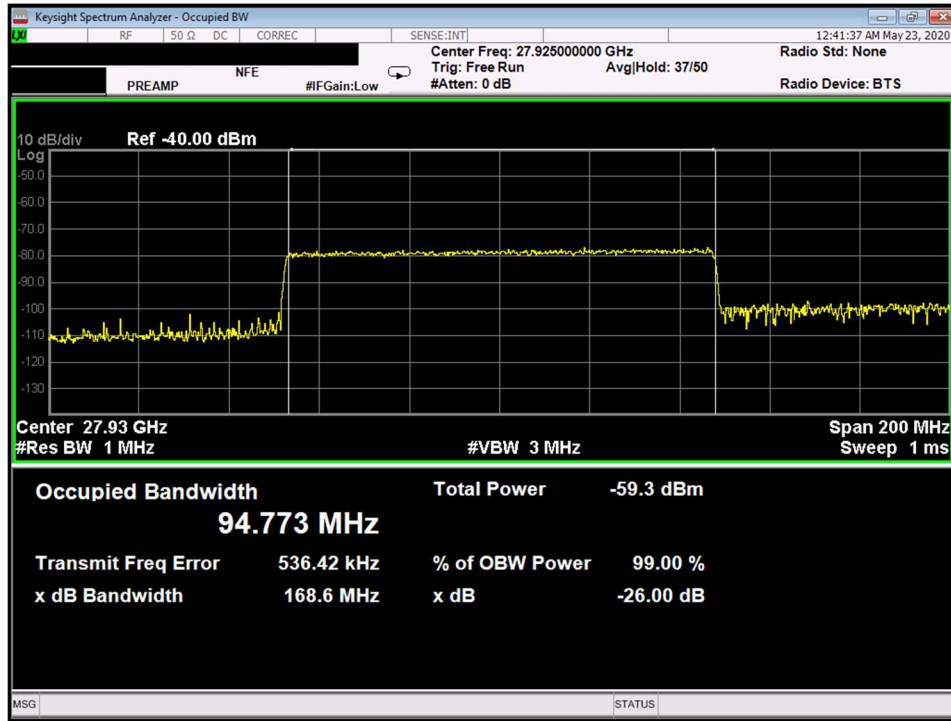


Occ BW_MIMO DL In - MIMO DL Out_400 MHz BW_p19c0009_Output Signal_Pin + 3 dB

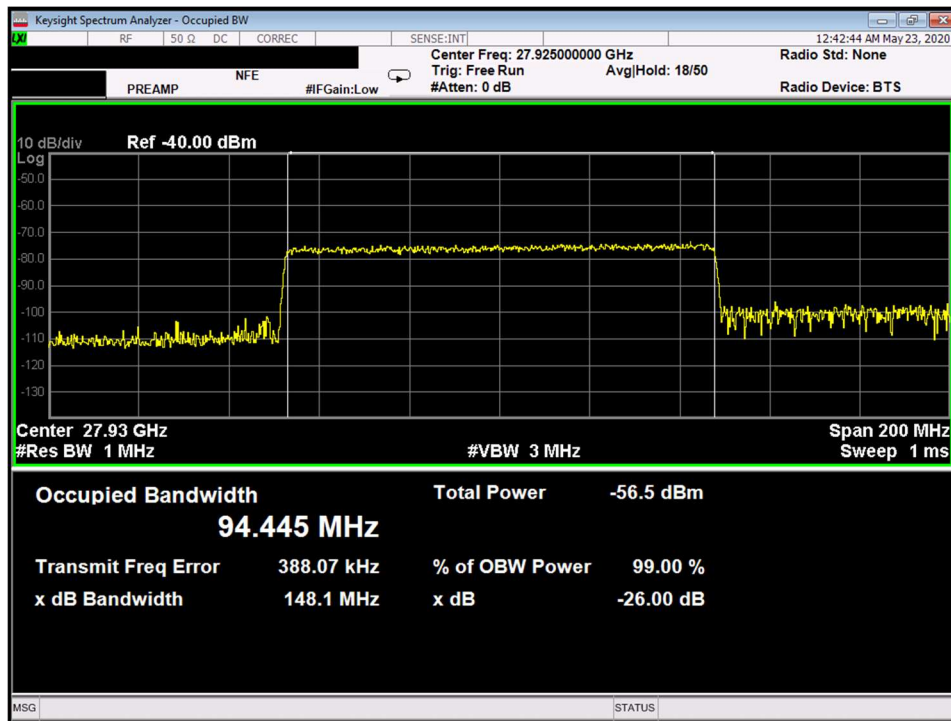




Occ BW_MIMO UL In - MIMO UL Out_100 MHz BW_p19c0009_Input Signal Signal_Pin - 57 dBm

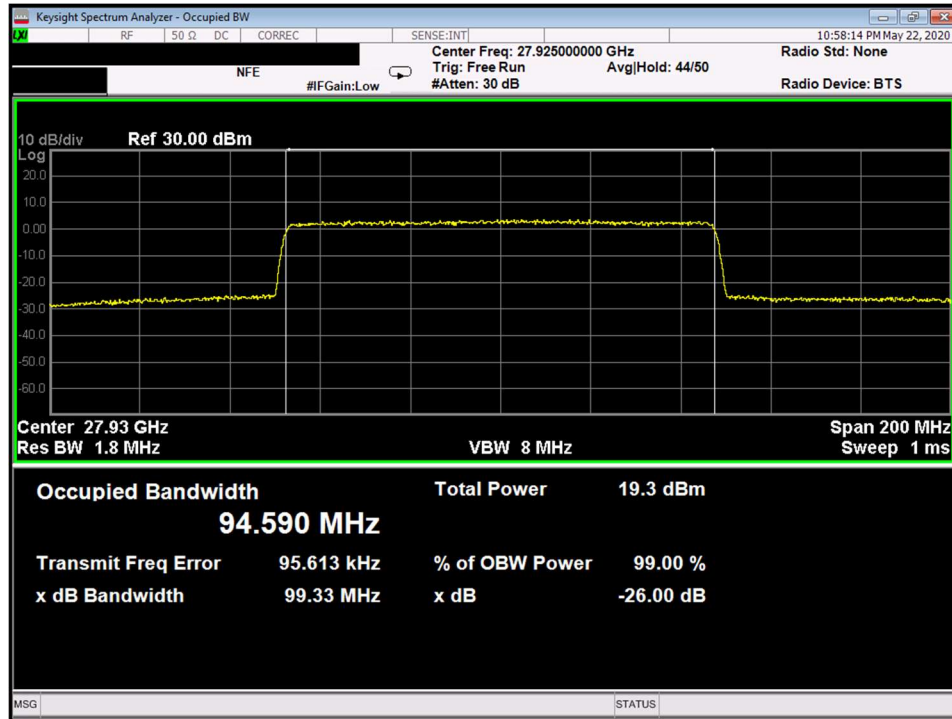


Occ BW_MIMO UL In - MIMO UL Out_100 MHz BW_p19c0009_Input Signal Signal_Pin - 57 dBm + 3 dB

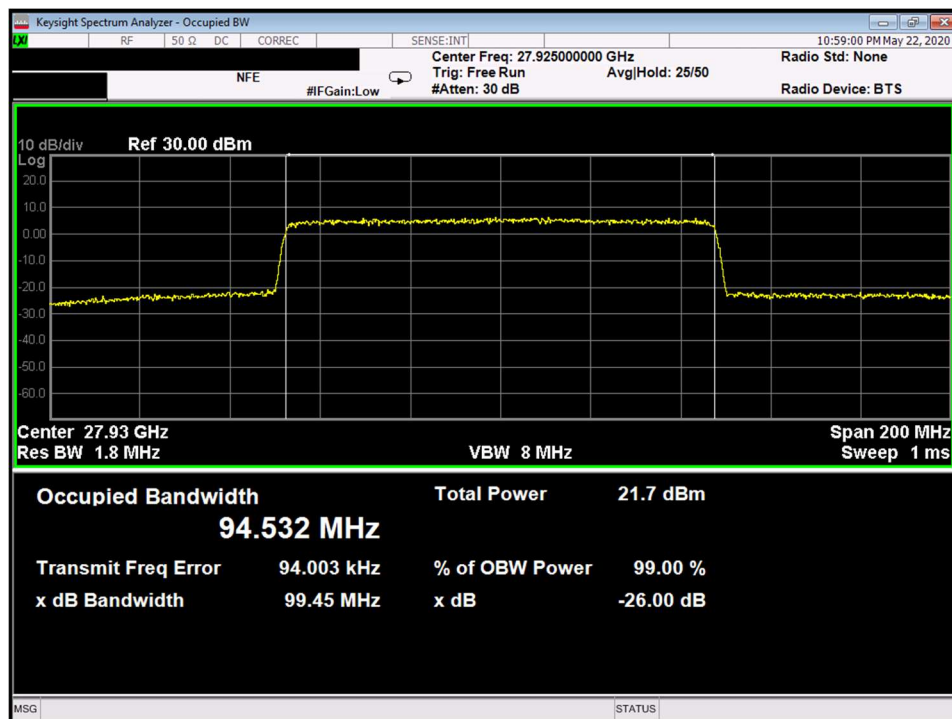




Occ BW_MIMO UL In - MIMO UL Out_100 MHz BW_p19c0009_Output Signal

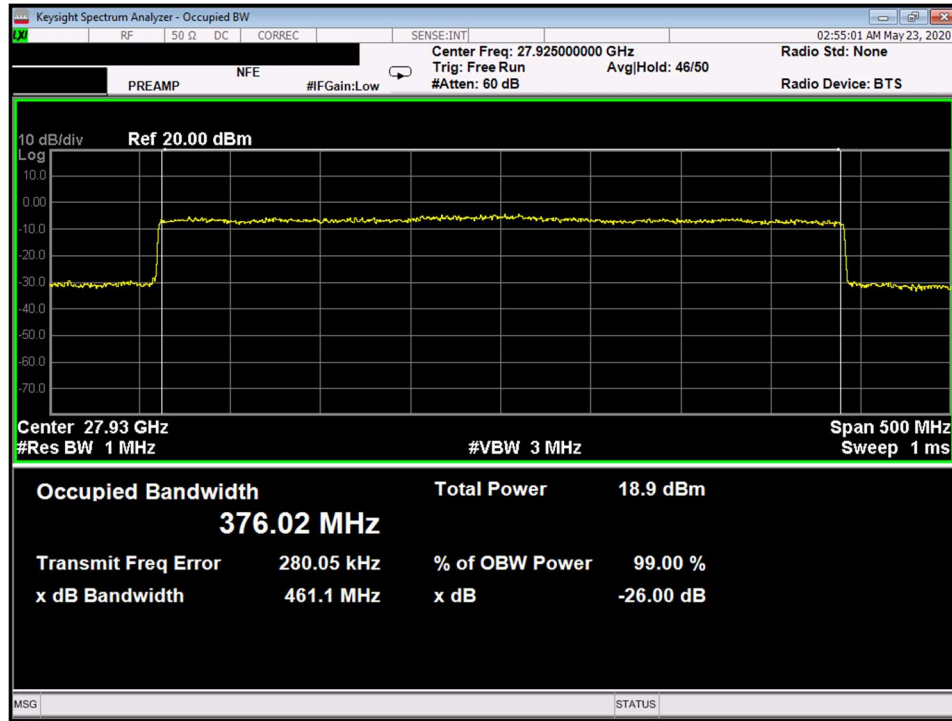


Occ BW_MIMO UL In - MIMO UL Out_100 MHz BW_p19c0009_Output Signal_Pin + 3 dB

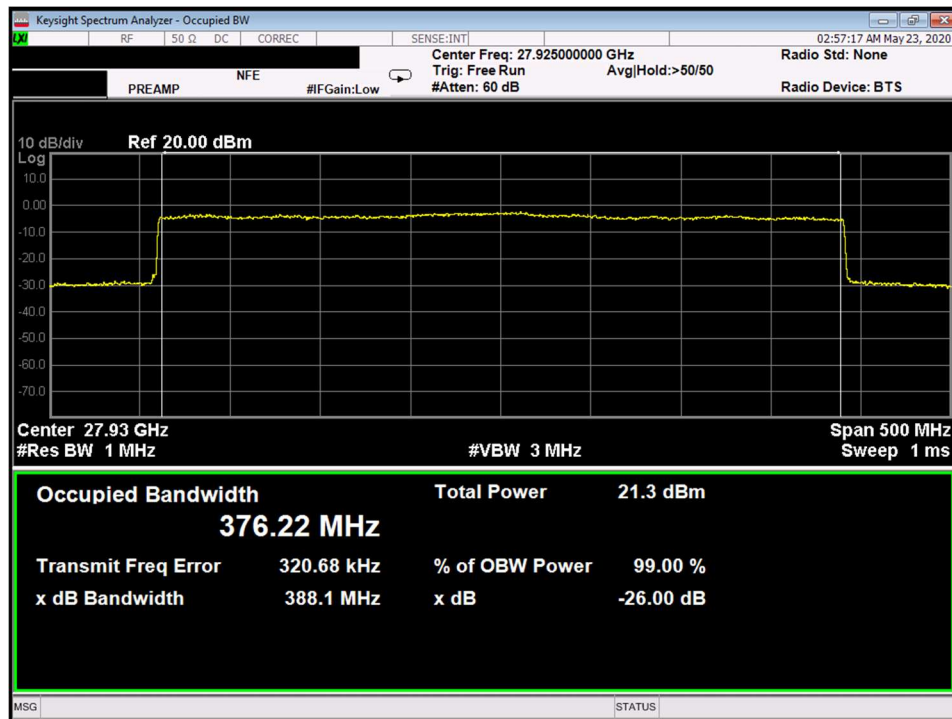




Occ BW_MIMO UL In - MIMO UL Out_400 MHz BW_p19c0009_Output Signal

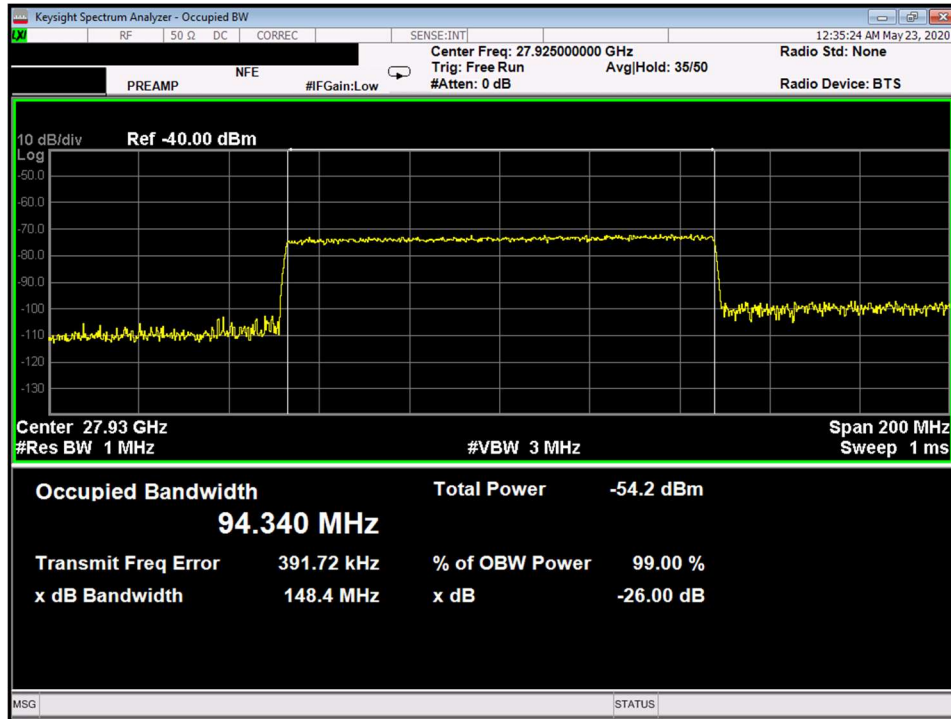


Occ BW_MIMO UL In - MIMO UL Out_400 MHz BW_p19c0009_Output Signal_Pin + 3 dB

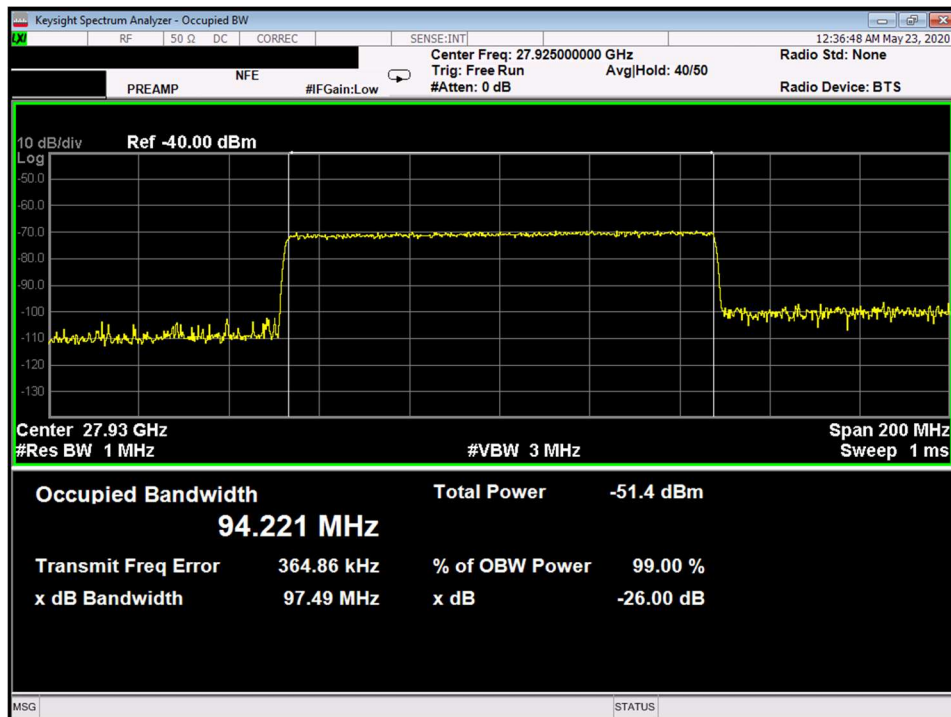




Occ BW_SISO DL In - SISO DL Out_100 MHz BW_p19c0009_Input Signal_Pin - 52 dBm

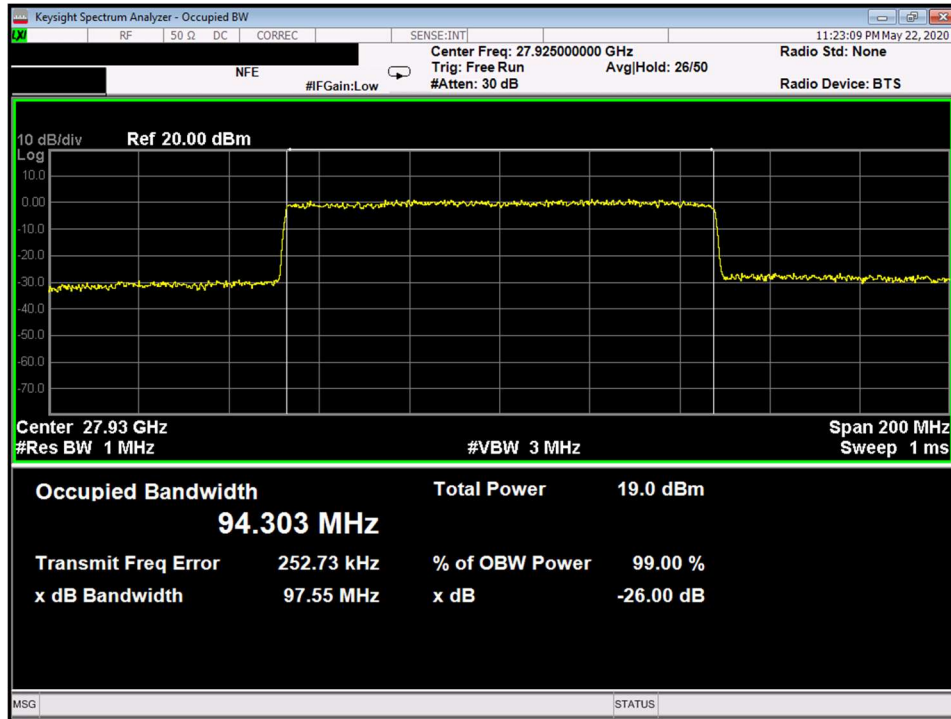


Occ BW_SISO DL In - SISO DL Out_100 MHz BW_p19c0009_Input Signal_Pin - 52 dBm + 3 dB

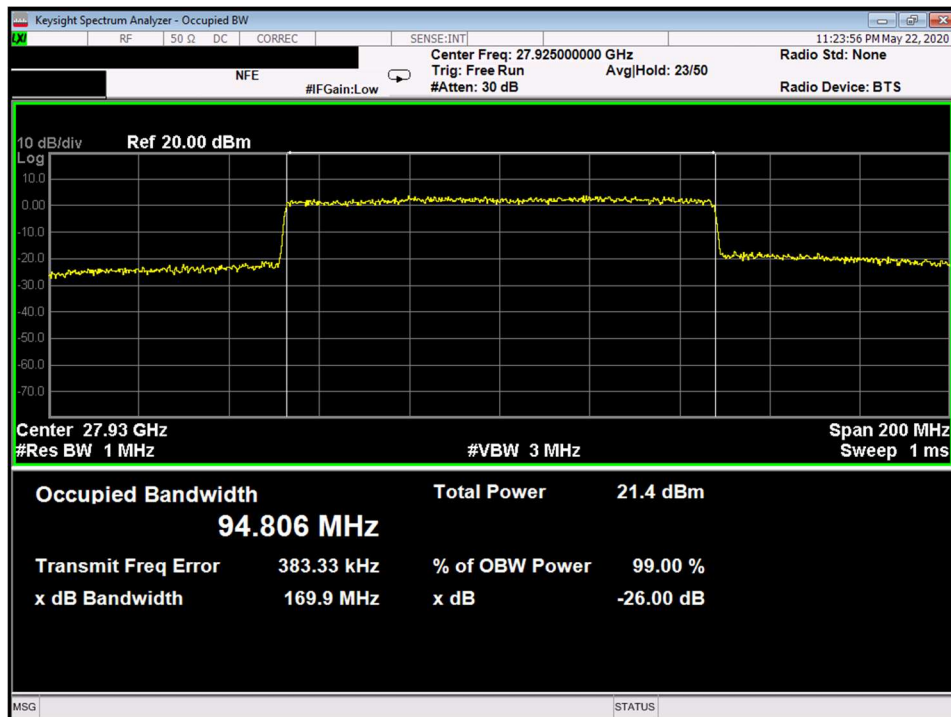




Occ BW_SISO DL In - SISO DL Out_100 MHz BW_p19c0009_Output Signal

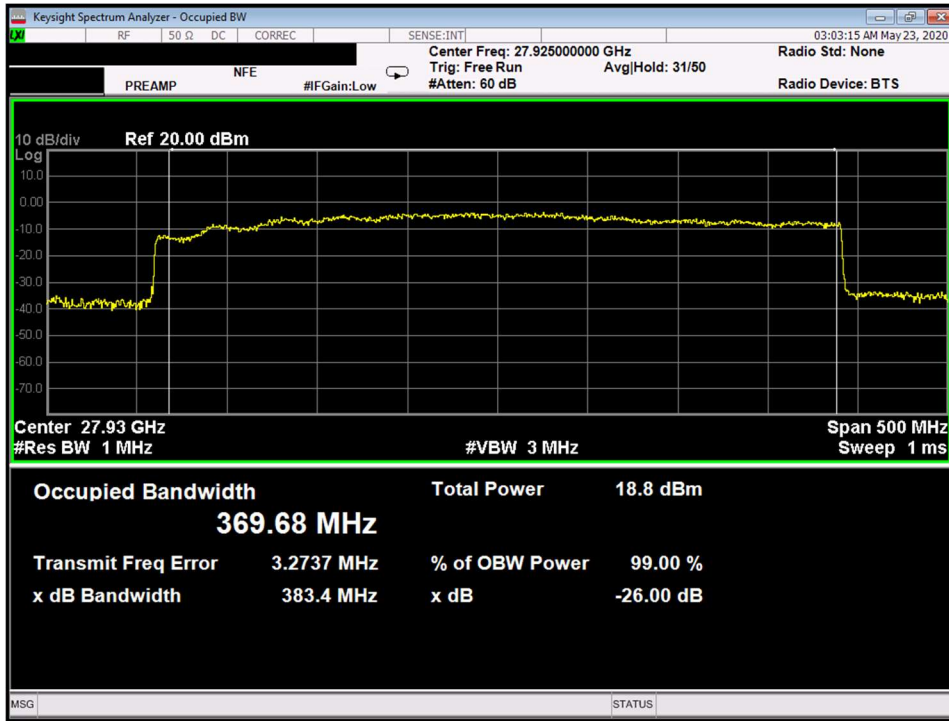


Occ BW_SISO DL In - SISO DL Out_100 MHz BW_p19c0009_Output Signal_Pin + 3 dB

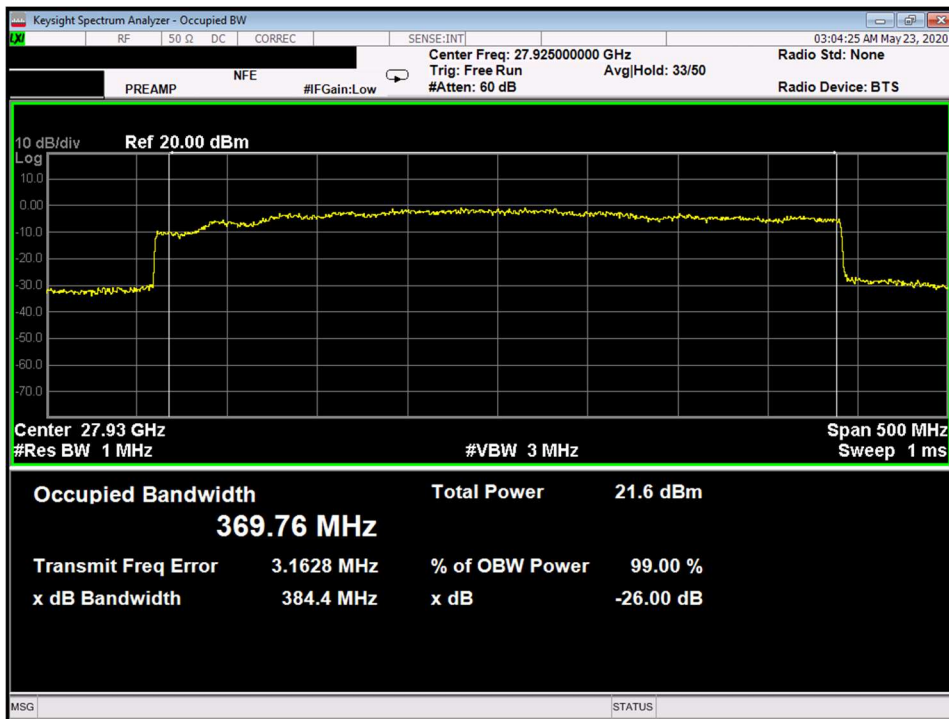




Occ BW_SISO DL In - SISO DL Out_400 MHz BW_p19c0009_Output Signal

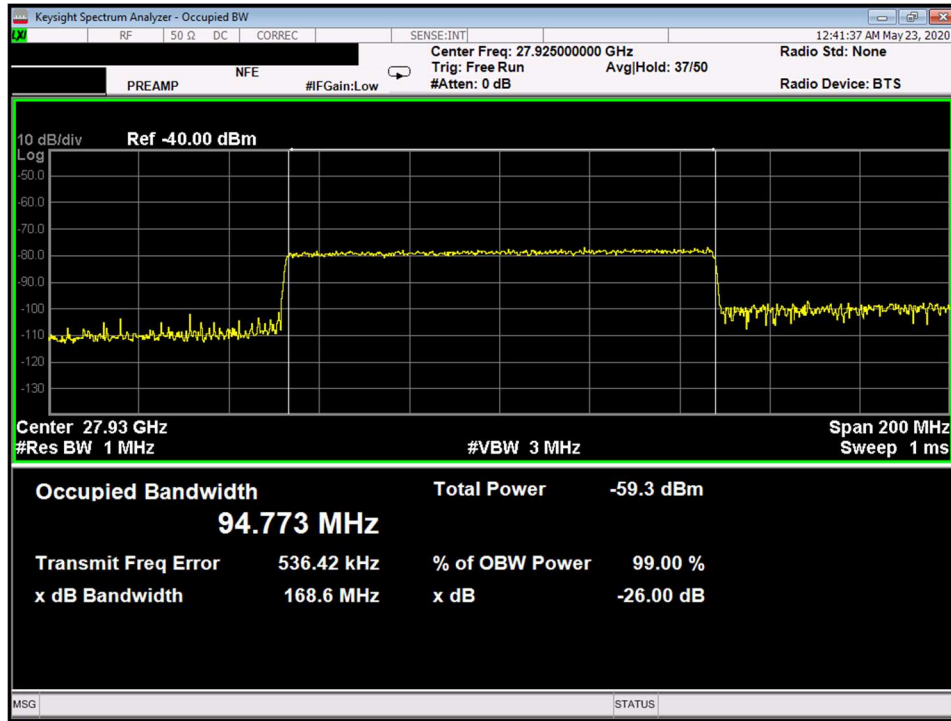


Occ BW_SISO DL In - SISO DL Out_400 MHz BW_p19c0009_Output Signal_Pin + 3 dB

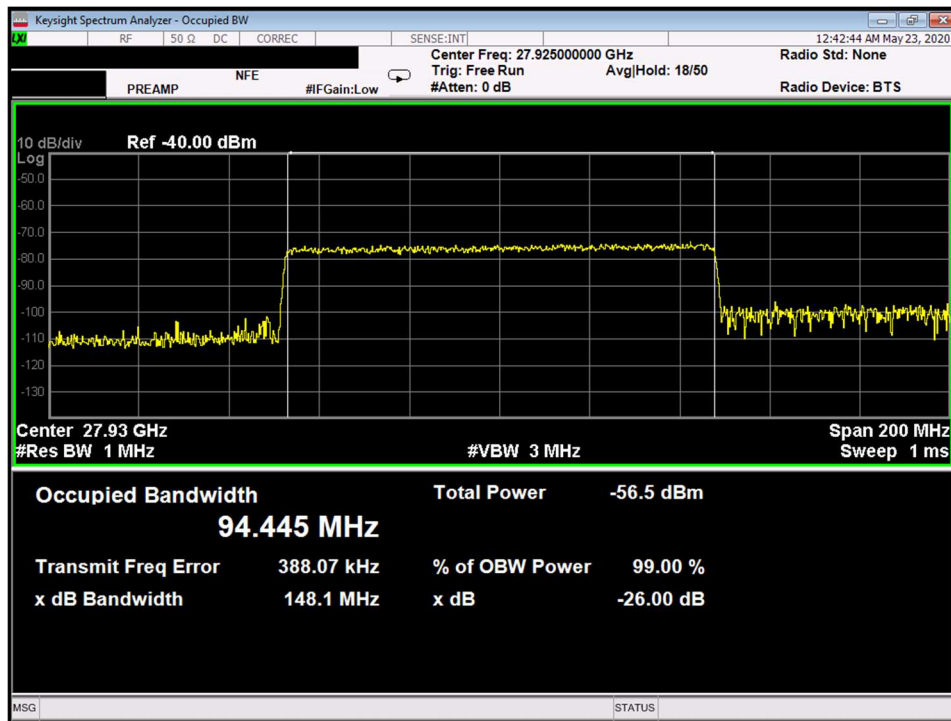




Occ BW_SISO UL In - SISO UL Out_100 MHz BW_p19c0009_Input Signal Signal_Pin - 57 dBm

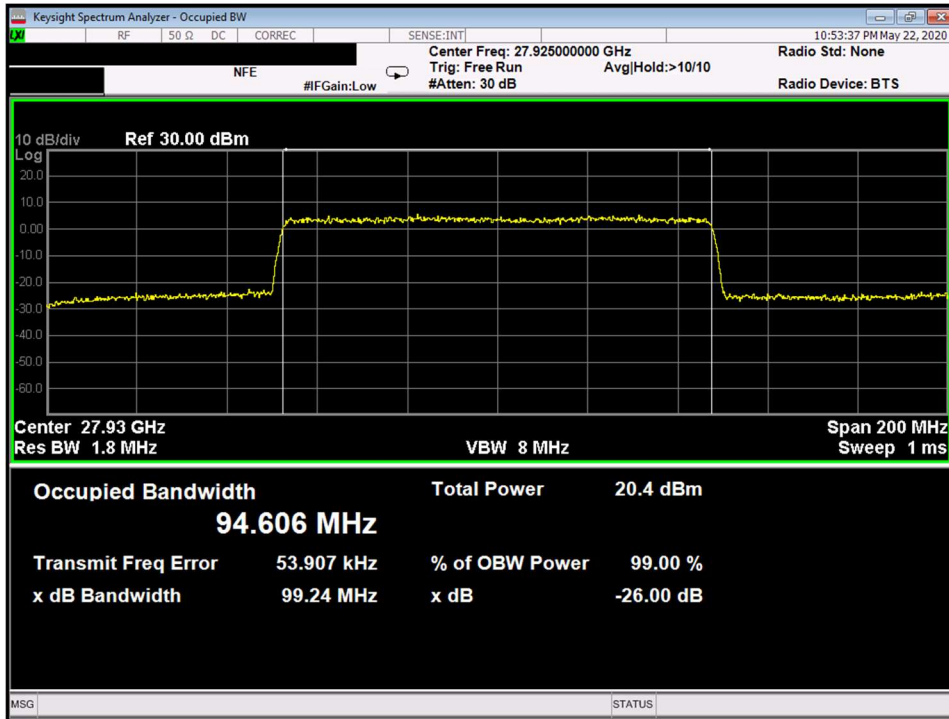


Occ BW_SISO UL In - SISO UL Out_100 MHz BW_p19c0009_Input Signal Signal_Pin - 57 dBm_+ 3 dB

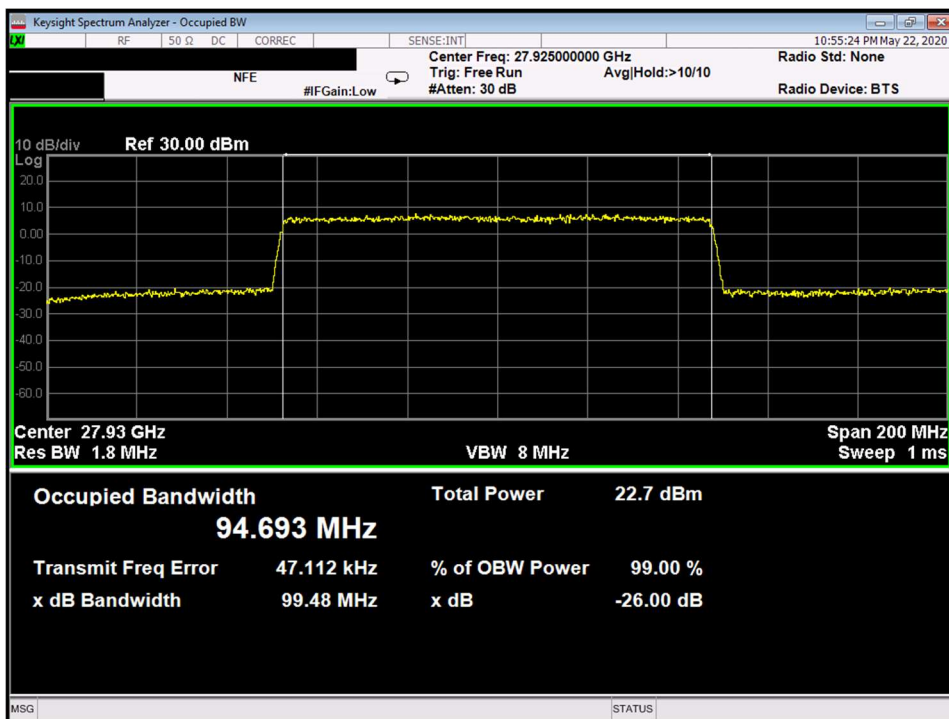




Occ BW_SISO UL In - SISO UL Out_100 MHz BW_p19c0009_Output Signal

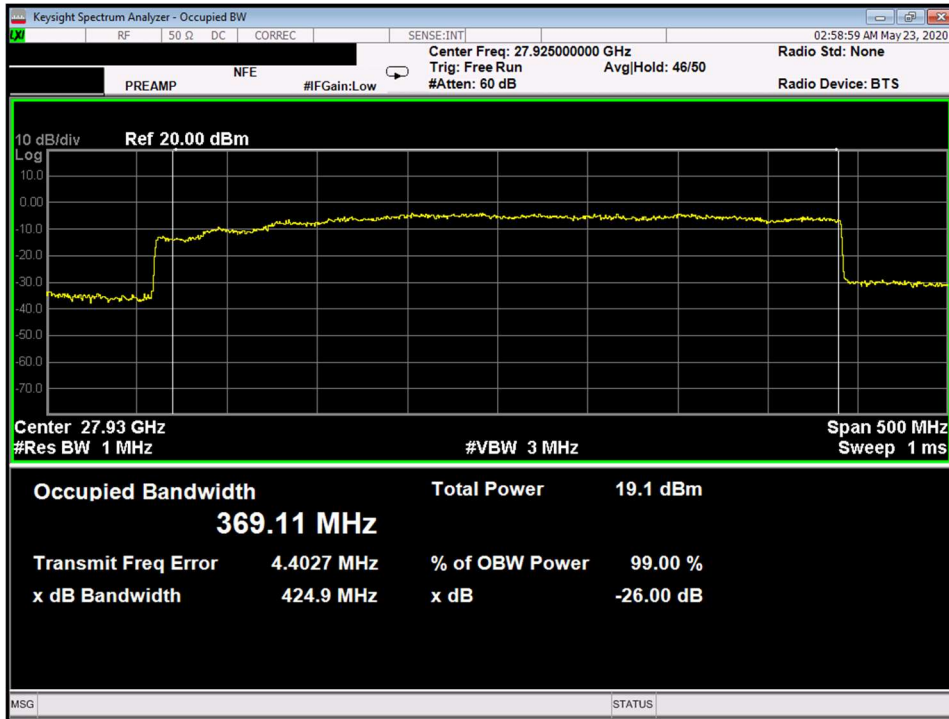


Occ BW_SISO UL In - SISO UL Out_100 MHz BW_p19c0009_Output Signal_Pin + 3 dB





Occ BW_SISO UL In - SISO UL Out_400 MHz BW_p19c0009_Output Signal



Occ BW_SISO UL In - SISO UL Out_400 MHz BW_p19c0009_Output Signal_Pin + 3 dB

