SPECIFICATIONS

Customer	
Product Name	Chip Antenna
Shannon Part Number	SNCA31-2500M-S2TF
Customer Part Number	

[⊠New Released, ☐Revised] SPEC No.: SNCA190006

【This SPEC is total 10 pages including specifications and appendix.】
【ROHS Compliant Parts】

Approved By	Checked By	Issued By

Shenzhen Shannon Semiconductor Co., Ltd.

【For Customer approval Only】 Date:				
Qualification Status: Full Restricted Rejected			cted	
Approved By	Verified By	Re-ched	ked By	Checked By
omments:				
ommonto.				

Shannon Semi

【Version change history】

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	Jun.17,2019	New Release	1	Hai Guo

Categories: general confidential

Page 3 of 10 Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships, etc.)
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

1. Scope

This specification applies to SNCA-2500M-S2TF of Multi-layer Chip Antenna.

2. Product Description and Identification (Part Number)

- 1) Description : Multi-layer Chip Antenna
- 2) Product Identification (Part Number)

SNCA	31	-2500M	<u>-S2</u>	<u>T</u>	F
1	2	3	4	(5)	6

① Ty		Туре
	SNCA	Shannon Chip Antenna

② External Dimensions (LxW) (mm		
	31	3.2×1.6

③ Center Frequency		
2500M	2500.0MHz	

4	Series Code	
	S2	

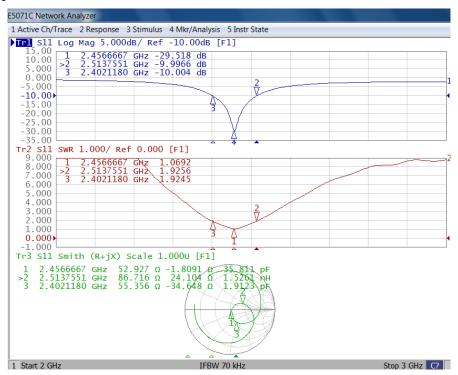
⑤ Packing	
Т	Tape Carrier Package

6	Hazardous Substance				
Free Products					
	F				

3. Electrical Characteristics

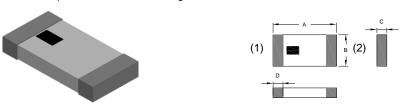
Part Number	Band Width	Peak Gain	Average Gain	VSWR	Impedance	Power Capacity
	MHz	-1.78dBi	0.5 dBi	In BW	Ω	W
SNCA31-2500M-S2TF	≥100	@(XZ-total)	@(XZ-total)	< 2	50	2 W max

- 1) Operating and storage temperature range (individual chip without packing): -40°C ~ +85°C.
- 2) Storage temperature range (packaging conditions): -10°C ~ +40°C and RH 70% (Max.).
- 3) Test equipment: Network Analyzer: E5071C.
- 4) Measuring diagram.



4. Shape and Dimensions

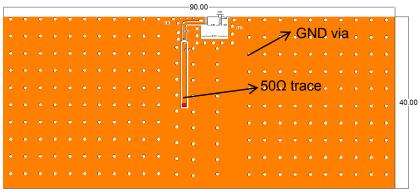
Dimensions and recommended PCB pattern for reflow soldering:



Item	А	В	С	D.
SNCA31	3.2±0.2	1.6±0.2	0.5±0.1	0.5±0.2

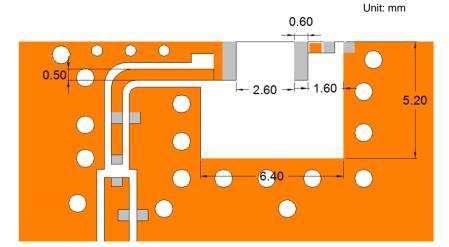
Unit: mm

Demo-board:

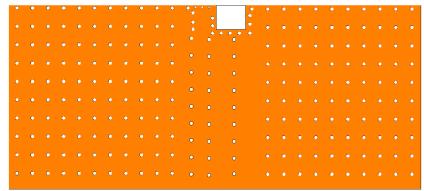


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Top view



Detail view



Bottom view

1) Terminal Configuration:



No.	Terminal Name	No.	Terminal Name
(1)	Feeding Point	(2)	Soldering terminal

5. **Test and Measurement Procedures**

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

a. Ambient Temperature: 20±15℃

Relative Humidity: 65±20% b.

Air Pressure: 86 Pa to 106KPa

If any doubt on the results, measurements/tests should be made within the following limits:

Ambient Temperature: 20±2℃

Relative Humidity: 65±5% b.

Air Pressure: 86KPa to 106KPa

5.2 Visual Examination

Inspection Equipment: 20 X magnifier

5.3 Reliability Test

Shaillon Seill	Categories: general confidential	Specifications for Chip Antenna Page 6 or 10
Items	Requirements	Test Methods and Remarks
5.3.1 Terminal Strength	No visible mechanical damage.	Solder the Antenna to the testing jig (glass epoxy board shown as the following figure) using leadfree solder. Then apply a force in the direction of the arrow. 10N force for 3216 series. Keep time: 10±1sec.
		Mounting Pad 10N/10±1s Speed: 1.0mm/s Glass Epoxy Board
5.3.2 Resistance to Flexure	No visible mechanical damage.	 Solder the chip to the test jig (glass epoxy board) using a leadfree solder. Then apply a force in the direction shown as the following figure. Solder the chip to the test jig (glass epoxy board) using leadfree solder. Then apply a force in the direction. Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: ≥30 sec
	Unit: mm R10	20 10 Flexure: 2
5.3.3 Dropping	No visible mechanical damage.	SNCA series: Drop the chip 5 times on a wood floor from a height of 50 cm.
5.3.4 Solderability	 No visible mechanical damage. Wetting shall be exceeded 75% coverage. 	 Solder temperature: 240±2℃ Duration: 3sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight
5.3.5 Resistance to Soldering Heat	No visible mechanical damage.	 Solder temperature: 260±5℃ Duration: 5 sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.3.6 Thermal Shock	No visible mechanical damage. Satisfy electrical Characteristic.	 Temperature and time: -40°C for 30±3 min→85°C for 30±3min Transforming interval: Max. 20 sec. Tested cycle: 10 cycles The chip shall be stabilized at normal condition for 1~2 hours before measuring.
		30 min. 85°C Ambient Temperature -40°C 30 min. 30 min. 20sec. (max.)

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5.3.7	① No visib	le mechanical	1	Temperature: 60±2°C	
Damp Heat	damage		2	Humidity: 90% to 95% RH	
(Steady States)	② Satisfy e	electrical	3	Duration: 96 ⁺²⁴ hours	
	Charact	eristic.	4	The chip shall be stabilized at normal cond	ition for 1~2
				hours before measuring.	
5.3.8	① No visib	le mechanical	1	Temperature: 85±2°C	
Resistance to High temperature	damage		2	Duration: 96 ⁺²⁴ hours	
	② Satisfy e	electrical	3	The chip shall be stabilized at normal cond	ition for 1~2
	Charact	eristic.		hours before measuring.	

6. Packaging and Storage

6.1 Packaging

There is one type of packaging for the Multi-layer Chip Antennas. Please specify the packing code when ordering.

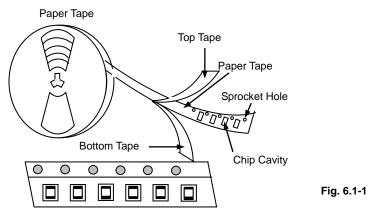
6.1.1 Tape Carrier Packaging:

Packaging code: T

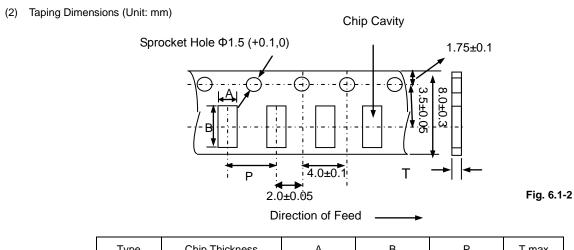
- a. Tape carrier packaging are specified in attached figure Fig. 6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

Туре	3216[1206]	
Tape	Paper Tape	
Quantity	3K	

(1) Taping Drawings (Unit: mm)

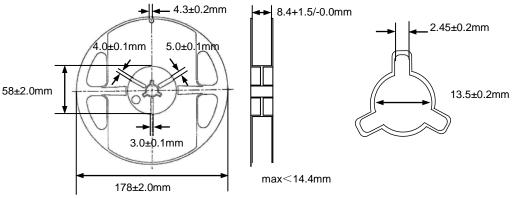


Remark: The sprocket holes are to the right as the tape is pulled toward the user.



Туре	Chip Thickness	Α	В	Р	T max
SNCA31	0.5±0.1	1.8±0.1	3.5±0.1	4.0±0.10	0.75

(3)Reel Dimensions (Unit: mm)



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Fig. 6.1-3

6.2 Storage

- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- C. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- Solderability specified in Clause 5.3.5 shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in Clause 3. For those parts, which passed more than 12 months shall be checked solder-ability before use.

Recommended Soldering Technologies

7.1 Reflow Profile

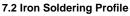
 \wedge Preheat condition: 150 ~200°C/60~120sec.

Δ Allowed time above 217°C: 60~90sec.

 \triangle Max temp: 260°C

 \triangle Max time at max temp: 10sec. Λ Solder paste: Sn/3.0Ag/0.5Cu Δ Allowed Reflow time: 2x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



Iron soldering power: Max.30W

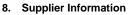
Pre-heating: 150 °C / 60 sec. Δ

Soldering tip temperature: 350 °C Max. \wedge

Soldering time: 3 sec Max. \wedge \wedge Solder paste: Sn/3.0Ag/0.5Cu

 \triangle Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



a) Supplier:

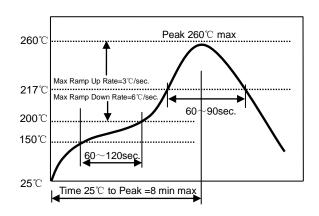
Shenzhen Shannon Semiconductor Co., Ltd.

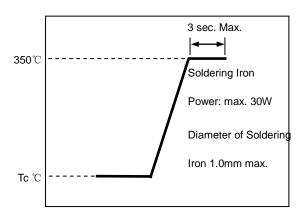
b) Manufacturer:

Shenzhen Shannon Semiconductor Co., Ltd.

c) Manufacturing Address:

Shenzhen nanshan district and high - tech zone Gao Xinnan four W2 - 502 A Zip: 518000





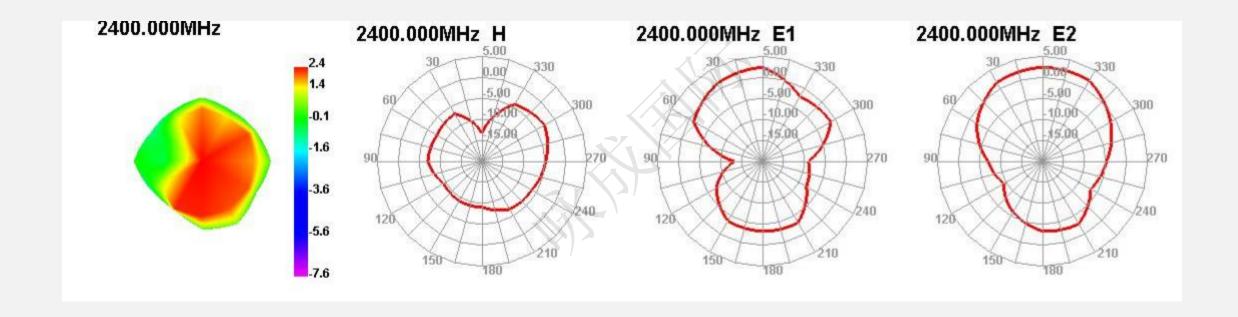
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Antenna efficiency gain 天线效率与增益图

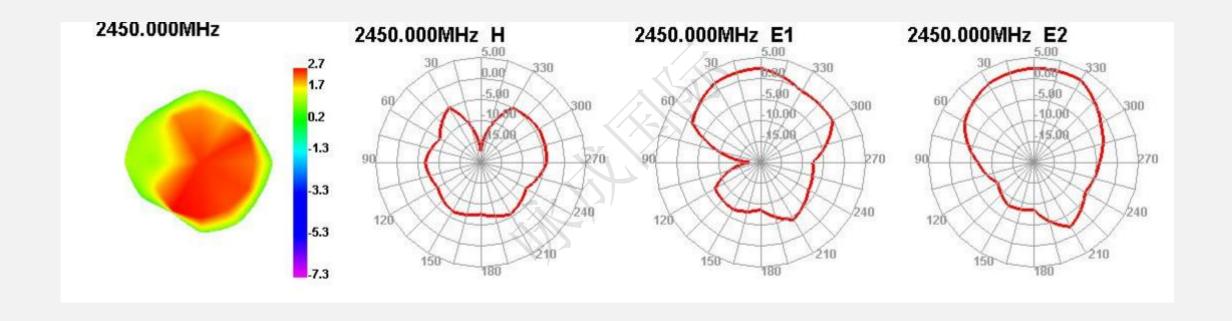
L/R

Passive Test For BT2.4						
Freq (MHz)	Effi (%)	Effi (dB)	Gain (dBi)	Gain (dBd)		
2400	39.04	-4. 09	0.52	-1.63		
2410	39.11	-4.08	0.73	-1.42		
2420	35. 33	-4. 52	0.47	-1.68		
2430	39. 98	-3.98	1.14	-1.01		
2440	37.5	-4.26	0.98	-1.17		
2450	44.14	-3.55	1.69	-0.46		
2460	39.63	-4.02	1.17	-0.98		
2470	38. 25	-4.17	0.95	-1.2		
2480	49.71	-3.04	2.06	-0.09		
2490	41.14	-3.86	1.2	-0.95		
2500	43.15	-3.65	1.32	-0.83		

Antenna radiation pattern 2400.00MHZ



Antenna radiation pattern 2450.00MHZ



Antenna radiation pattern 2500.00MHZ

