

# SEECODE CO., LTD.

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Product Name : Remote Speaker Microphone  
Model Number : SCP960  
FCC ID : 2AEXZSCP960

## 1. EDR Clear

Relate EDR function off, Main Bluetooth chipset can be controlled by below register bit-fields settings using Software control.

We can off(set to 0) related Enhanced Data rate field, it makes totally EDR off by software.

## NAME

PSKEY\_LOCAL\_SUPPORTED\_FEATURES (0x00ef)

## SUMMARY

=> The chipset which start with the CSR87EE6 is Bluetooth Main chipset.

RFX2401C is RF AMP chipset for make the Bluetooth RF Class1. In Block diagram,

Local supported features block

## TYPE

3. Circuit Description: This exhibit states this device is compliant with Bluetooth V3.0,  
uint16[]

## DEFAULT VALUE

{ 0xffff, 0xfe8f, 0xff9b, 0x8759 }

## DESCRIPTION

A 4 element array of uint16s holding a bitfield that describes the features supported by the local device.

Coding matches the "coding of features" section in the LMP specification. The first byte of the coding of features is held in the lower byte of the first uint16, etc.

The key's default value gives the firmware's capabilities. Switching a bit from 0 to 1 does not magically create a missing capability.

This key configures the local LM. If a feature is marked as provided by the default value, then clearing the key's bit turns off the corresponding functionality. For example, setting the Encryption bit to zero stops the local device supporting encryption.

The three "flow control lag" bits should be left at zero.

The order in 1.2 of the spec is as follows.

First word, low to high (X => turned on in default config;

(X) => turned on if supported by hardware;) On Bit Feature X 0 3-slot packets X 1 5-slot packets

X 2 Encryption X 3 Slot offset X 4 Timing accuracy X 5 Master/slave switch X 6 Hold mode X 7 Sniff

X 8 Park mode X 9 Power control requests X 10 Data rate driven by channel quality X 11 SCO link

**X 12 HV2 packets X 13 HV3 packets X 14 mu-law voice encoding X 15 A-law voice encoding**

**Second word X 0 CVSD X 1 Paging scheme X 2 Power control X 3 Transparent SCO data**

**4 } 5 } L2CAP flow control lag (our default = 0) 6 } [1.2 features from this point] X 7 Broadcast**

**X 7 Sniff mode X 8 Park mode X 9 Power control requests X 10 Data rate driven by channel quality X 11 link X 12 HV2 packets .....**

**Fourth word X 0 Extended Inquiry Response 1-2 Reserved X 3 Secure Simple Pairing X 4 Encapsulated X 10 Enhanced Power Control 11-14 Reserved X 15 Extended features [i.e. highest bit in feature mask]**

<Name and signature of applicant>



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