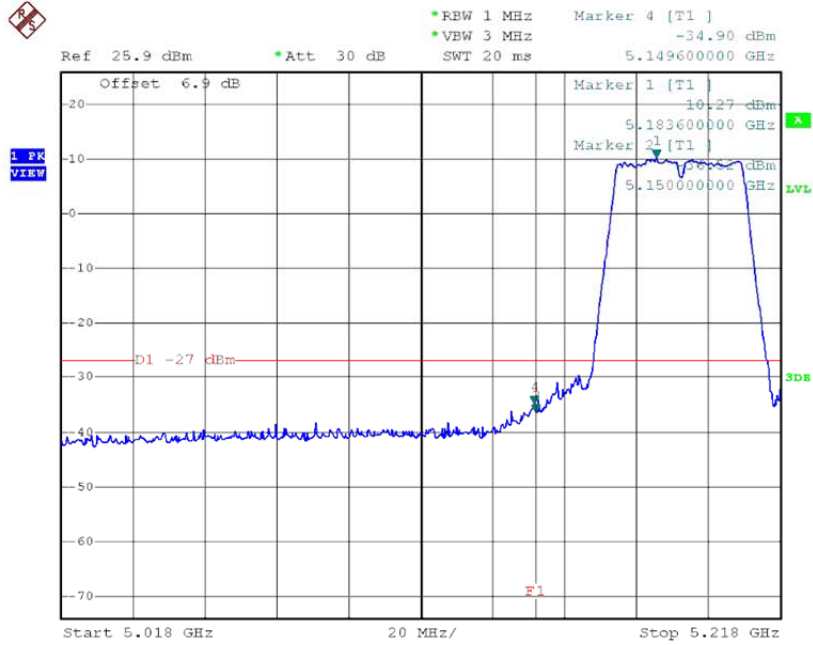


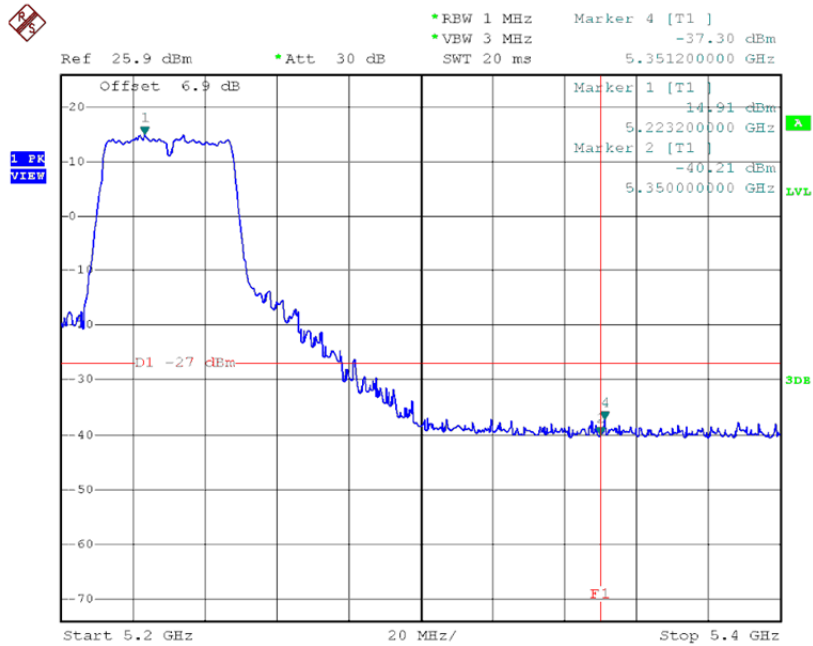
**Test Mode:** UNII-1/TX AC40 Mode\_ANT 6

**TX mode CH38**



Date: 19.APR.2015 15:18:15

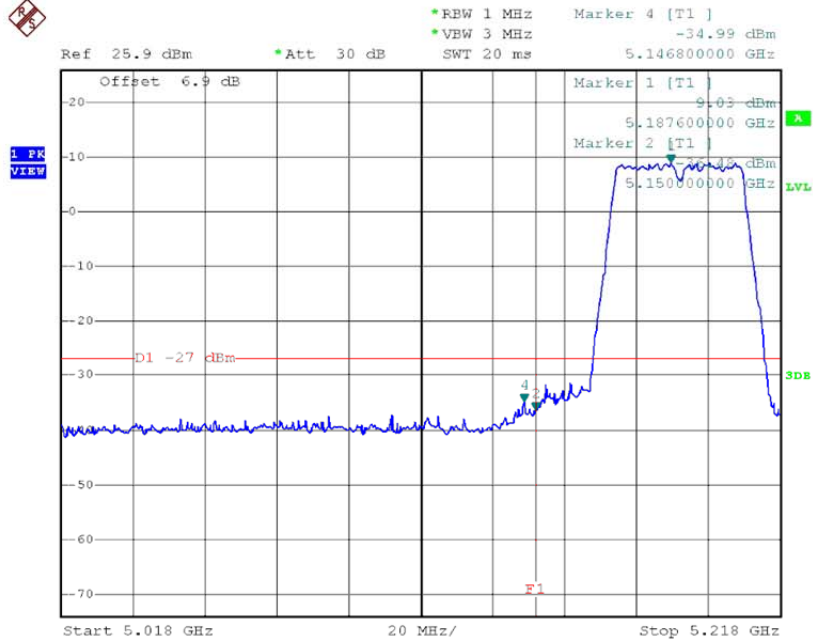
**TX mode CH46**



Date: 19.APR.2015 15:22:27

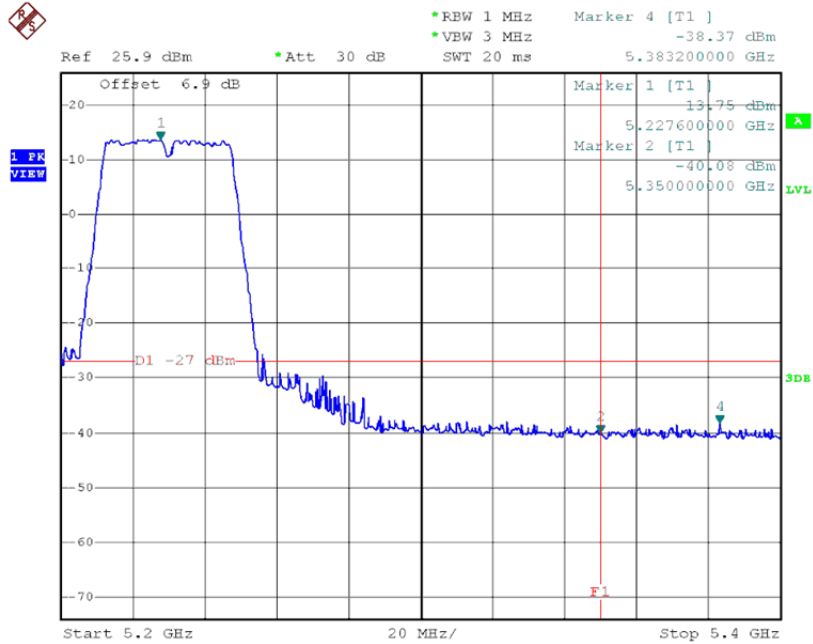
**Test Mode: UNII-1/TX AC40 Mode\_ANT 7**

**TX mode CH38**



Date: 19.APR.2015 15:19:07

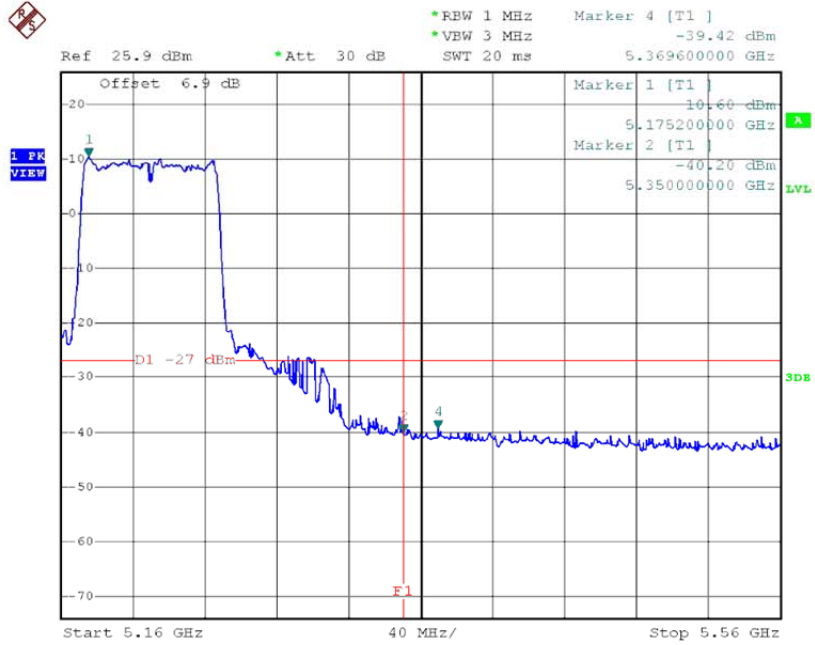
**TX mode CH46**



Date: 19.APR.2015 15:21:24

**Test Mode:** UNII-1/TX AC80 Mode\_ANT 4

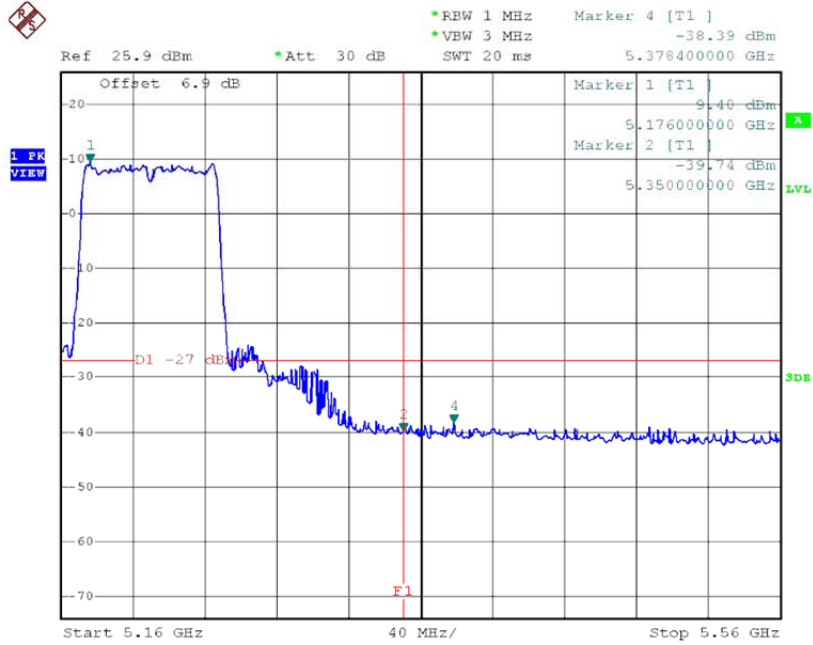
**TX mode CH42**



Date: 19.APR.2015 16:07:37

**Test Mode:** UNII-1/TX AC80 Mode\_ANT 5

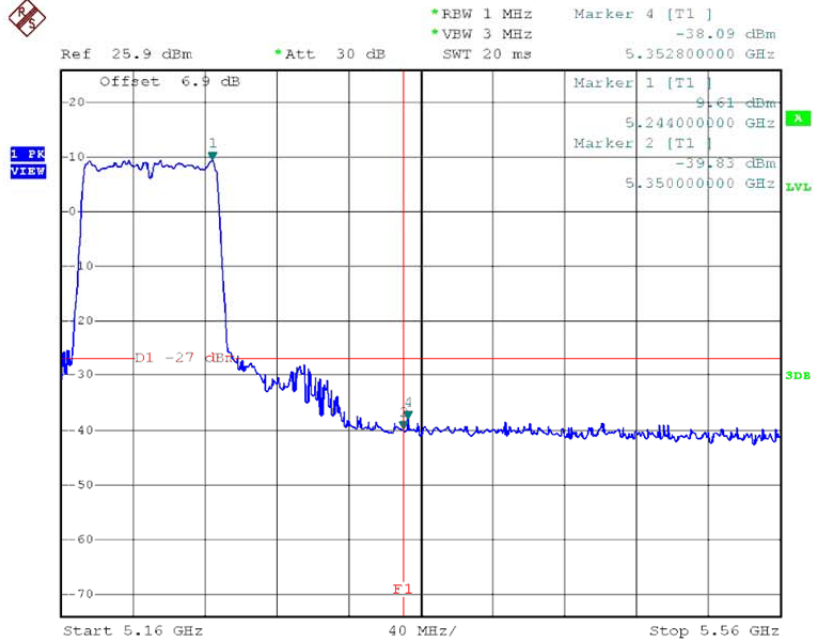
**TX mode CH42**



Date: 19.APR.2015 16:06:30

**Test Mode:** UNII-1/TX AC80 Mode\_ANT 6

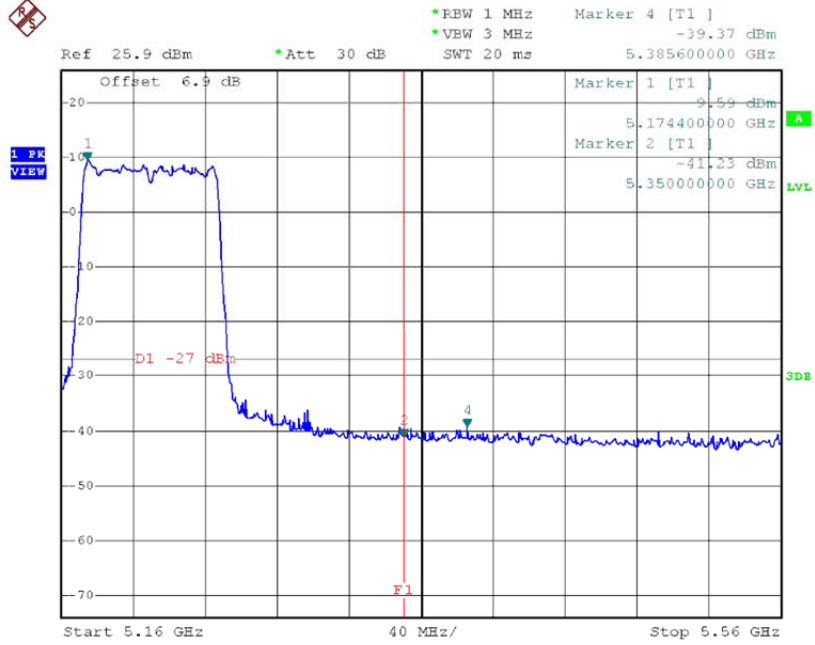
**TX mode CH42**



Date: 19.APR.2015 16:04:39

**Test Mode:** UNII-1/TX AC80 Mode\_ANT 7

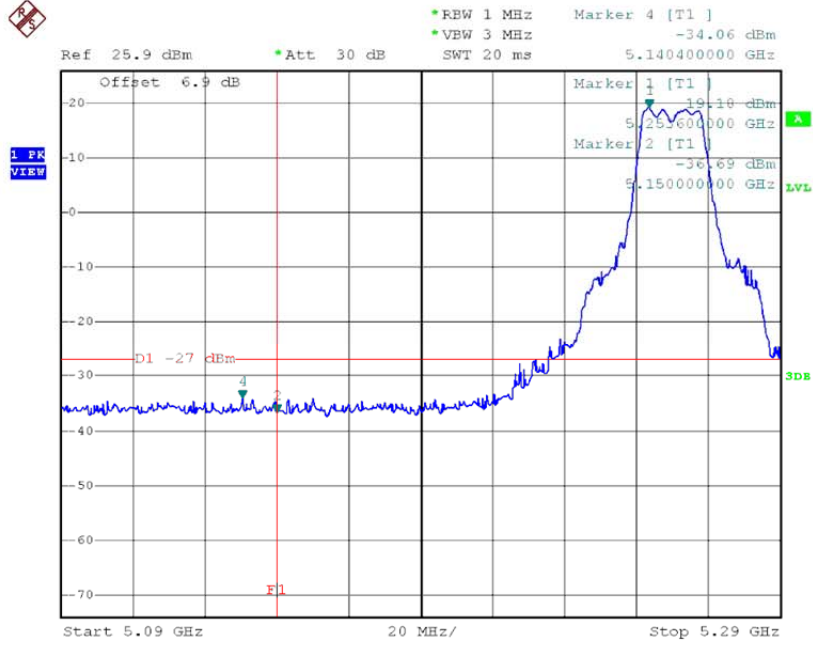
**TX mode CH42**



Date: 19.APR.2015 16:02:22

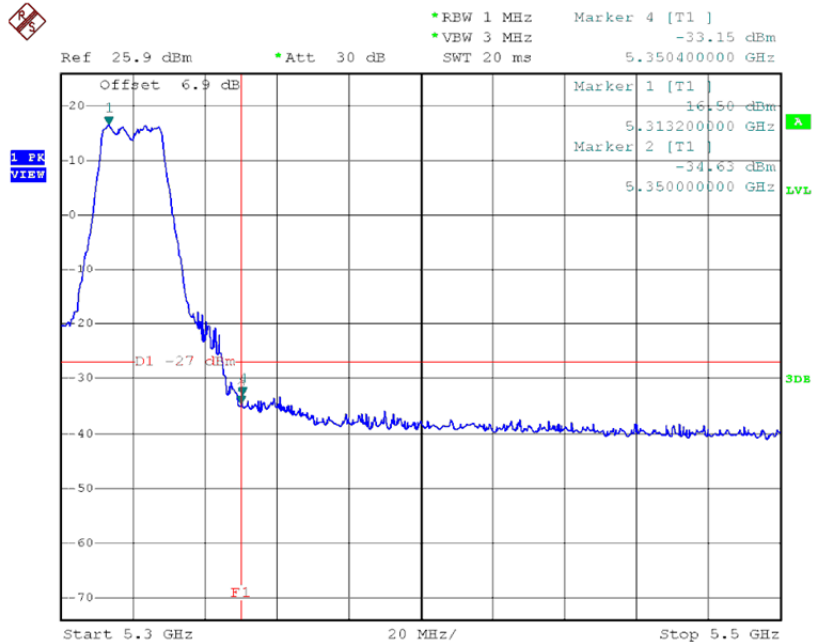
Test Mode: UNII-2A/TX AC20 Mode\_ANT 4

TX mode CH52



Date: 19.APR.2015 15:01:52

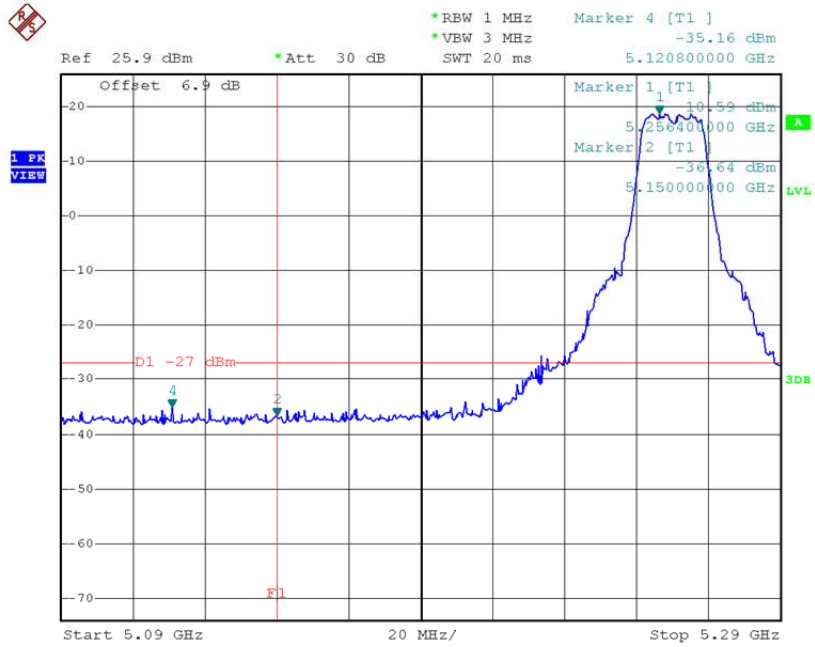
TX mode CH64



Date: 19.APR.2015 14:58:55

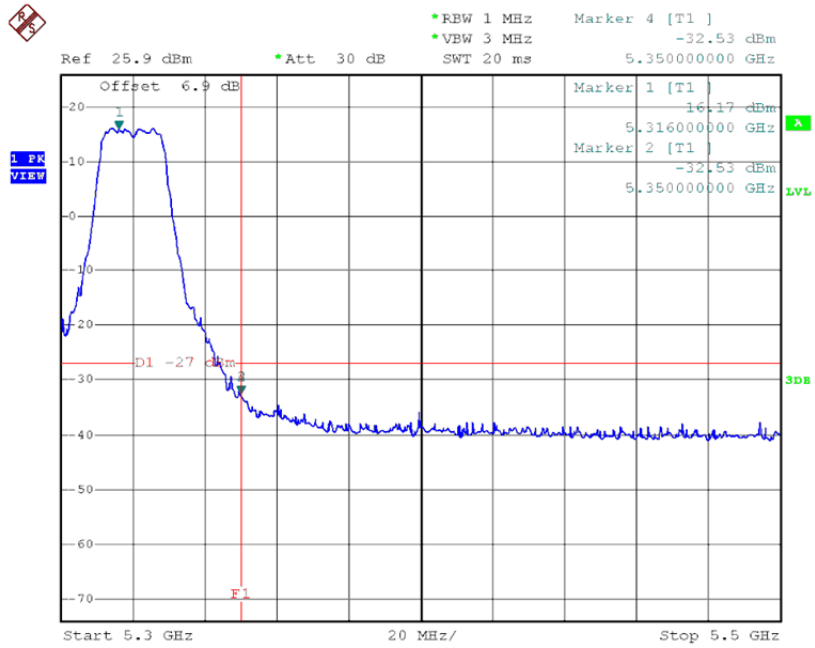
Test Mode: UNII-2A/TX AC20 Mode\_ANT 5

**TX mode CH52**



Date: 19.APR.2015 15:01:32

**TX mode CH64**

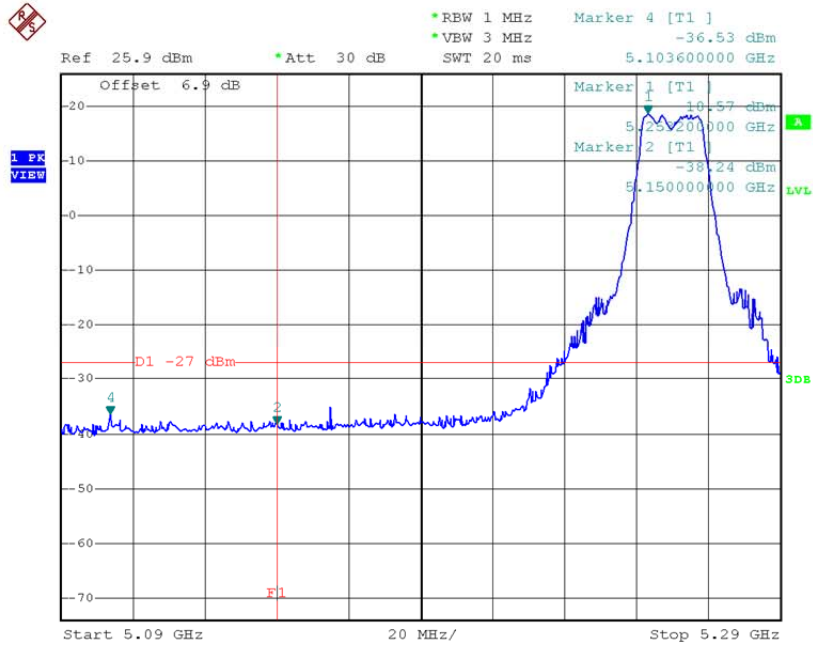


Date: 19.APR.2015 14:59:16



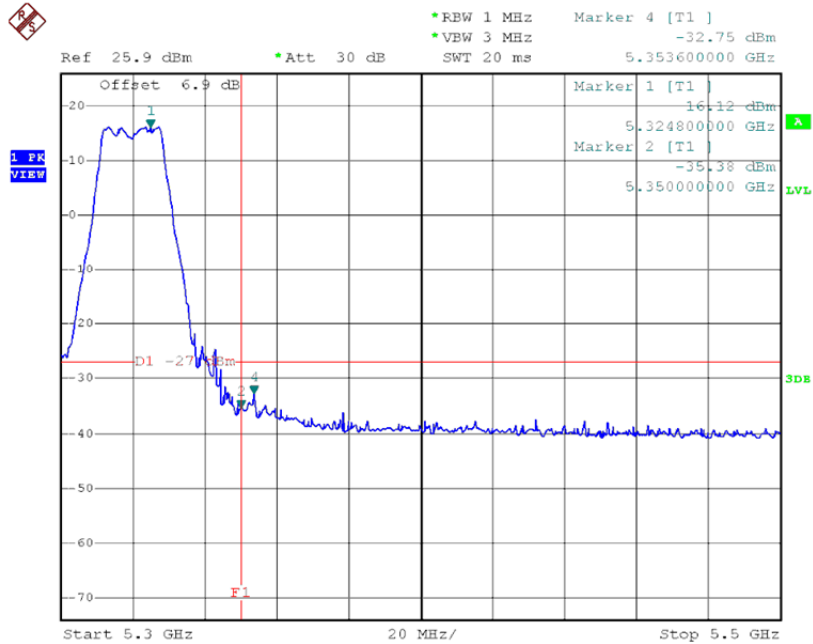
**Test Mode:** UNII-2A/TX AC20 Mode\_ANT 6

**TX mode CH52**



Date: 19.APR.2015 15:01:06

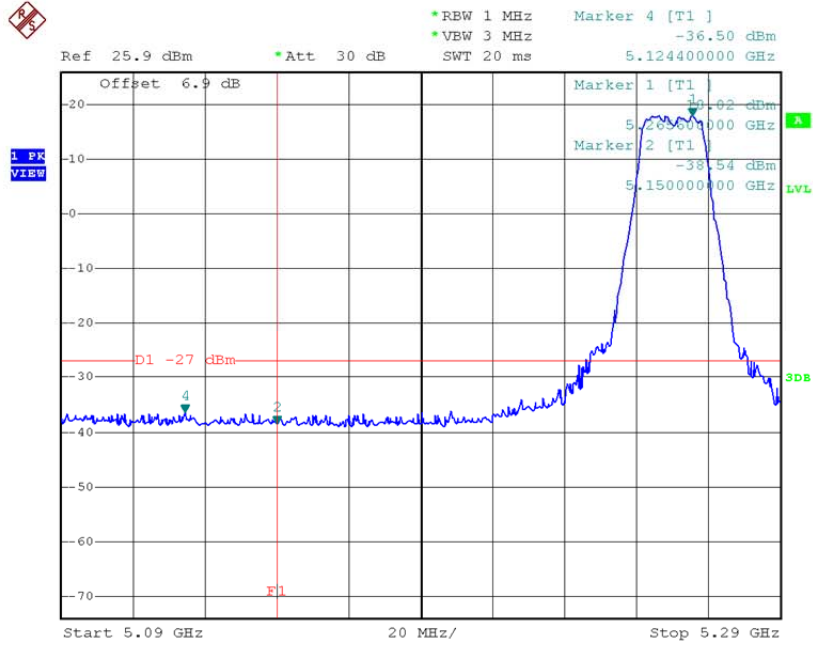
**TX mode CH64**



Date: 19.APR.2015 14:59:36

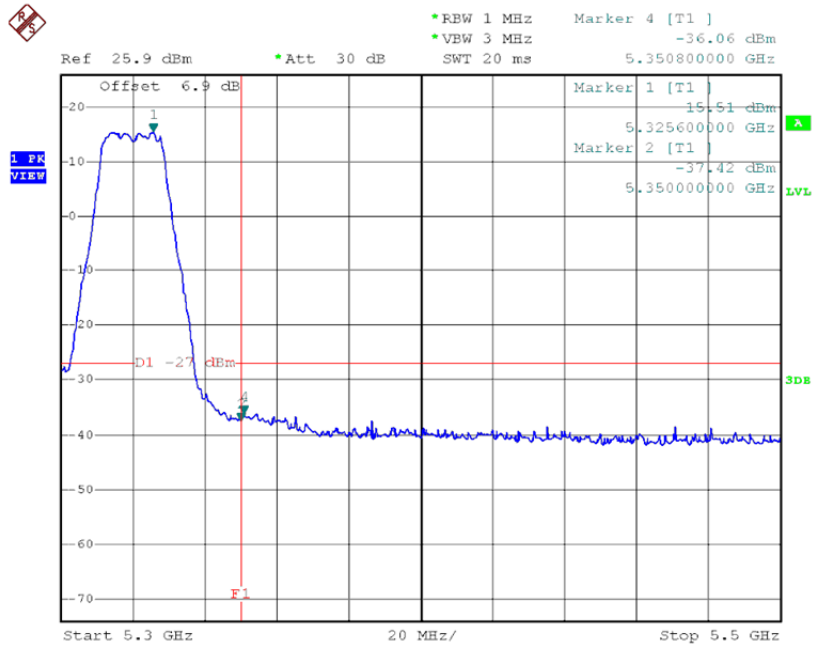
**Test Mode: UNII-2A/TX AC20 Mode\_ANT 7**

**TX mode CH52**



Date: 19.APR.2015 15:00:43

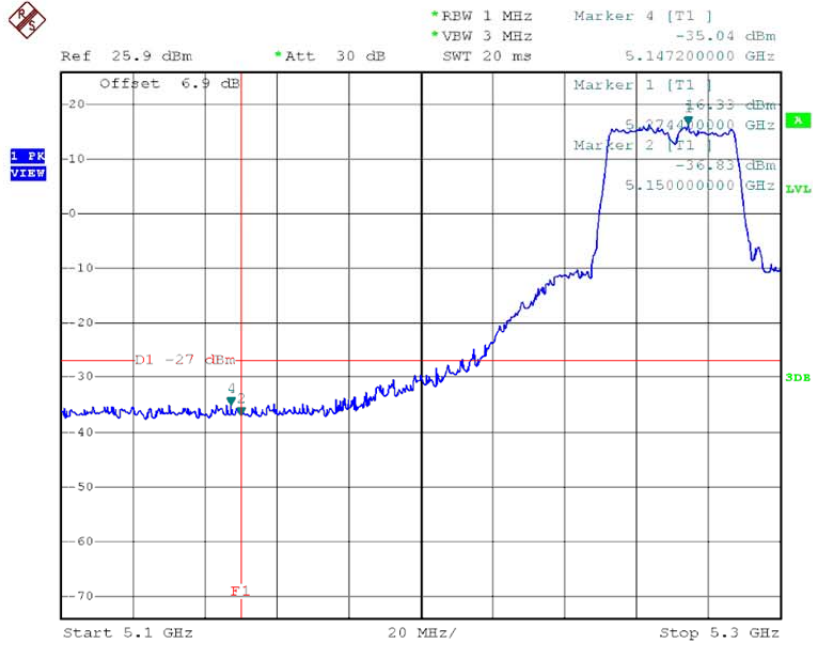
**TX mode CH64**



Date: 19.APR.2015 14:59:59

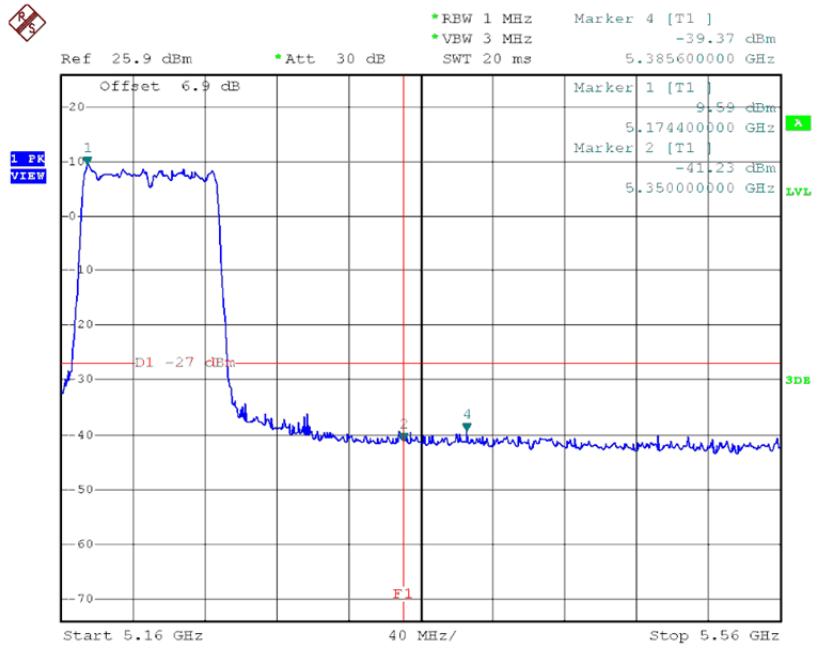
**Test Mode:** UNII-2A/TX AC40 Mode\_ANT 4

**TX mode CH54**



Date: 19.APR.2015 15:27:10

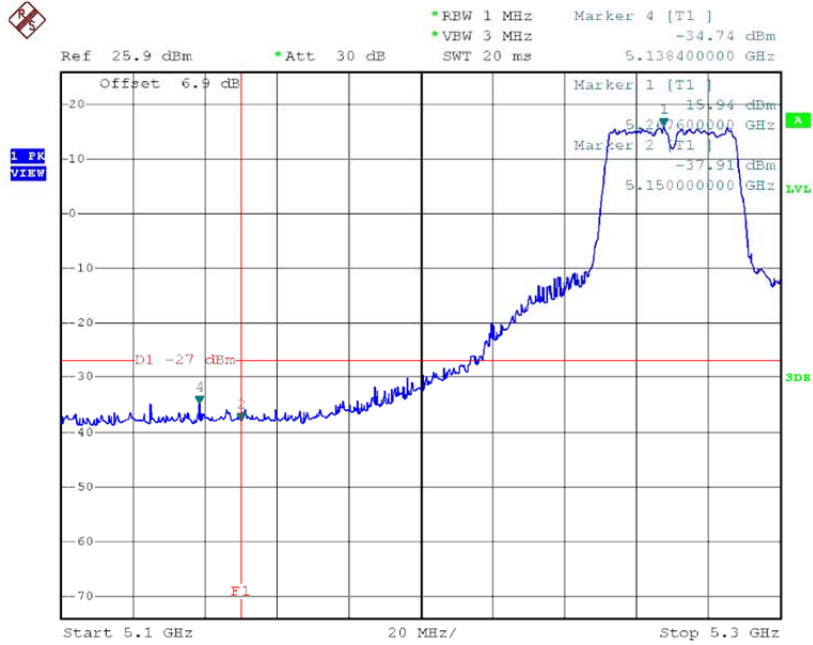
**TX mode CH62**



Date: 19.APR.2015 16:02:22

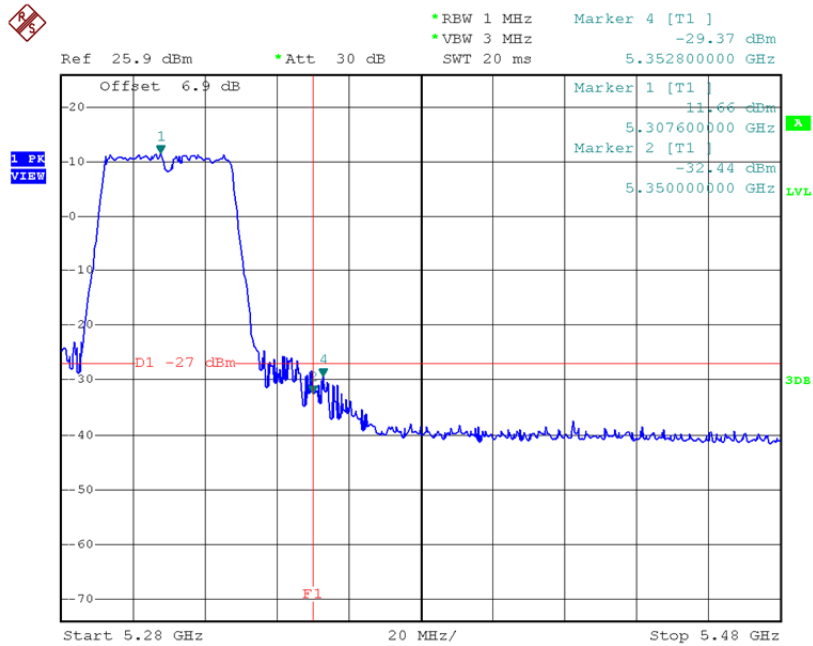
**Test Mode:** UNII-2A/TX AC40 Mode\_ANT 5

**TX mode CH54**



Date: 19.APR.2015 15:28:18

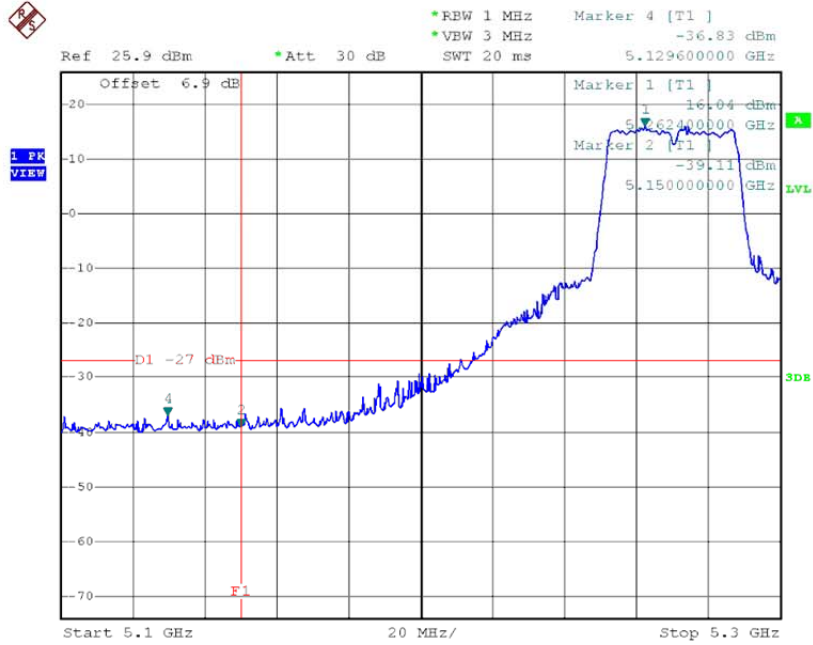
**TX mode CH62**



Date: 19.APR.2015 15:33:07

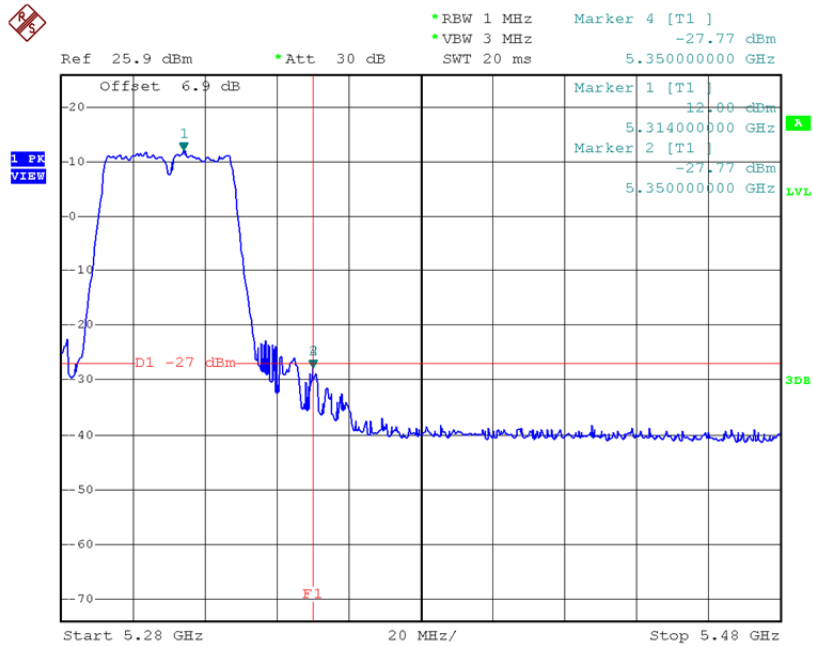
**Test Mode:** UNII-2A/TX AC40 Mode\_ANT 6

**TX mode CH54**



Date: 19.APR.2015 15:29:09

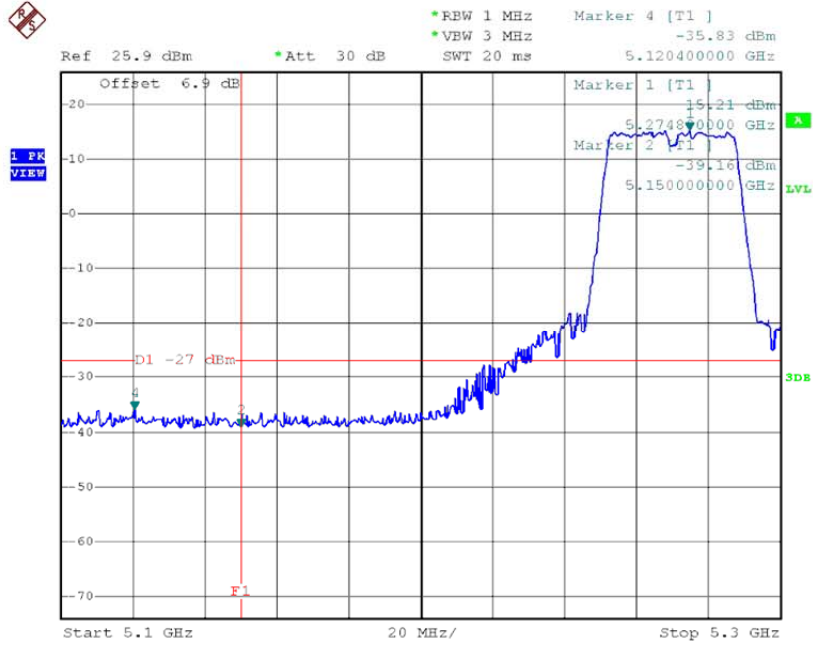
**TX mode CH62**



Date: 19.APR.2015 15:32:03

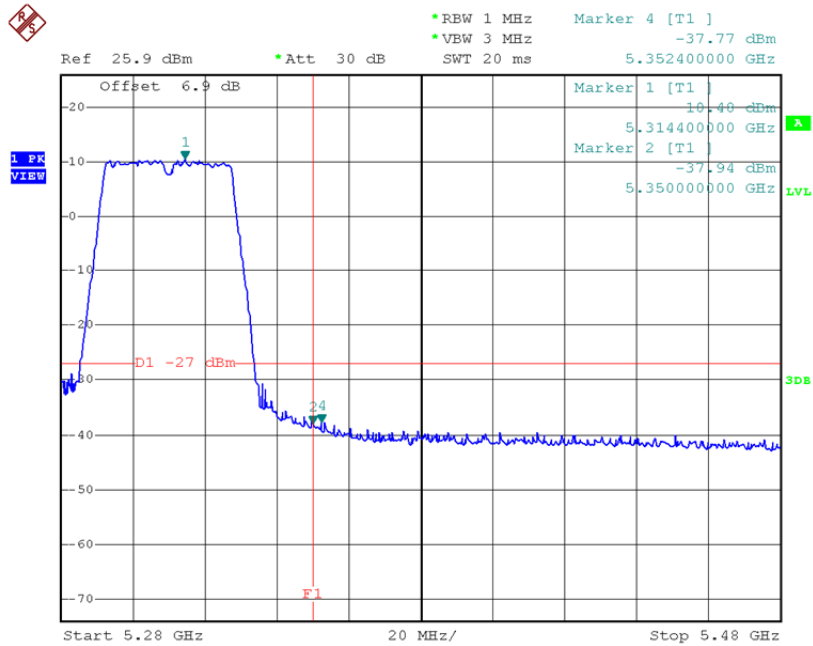
**Test Mode:** UNII-2A/TX AC40 Mode\_ANT 7

**TX mode CH54**



Date: 19.APR.2015 15:30:03

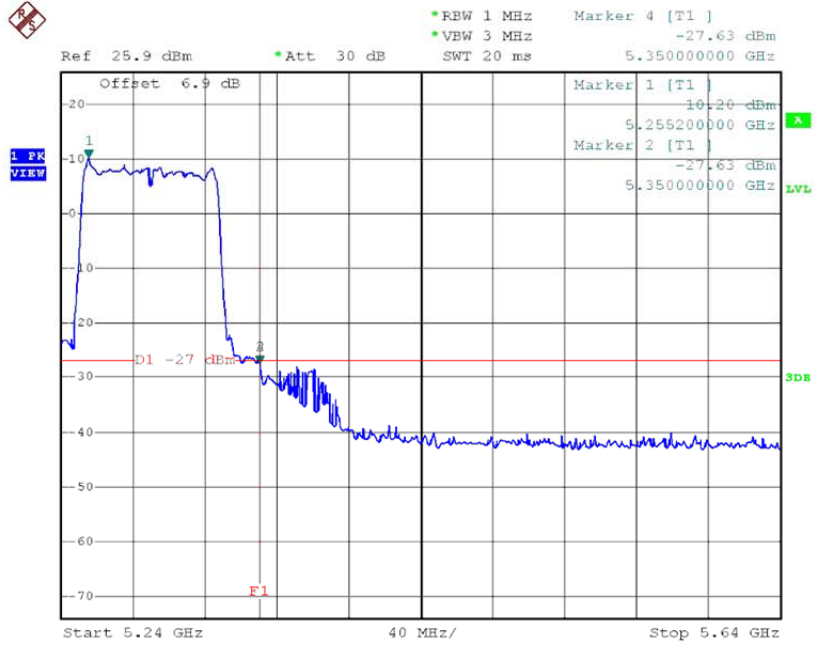
**TX mode CH62**



Date: 19.APR.2015 15:31:08

**Test Mode:** UNII-2A/TX AC80 Mode\_ANT 4

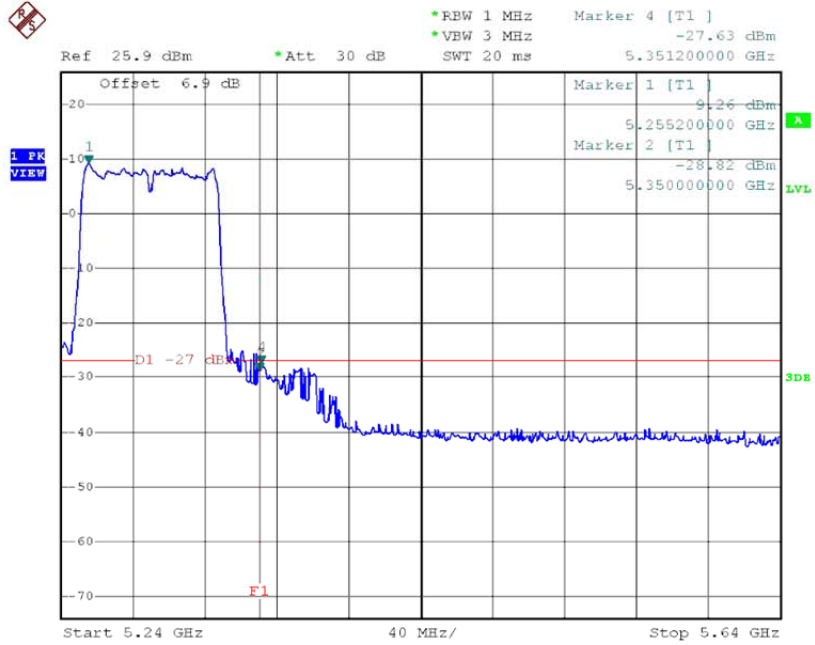
**TX mode CH58**



Date: 19.APR.2015 16:09:55

**Test Mode:** UNII-2A/TX AC80 Mode\_ANT 5

**TX mode CH58**

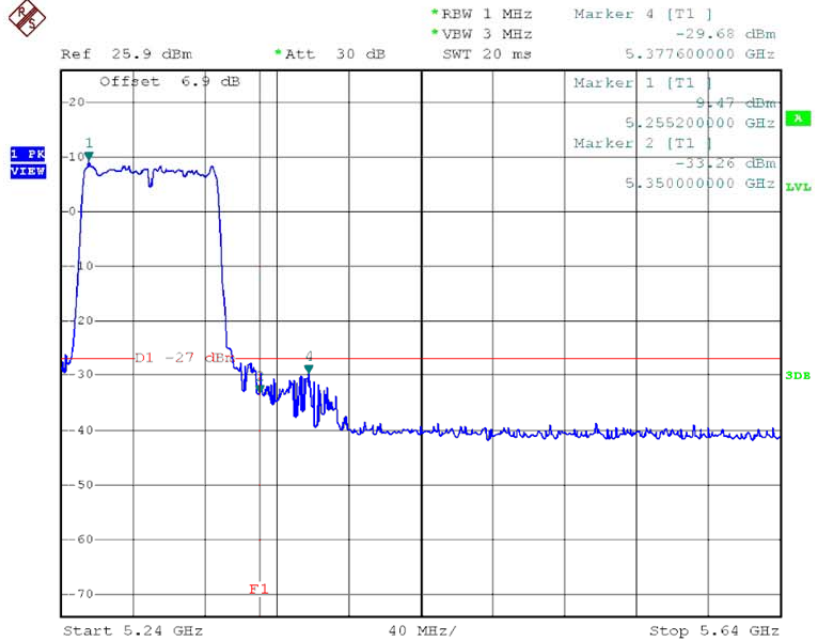


Date: 19.APR.2015 16:11:07



**Test Mode:** UNII-2A/TX AC80 Mode\_ANT 6

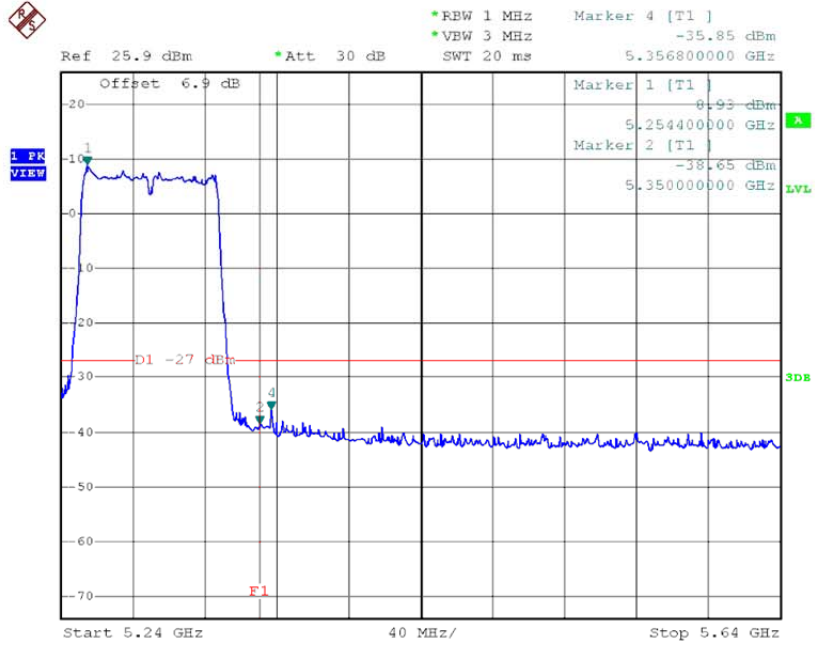
**TX mode CH58**



Date: 19.APR.2015 16:21:02

**Test Mode:** UNII-2A/TX AC80 Mode\_ANT 7

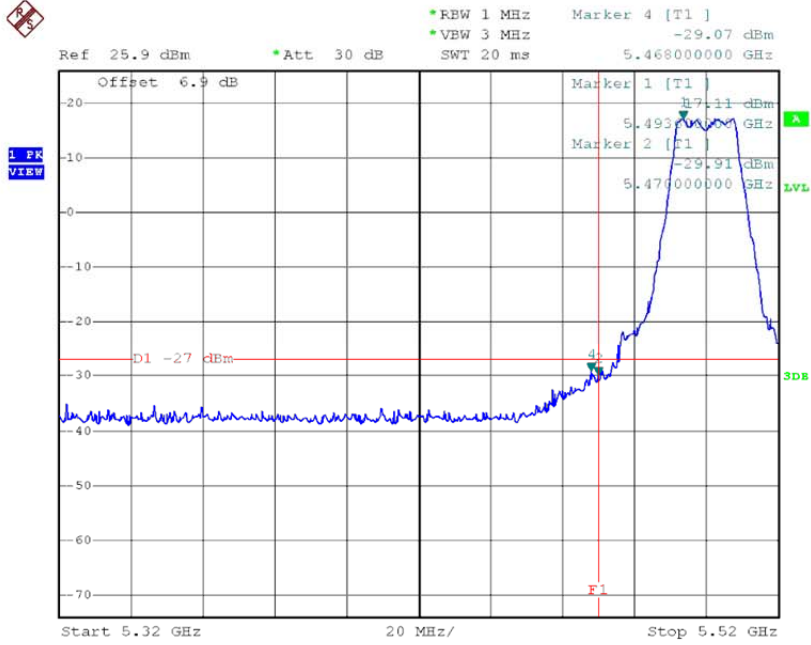
**TX mode CH58**



Date: 19.APR.2015 16:22:36

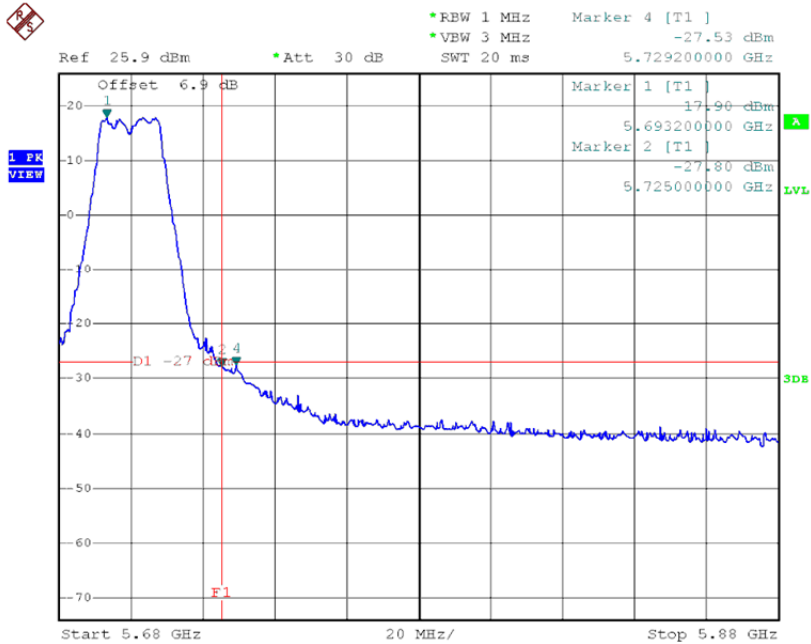
**Test Mode:** UNII-2C/TX AC20 Mode\_ANT 4

**TX mode CH100**



Date: 19.APR.2015 14:57:40

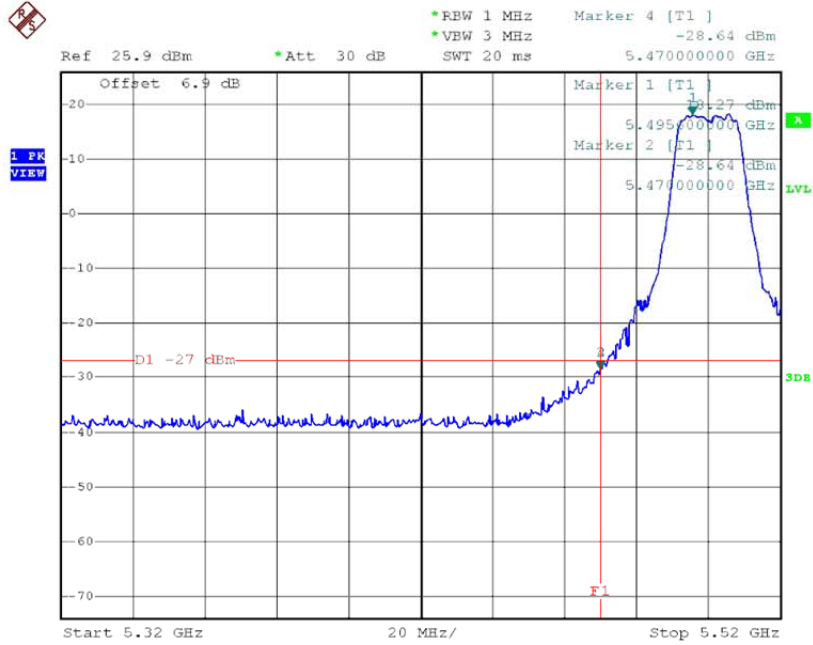
**TX mode CH140**



Date: 19.APR.2015 14:53:17

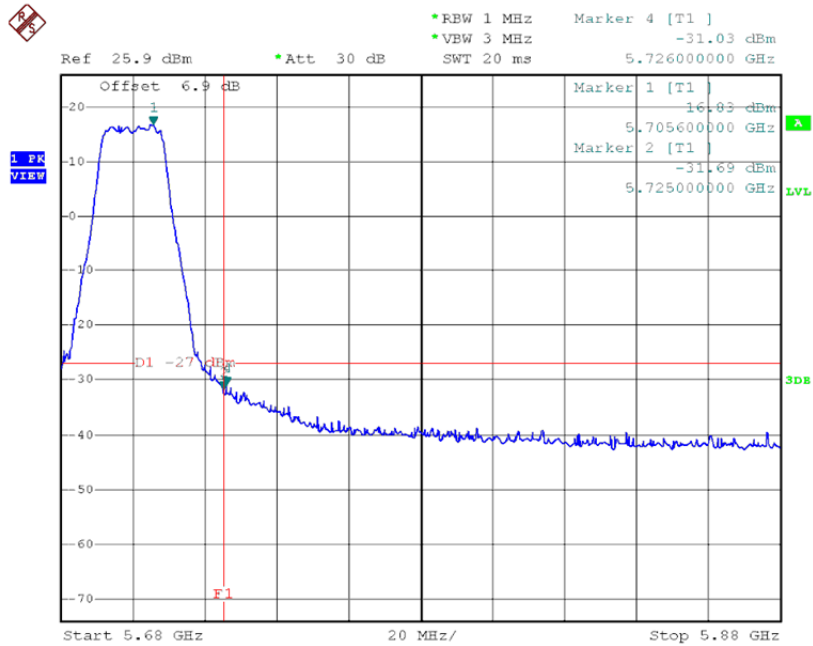
**Test Mode:** UNII-2C/TX AC20 Mode\_ANT 5

**TX mode CH100**



Date: 19.APR.2015 14:57:19

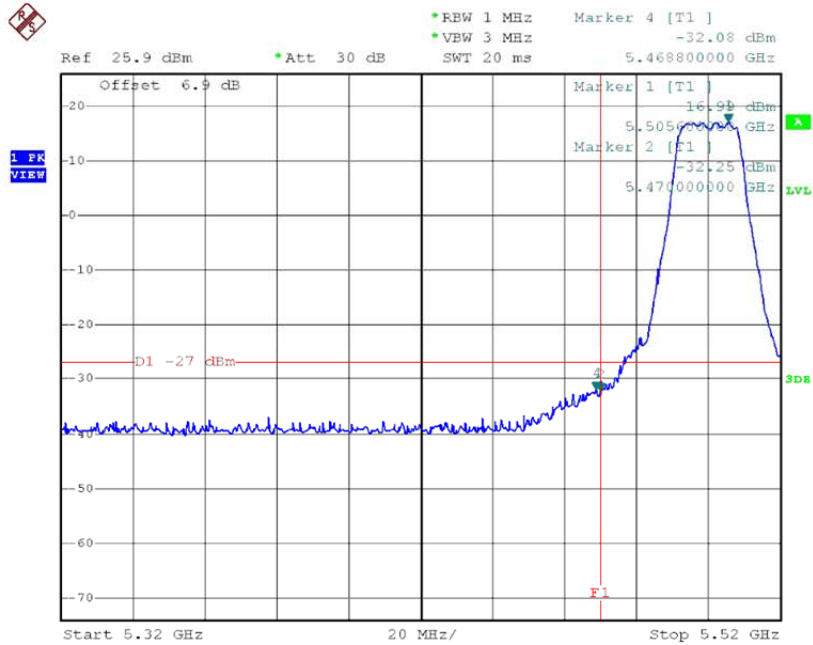
**TX mode CH140**



Date: 19.APR.2015 14:53:38

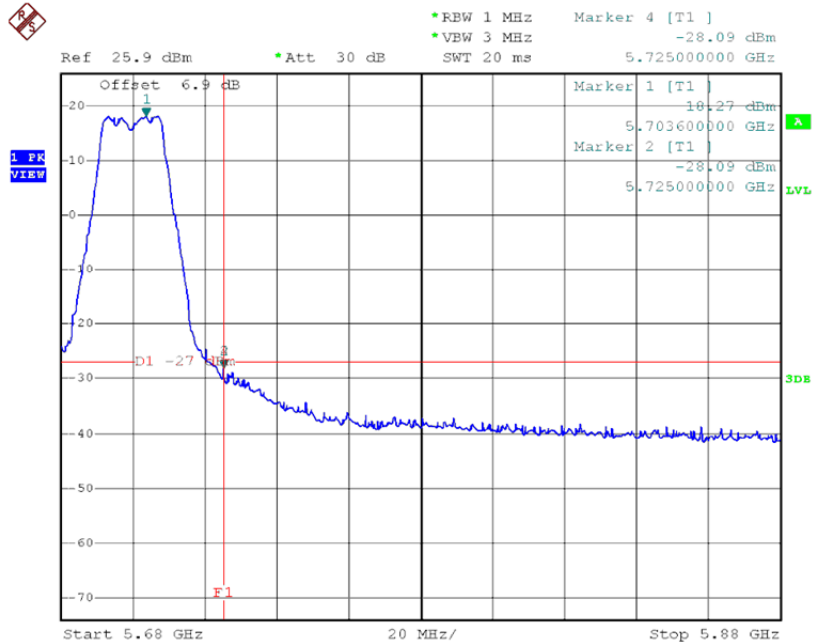
**Test Mode:** UNII-2C/TX AC20 Mode\_ANT 6

**TX mode CH100**



Date: 19.APR.2015 14:56:50

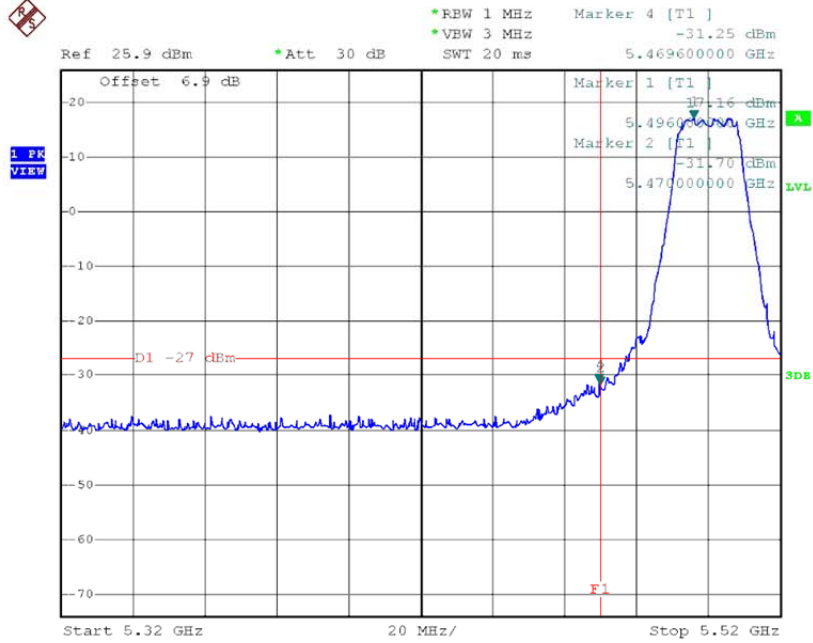
**TX mode CH140**



Date: 19.APR.2015 14:54:22

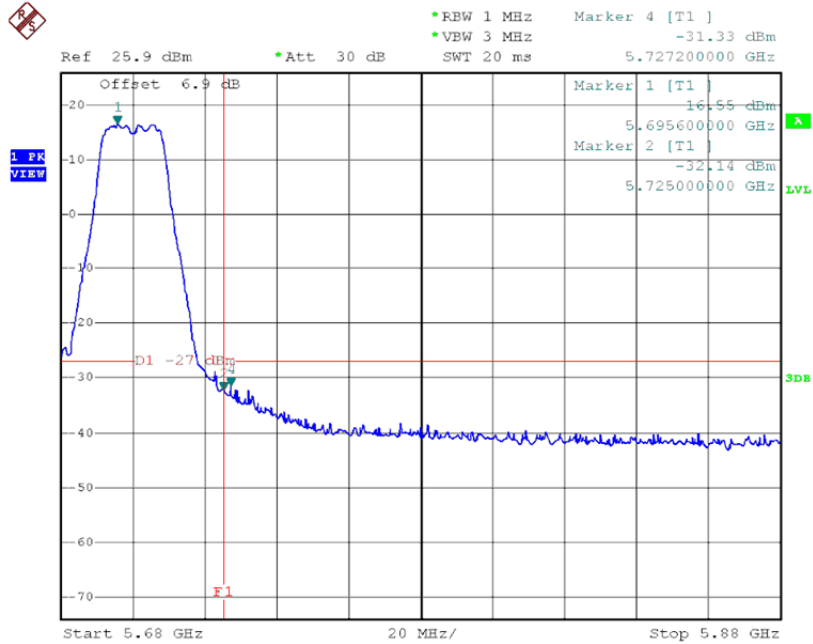
**Test Mode:** UNII-2C/TX AC20 Mode\_ANT 7

**TX mode CH100**



Date: 19.APR.2015 14:56:28

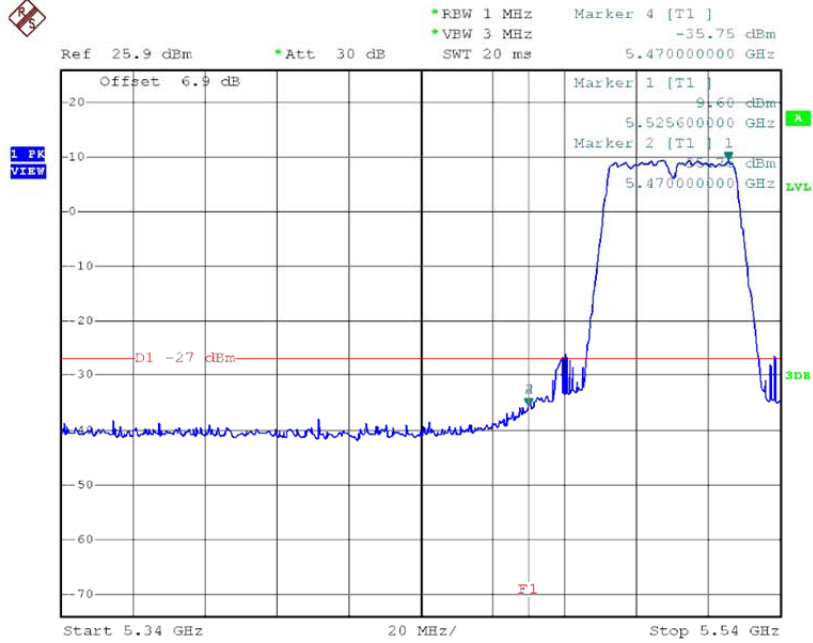
**TX mode CH140**



Date: 19.APR.2015 14:54:44

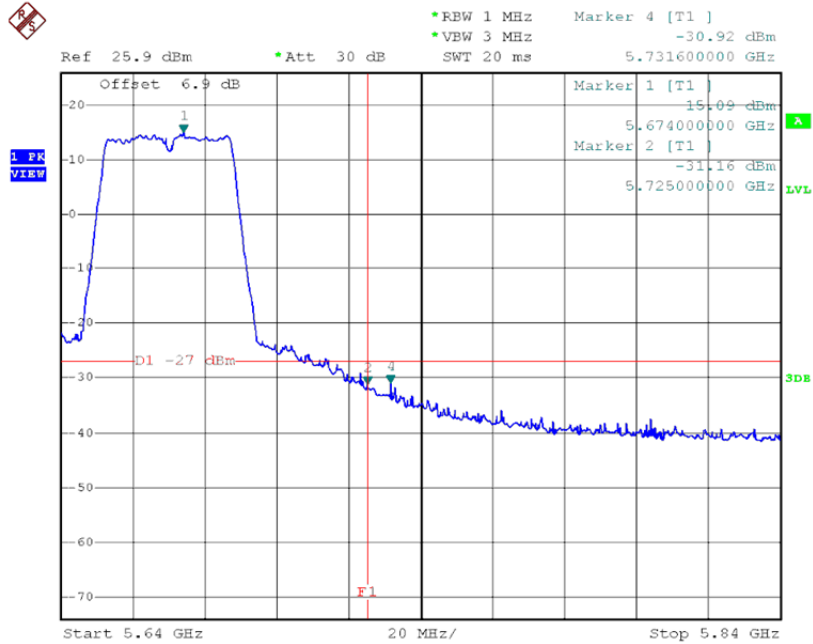
**Test Mode:** UNII-2C/TX AC40 Mode\_ANT 4

**TX mode CH102**



Date: 19.APR.2015 15:35:13

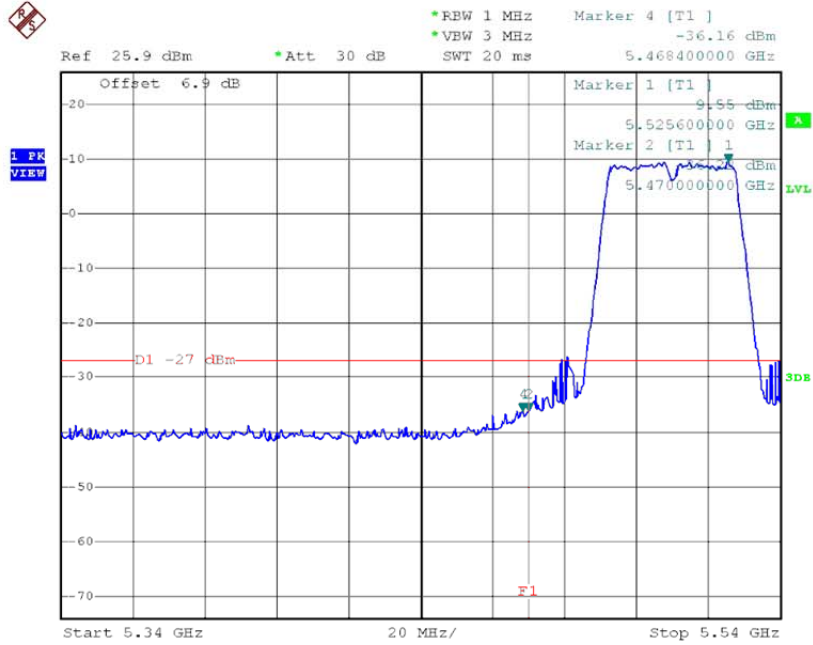
**TX mode CH134**



Date: 19.APR.2015 15:44:47

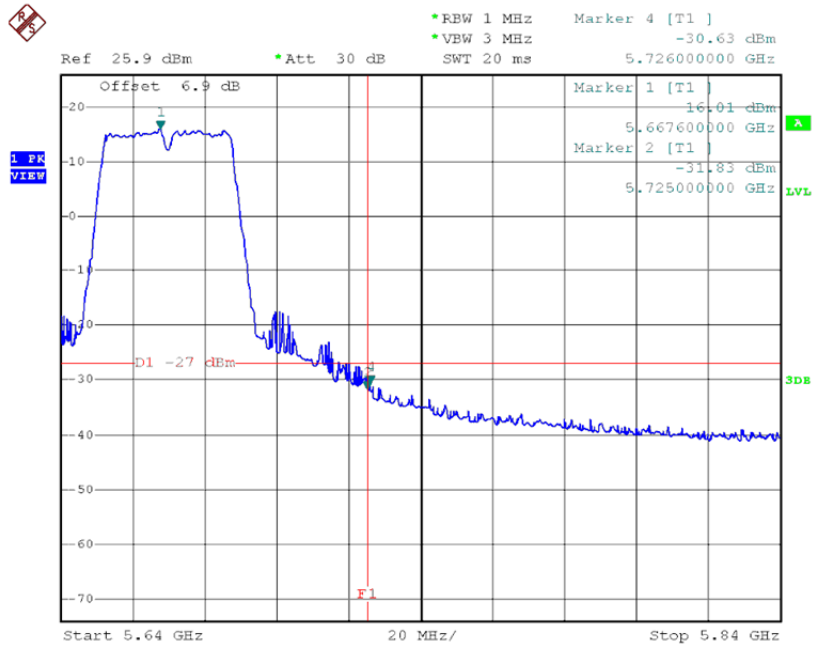
**Test Mode: UNII-2C/TX AC40 Mode\_ANT 5**

**TX mode CH102**



Date: 19.APR.2015 15:36:02

**TX mode CH134**

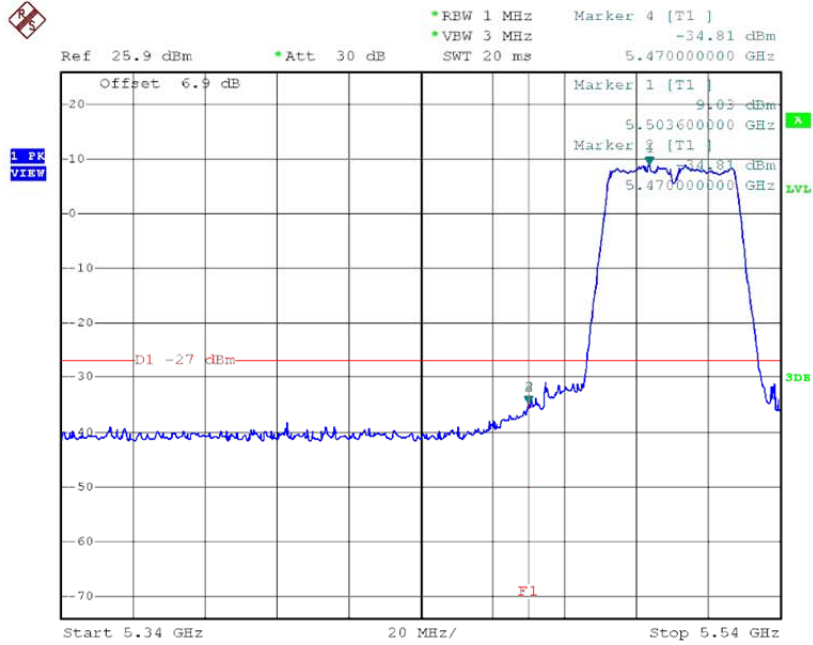


Date: 19.APR.2015 15:45:35



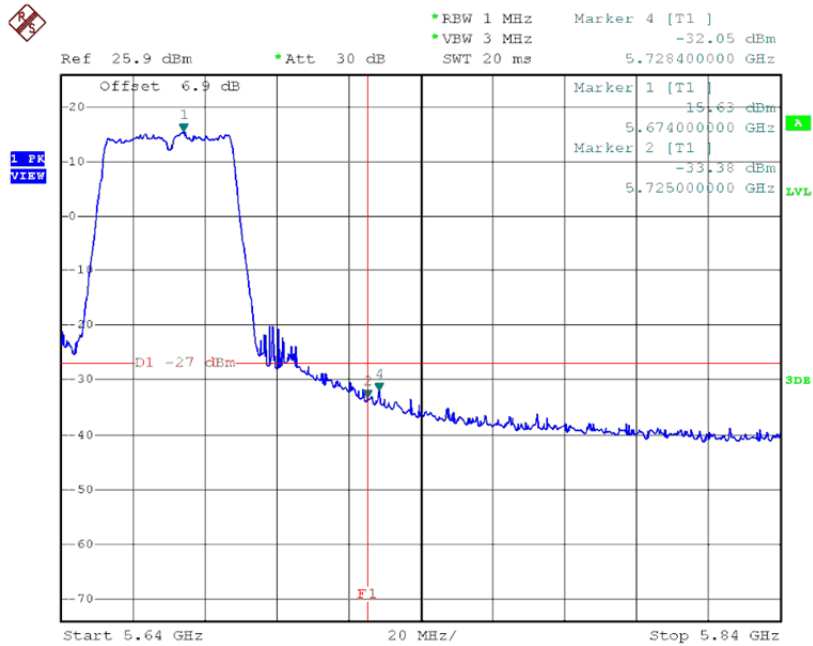
**Test Mode: UNII-2C/TX AC40 Mode\_ANT 6**

**TX mode CH102**



Date: 19.APR.2015 15:37:01

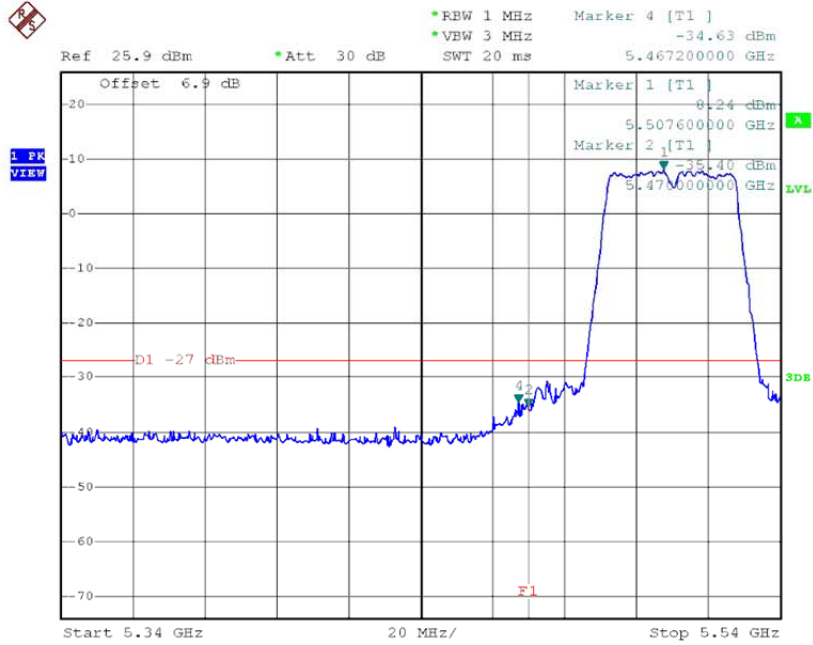
**TX mode CH134**



Date: 19.APR.2015 15:46:23

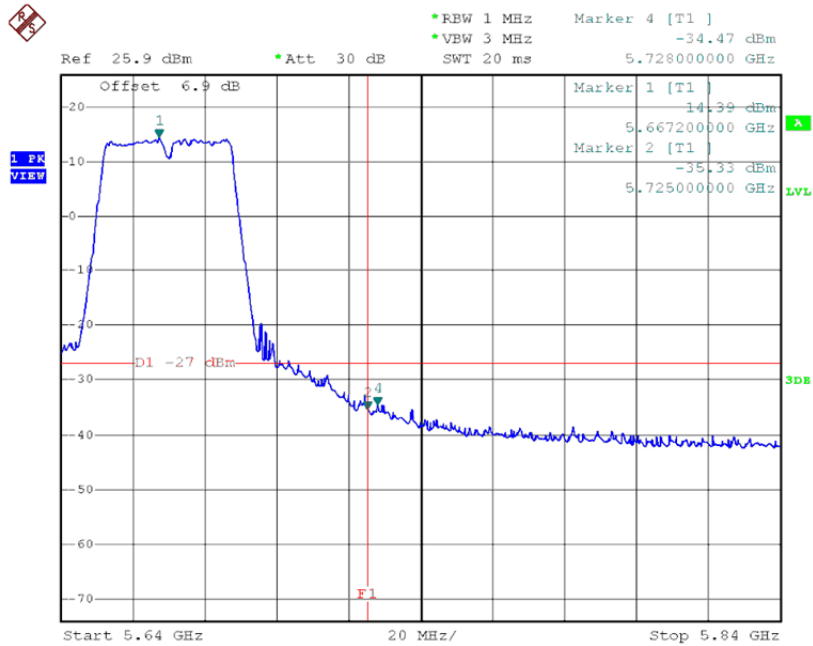
**Test Mode:** UNII-2C/TX AC40 Mode\_ANT 7

**TX mode CH102**



Date: 19.APR.2015 15:39:59

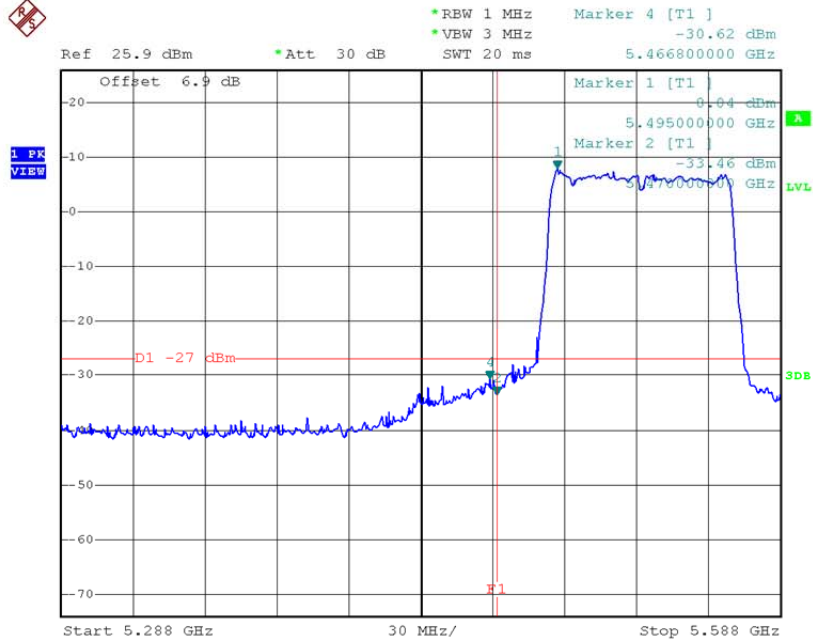
**TX mode CH134**



Date: 19.APR.2015 15:47:11

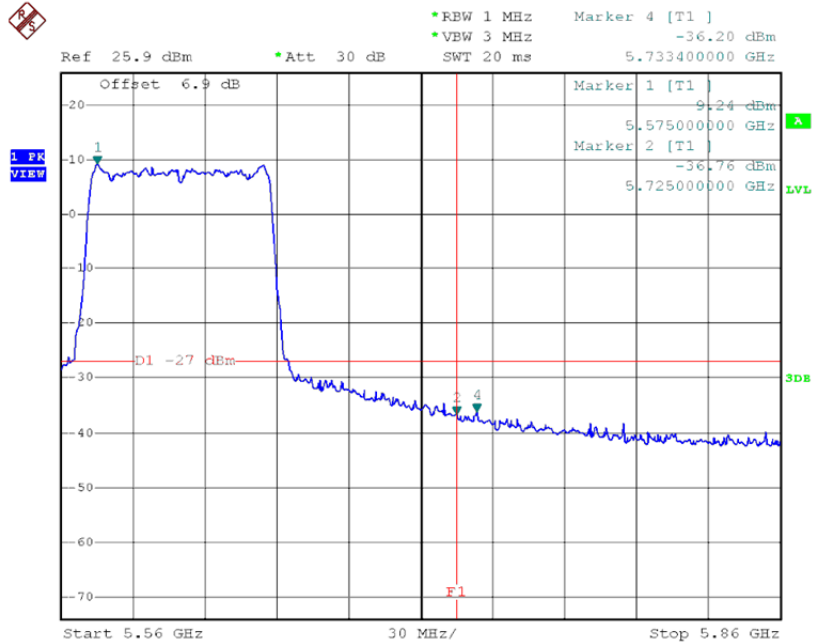
**Test Mode:** UNII-2C/TX AC80 Mode\_ANT 4

**TX mode CH106**



Date: 19.APR.2015 16:27:50

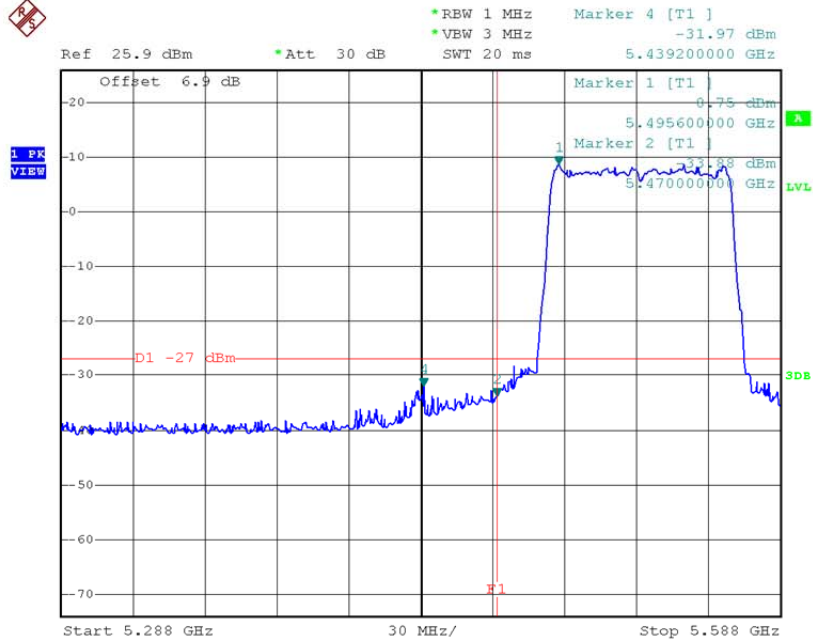
**TX mode CH122**



Date: 19.APR.2015 16:29:20

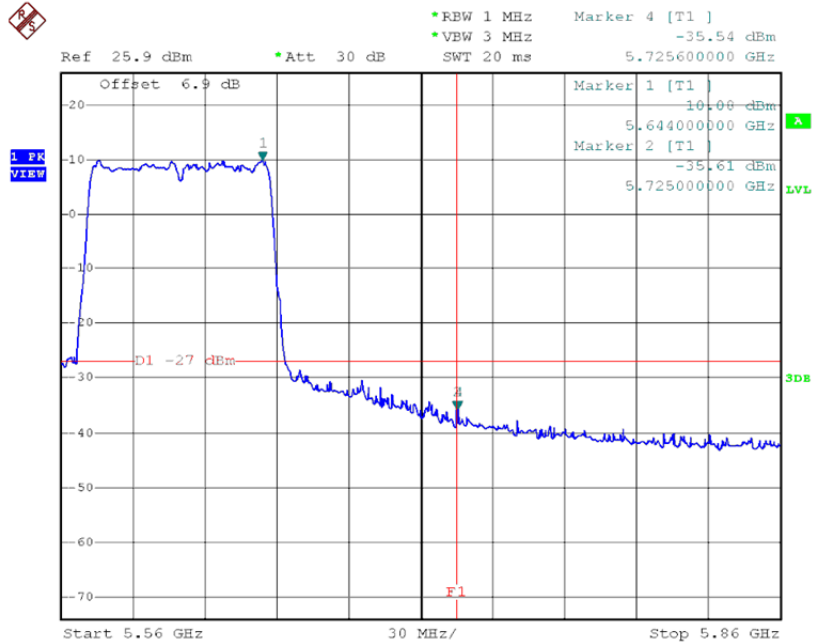
**Test Mode:** UNII-2C/TX AC80 Mode\_ANT 5

**TX mode CH106**



Date: 19.APR.2015 16:26:42

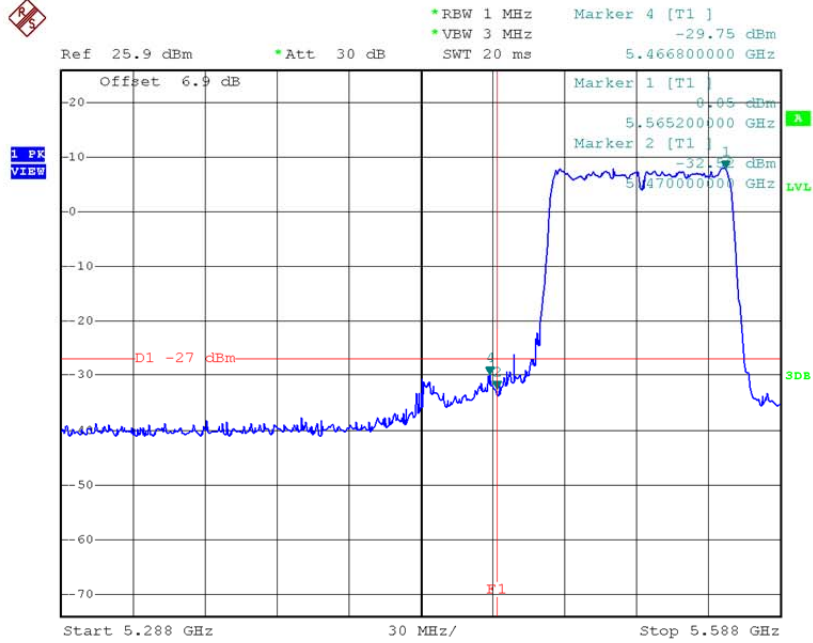
**TX mode CH122**



Date: 19.APR.2015 16:30:49

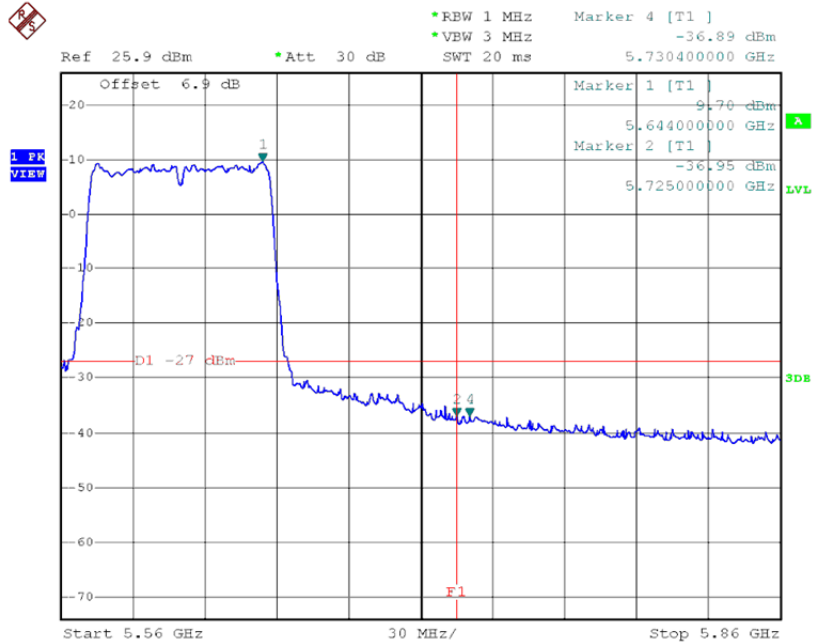
**Test Mode:** UNII-2C/TX AC80 Mode\_ANT 6

**TX mode CH106**



Date: 19.APR.2015 16:25:24

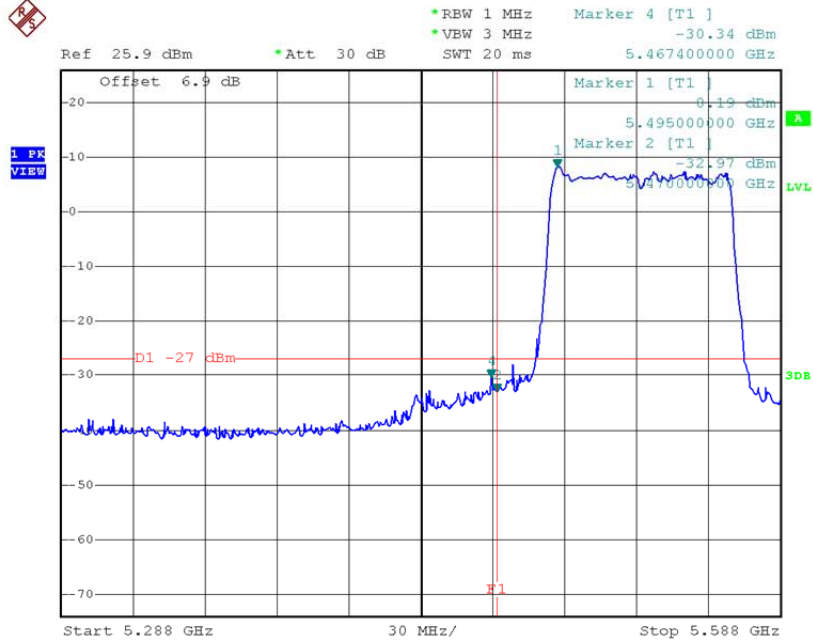
**TX mode CH122**



Date: 19.APR.2015 16:32:10

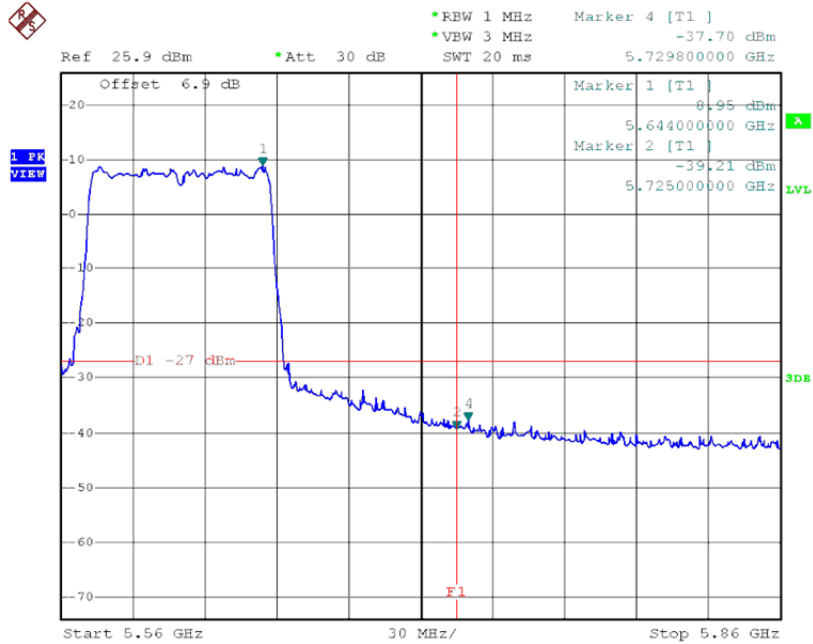
**Test Mode:** UNII-2C/TX AC80 Mode\_ANT 7

**TX mode CH106**



Date: 19.APR.2015 16:24:00

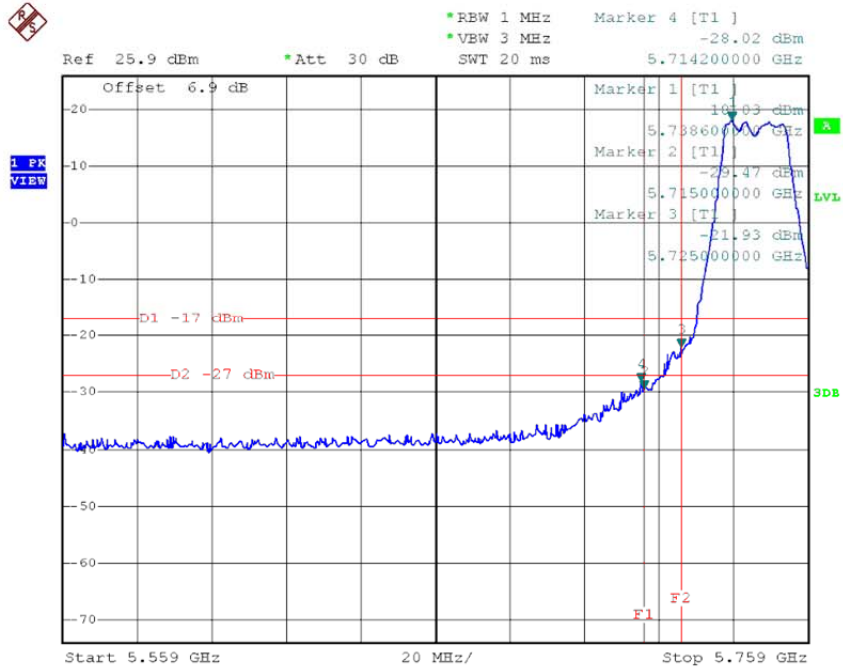
**TX mode CH122**



Date: 19.APR.2015 16:33:22

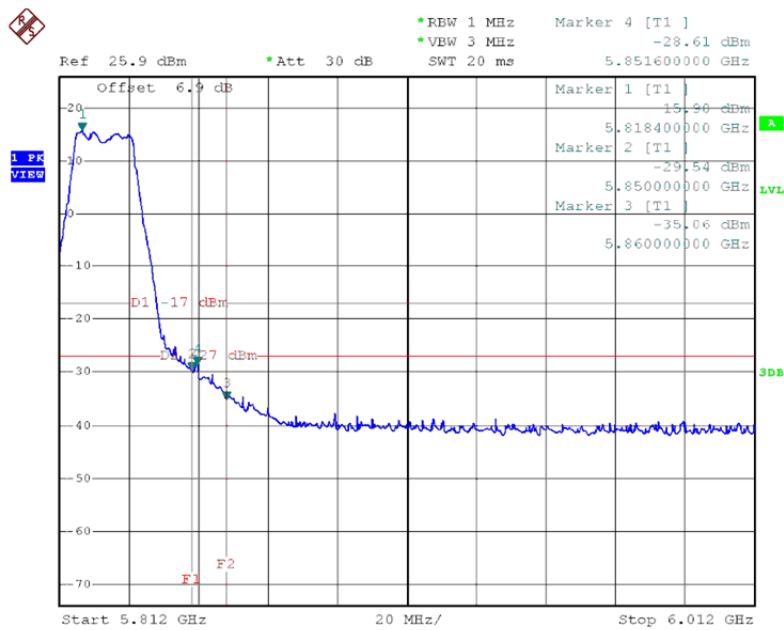
Test Mode: UNII-3/TX AC20 Mode\_ANT 4

### TX AC HT20 mode CH149



Date: 19.APR.2015 14:52:07

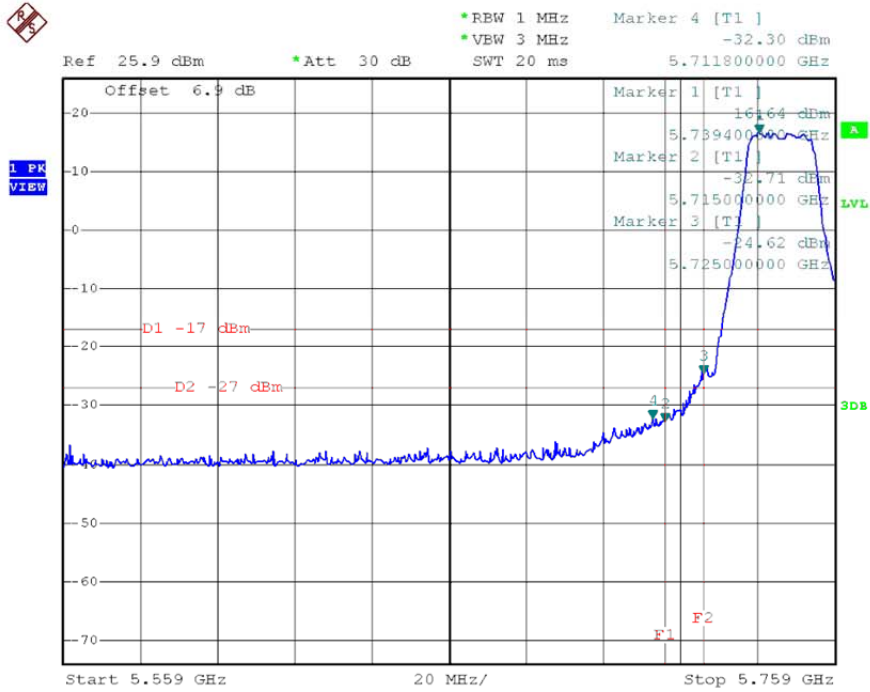
### TX AC HT20 mode CH165



Date: 19.APR.2015 14:48:33

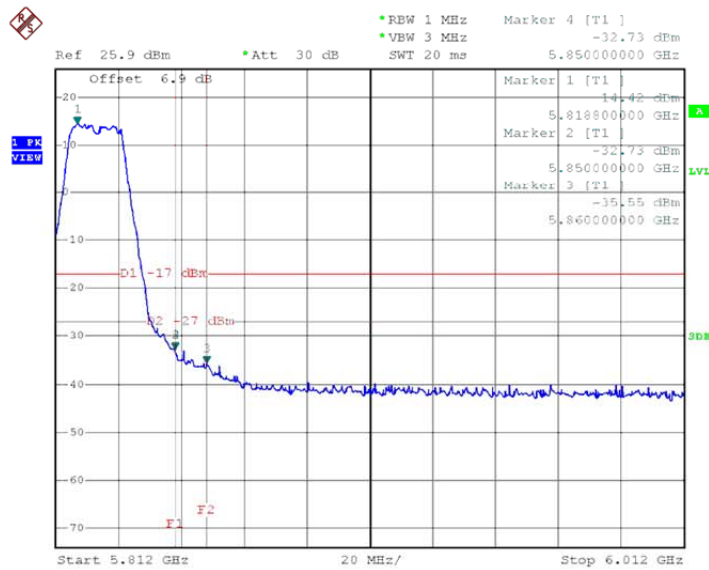
Test Mode: UNII-3/TX AC20 Mode\_ANT 5

### TX AC HT20 mode CH149



Date: 19.APR.2015 14:51:44

### TX AC HT20 mode CH165

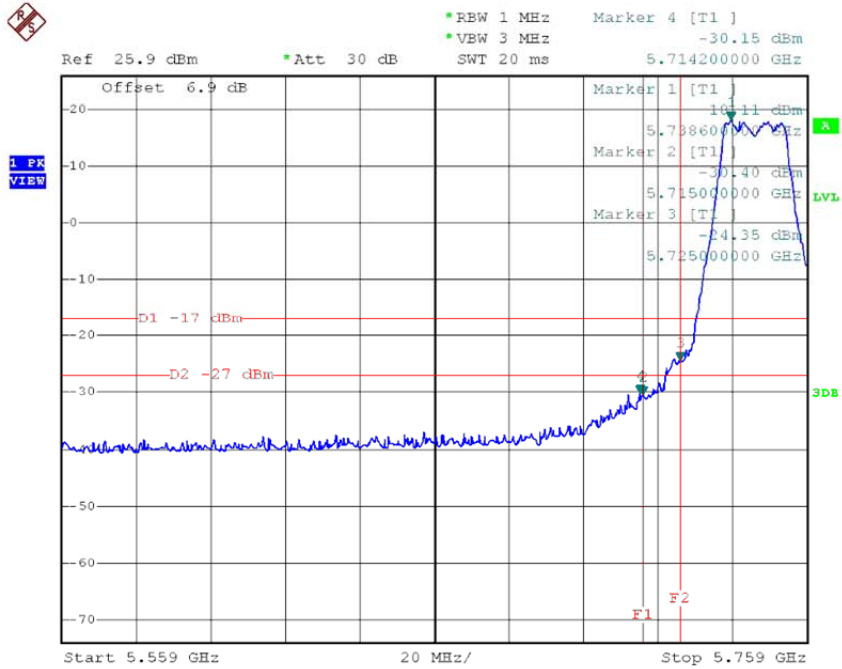


Date: 19.APR.2015 14:49:01



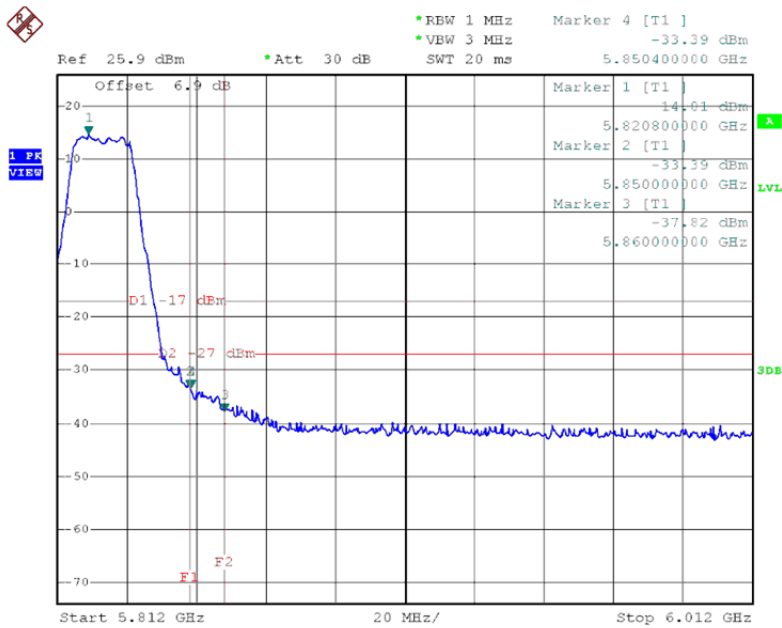
Test Mode: UNII-3/TX AC20 Mode\_ANT 6

### TX AC HT20 mode CH149



Date: 19.APR.2015 14:51:14

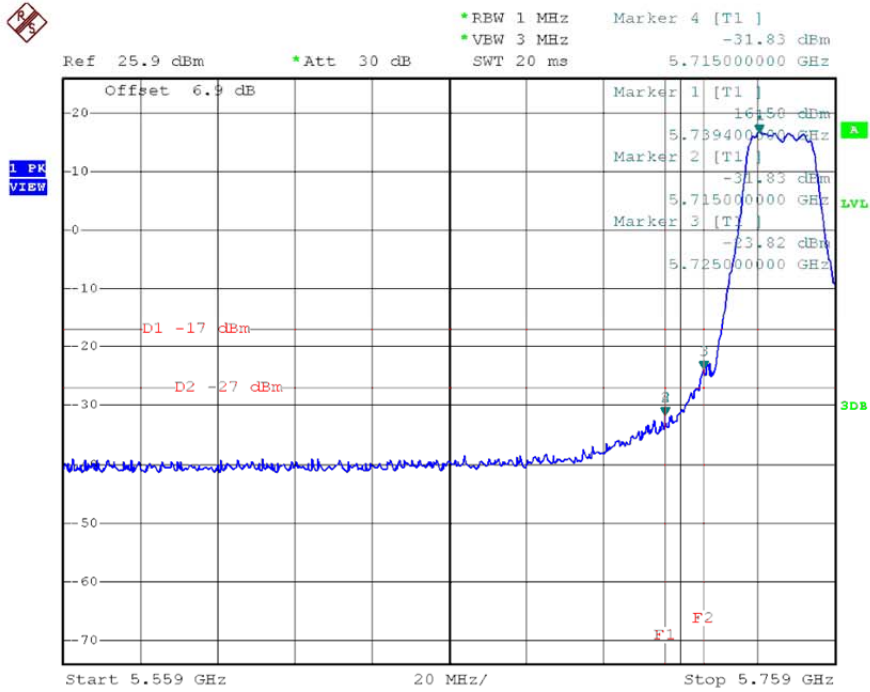
### TX AC HT20 mode CH165



Date: 19.APR.2015 14:49:22

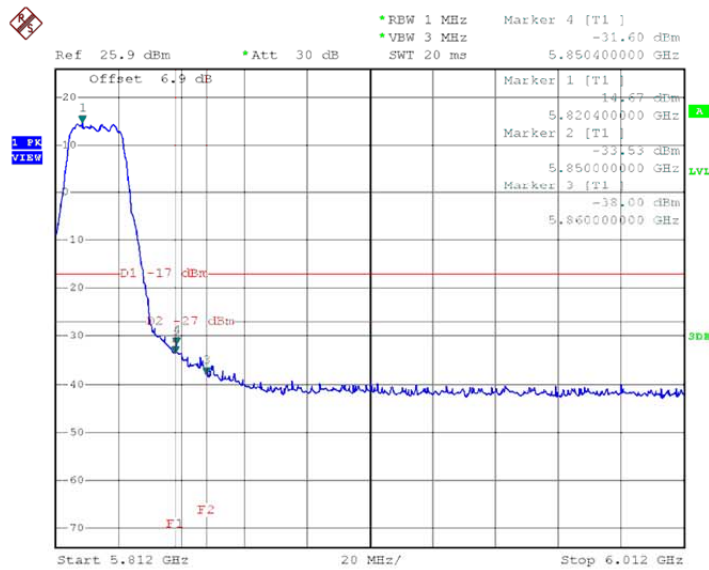
**Test Mode: UNII-3/TX AC20 Mode\_ANT 7**

**TX AC HT20 mode CH149**



Date: 19.APR.2015 14:50:52

**TX AC HT20 mode CH165**



Date: 19.APR.2015 14:49:50