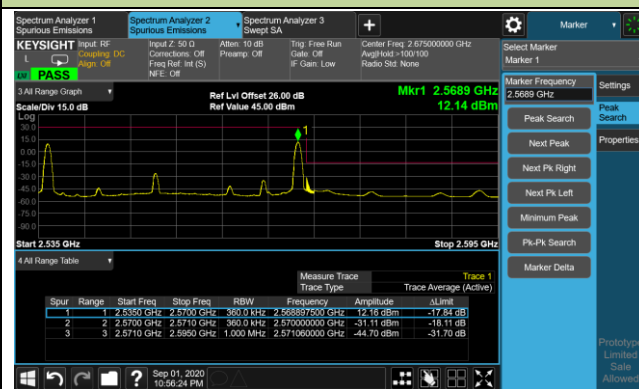
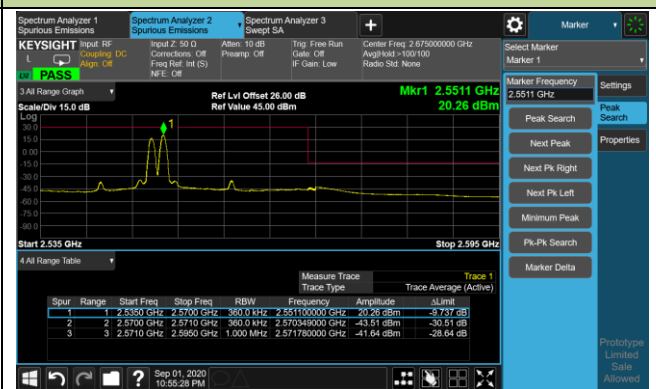


Upper ACP RB = 0 & 99

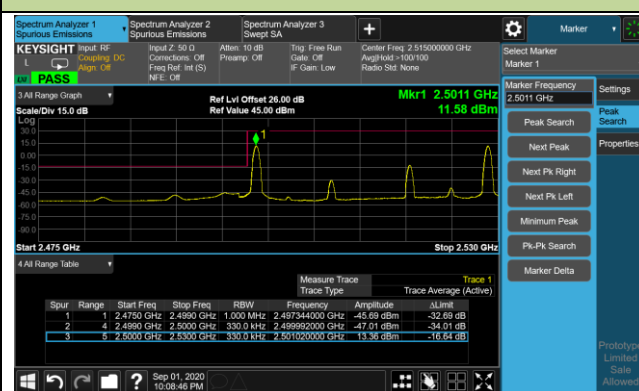


Upper ACP RB = 74 & 0

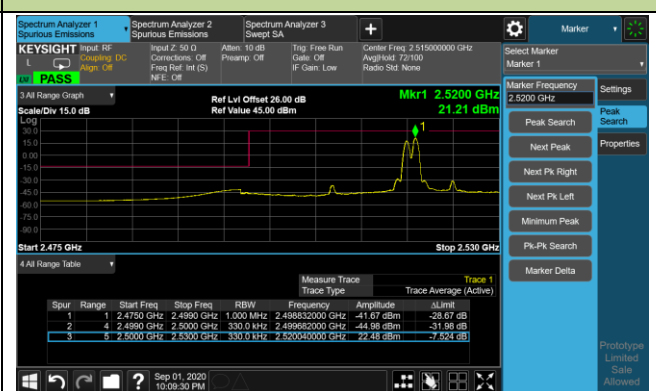


20MHz+10MHz Channel Bandwidth

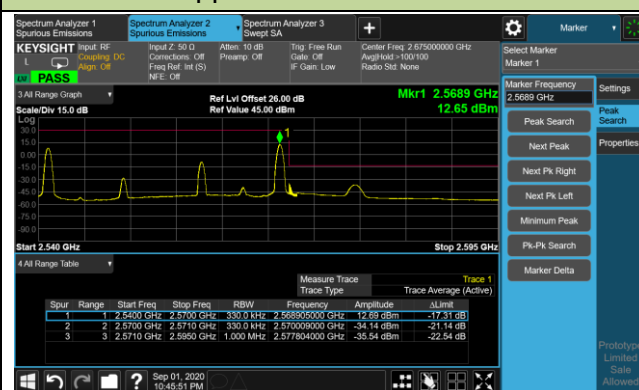
Lower ACP RB = 0 & 49



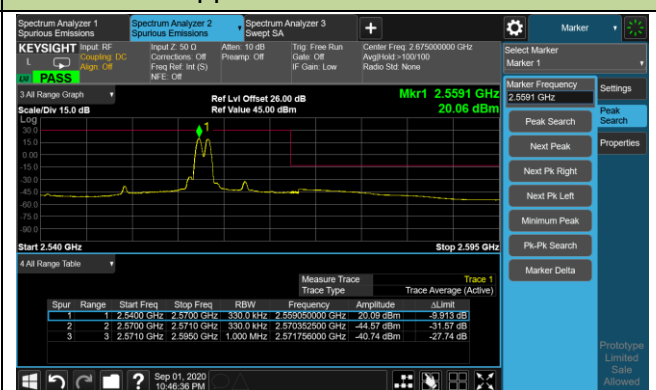
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 49

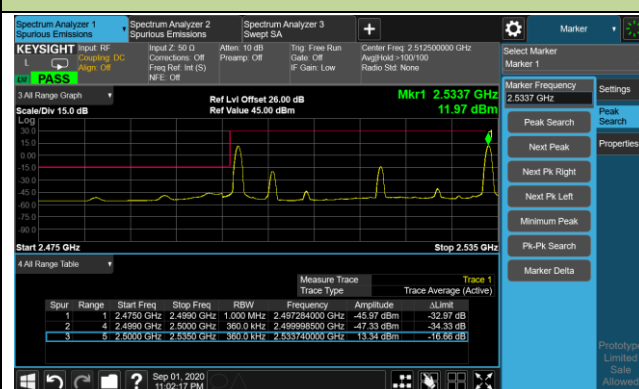


Upper ACP RB = 99 & 0

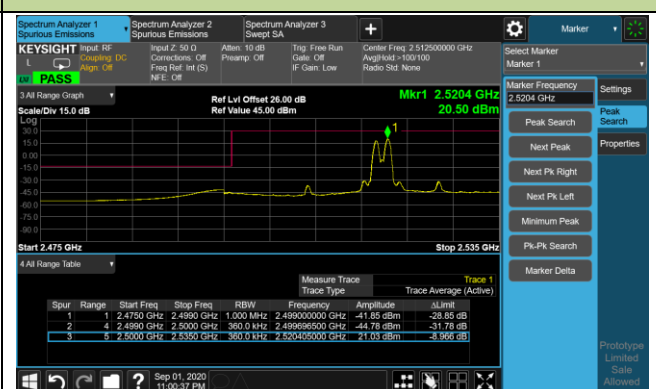


20MHz+15MHz Channel Bandwidth

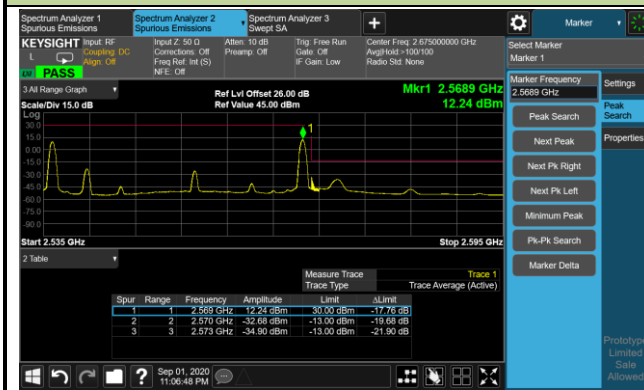
Lower ACP RB = 0 & 74



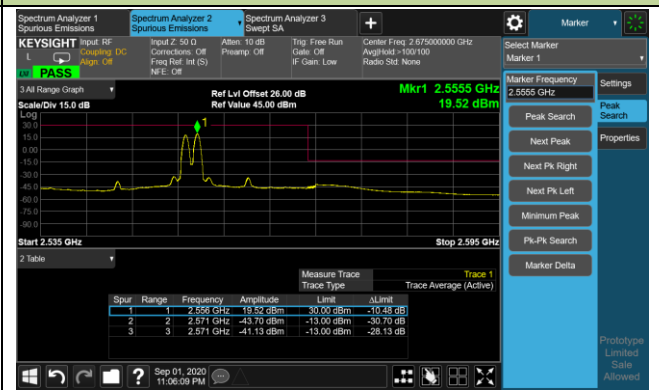
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 74

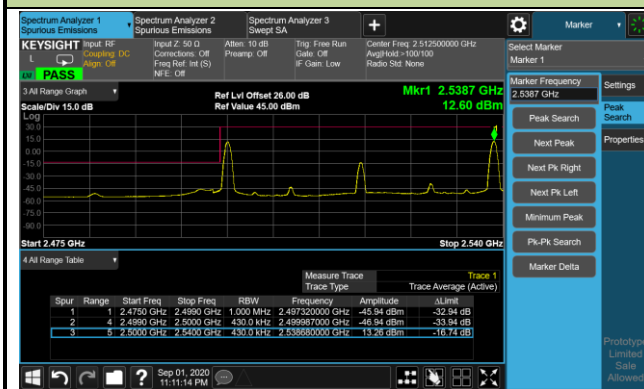


Upper ACP RB = 99 & 0

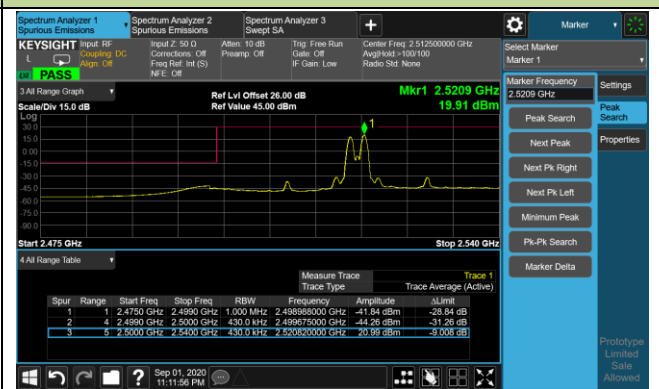


20MHz+20MHz Channel Bandwidth

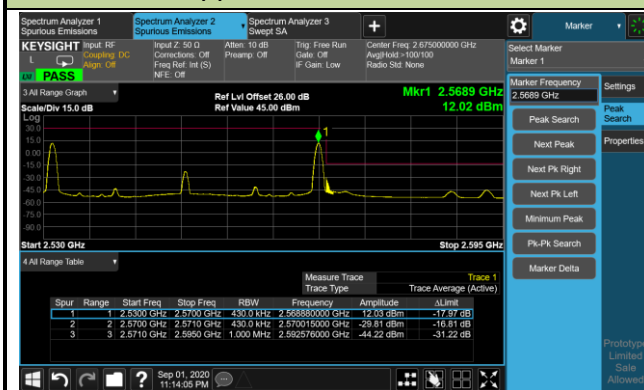
Lower ACP RB = 0 & 99



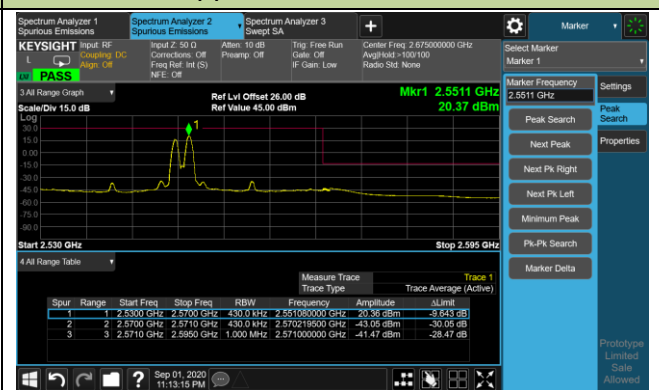
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 99

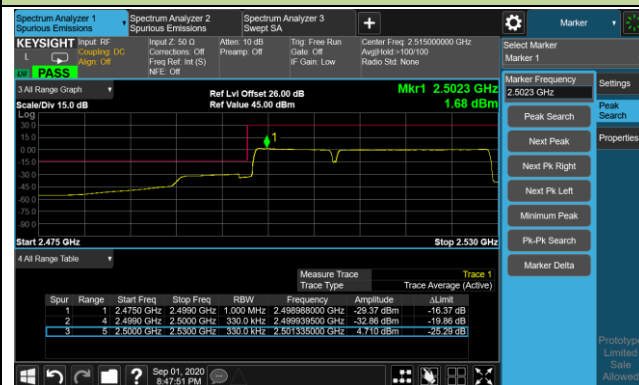


Upper ACP RB = 99 & 0

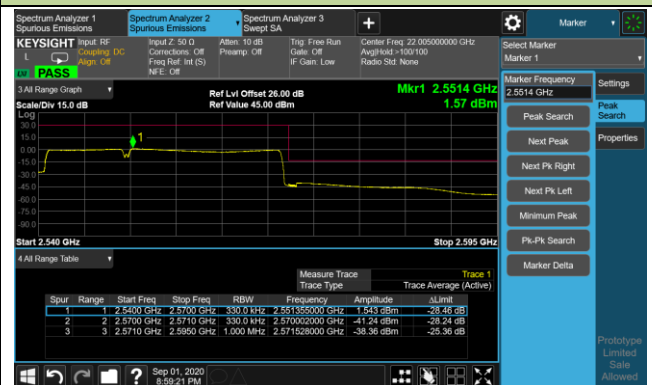


10MHz+20MHz Channel Bandwidth Full RB

Lower ACP

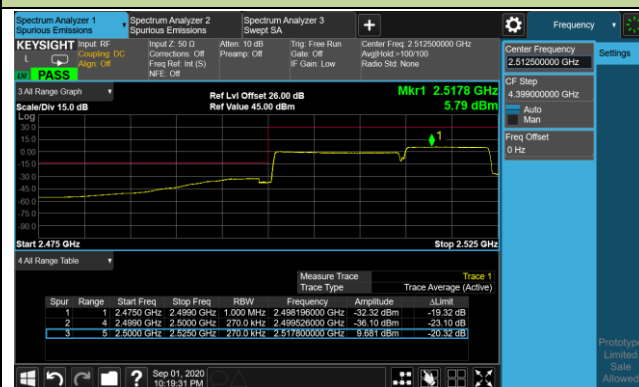


Upper ACP

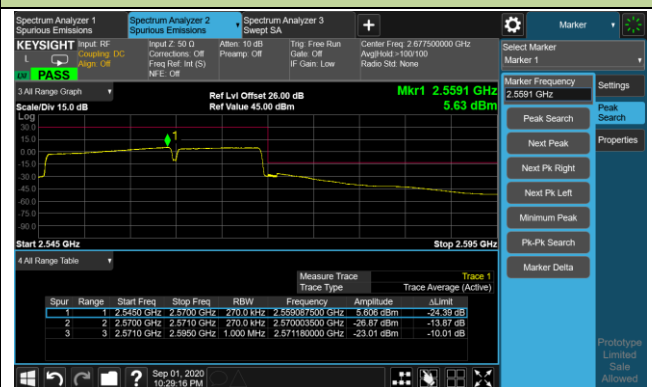


15MHz+10MHz Channel Bandwidth Full RB

Lower ACP

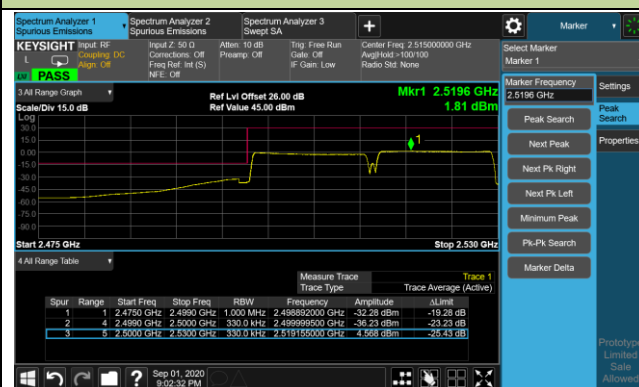


Upper ACP

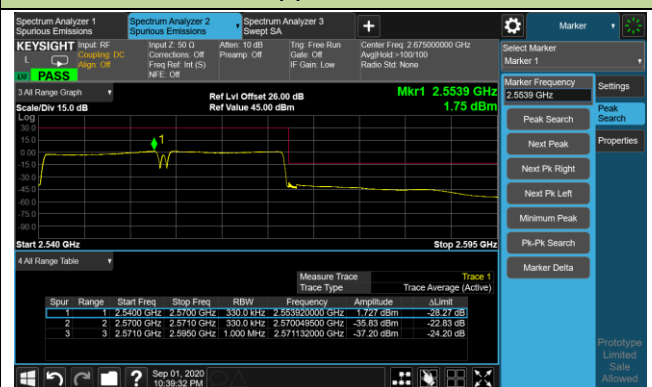


15MHz+15MHz Channel Bandwidth Full RB

Lower ACP

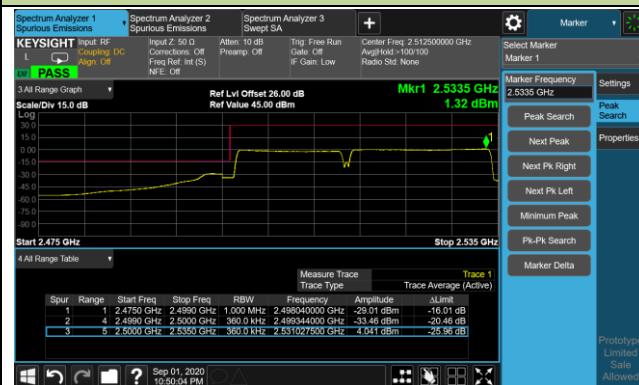


Upper ACP

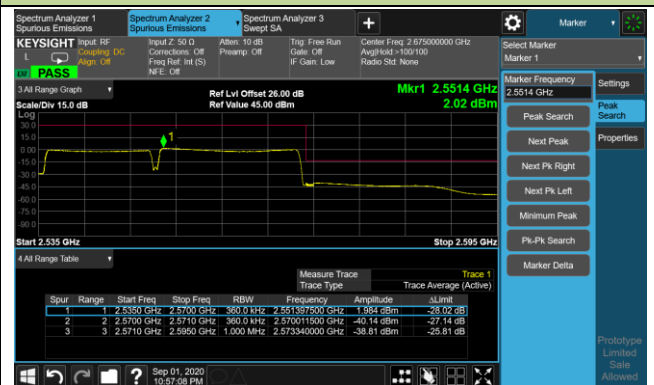


15MHz+20MHz Channel Bandwidth Full RB

Lower ACP

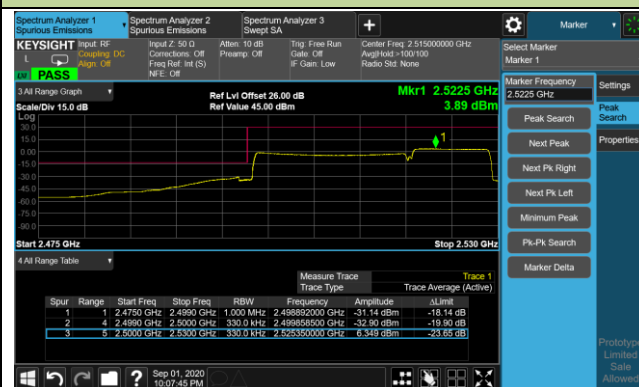


Upper ACP

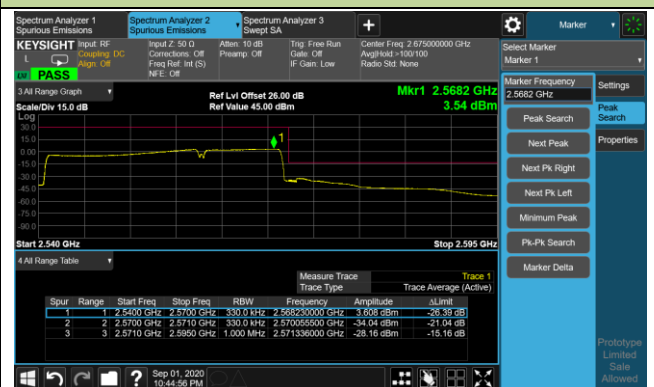


20MHz+10MHz Channel Bandwidth Full RB

Lower ACP

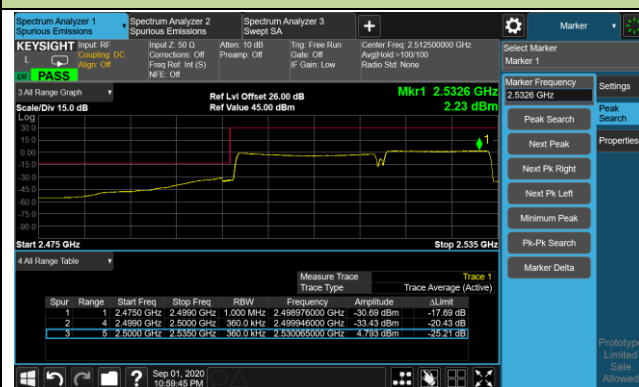


Upper ACP

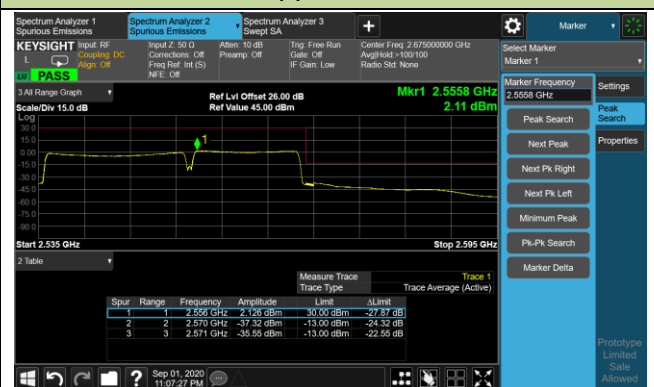


20MHz+15MHz Channel Bandwidth Full RB

Lower ACP

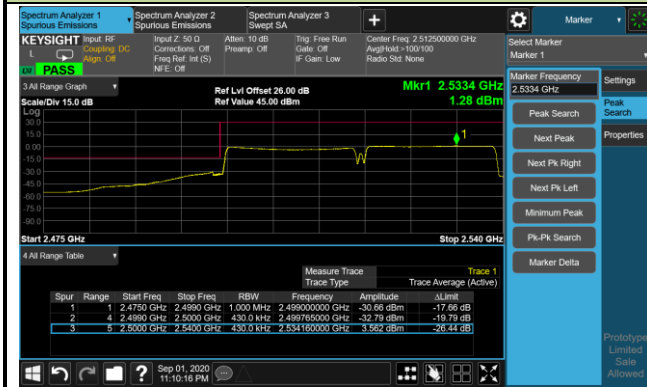


Upper ACP

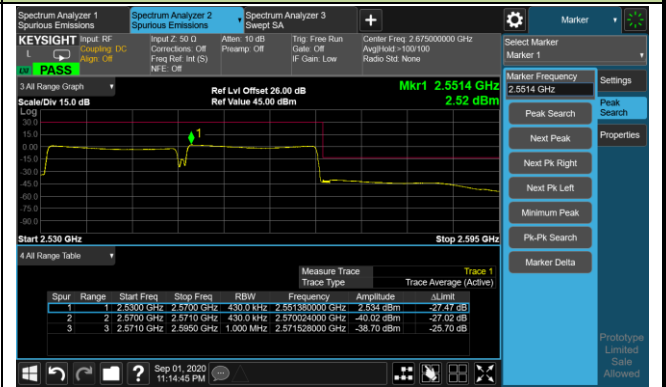


20MHz+20MHz Channel Bandwidth Full RB

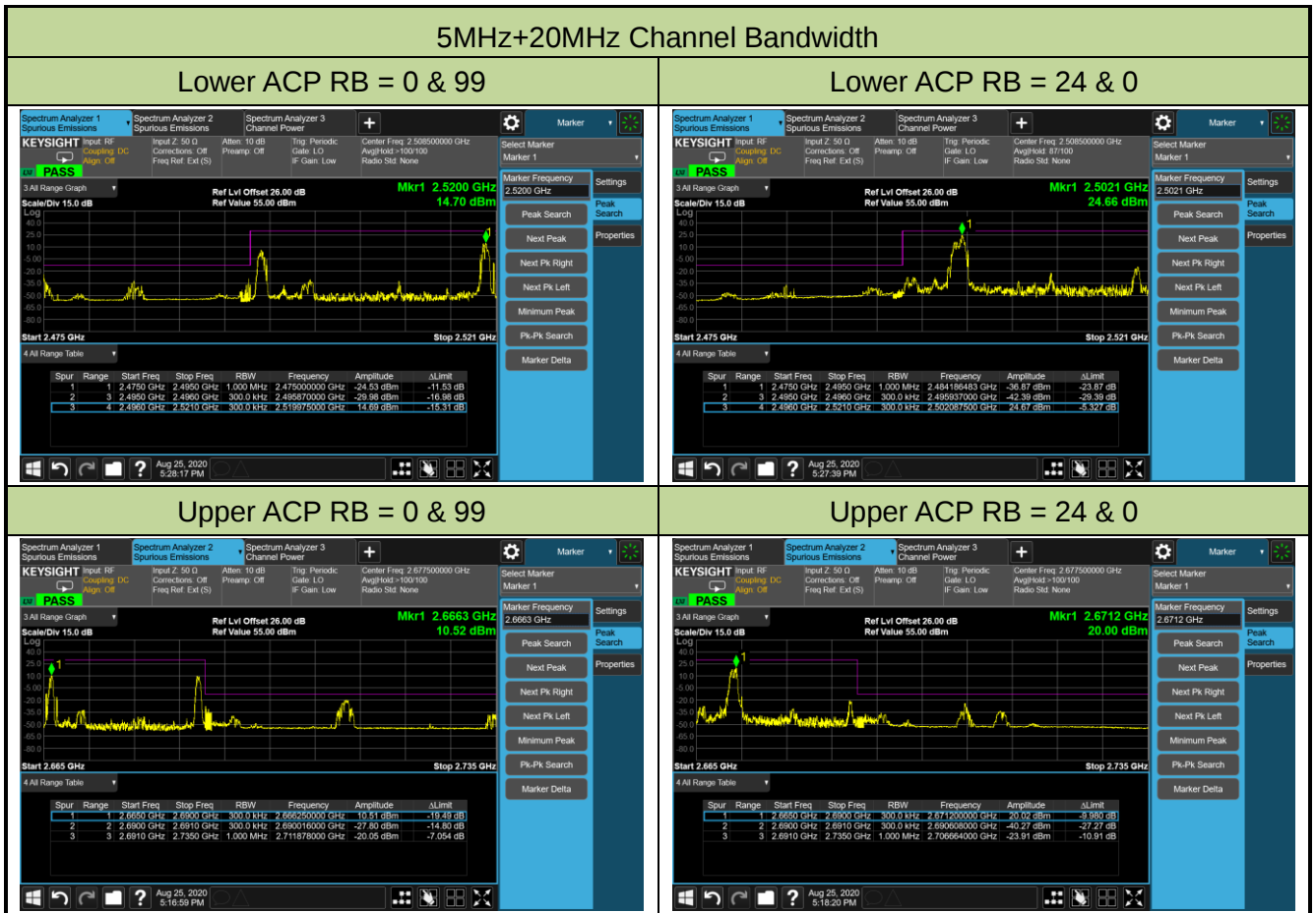
Lower ACP



Upper ACP

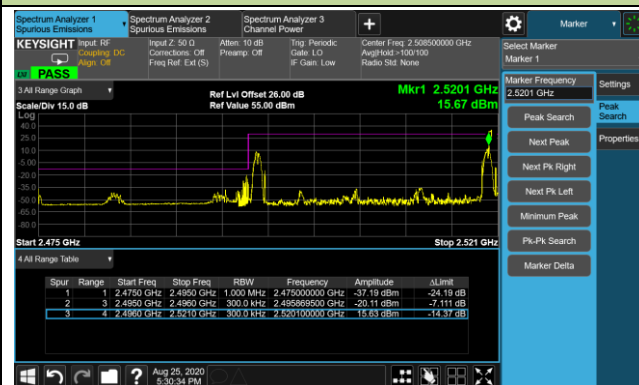


Model Number	4G03-A	Test Engineer	Eric Xu
Test Site	SR6	Test Date	2020/08/25
Test Band	Intra-Band CA_41C		

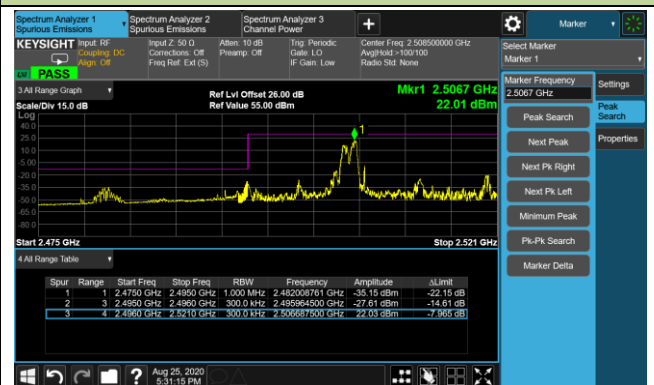


10MHz+15MHz Channel Bandwidth

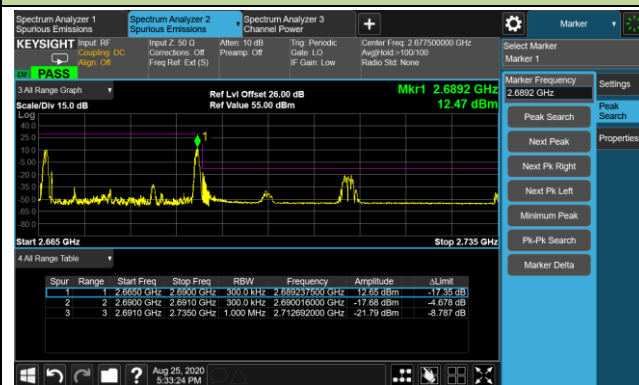
Lower ACP RB = 0 & 74



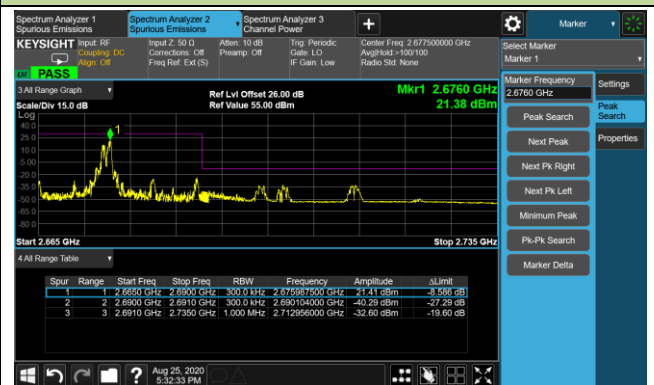
Lower ACP RB = 49 & 0



Upper ACP RB= 0 & 74

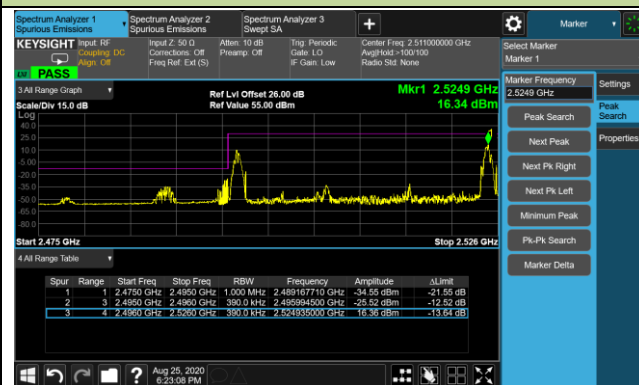


Upper ACP RB = 49 & 0

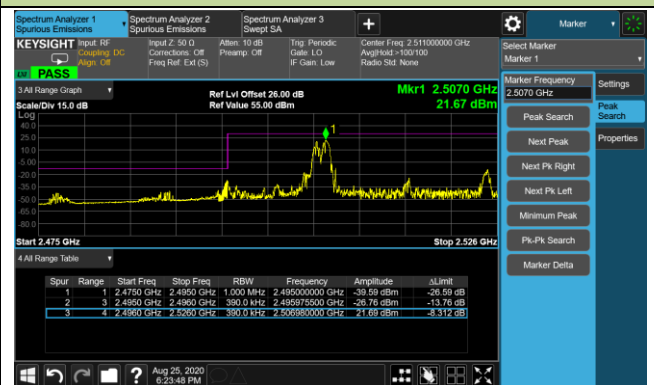


10MHz+20MHz Channel Bandwidth

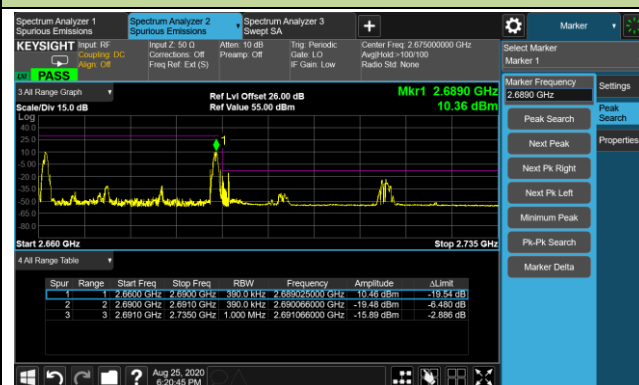
Lower ACP RB = 0 & 99



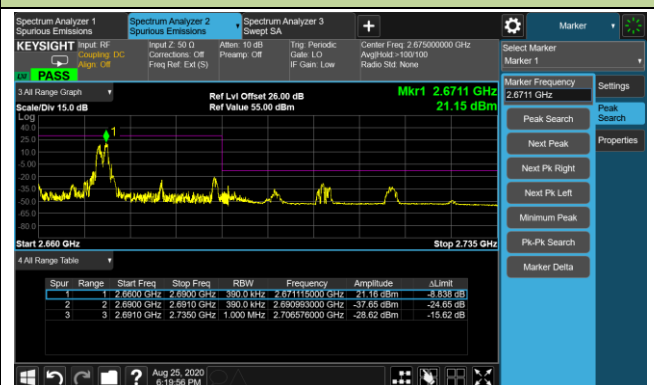
Lower ACP RB= 49 & 0



Upper ACP RB= 0 & 99

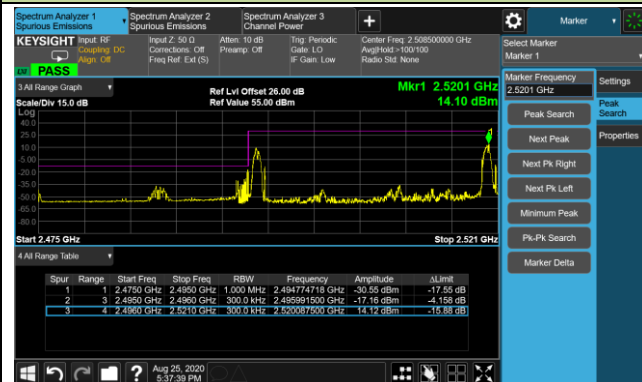


Upper ACP RB = 49 & 0

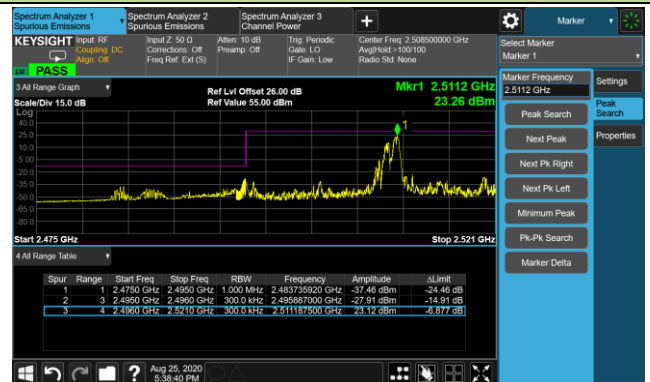


15MHz+10MHz Channel Bandwidth

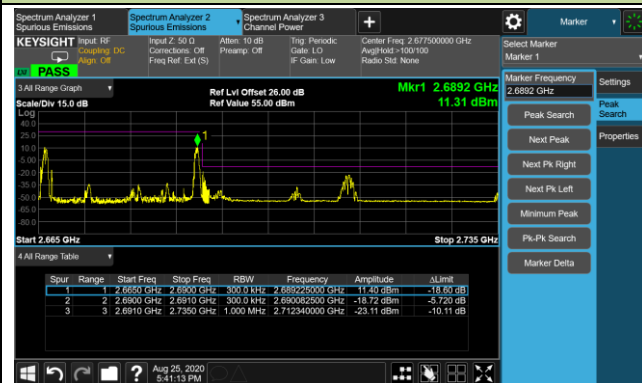
Lower ACP RB = 0 & 49



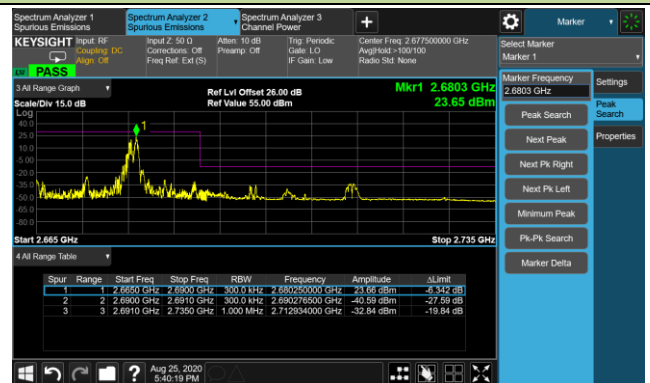
Lower ACP RB = 74 & 0



Upper ACP RB = 0 & 49

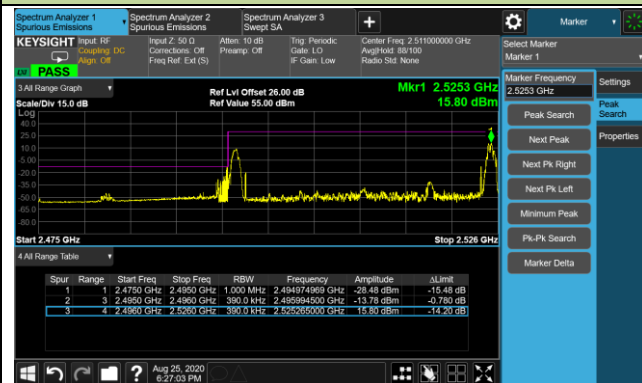


Upper ACP RB = 74 & 0

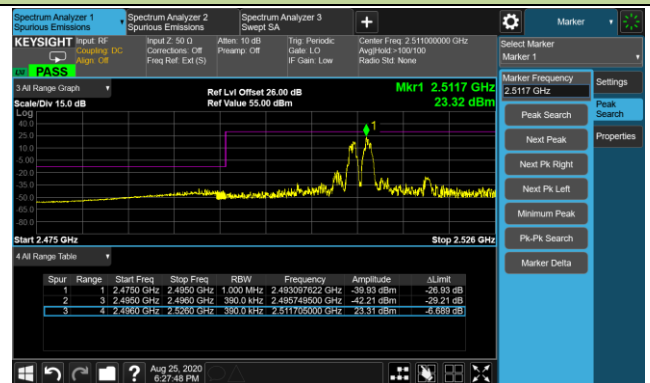


15MHz+15MHz Channel Bandwidth

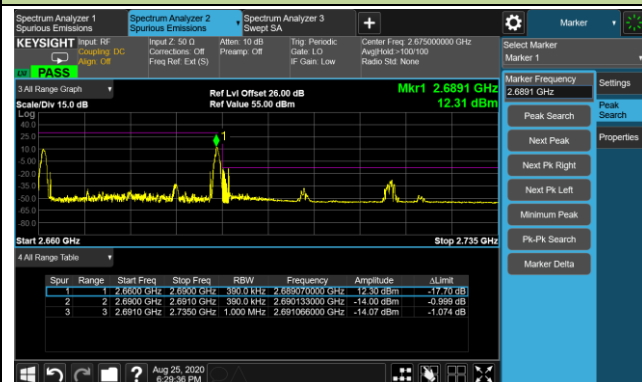
Lower ACP RB = 0 & 74



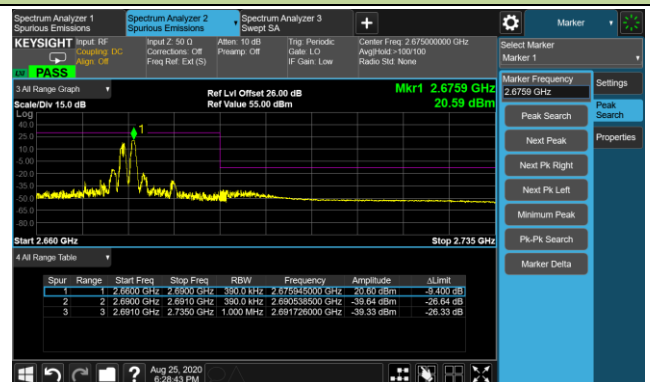
Lower ACP RB= 74 & 0



Upper ACP RB = 0 & 74

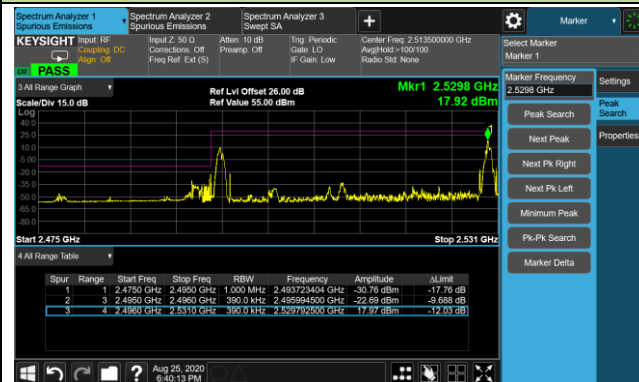


Upper ACP RB = 74 & 0

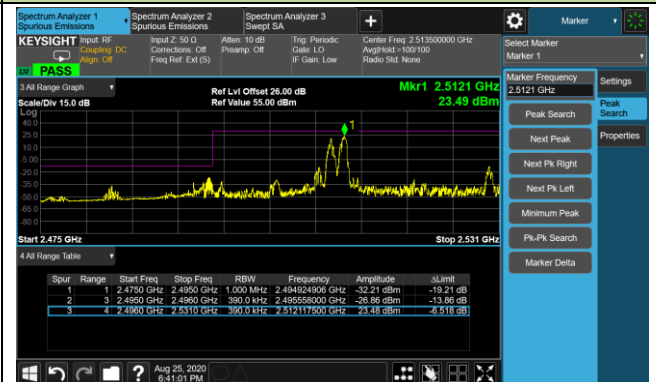


15MHz+20MHz Channel Bandwidth

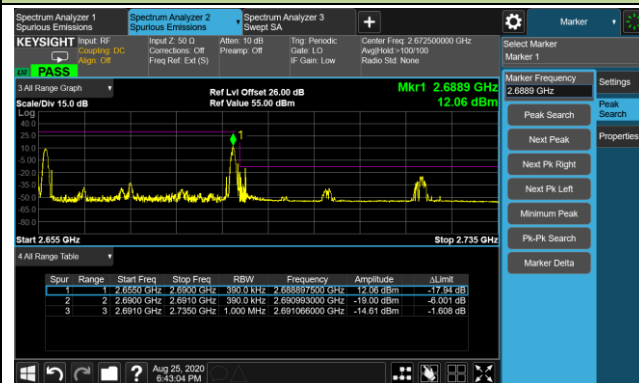
Lower ACP RB = 0 & 99



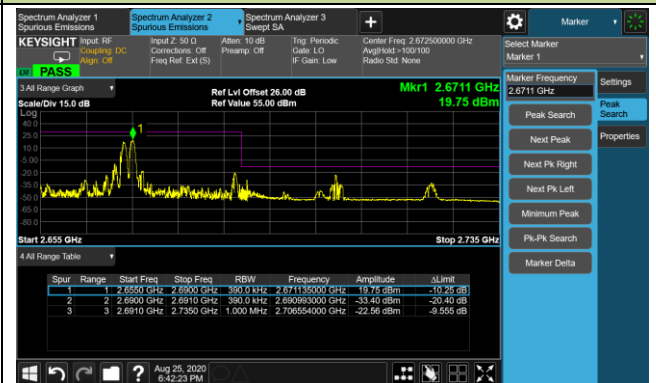
Lower ACP RB = 74 & 0



Upper ACP RB = 0 & 99

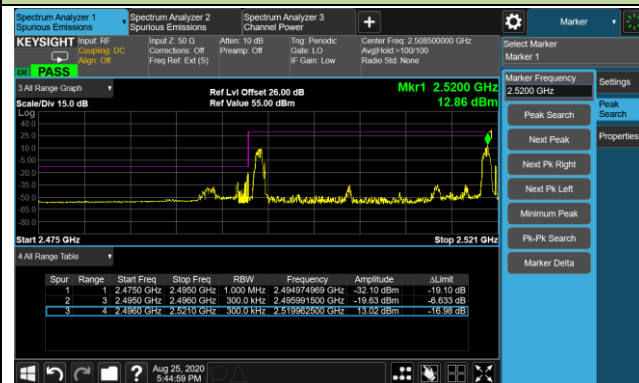


Upper ACP RB = 74 & 0

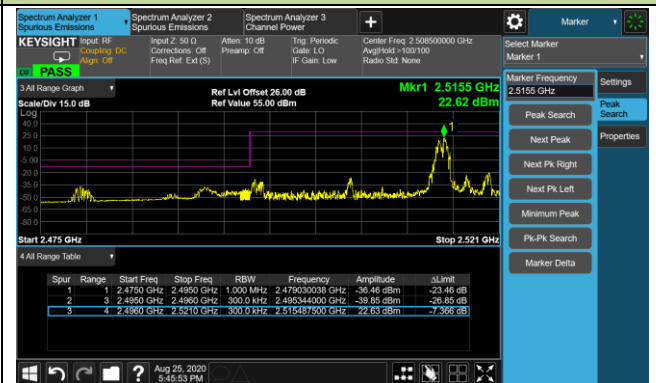


20MHz+5MHz Channel Bandwidth

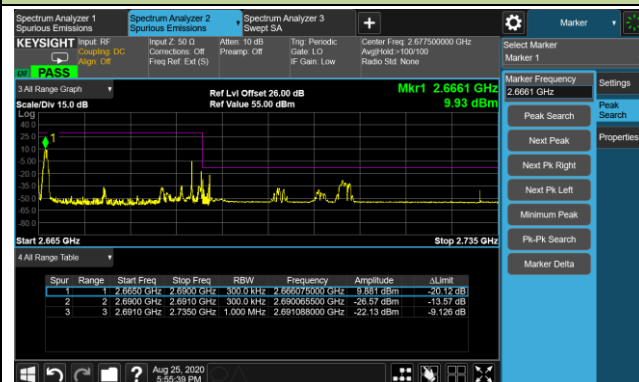
Lower ACP RB = 0 & 24



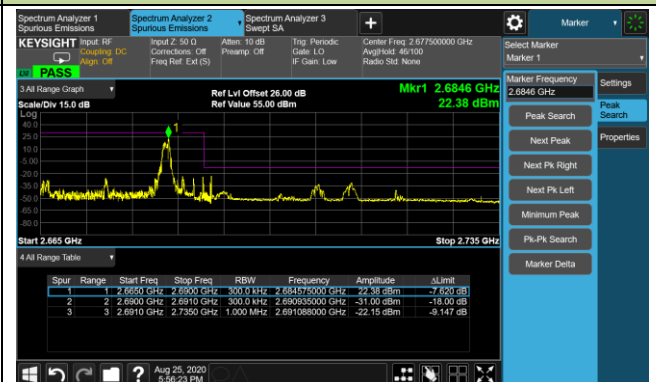
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 24

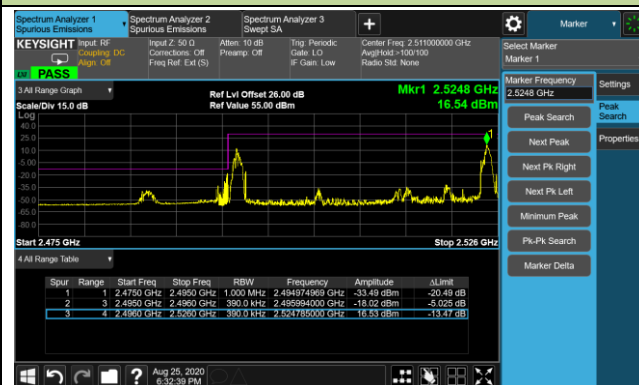


Upper ACP RB = 99 & 0

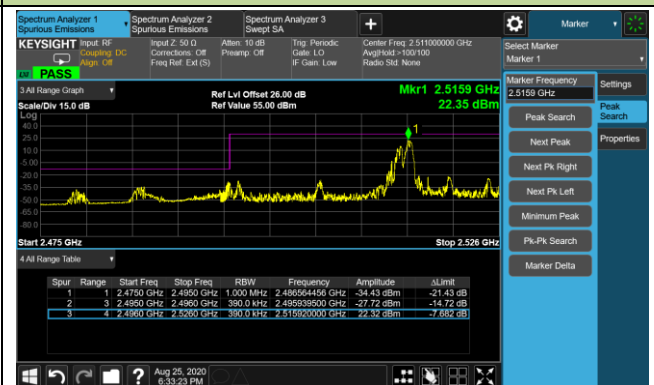


20MHz+10MHz Channel Bandwidth

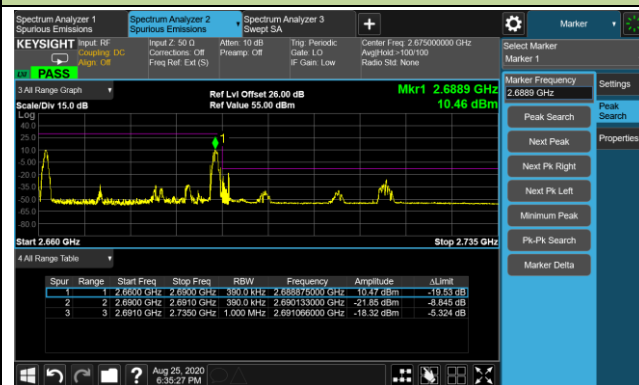
Lower ACP RB = 0 & 49



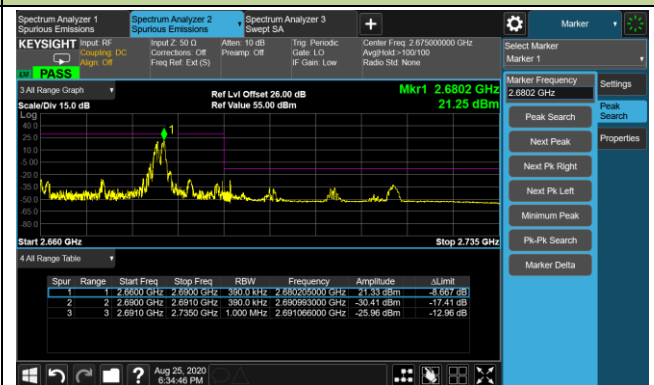
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 49

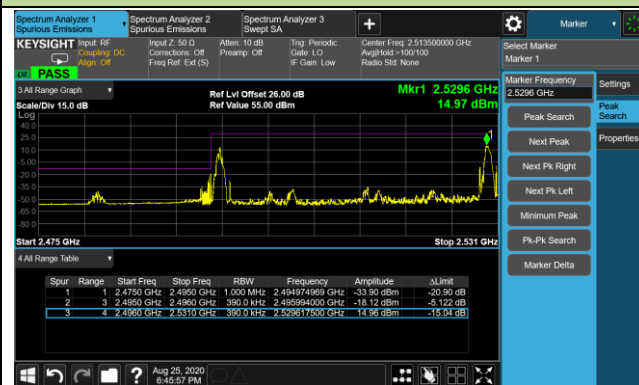


Upper ACP RB = 99 & 0

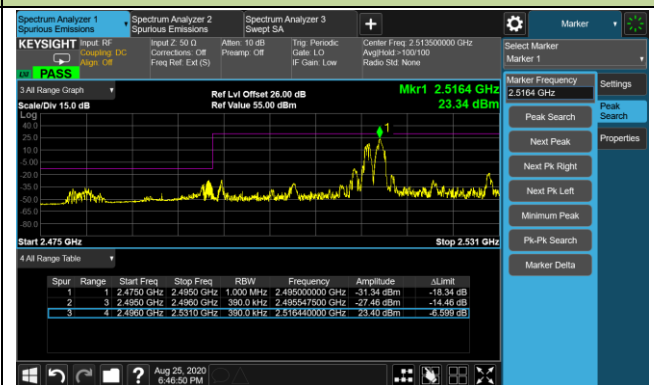


20MHz+15MHz Channel Bandwidth

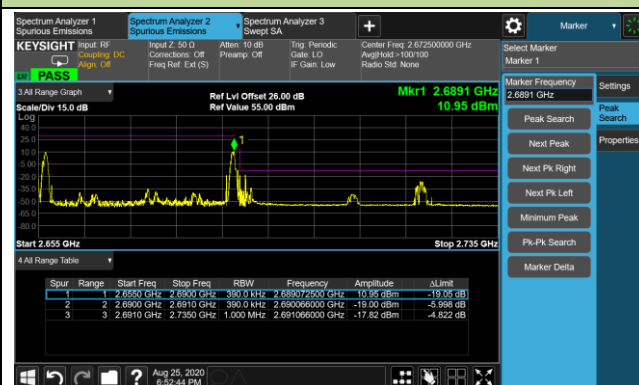
Lower ACP RB = 0 & 74



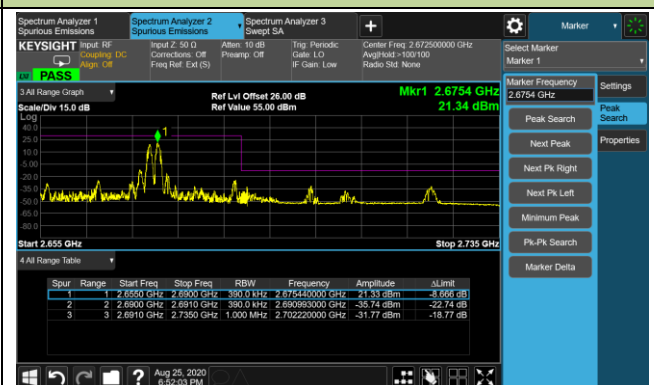
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 74

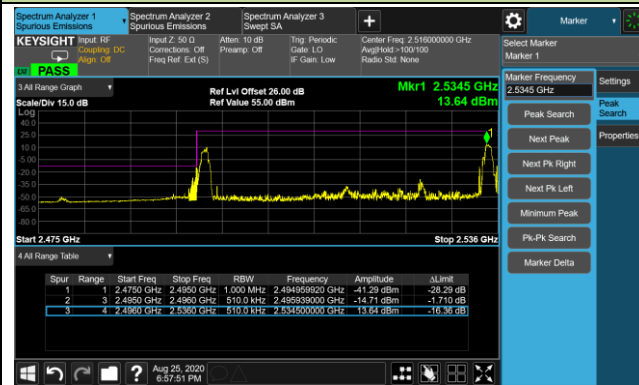


Upper ACP RB = 99 & 0

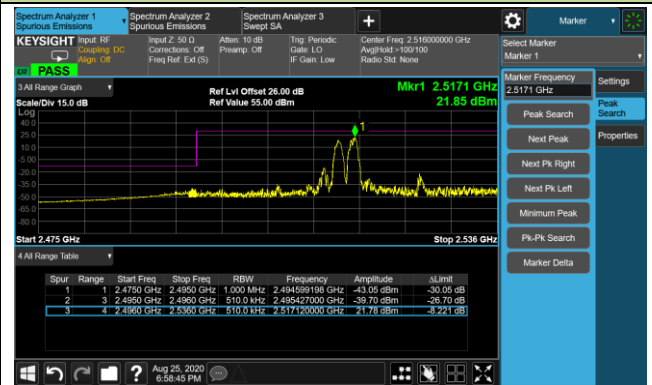


20MHz+20MHz Channel Bandwidth

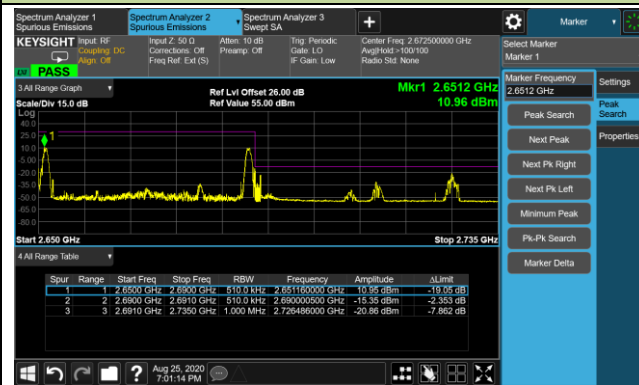
Lower ACP RB = 0 & 99



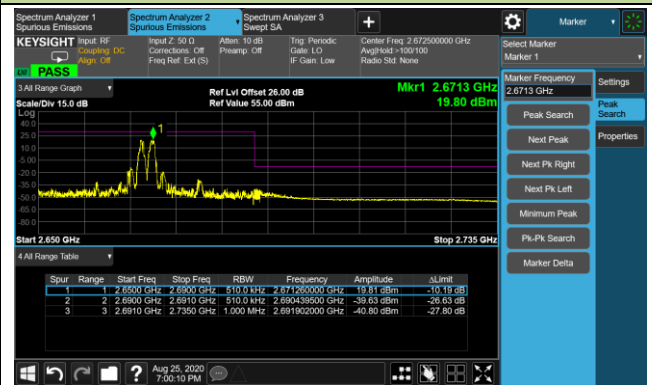
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 99

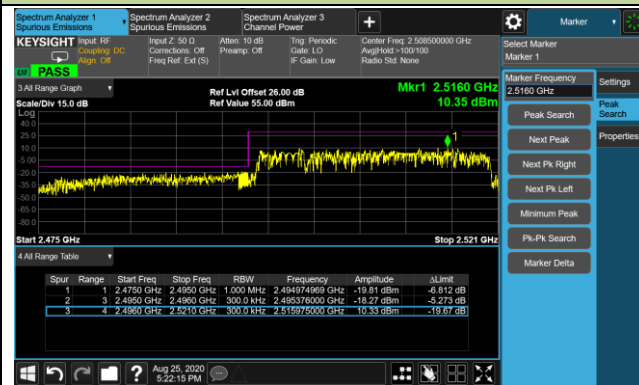


Upper ACP RB = 99 & 0



5MHz+20MHz Channel Bandwidth Full RB

Lower ACP



Upper ACP

