

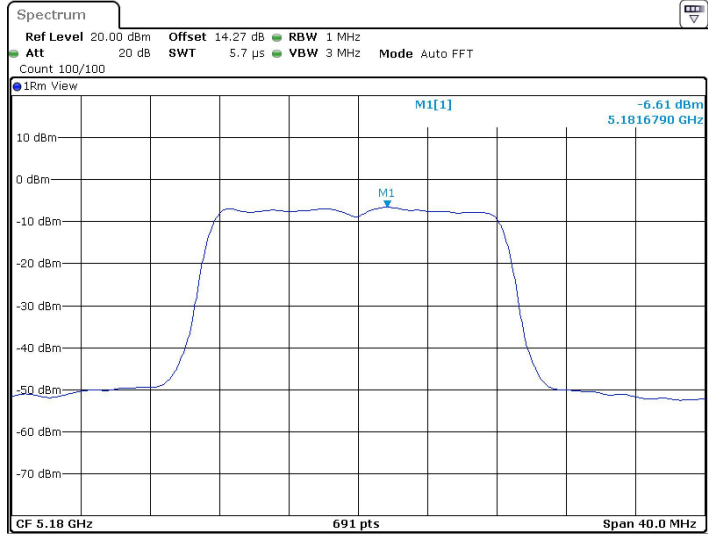
ANT1+ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11N20MIMO	5180	-0.71	≤10.22	PASS
	5200	-1.95	≤10.22	PASS
	5240	-2.14	≤10.22	PASS
	5745	-1.39	≤29.67	PASS
	5785	-1.84	≤29.67	PASS
	5825	-1.47	≤29.67	PASS
11N40MIMO	5190	-8.58	≤10.22	PASS
	5230	-10.60	≤10.22	PASS
	5755	-8.26	≤29.67	PASS
	5795	-8.61	≤29.67	PASS
11AC20MIMO	5180	-4.95	≤10.22	PASS
	5200	-4.93	≤10.22	PASS
	5240	-6.08	≤10.22	PASS
	5745	-6.18	≤29.67	PASS
	5785	-6.57	≤29.67	PASS
	5825	-5.07	≤29.67	PASS
11AC40MIMO	5190	-8.46	≤10.22	PASS
	5230	-10.22	≤10.22	PASS
	5755	-8.16	≤29.67	PASS
	5795	-8.32	≤29.67	PASS

Remark:

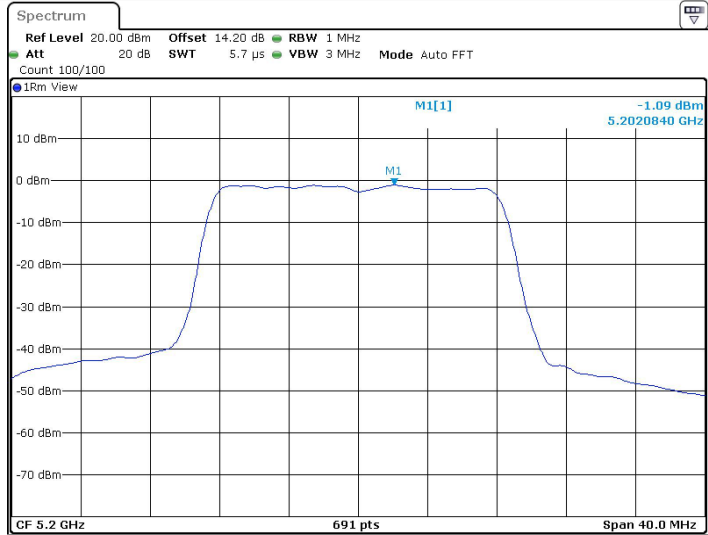
PSD = Meas PSD + Duty Cycle Factor

11A_Ant1_5180



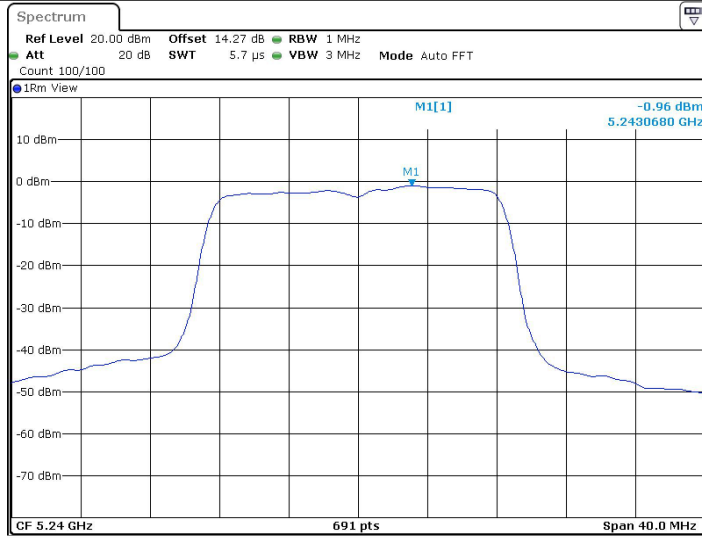
Date: 14.MAR 2024 14:51:24

11A_Ant1_5200



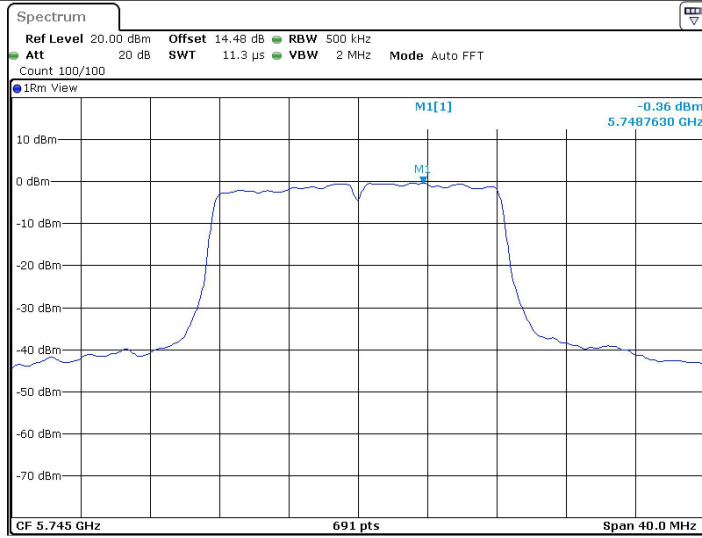
Date: 21.MAR 2024 19:36:17

11A_Ant1_5240



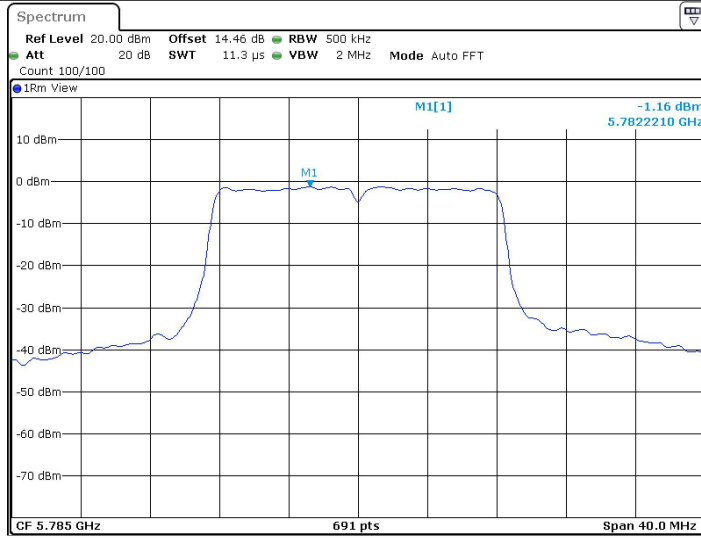
Date: 21.MAR.2024 19:37:46

11A_Ant1_5745



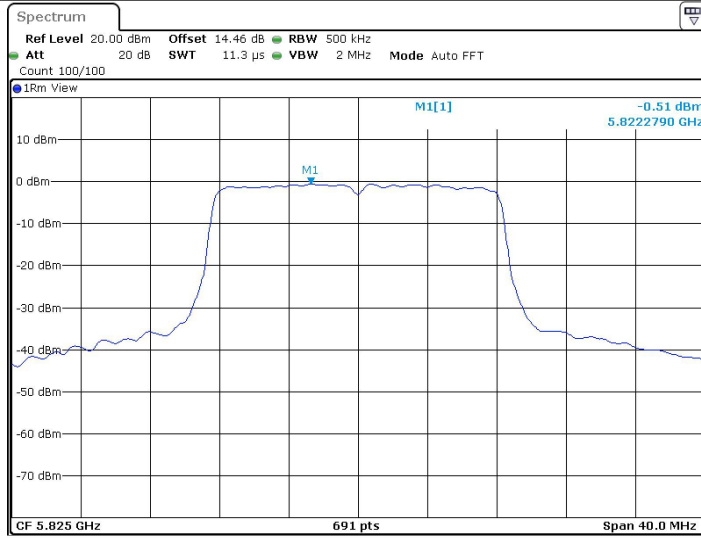
Date: 21.MAR.2024 19:41:22

11A_Ant1_5785



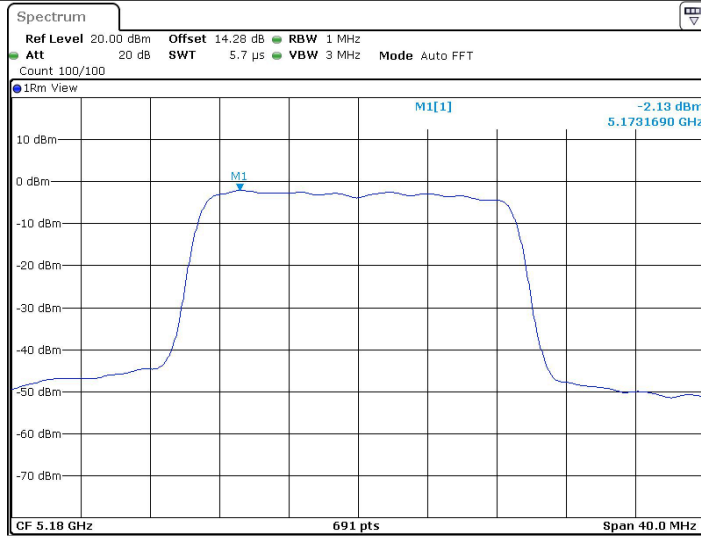
Date: 21.MAR.2024 19:43:39

11A_Ant1_5825



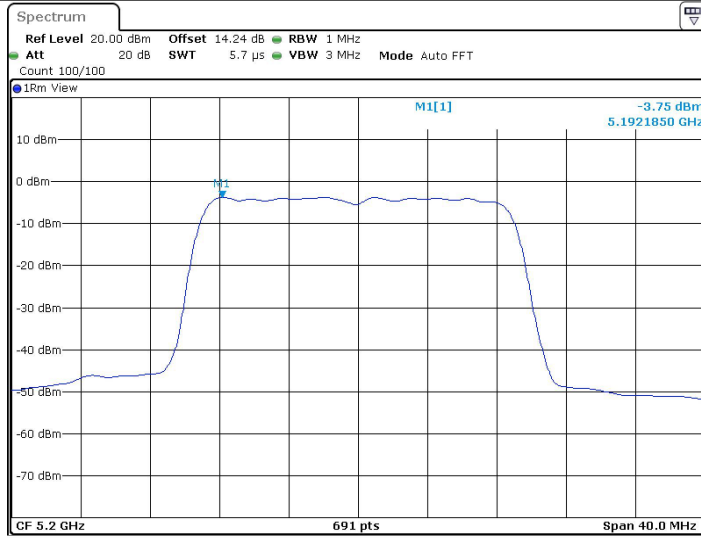
Date: 21.MAR.2024 19:44:55

11N20SISO_Ant1_5180



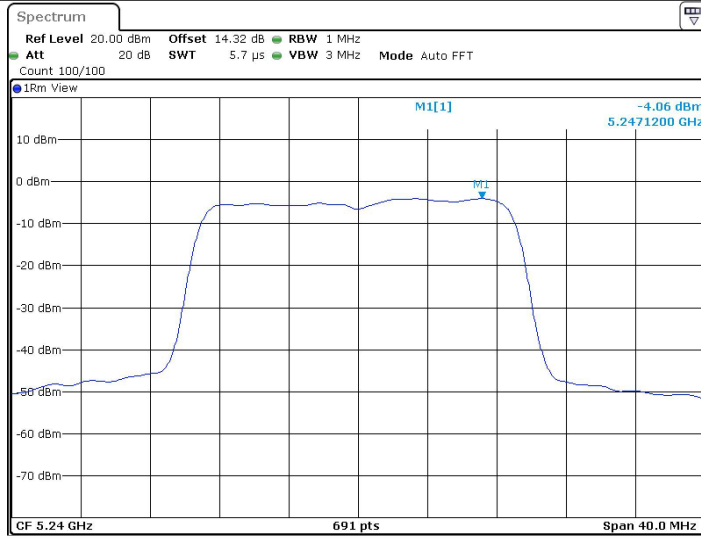
Date: 21.MAR.2024 19:48:46

11N20SISO_Ant1_5200



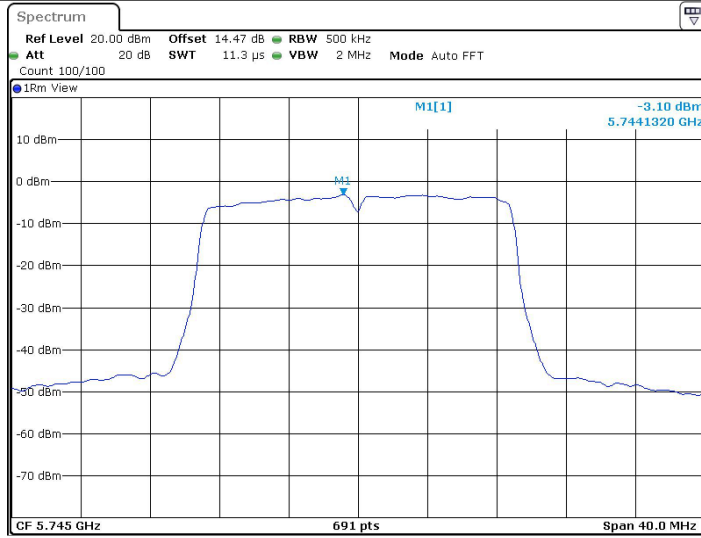
Date: 21.MAR.2024 19:51:11

11N20SISO_Ant1_5240



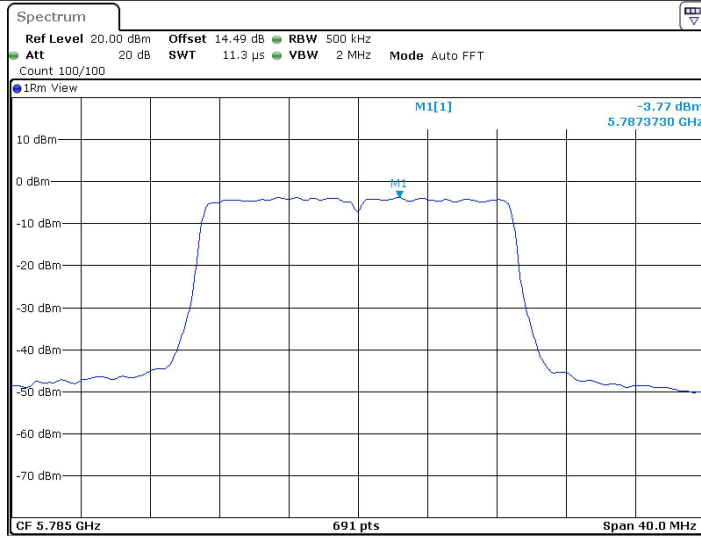
Date: 21.MAR.2024 19:52:30

11N20SISO_Ant1_5745



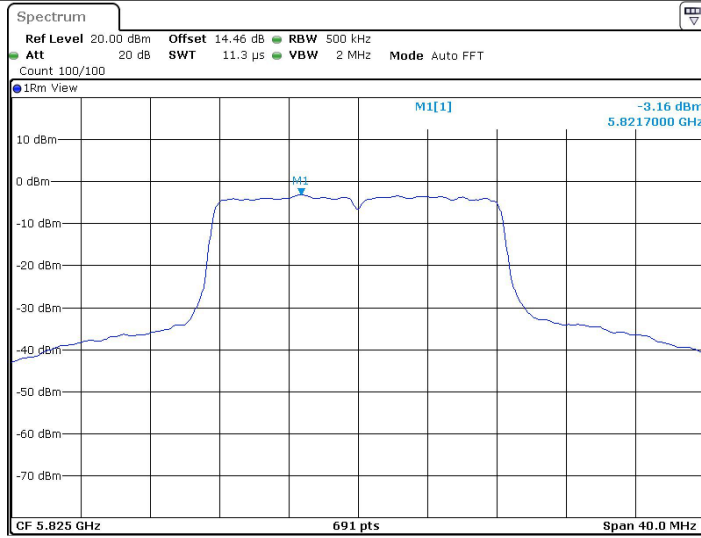
Date: 21.MAR.2024 19:55:17

11N20SISO_Ant1_5785



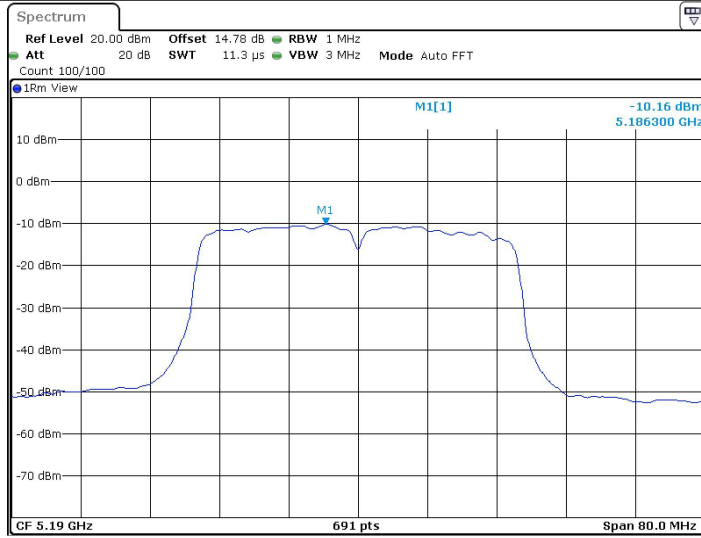
Date: 21.MAR.2024 19:57:47

11N20SISO_Ant1_5825



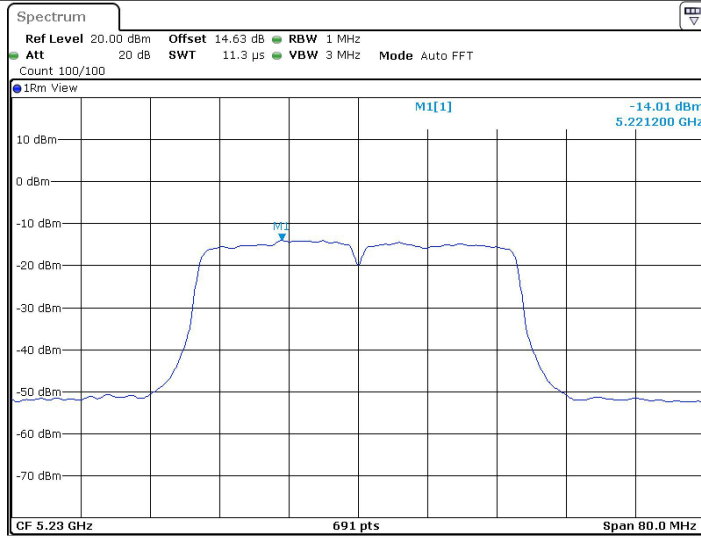
Date: 22.MAR.2024 14:49:08

11N40SISO_Ant1_5190



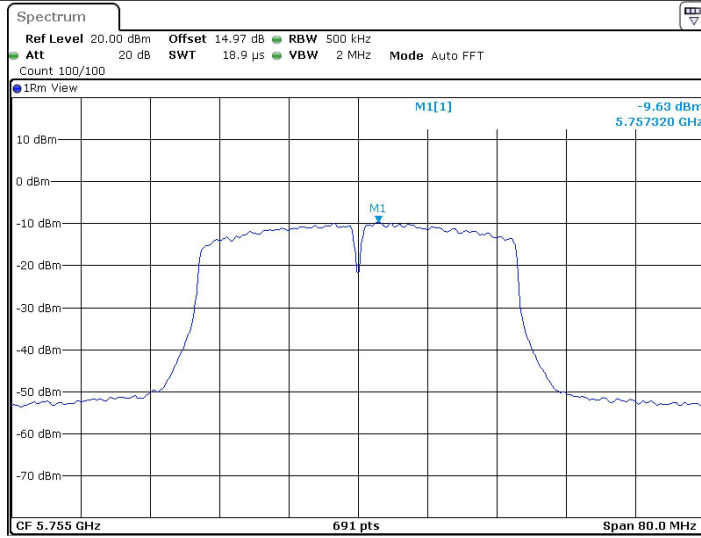
Date: 21.MAR.2024 20:01:53

11N40SISO_Ant1_5230



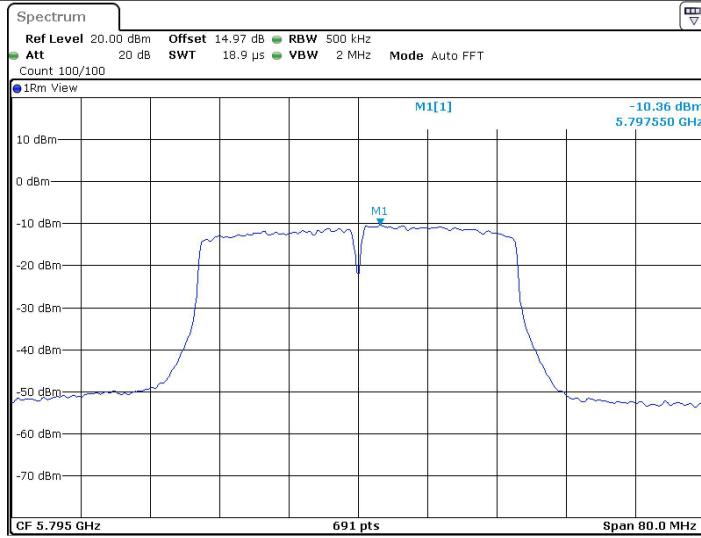
Date: 22.MAR.2024 14:52:22

11N40SISO_Ant1_5755



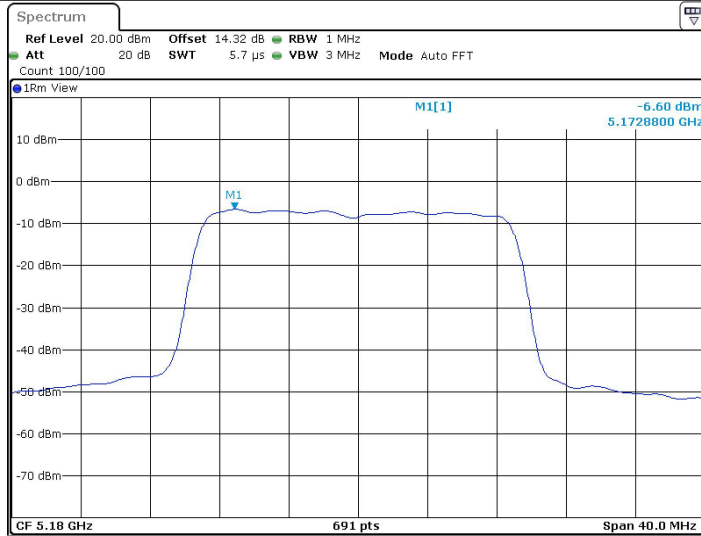
Date: 21.MAR.2024 20:07:13

11N40SISO_Ant1_5795



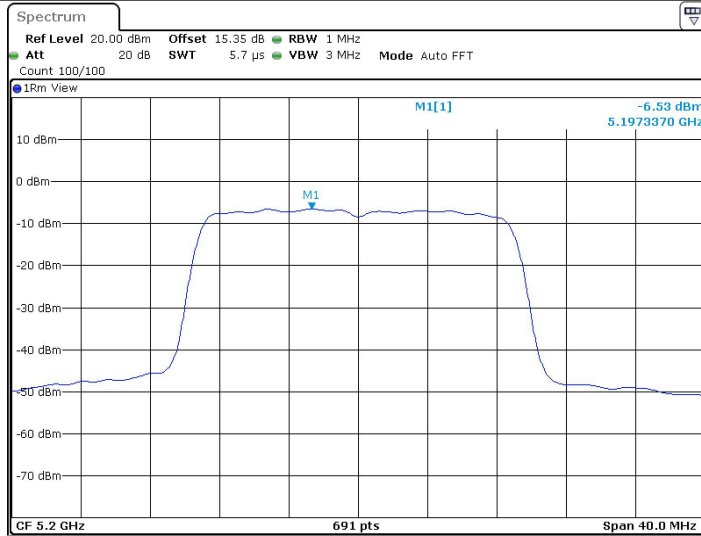
Date: 21.MAR.2024 20:09:45

11AC20SISO_Ant1_5180



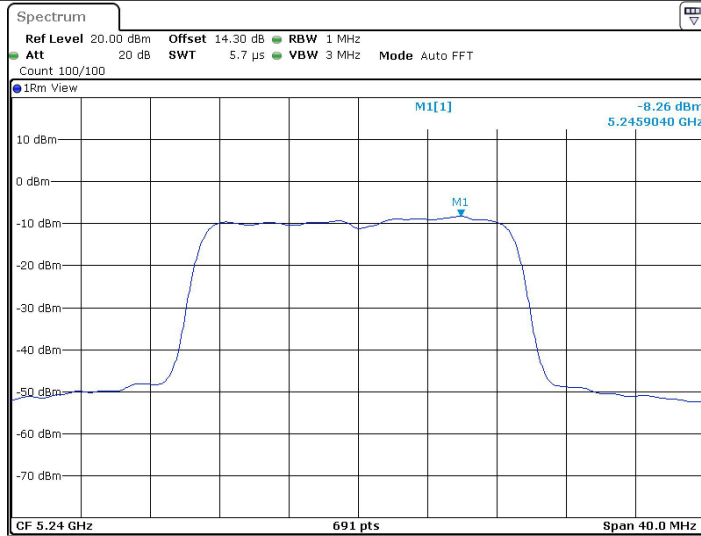
Date: 21.MAR.2024 20:12:27

11AC20SISO_Ant1_5200



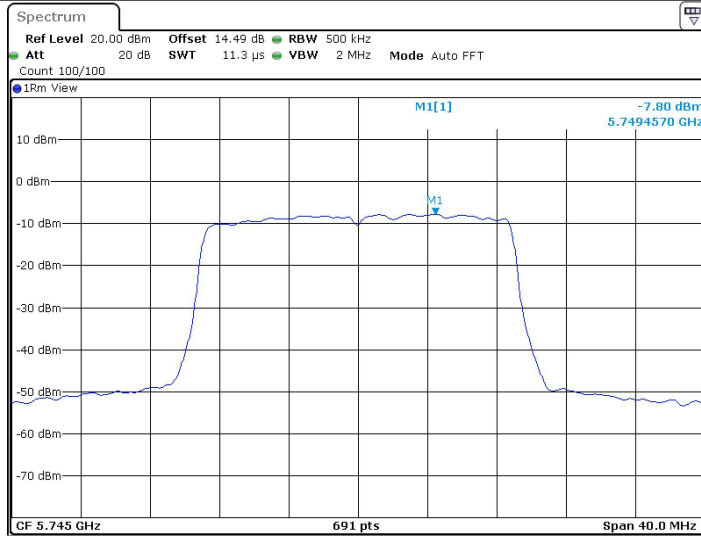
Date: 21.MAR.2024 20:15:03

11AC20SISO_Ant1_5240



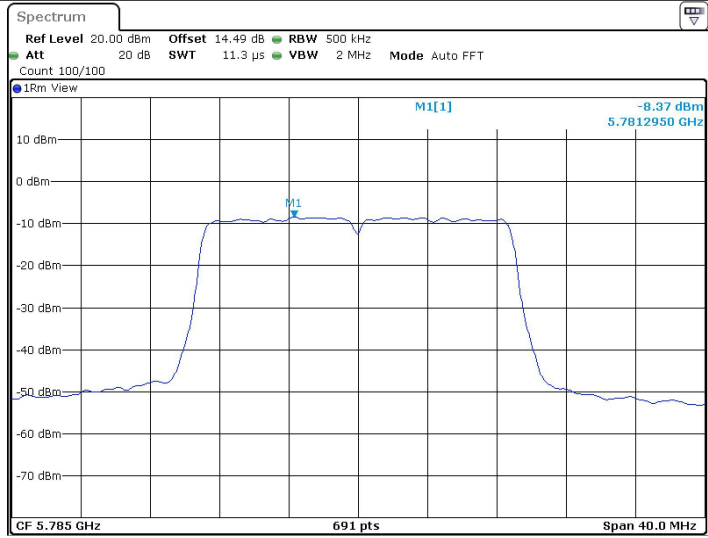
Date: 21.MAR.2024 20:16:22

11AC20SISO_Ant1_5745



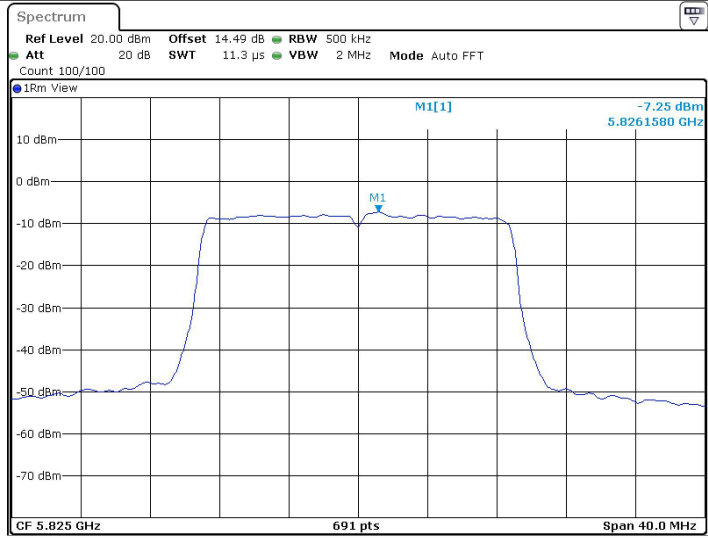
Date: 21.MAR.2024 20:18:52

11AC20SISO_Ant1_5785



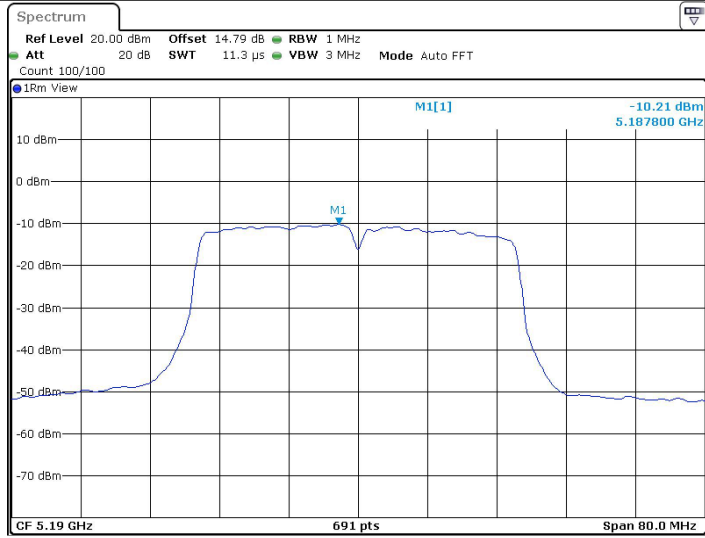
Date: 21.MAR.2024 20:21:14

11AC20SISO_Ant1_5825



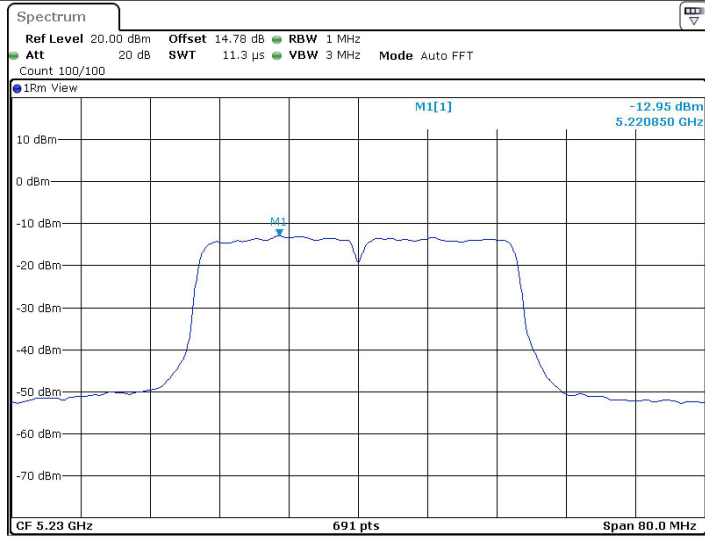
Date: 21.MAR.2024 20:22:23

11AC40SISO_Ant1_5190



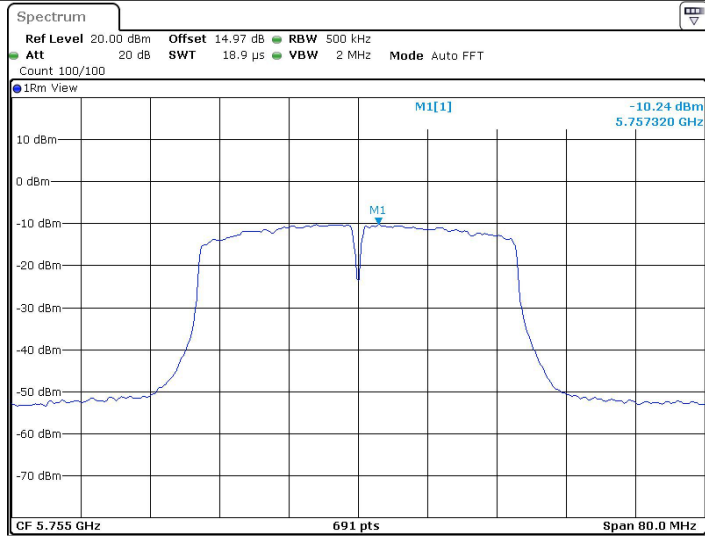
Date: 21.MAR.2024 20:25:00

11AC40SISO_Ant1_5230



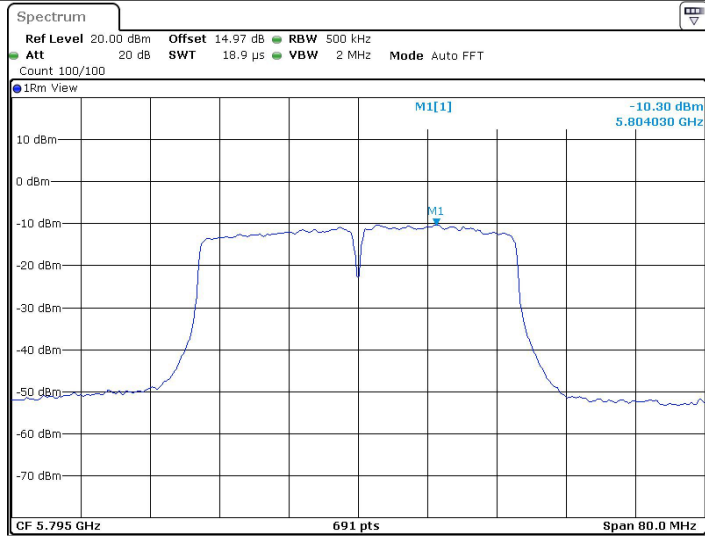
Date: 22.MAR.2024 14:55:44

11AC40SISO_Ant1_5755



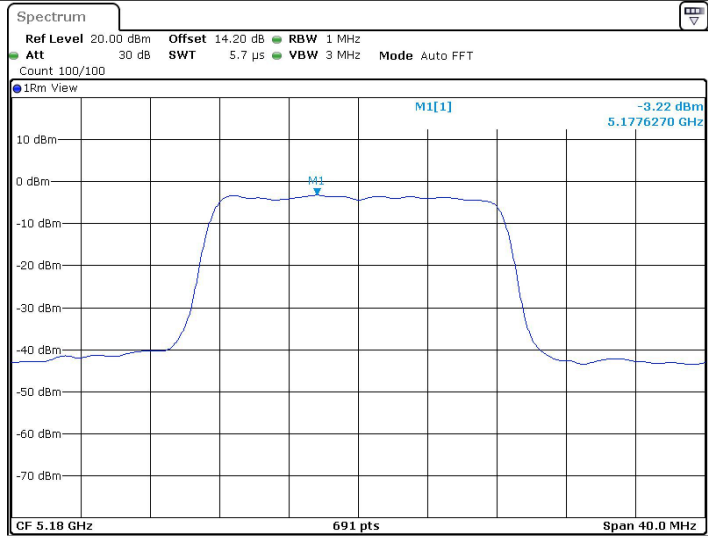
Date: 21.MAR.2024 20:33:13

11AC40SISO_Ant1_5795



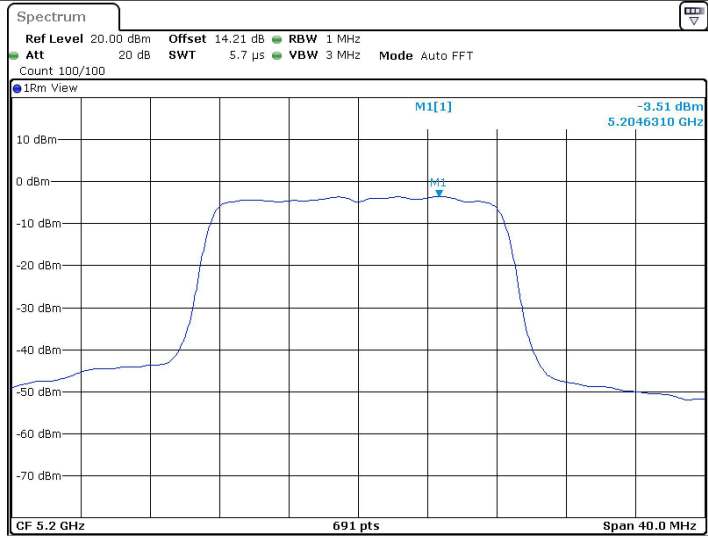
Date: 21.MAR.2024 20:35:54

11A_Ant2_5180



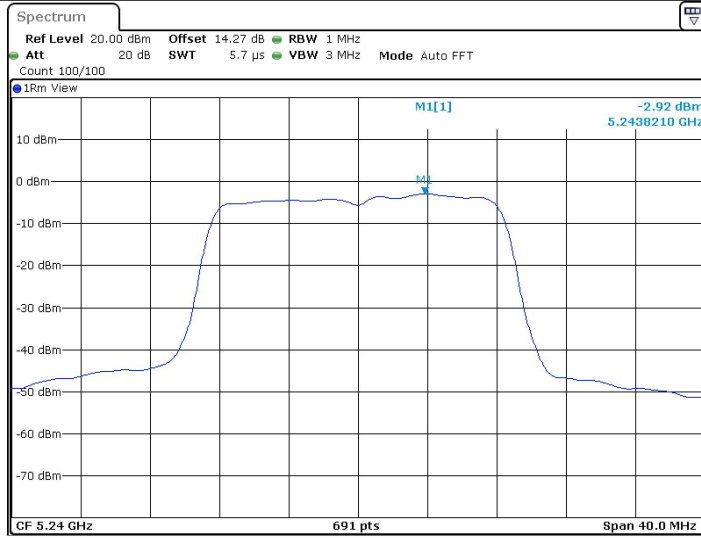
Date: 22.MAR.2024 10:46:08

11A_Ant2_5200



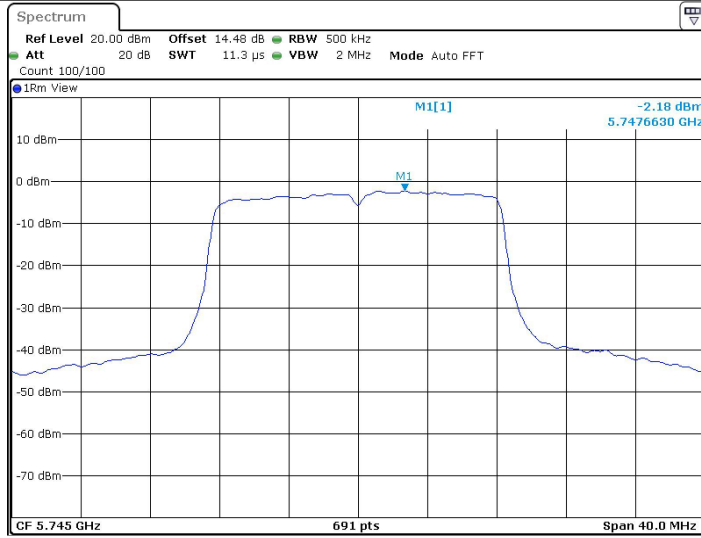
Date: 22.MAR.2024 10:50:56

11A_Ant2_5240



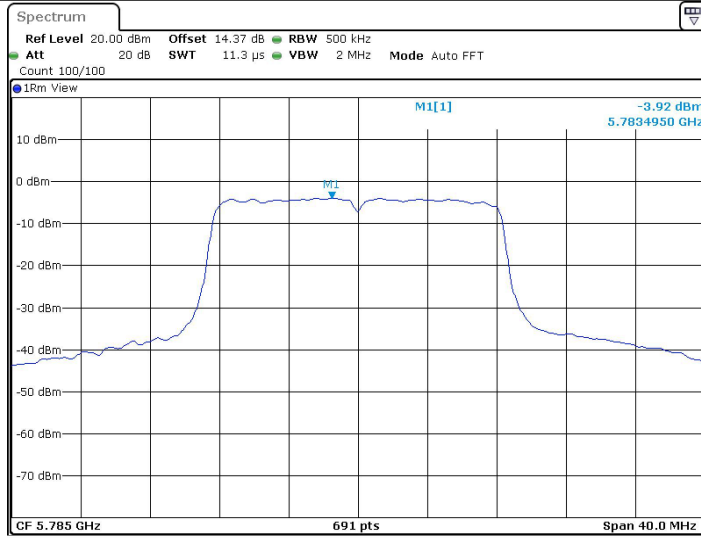
Date: 22.MAR.2024 10:52:14

11A_Ant2_5745



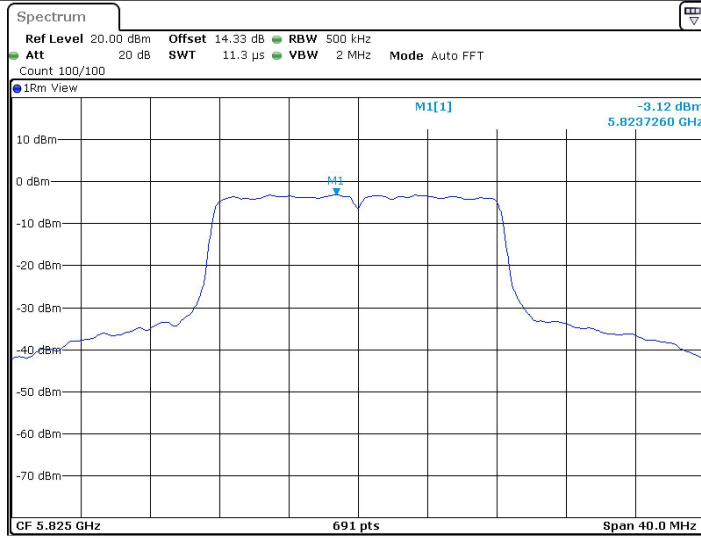
Date: 22.MAR.2024 14:05:04

11A_Ant2_5785



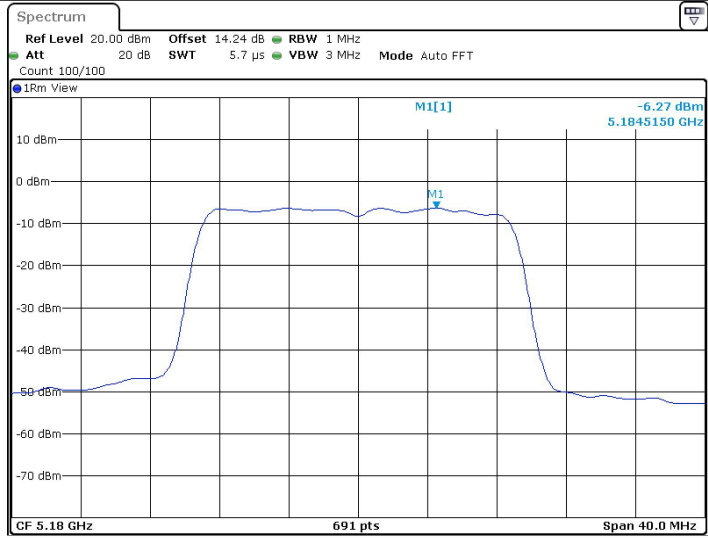
Date: 22.MAR.2024 12:35:15

11A_Ant2_5825



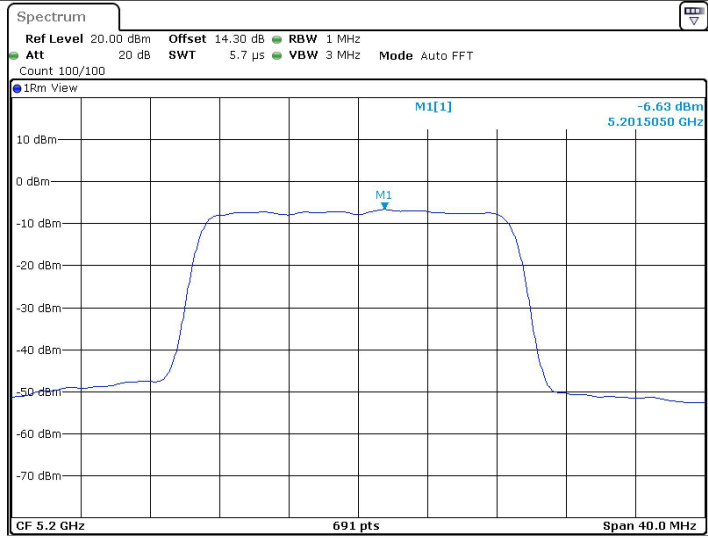
Date: 22.MAR.2024 12:36:42

11N20SISO_Ant2_5180



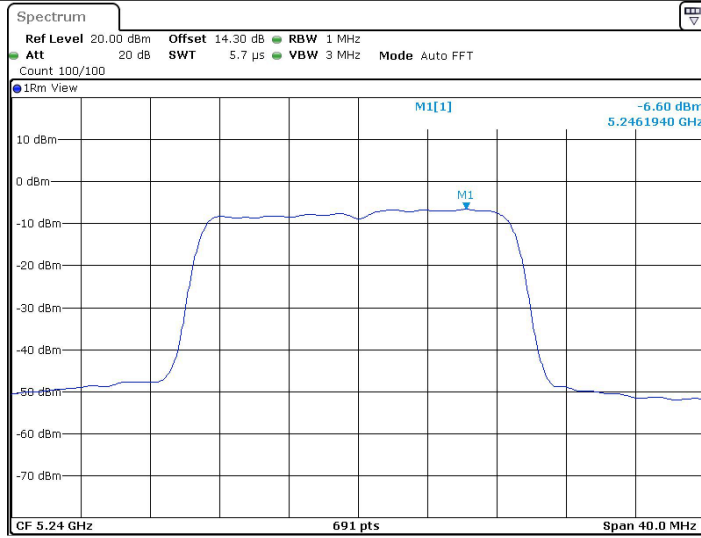
Date: 22.MAR.2024 12:40:47

11N20SISO_Ant2_5200



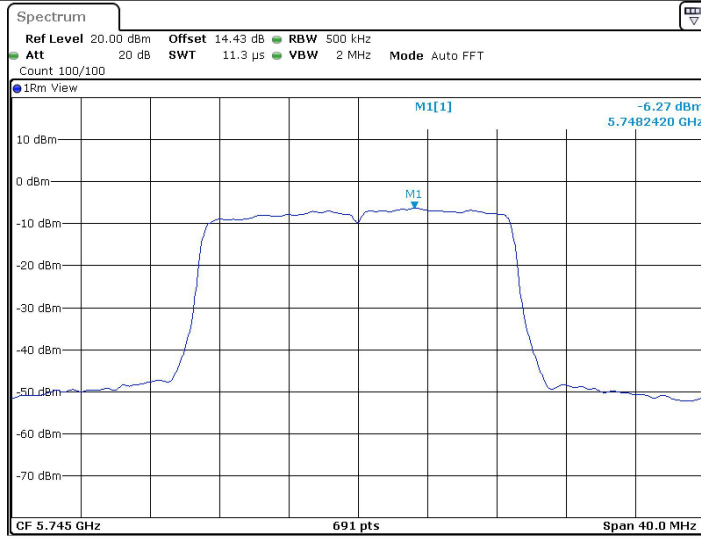
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11N20SISO_Ant2_5240



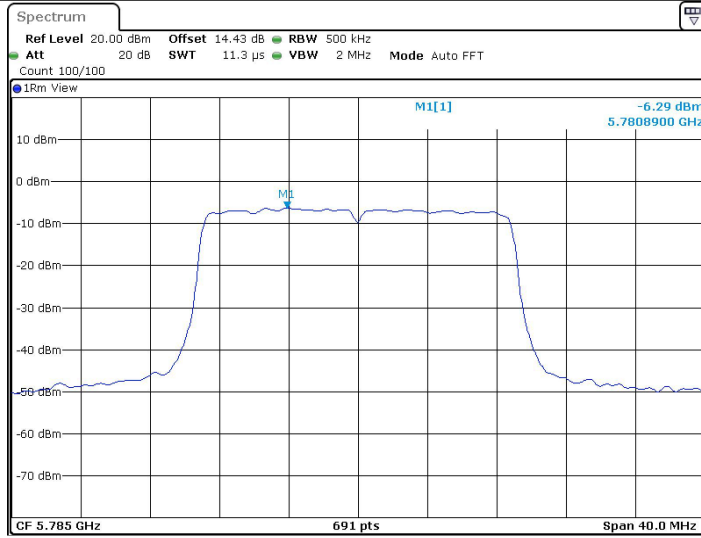
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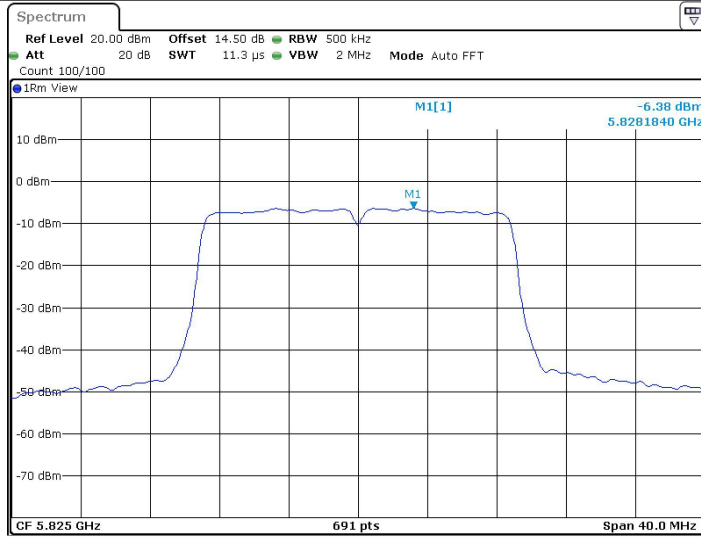
Date: 22.MAR.2024 12:48:12

11N20SISO_Ant2_5785



Date: 22.MAR.2024 12:50:33

11N20SISO_Ant2_5825



Date: 22.MAR.2024 12:56:20