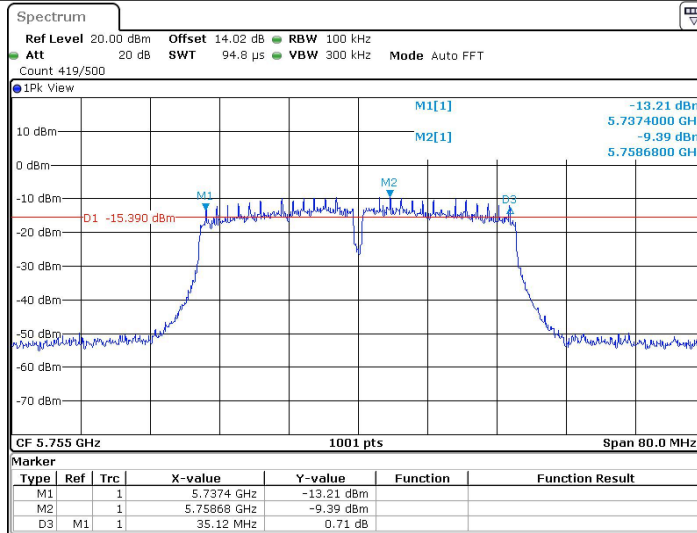
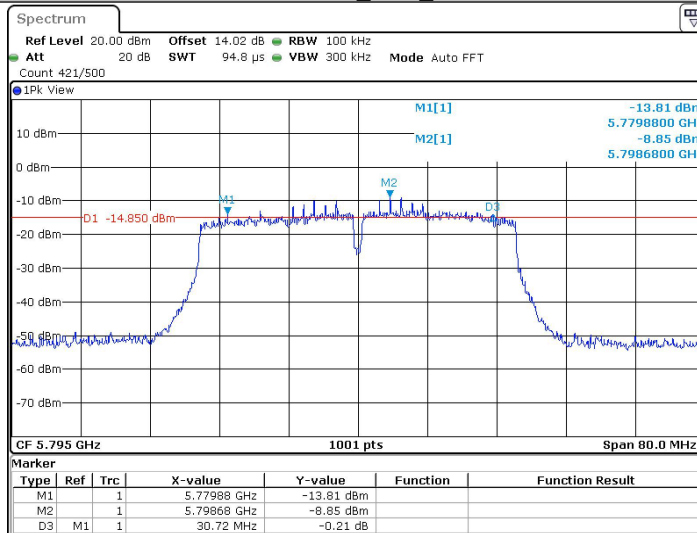


11N40SISO_Ant2_5755



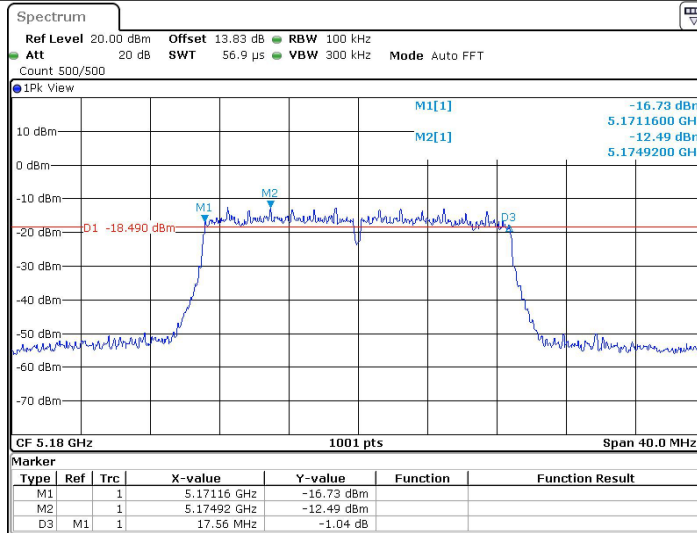
Date: 21.MAR.2024 16:12:35

11N40SISO_Ant2_5795



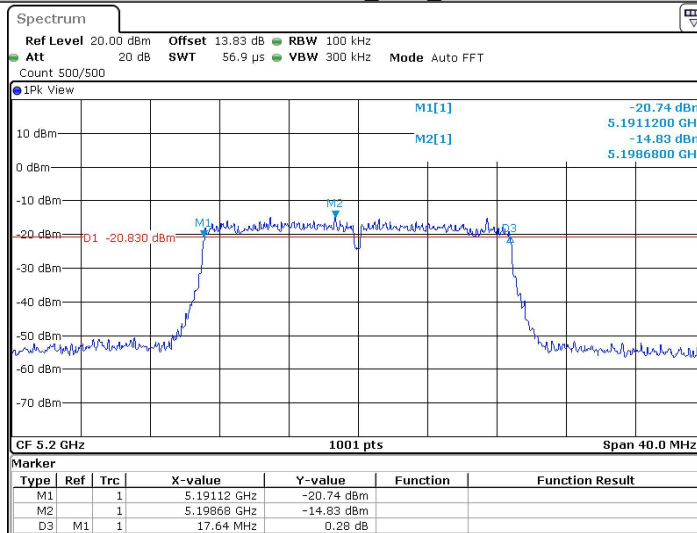
Date: 21.MAR.2024 16:18:43

11AC20SISO_Ant2_5180



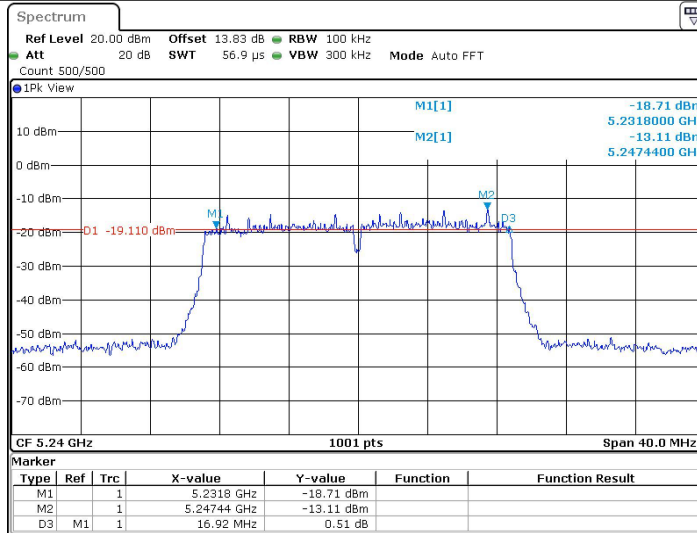
Date: 21.MAR.2024 16:21:18

11AC20SISO_Ant2_5200



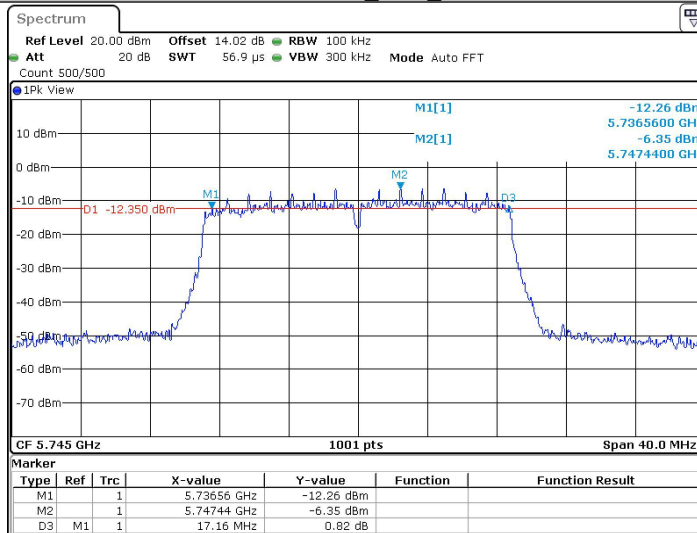
Date: 21.MAR.2024 16:25:56

11AC20SISO_Ant2_5240



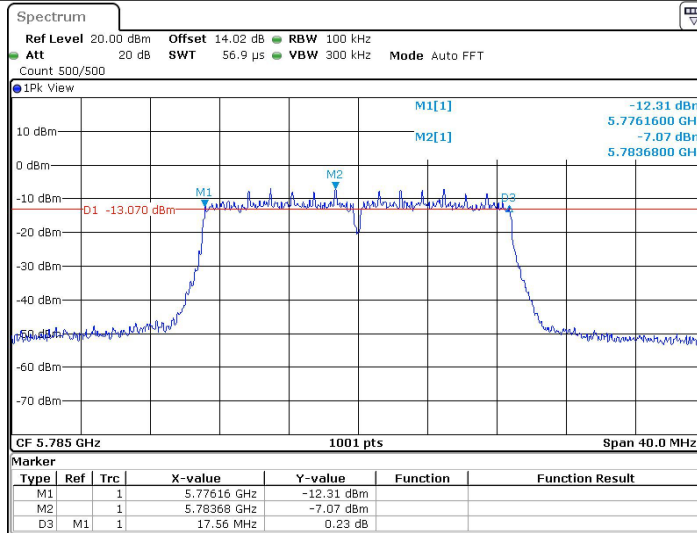
Date: 21.MAR.2024 16:30:24

11AC20SISO_Ant2_5745



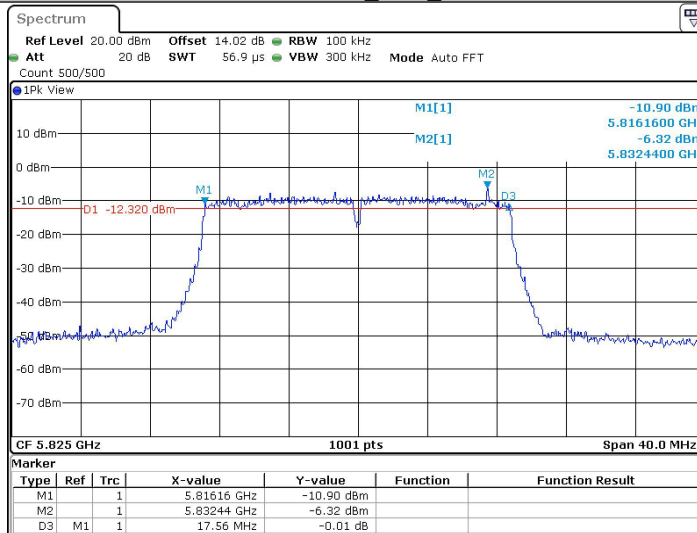
Date: 21.MAR.2024 16:33:57

11AC20SISO_Ant2_5785



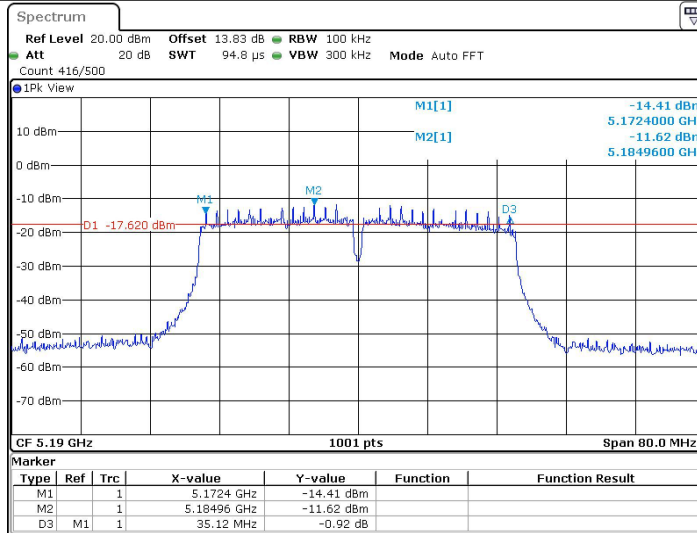
Date: 21.MAR.2024 16:36:54

11AC20SISO_Ant2_5825



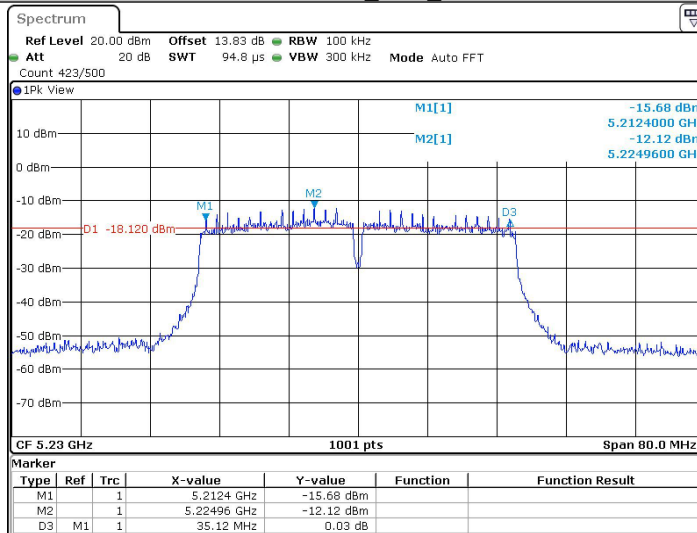
Date: 21.MAR.2024 16:41:52

11AC40SISO_Ant2_5190



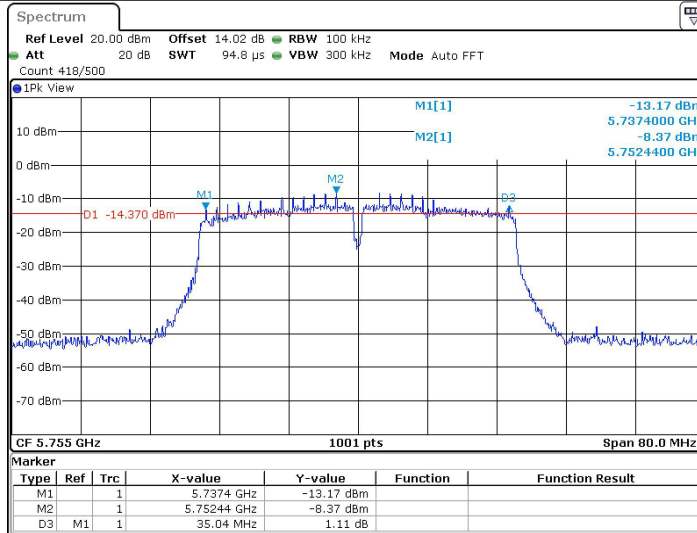
Date: 21.MAR.2024 17:29:21

11AC40SISO_Ant2_5230



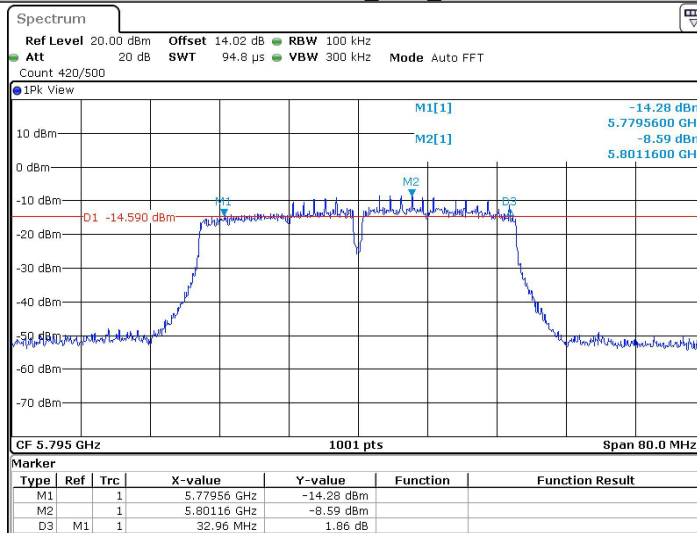
Date: 21.MAR.2024 17:32:48

11AC40SISO_Ant2_5755



Date: 21.MAR.2024 17:35:47

11AC40SISO_Ant2_5795



Date: 21.MAR.2024 17:39:48

Appendix B): Maximum Conduct Output Power

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II E

Limit:

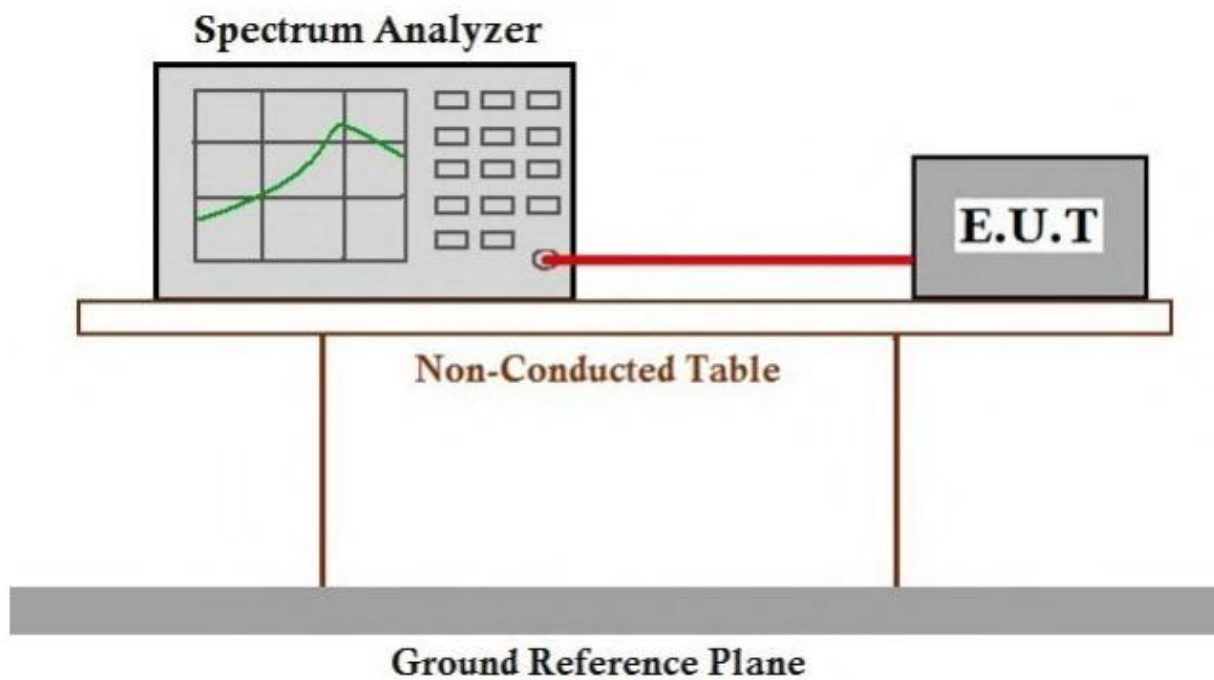
Frequency band(MHz)	Limit
5150-5250	≤1W(30dBm) for master device
	≤250mW(24dBm) for client device
5250-5350	≤250mW(24dBm) for client device or 11dBm+10logB*
5470-5725	≤250mW(24dBm) for client device or 11dBm+10logB*
5725-5850	≤1W(30dBm)
Remark:	* Where B is the 26dB emission bandwidth in MHz. The maximum conducted output power must be measured over any interval of continuous transmission using instrumentation calibrated in terms of an rms-equivalent voltage.

Test Procedure:

Method SA-2 (trace averaging across on and off times of the EUT transmissions, followed by duty cycle correction).

- (1) Set RBW = 1 MHz.
- (2) Set VBW ≥ 3 MHz.
- (3) Detector = power average
- (4) Sweep time = auto.
- (5) Add duty cycle to the measured average power.

Test Setup Diagram



Measurement Data

ANT1:

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	9.39	≤23.98	PASS
		5200	9.00	≤23.98	PASS
		5240	8.43	≤23.98	PASS
		5745	10.97	≤30.00	PASS
		5785	10.59	≤30.00	PASS
		5825	11.27	≤30.00	PASS
11N20SISO	Ant1	5180	8.12	≤23.98	PASS
		5200	6.80	≤23.98	PASS
		5240	6.17	≤23.98	PASS
		5745	10.28	≤30.00	PASS
		5785	10.27	≤30.00	PASS
		5825	1.28	≤30.00	PASS
11N40SISO	Ant1	5190	3.54	≤23.98	PASS
		5230	2.94	≤23.98	PASS
		5755	-0.50	≤30.00	PASS
		5795	-0.65	≤30.00	PASS
11AC20SISO	Ant1	5180	5.35	≤23.98	PASS
		5200	4.44	≤23.98	PASS
		5240	3.56	≤23.98	PASS
		5745	-0.37	≤30.00	PASS
		5785	6.15	≤30.00	PASS
		5825	6.97	≤30.00	PASS
11AC40SISO	Ant1	5190	4.89	≤23.98	PASS
		5230	-1.01	≤23.98	PASS
		5755	-2.68	≤30.00	PASS
		5795	-2.76	≤30.00	PASS

ANT2:

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	7.92	≤23.98	PASS
		5200	8.62	≤23.98	PASS
		5240	7.36	≤23.98	PASS
		5745	10.61	≤30.00	PASS
		5785	-1.14	≤30.00	PASS
		5825	-0.25	≤30.00	PASS
11N20SISO	Ant2	5180	7.31	≤23.98	PASS
		5200	6.24	≤23.98	PASS
		5240	4.94	≤23.98	PASS
		5745	8.26	≤30.00	PASS
		5785	8.48	≤30.00	PASS
		5825	8.79	≤30.00	PASS
11N40SISO	Ant2	5190	1.60	≤23.98	PASS
		5230	0.65	≤23.98	PASS
		5755	4.67	≤30.00	PASS
		5795	4.44	≤30.00	PASS
11AC20SISO O	Ant2	5180	0.12	≤23.98	PASS
		5200	-1.62	≤23.98	PASS
		5240	-1.91	≤23.98	PASS
		5745	4.81	≤30.00	PASS
		5785	4.42	≤30.00	PASS
		5825	6.11	≤30.00	PASS
11AC40SISO O	Ant2	5190	2.42	≤23.98	PASS
		5230	1.86	≤23.98	PASS
		5755	5.87	≤30.00	PASS
		5795	5.49	≤30.00	PASS

ANT1+ANT2:

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11N20MIM O	Ant2	5180	10.74	≤23.2	PASS
		5200	9.54	≤23.2	PASS
		5240	8.61	≤23.2	PASS
		5745	12.40	≤29.67	PASS
		5785	12.48	≤29.67	PASS
		5825	9.50	≤29.67	PASS
11N40MIM O	Ant2	5190	5.69	≤23.2	PASS
		5230	4.95	≤23.2	PASS
		5755	5.82	≤29.67	PASS
		5795	5.61	≤29.67	PASS
11AC20MI MO	Ant2	5180	6.49	≤23.2	PASS
		5200	5.40	≤23.2	PASS
		5240	4.64	≤23.2	PASS
		5745	5.96	≤29.67	PASS
		5785	8.38	≤29.67	PASS
		5825	9.57	≤29.67	PASS
11AC40MI MO	Ant2	5190	6.84	≤23.2	PASS
		5230	3.67	≤23.2	PASS
		5755	6.44	≤29.67	PASS
		5795	6.10	≤29.67	PASS

Remark:

 $Av.Power = Meas.Level + 10 \log(1/duty\ cycle)$

Appendix C): Maximum Power Spectral Density

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II F

Test Procedure:

For 5150-5725MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 1MHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

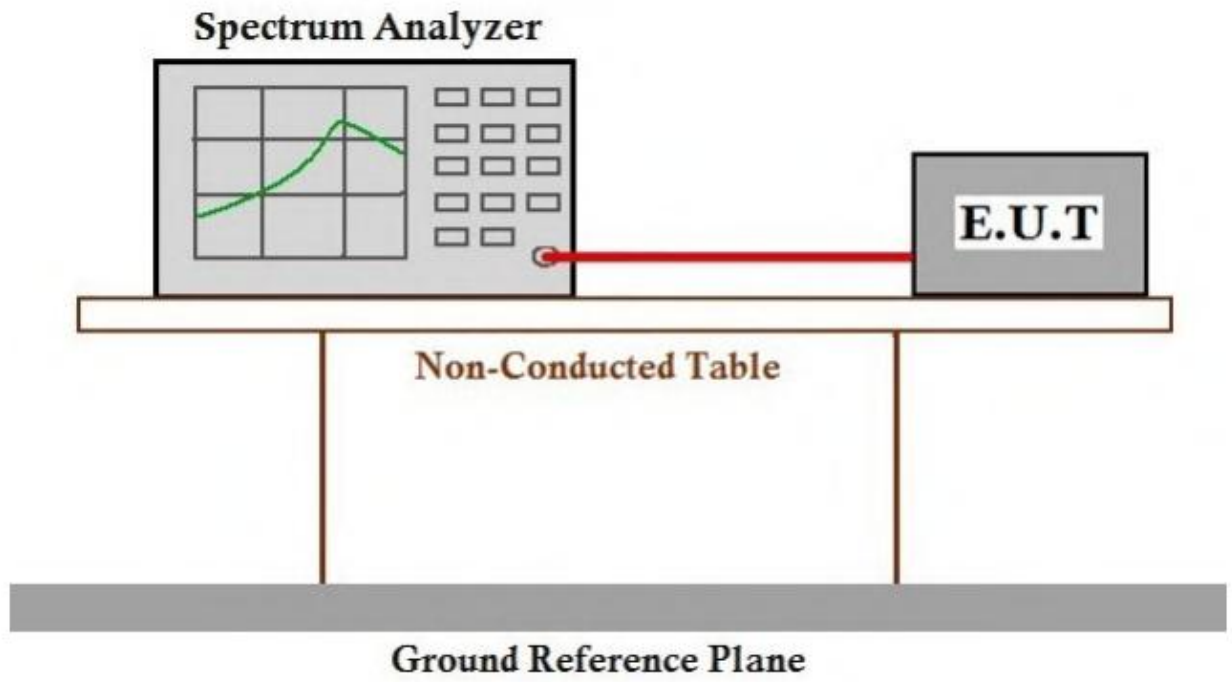
For 5725-5850MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 500KHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

Limit:

Frequency band(MHz)	Limit
5150-5250	≤ 17 dBm in 1MHz for master device
	≤ 11 dBm in 1MHz for client device
5250-5350	≤ 11 dBm in 1MHz for client device
5470-5725	≤ 11 dBm in 1MHz for client device
5725-5850	≤ 30 dBm in 500 kHz
Remark:	The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test.

Test Setup Diagram



Result Table

ANT1:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	-1.46	≤11.00	PASS
	5200	-2.22	≤11.00	PASS
	5240	-2.31	≤11.00	PASS
	5745	-3.04	≤30.00	PASS
	5785	-3.8	≤30.00	PASS
	5825	-2.82	≤30.00	PASS
11N20SISO	5180	-3.49	≤11.00	PASS
	5200	-4.61	≤11.00	PASS
	5240	-5.11	≤11.00	PASS
	5745	-4.02	≤30.00	PASS
	5785	-3.91	≤30.00	PASS
	5825	-12.75	≤30.00	PASS
11N40SISO	5190	-10.19	≤11.00	PASS
	5230	-11.23	≤11.00	PASS
	5755	-17.04	≤30.00	PASS
	5795	-16.52	≤30.00	PASS
11AC20SISO	5180	-5.85	≤11.00	PASS
	5200	-6.93	≤11.00	PASS
	5240	-7.56	≤11.00	PASS
	5745	-14.48	≤30.00	PASS
	5785	-8.51	≤30.00	PASS
	5825	-7.24	≤30.00	PASS
11AC40SISO	5190	-9.42	≤11.00	PASS
	5230	-15.39	≤11.00	PASS
	5755	-19.54	≤30.00	PASS
	5795	-19.58	≤30.00	PASS

ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	-3.22	≤11.00	PASS
	5200	-3.51	≤11.00	PASS
	5240	-2.92	≤11.00	PASS
	5745	-2.18	≤30.00	PASS
	5785	-3.92	≤30.00	PASS
	5825	-3.12	≤30.00	PASS
11N20SISO	5180	-6.27	≤11.00	PASS
	5200	-6.63	≤11.00	PASS
	5240	-6.6	≤11.00	PASS
	5745	-6.27	≤30.00	PASS
	5785	-6.29	≤30.00	PASS
	5825	-6.38	≤30.00	PASS
11N40SISO	5190	-13.74	≤11.00	PASS
	5230	-13.24	≤11.00	PASS
	5755	-13.92	≤30.00	PASS
	5795	-13.41	≤30.00	PASS
11AC20SISO	5180	-9.96	≤11.00	PASS
	5200	-10.04	≤11.00	PASS
	5240	-10.13	≤11.00	PASS
	5745	-11.25	≤30.00	PASS
	5785	-11.26	≤30.00	PASS
	5825	-9.1	≤30.00	PASS
11AC40SISO	5190	-13.25	≤11.00	PASS
	5230	-13.52	≤11.00	PASS
	5755	-12.36	≤30.00	PASS
	5795	-12.69	≤30.00	PASS

ANT1+ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11N20MIMO	5180	-1.65	≤10.22	PASS
	5200	-2.49	≤10.22	PASS
	5240	-2.78	≤10.22	PASS
	5745	-1.99	≤29.67	PASS
	5785	-1.93	≤29.67	PASS
	5825	-5.48	≤29.67	PASS
11N40MIMO	5190	-8.60	≤10.22	PASS
	5230	-9.11	≤10.22	PASS
	5755	-12.20	≤29.67	PASS
	5795	-11.68	≤29.67	PASS
11AC20MIMO	5180	-4.43	≤10.22	PASS
	5200	-5.20	≤10.22	PASS
	5240	-5.65	≤10.22	PASS
	5745	-9.56	≤29.67	PASS
	5785	-6.66	≤29.67	PASS
	5825	-5.06	≤29.67	PASS
11AC40MIMO	5190	-7.92	≤10.22	PASS
	5230	-11.34	≤10.22	PASS
	5755	-11.60	≤29.67	PASS
	5795	-11.88	≤29.67	PASS

Remark:

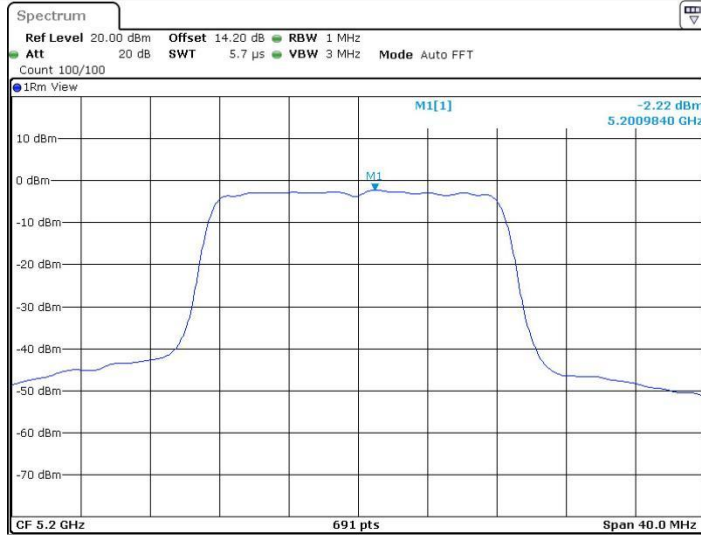
PSD = Meas PSD + Duty Cycle Factor

11A_Ant1_5180



Date: 14.MAR 2024 16:41:17

11A_Ant1_5200



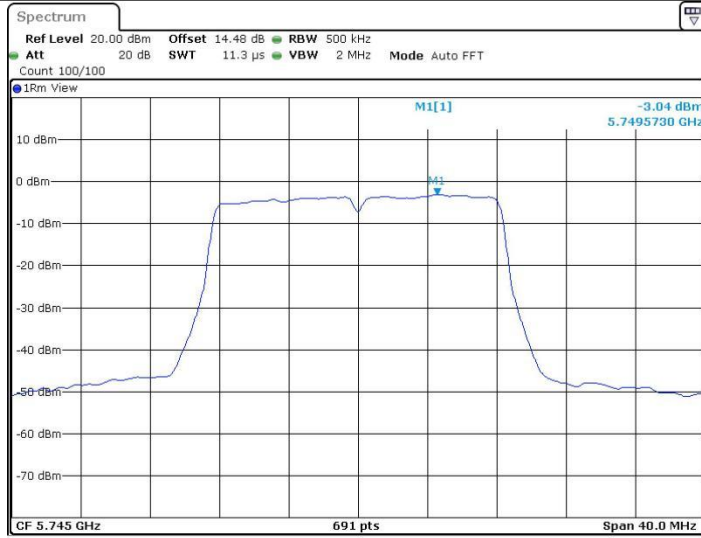
Date: 14.MAR 2024 16:45:14

11A_Ant1_5240



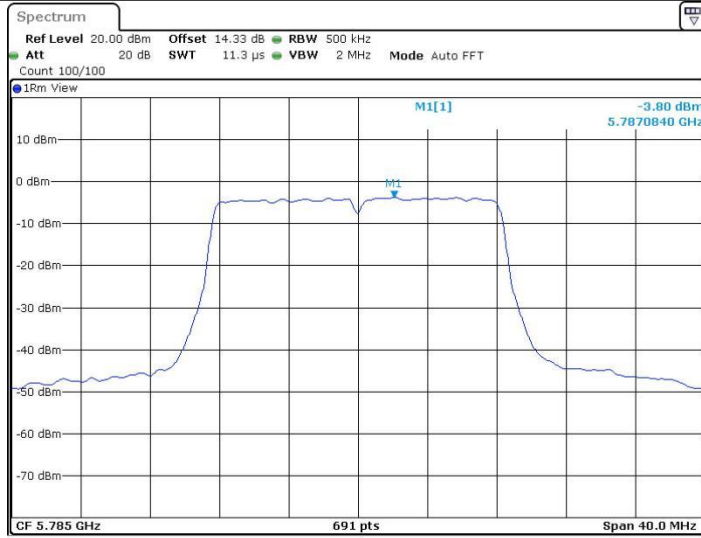
Date: 14.MAR.2024 16:46:49

11A_Ant1_5745



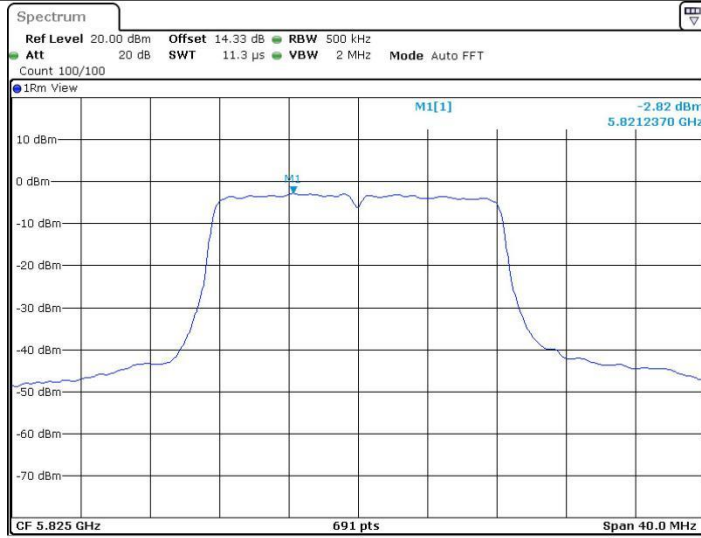
Date: 15.MAR.2024 13:50:12

11A_Ant1_5785



Date: 15 MAR 2024 13:52:51

11A_Ant1_5825



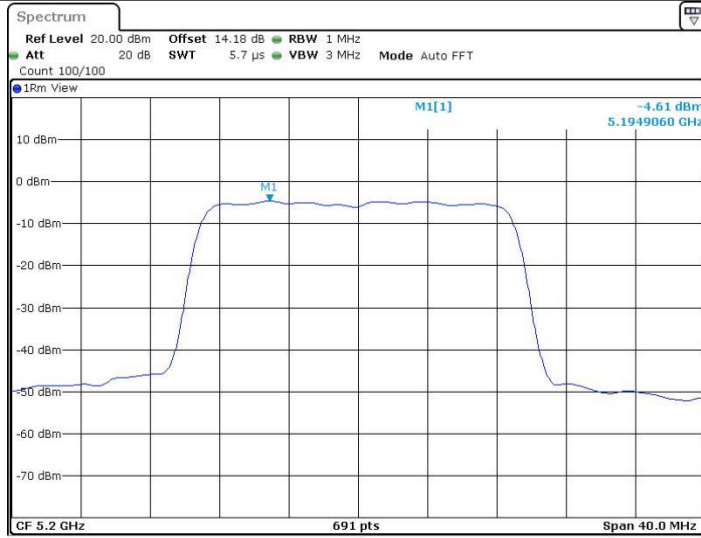
Date: 15 MAR 2024 13:54:22

11N20SISO_Ant1_5180



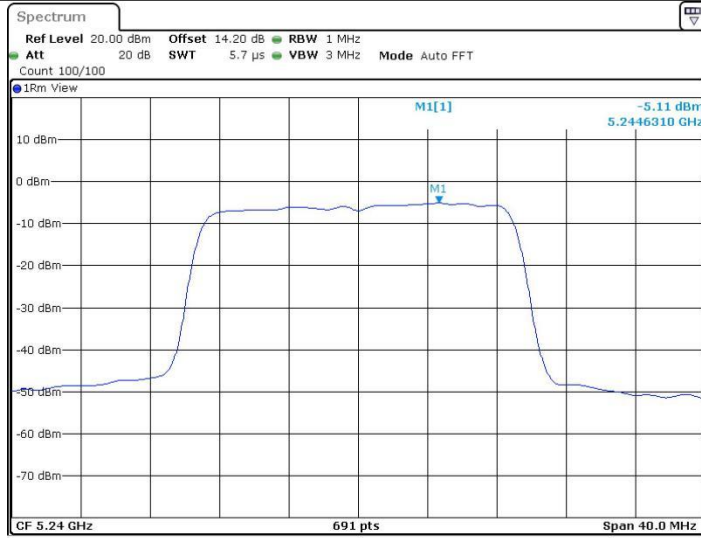
Date: 15 MAR 2024 14:06:34

11N20SISO_Ant1_5200



Date: 15 MAR 2024 14:12:50

11N20SISO_Ant1_5240



Date: 15 MAR 2024 14:14:32

11N20SISO_Ant1_5745



Date: 15 MAR 2024 14:17:13

11N20SISO_Ant1_5785



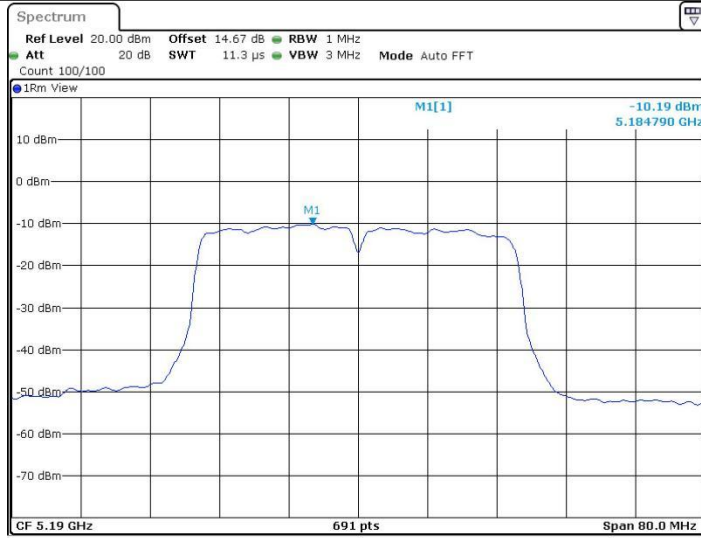
Date: 15 MAR 2024 14:20:25

11N20SISO_Ant1_5825



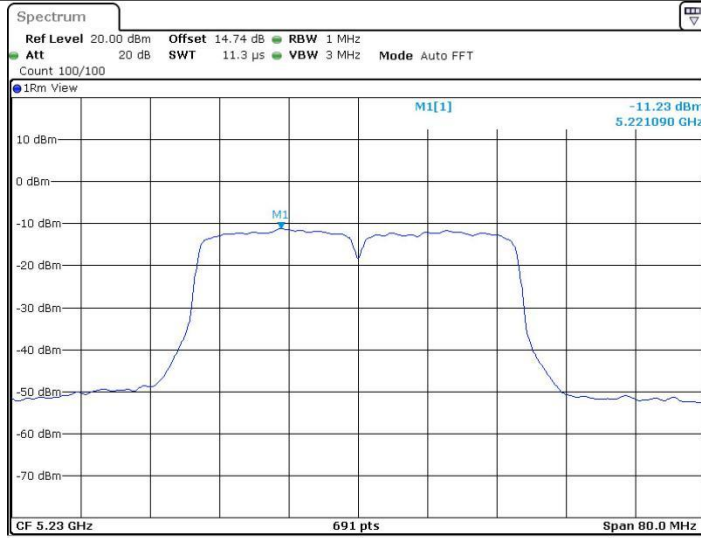
Date: 15 MAR 2024 14:29:23

11N40SISO_Ant1_5190



Date: 15 MAR 2024 14:33:08

11N40SISO_Ant1_5230

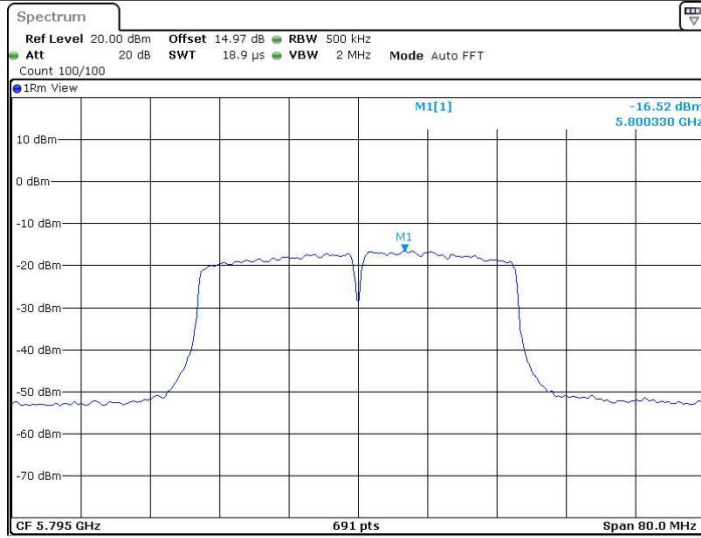


Date: 15 MAR 2024 14:36:48

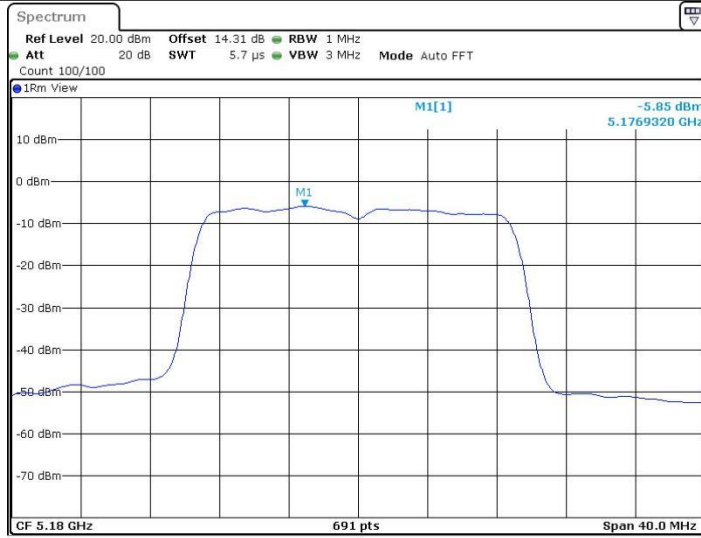
11N40SISO_Ant1_5755



11N40SISO_Ant1_5795

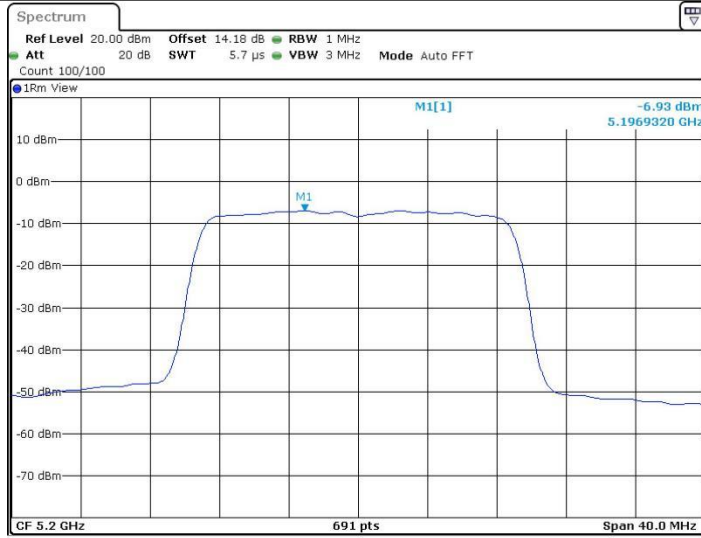


11AC20SISO_Ant1_5180



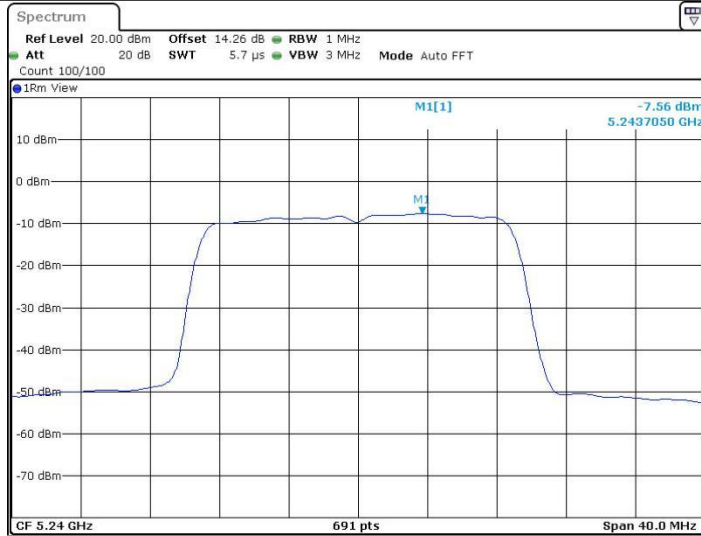
Date: 15 MAR 2024 14:46:41

11AC20SISO_Ant1_5200



Date: 15 MAR 2024 14:49:45

11AC20SISO_Ant1_5240



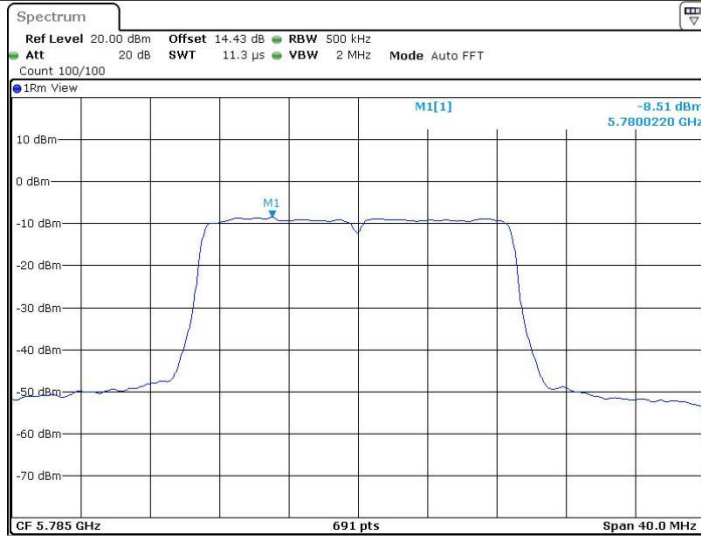
Date: 15 MAR 2024 14:53:29

11AC20SISO_Ant1_5745



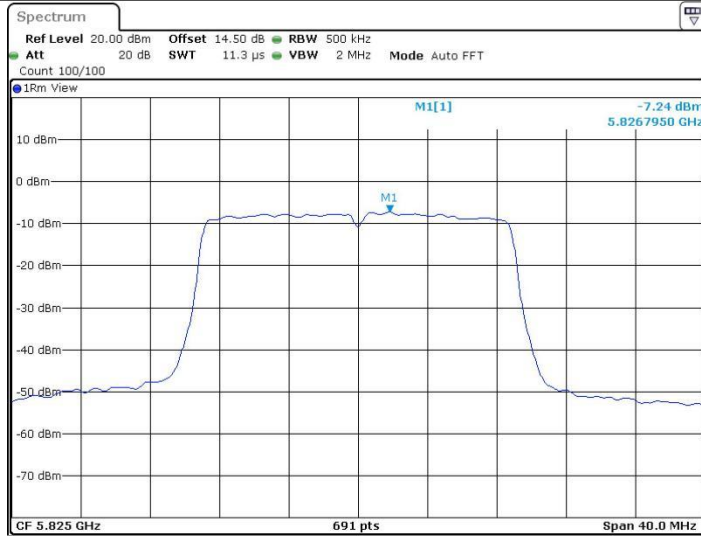
Date: 15 MAR 2024 15:09:11

11AC20SISO_Ant1_5785



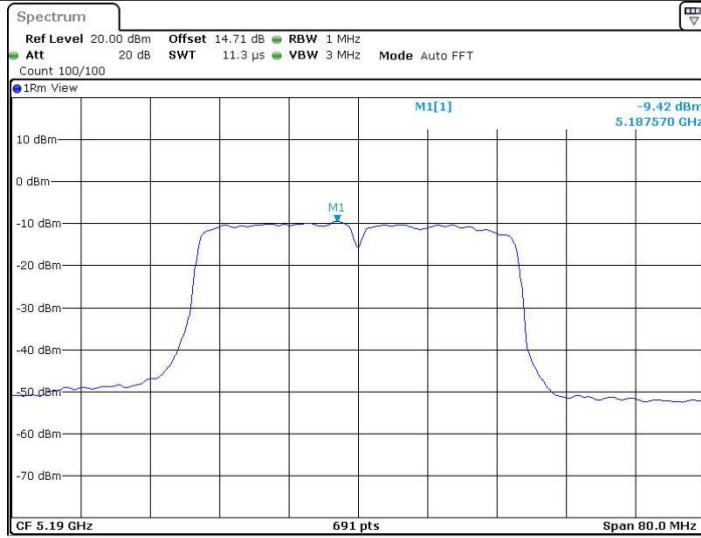
Date: 21 MAR 2024 20:40:00

11AC20SISO_Ant1_5825



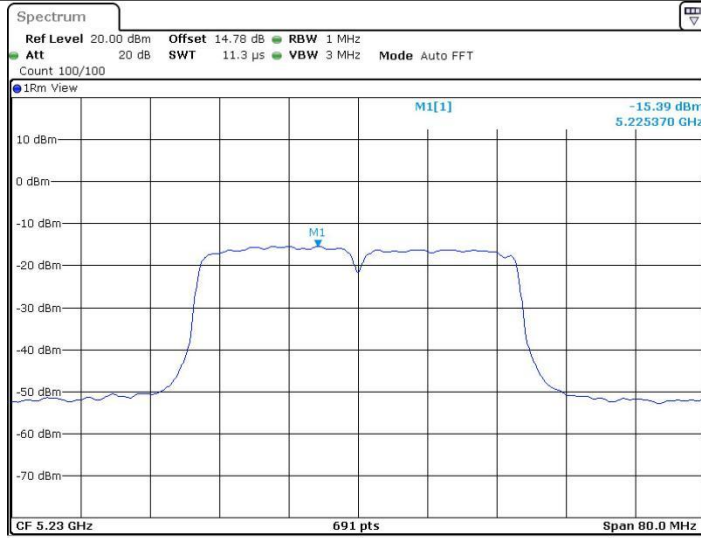
Date: 21 MAR 2024 20:42:04

11AC40SISO_Ant1_5190



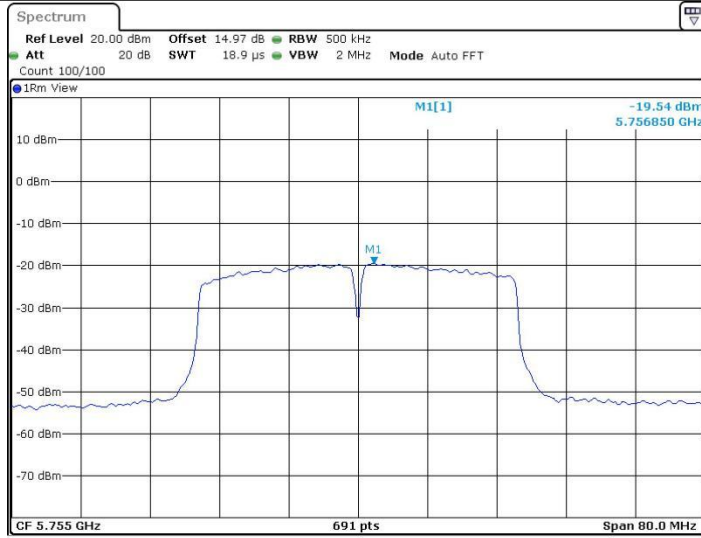
Date: 15 MAR 2024 15:19:21

11AC40SISO_Ant1_5230

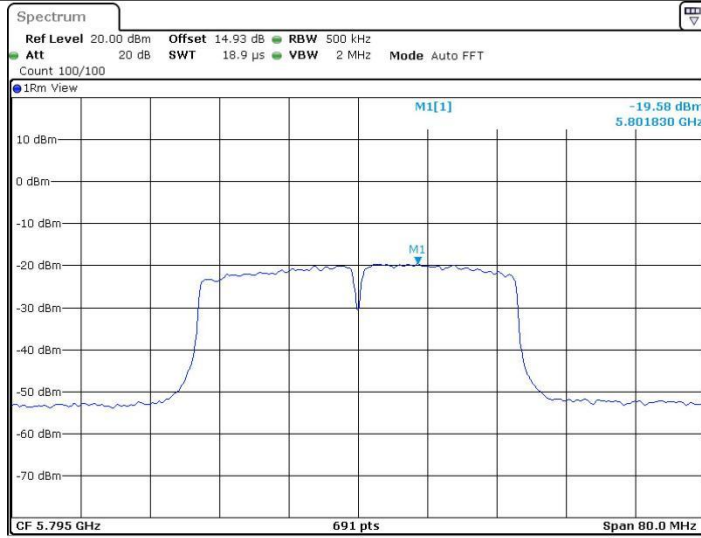


Date: 15 MAR 2024 15:25:09

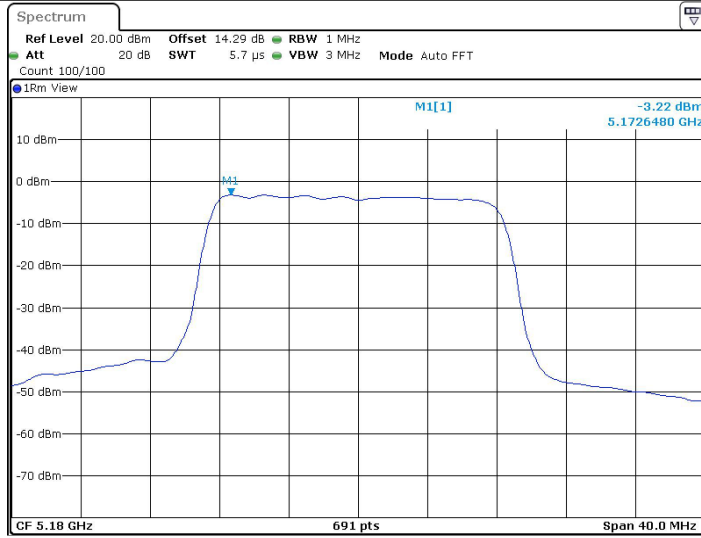
11AC40SISO_Ant1_5755



11AC40SISO_Ant1_5795

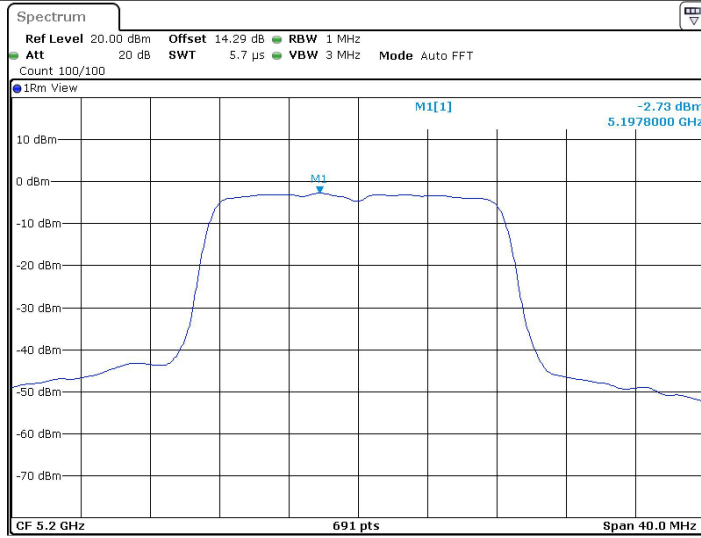


11A_Ant2_5180



Date: 22.MAR.2024 15:41:14

11A_Ant2_5200



Date: 21.MAR.2024 15:16:53