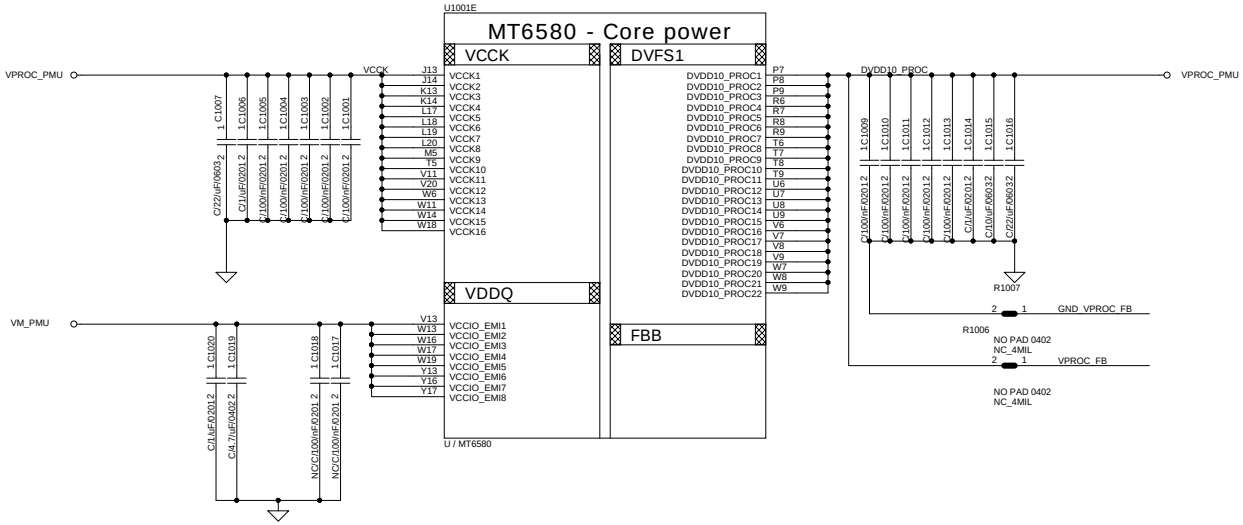
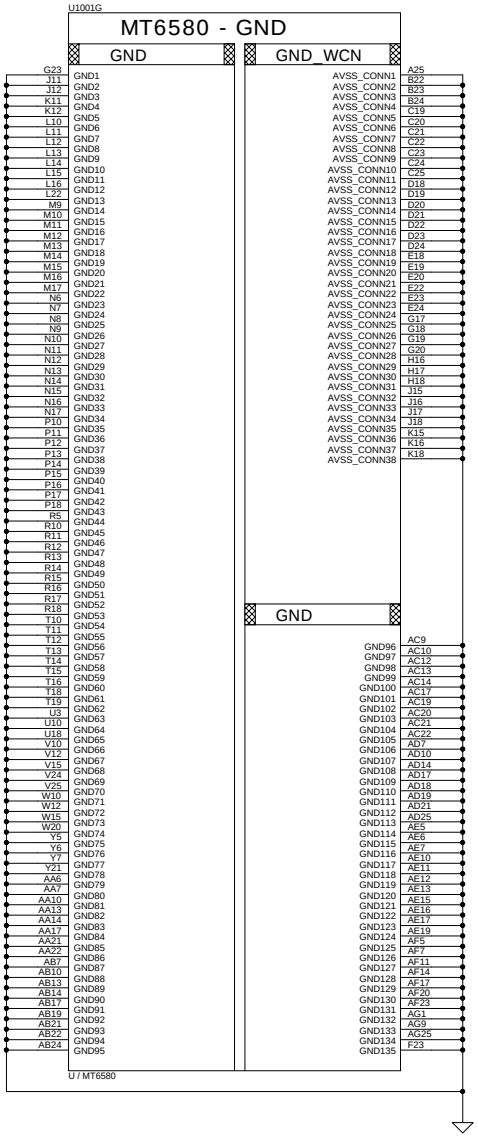
Power Feedback

VPROC_FB
GND_VPROC_FB

VPROC_FB
GND_VPROC_FB





MEDIATEK	
Title	10_BB_POWER_1
Size C / Document Number	MT6580 PHONE / Rev V1.0
Date: Monday, June 25, 2018	Sheet of

Misc

20,65	SYSRSTB	SYSRSTB
20	WATCHDOG	WATCHDOG
20	EINT_PMIC	EINT_PMIC
20	SRCLKENA0	SRCLKENA0
20	CLK32K_BB	CLK32K_BB

PMIC Contorl IF

20	PWRAP_SPI0_CSN	PWRAP_SPI0_CSN
20	PWRAP_SPI0_CK	PWRAP_SPI0_CK
20	PWRAP_SPI0_MO	PWRAP_SPI0_MO
20	PWRAP_SPI0_MI	PWRAP_SPI0_MI

AP to PMIC IF

Audio IF

20	AUD_CLK_MOSI	AUD_CLK_MOSI
20	AUD_DAT_MOSI	AUD_DAT_MOSI
20	AUD_DAT_MISO	AUD_DAT_MISO

Tmp sensor

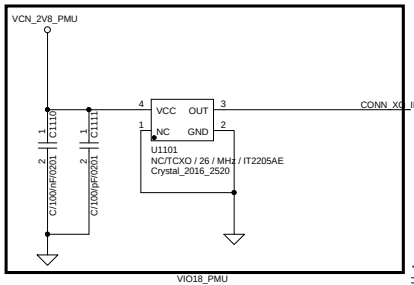
WCN RF Interface

50	WB_GPS_RFIN	WB_GPS_RFIN
11,12	GPS_LNA_EN	GPS_LNA_EN
60	FM_LANT_P	FM_LANT_P
60	FM_LANT_N	FM_LANT_N

SIM IF

20	SIM1_SCLK	SIM1_SCLK
20	SIM1_SIO	SIM1_SIO
20	SIM2_SCLK	SIM2_SCLK
20	SIM2_SIO	SIM2_SIO

CONNSYS TCXO



U1001A

MT6580-ABB/ WCN

PMU_IF

SYSRSTB	T1	SYSRSTB
WATCHDOG	R2	WATCHDOG
EINT_PMIC	AC2	EINTX
SRCLKENA0	V4	SRCLKENA0
	V3	SRCLKENAI
CLK32K_BB	T3	RTC32K_CK
PWRAP_SPI0_CSN	V1	PWRAP_SPI0_CSN
PWRAP_SPI0_CK	W2	PWRAP_SPI0_CK
PWRAP_SPI0_MO	W3	PWRAP_SPI0_MO
PWRAP_SPI0_MI	W1	PWRAP_SPI0_MI
AUD_CLK_MOSI	U2	AUD_CLK_MOSI
AUD_DAT_MOSI	T2	AUD_DAT_MOSI
AUD_DAT_MISO	V2	AUD_DAT_MISO

REF POWER

REFP	J2	REFP
	J1	REFN

AUX IN

	K2	AUX_IN2
AP_TEMPERATURE	L2	AUX_IN1
	L1	AUX_IN0

WCN_IF

WB_EXT_G	F21	
WB_GPS_RFIN	B25	WB_GPS_RFIN
GPS_DPX_RFOUT	A24	
GPS_RFIN	A18	GPS_ANT
FM_LANT_N	A21	FM_LANT_N
FM_LANT_P	A20	FM_LANT_P
XO_IN	E16	CONN_XO_IN

Misc

TESTMODE	D15	
FSOURCE_P	F11	VGP2_eFUSE

PLLs Test Pin

PAD_TP_MEMPLL	AB23	

SIM

SIM1_SCLK	N2	SIM1_SCLK
SIM1_SIO	P1	SIM1_SIO
SIM2_SCLK	P2	SIM2_SCLK
SIM2_SIO	N1	SIM2_SIO

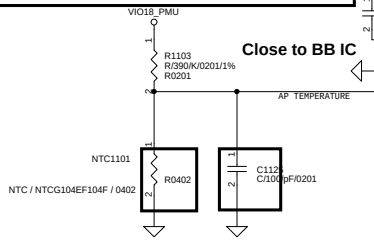
U / MT6580

50 Ohm

For eFUSE

C1126 C/100nF/0201

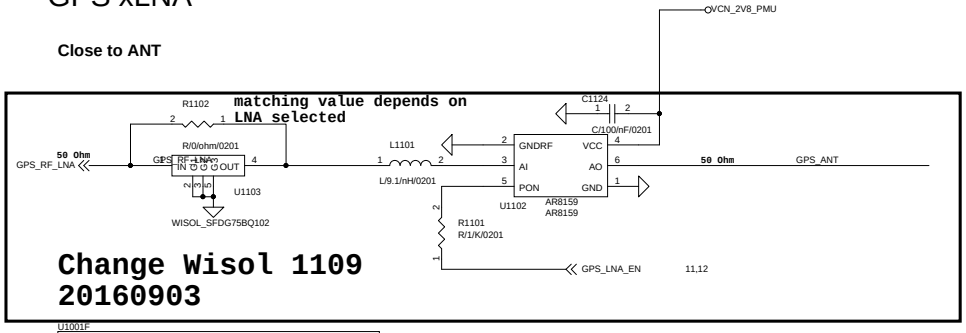
Close to BB IC



C1108 C/1uF/0201

GPS xLNA

Close to ANT



Change Wisol 1109 20160903

U1001F

MT6580 - Peripheral Power

C1123 Close DVDD18_MSDC0
C1107 Close DVDD28_MSDC1

PERI_D

VIO18_PMU	R1	DVDD18_IO1
	G24	DVDD18_IO2
	W25	DVDD18_IO5
	AC1	DVDD18_MSDC0
VMC_PMU	H22	DVDD28_MSDC1
20,61,63 VIO28_PMU	A11	DVDD18
	C12	DVDD28
	R25	DVDD18_MIPII1
	R24	DVDD18_MIPIT1
	N21	DVSS18_MIPII2
	P21	DVSS18_MIPII3
	R19	DVSS18_MIPIT2

PERI_A

VIO18_PMU	K3	AVDD18_AP
VA_PMU	M1	AVDD28_DAC
VUSB_PMU	M21	AVDD33_USB
VIO18_PMU	J24	AVDD18_USB
VIO18_PMU	J3	AVDD18_MD
	M4	AVSS18_ABB
	K23	AVSS33_USB

PERI_WCN

AVDD18_WBT	F22	
AVDD18_WBT_AFE	J20	
AVDD18_FM	B21	
AVDD18_GPS	A18	
AVDD33_WBT	A22	

U / MT6580

VCN_1V8_PMU

C1105 C/1uF/0201

Cloesd to MT6350

VCN_3V3_PMU

Cloesd to MT6350

AVDD28_DAC change power source by "VA_PMU".

MEDIATEK			
Title	11_BB_1		
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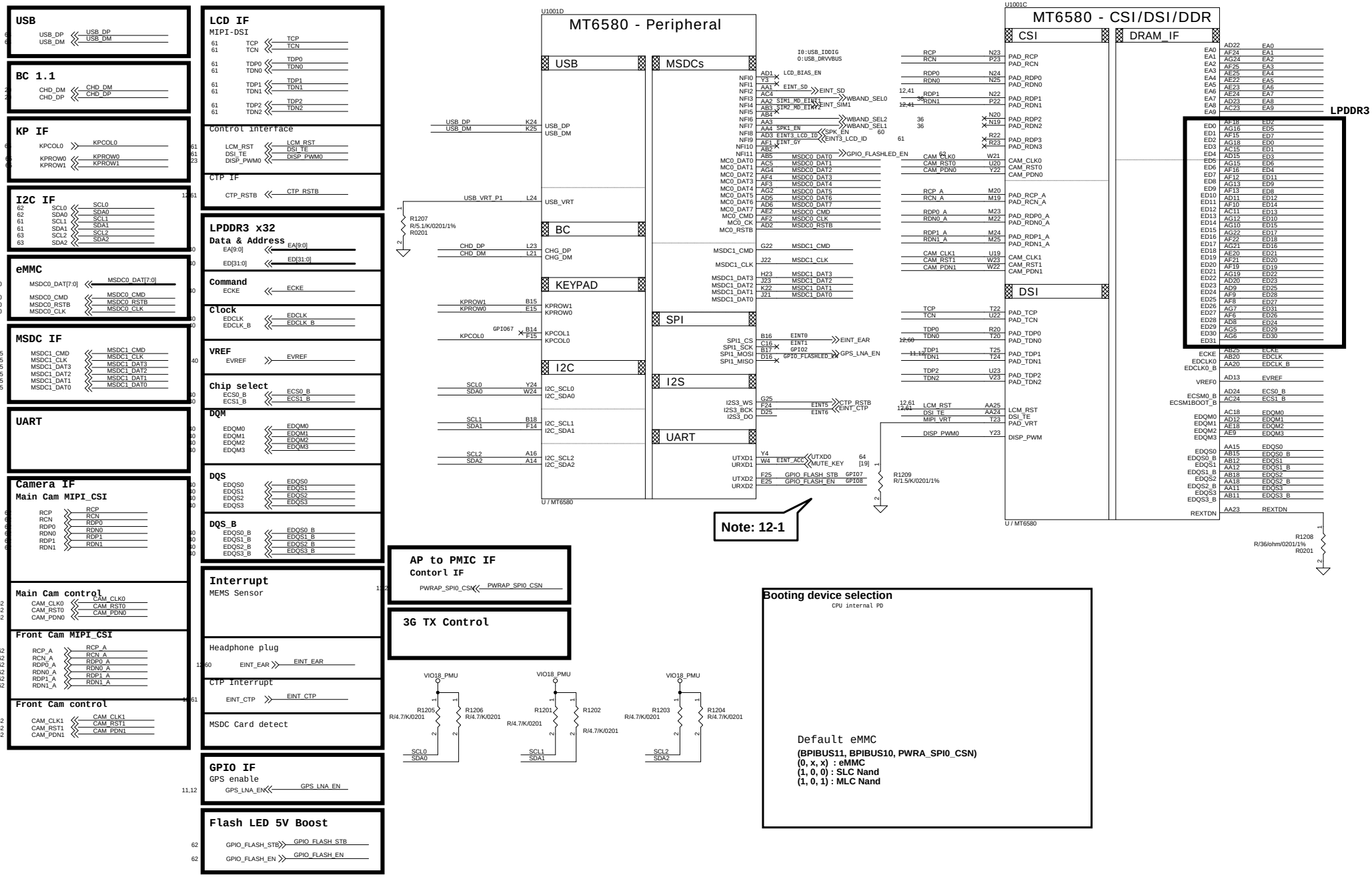


Schematic design notice of "12_BB_2" page.

Note 12-1: Do not use UART1 as Power solution enable.
Do not use UART2 as power solution enable, if select UART1 as D/L or calibration path,

MEDIATEK

Title	12_BB_2	
Size C	Document Number MT6580 PHONE	Rev V1.0
Date: Monday, June 25, 2018	Sheet of	

Audio IF

Downlink

AU_SPKP / AU_SPKN
AU_HPL / AU_HPR
AU_HSP / AU_HSN

Uplink

AU_VIN0_P / AU_VIN0_N
AU_VIN1_N / AU_VIN1_P

HP detect

ACCDET

Vcore FB

VPROC_FB / GND_VPROC_FB

AP to PMIC IF

Audio IF

AUD_CLK_MOSI / AUD_DAT_MOSI / AUD_DAT_MISO

PMIC_SPI IF

PWRAP_SPI0_CSN / PWRAP_SPI0_CK / PWRAP_SPI0_MO / PWRAP_SPI0_MI

Misc

SRCLKENA0 / EINT_PMIC / WATCHDOG / SYSRSTB

CLK

CLK4_AUDIO / CLK32K_BB / OUT_32K

BC 1.1

CHD_DM / CHD_DP

CO-TMS

AUXADC_REF / AUXADC_TSX

SIM IF

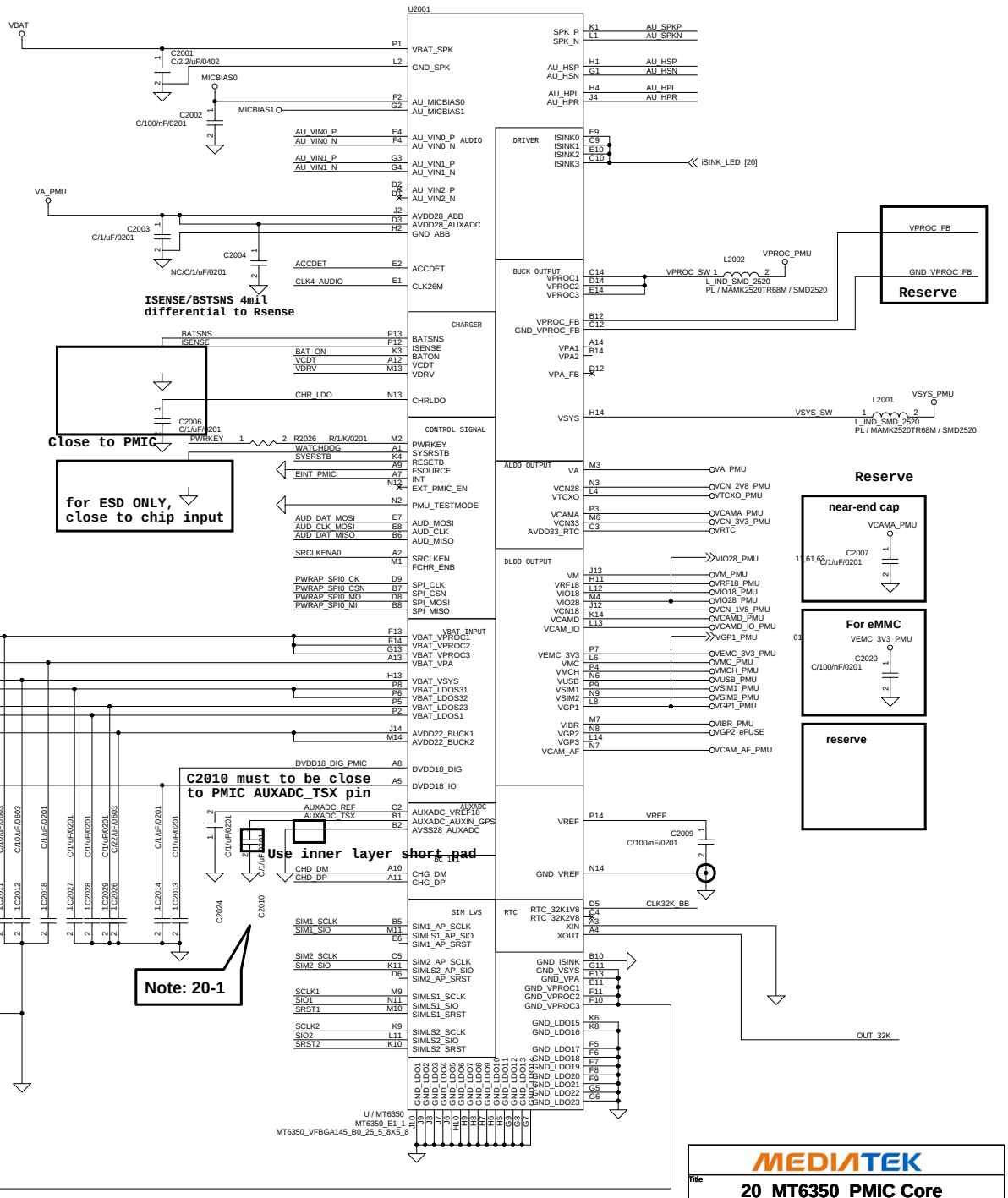
SIM1_SCLK / SIM1_SIO
SIM2_SCLK / SIM2_SIO
SCLK1 / SIO1 / SRST1
SCLK2 / SIO2 / SRST2

PWRKEY

Charger IF

CHR_LDO / VCDT
VDRV / ISENSE
BATSNS / AUXADC_REF
BAT_ON

Block	Name	Vout	Current	Default On	On/Off control
DC/DC	VCORE	0.7~1.4 (DC/DC+DVS)	2800	V	1. Always On
	VSYS	1.825/2.2	1400	V	1. Always on
	VPA	0.5~3.4V	600		Register (default) SRCLKEN
VSYS LDO	VM	1.2/1.35/1.5/1.8	300 (1.8V) 700 (1.2V)	V	1. Always on
	VRF18	1.825	200		1. Register (default) 2. SRCLKEN
	VIO18	1.8	300	V	1. Always on
	VCN_1V8	1.8	120		1. Register (default) 2. SRCLKEN
	VCAMD	1.2/1.3/1.5/1.8	150		Register
	VCAM_IO	1.8	100		Register
	VGP3	1.2/1.3/1.5/1.8	200		Register
Analog LDO	VA	2.8	150	V	1. Always On 2. Enable
	VTCXO	2.8	40	V	1. Register (default) 2. SRCLKEN
	VCN28	2.8	30		1. Register (default) 2. SRCLKEN
	VCAMA	1.5/1.8/2.5/2.8	150		Register
Digital LDO	VCN33	3.3/3.4/3.5/3.6	240		1. Register (default) 2. SRCLKEN
	VIO28	2.8	200	V	1. Always On 2. Enable
	VUSB	3.3	20	V	Always On
	VMC	1.8/3.3	100	V	1. Register (default) 2. SRCLKEN
	VMCH	3.0/3.3	400 (support SD 3.0)	V	1. Register (default) 2. SRCLKEN
	VEMC_3V3	3.0/3.3	400	V	1. Register (default) 2. SRCLKEN
	VCAM_AF	1.2/1.3/1.5/1.8/2.0 2.8/3.0/3.3	100		Register
	VSIM1	1.8/3.0	50		Register
	VSIM2	1.8/3.0	50		Register
	VGP1	1.2/1.3/1.5/1.8/2.0 2.8/3.0/3.3	100		Register
	VGP2	1.2/1.3/1.5/1.8/2.0 /2.5/2.8/3.0	100		Register
	VIBR	1.2/1.3/1.5/1.8/2.0 2.8/3.0/3.3	100		Register
	VDIG18	1.8	20	V	Register
VRTC	VRTC	2.8	2	V	Always On



Schematic design notice of "20_Power_MT6350" page.

Note 20-1: Connect TMS GND (Pin2) to PMIC AUXADC_GND (Pin B2) first. Then connect to main GND by inner layer short pad (i.e., layer 2).

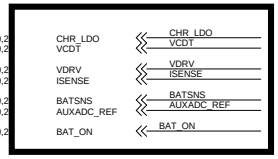
MEDIATEK

Title: 20_MT6350_PMIC Core

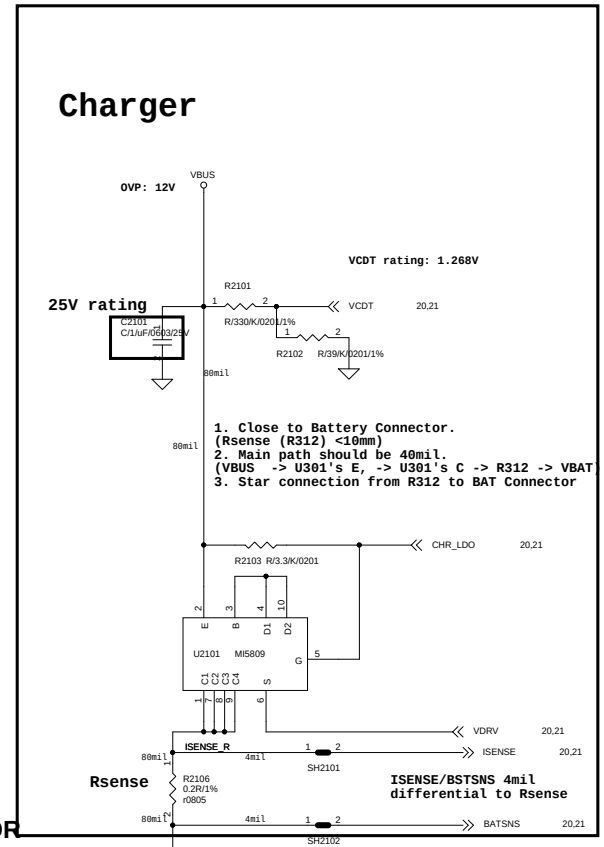
Size: C | Document Number: MT6580 PHONE | Rev: V1.0

Date: Monday, June 25, 2018 | Sheet | of

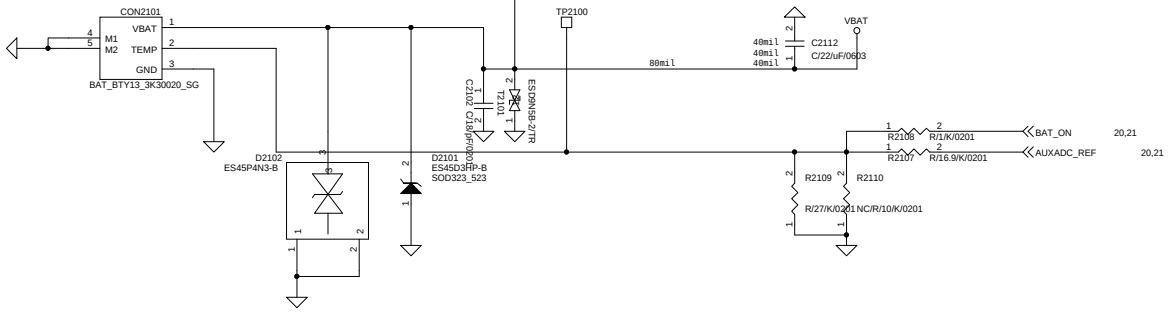
Charger IF



Charger

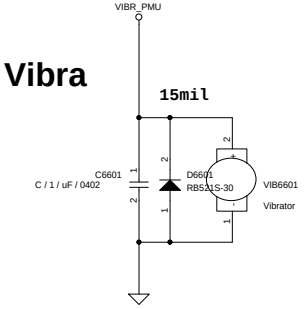


BATTERY CONNECTOR



if battery NTC is 10kohm,
R2107=16.9K, R2109=27K
if battery NTC is 47kohm,
R2107=61.9K, R2109=100K
Refer to MT6350 HW
design notice

Vibra



MEDIATEK

Title	21_Charger	
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Flash
LED

Flash LED 5V Boost

GPIO_FLASH_STB
GPIO_FLASH_EN
SCL2
SDA2

LCM Backlight Driver

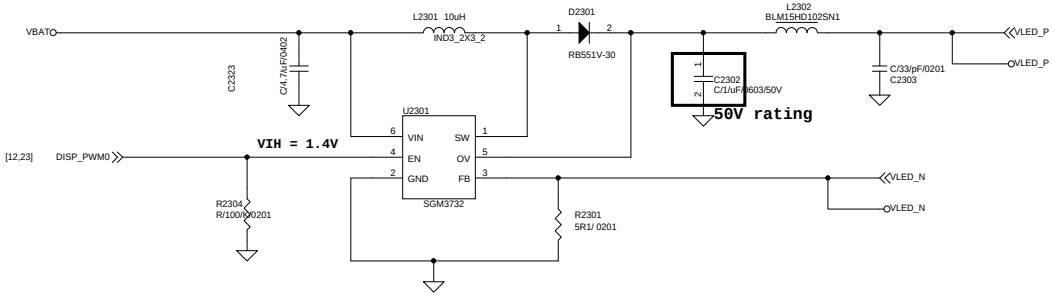
DISP_PWM0

Change 20131010

LCM Backlight LED Driver

Change 20131010

H-0703-223M91-001 (T)功率电感,22uH,±20%,3012,SWPA3012S220MTA01,sunlord



H-1401-2124ST-0A2 (N)IC,白光LED背光驱动,SOT23-6L,CP2124ST-A2,CHIPHOMER

MEDIATEK

Title: 23_POWER_THIRD PARTY		
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3G TX

[32] W_PA_B1_IN <<< W_PA_B1_IN

[32] W_PA_B5_B8_IN <<< W_PA_B5_B8_IN

3G TX-PA

[32] TRXB1 >>> TRXB1

[32] TRXB2 >>> TRXB2

[32] TRXB5 >>> TRXB5

[32] TRXB8 >>> TRXB8

3G RX

[32] 3GB1_RXP >>> 3GB1_RXP

[32] 3GB1_RXN >>> 3GB1_RXN

[32] 3GB2_RXP >>> 3GB2_RXP

[32] 3GB2_RXN >>> 3GB2_RXN

[32] 3GB5_RXP >>> 3GB5_RXP

[32] 3GB5_RXN >>> 3GB5_RXN

[32] 3GB8_RXP >>> 3GB8_RXP

[32] 3GB8_RXN >>> 3GB8_RXN

To MT6323 IF

Co-TSX

4 AUXADC_TSX >>> AUXADC_TSX

CLK

[20] OUT_32K >>> OUT_32K

[20] CLK4_AUDIO >>> CLK4_AUDIO

[32] VM0 >>> VM0

[32] VM1 >>> VM1

3G TX Control

EDGE TXM

VBAT

T3101 ESD5P5D-4

C3115 C / 22 / uF / 0201 / X5R

C3130 C / 1 / uF / 0201

C3117 C / 100 / nF / 0201

C3119 C / 33 / pF / 0201

U3101 PA / SKY77590

GND8 GND7 GND6 GND5 GND4 GND3 GND2 GND1 GND13 GND12 GND11 ANT GND10 TRX_6 TRX_5 GND9 MODE TXEN VRAMP TRX_4 TRX_3 TRX_2 TRX_1

TX_HB_IN TX_LB_IN BS2 BS1 VBATT VCC

SKY77590

GGE_PA_LB_IN — C3121 C / 18 / pF / 0201 — C3108 L / NM / nH / 0201

GGE_PA_HB_IN — C3113 C / 56 / pF / 0201 — C3104 L / NM / nH / 0201

BS2 — R3105 R / 1 / K / 0201 — C3109 C / 22 / pF / 0201 / X5R

BS1 — R3103 R / 1 / K / 0201 — C3107 C / 22 / pF / 0201 / X5R

TRXB8 TRXB1 TRXB5 TRXB2 2G_LB_RX 2G_HB_RX

C3118 C / 39 / pF / 0201

C3122 NC / 0201

C3116 L / 39 / nH / 0201

CON3101 RF_MINI_3_818000291_ECT

CON3102 CON / 4 / TA-RF02-001-03-811 RFCON_818000500

ANT3102 ANT3103 ANT3101 ANT / 9[illegible]

CON3103 CON / 4 / TA-RF02-001-03-811 RFCON_818000500

L3115 R / 0 / ohm / 0201

C3123 R / 0 / ohm / 0201

R3124 R / 0 / ohm / 0201

C3124 NC / 0201

C3131 NC / 0201

L3114 R / 0 / ohm / 0201

R / 10 / K / 0201 — R3119 — WG_GGE_PA_VRAMP

C3120 C / 220 / pF / 0201

R3118 R / 24 / K / 0201

R3104 R / 1 / K / 0201 GGE_PA_EN

C3106 C / 22 / pF / 0201 / X5R

R3123 R / 1 / K / 0201 Mode

C3105 C / 22 / pF / 0201 / X5R

U1001B

MT6580 - RF

BPI

BPI_BUS0 BPI_BUS1 BPI_BUS2 BPI_BUS3 BPI_BUS4 BPI_BUS5 BPI_BUS6 BPI_BUS7 BPI_BUS8 BPI_BUS9 BPI_BUS10 BPI_BUS11

BS2 BS1 Mode GGE_PA_EN W_LB_V1 W_LB_V2

[32] W_PA1_ON [32] W_PA5_ON [32] W_HB_V1

VM0 VM1

RFI-RX

change LNA 2016.09.27

3GB2_RXP 3GB2_RXN — RFIP_B1 RFIN_B1 — LNA 3

3GB1_RXP 3GB1_RXN — RFIP_B2 RFIN_B2 — LNA 1

3GB5_RXP 3GB5_RXN — RFIP_B5 RFIN_B5 — LNA 2

3GB8_RXP 3GB8_RXN — RFIP_B8 RFIN_B8 — LNA 4

LB_RXP LB_RXN — RFIP_2GLB RFIN_2GLB

HB_RXP HB_RXN — RFIP_2GHB RFIN_2GHB

RFI-TX

GGE_PA_HB_IN — TXO_HB1

W_PA_B1_IN — TXO_HB2

TXO_HB3

W_PA_B5_B8_IN — TXO_LB1

GGE_PA_LB_IN — TXO_LB2

CLK

VTCXO_PMU

VXODIG EN_32K_TP1 XMODE_TP2

CLK4_AUDIO — XO_NFC_TP3 XO_AUD_TP4

OUT_32K — OUT32K NC_F3

XO2 XO1 — XTAL2 XTAL1

U / MT6580

RF Power

AVDD18_VTXLF AVDD18_VTXHF — C10 A10

VTCXO22 AVDD18_VRXHF AVDD18_VRXLF — F1 E2 E1

GND_RF

AVSS_CEL1 … AVSS_CEL44

MISC

RCAL — F4

VCOMON — H7

DET DETGND — D10 E10 — PDET [32]

APC — M3 — WG_GGE_PA_VRAMP

TMEAS — F10

C3125 C / 470 / nF / 0201 / X5R

C3126 C / 470 / nF / 0201 / X5R

C3127 C / 1 / uF / 0201

C3128 C / 470 / nF / 0201 / X5R

C3129 C / 470 / nF / 0201 / X5R

VRF18_PMU

VTCXO_PMU

R3101 R / 2 / K / 0201 / 1%

Z3101 BAL / RFBLN2012090BM5T25

2G_LB_RX — L3112 R / 0 / ohm / 0201 — C3111 C / NC / 0201

2G_HB_RX — L3113 R / 0 / ohm / 0201 — C3114 L / 22 / nH / 0201

LBin HBin GND1 GND2 GND3 GND4 LBout HBout

L3102 L / 16 / nH / 0201

L3108 L / 39 / nH / 0201

L3103 L / 16 / nH / 0201

L3104 L / 3.3 / nH / 0201

L3110 L / 4 / nH / 0201

L3109 NC

L3105 L / 3.3 / nH / 0201

MODE \ Logic	DCXO_32K_EN	XMODE	VXODIG
VCTCX0	0(GND)	0(GND)	1(VIO18)
DCXO + 32K XO	0(GND)	1(VIO18)	1(VIO18)
default DCXO + 32K-Less	1(VTXCO22)	1(VTXCO22)	1(VTXCO22)

pad

X3101 THERM HOT2 HOT1 GND

X / 26 / MHz / CT2520DB26000C0FZZA1

Delete TSX co-lay sch 20160824

MEDIATEK

Title: 31_RF_TXM_2G

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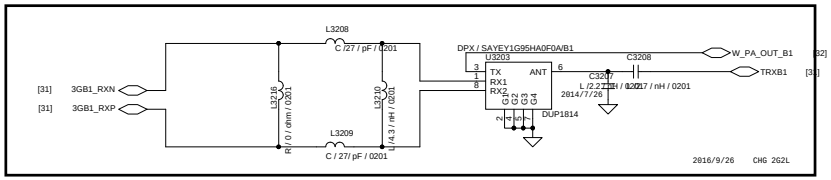
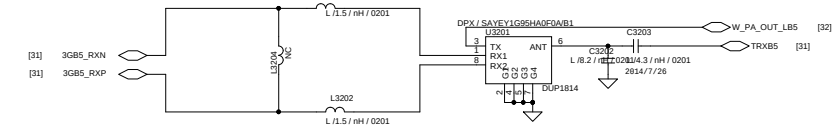
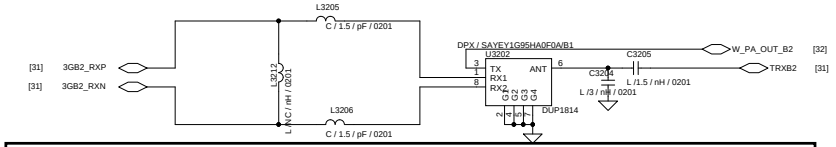
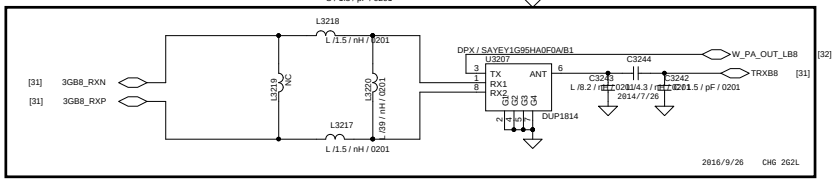
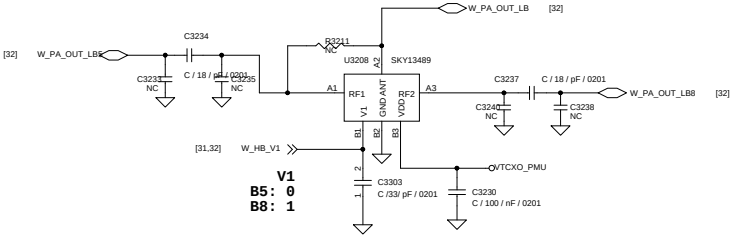
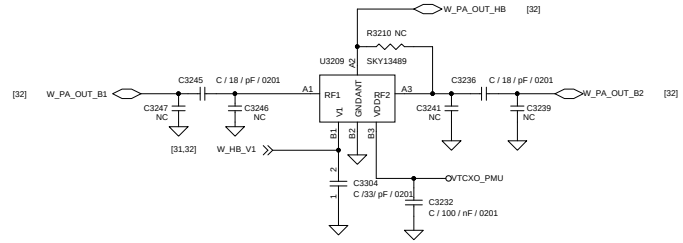


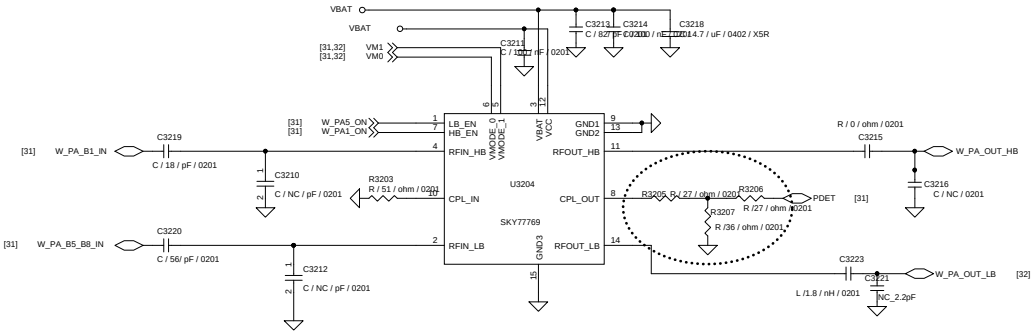


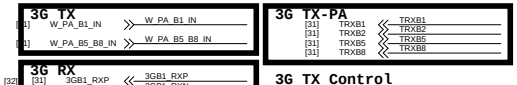


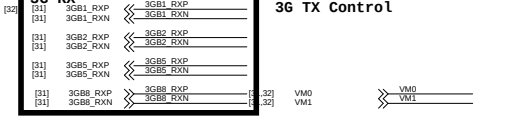






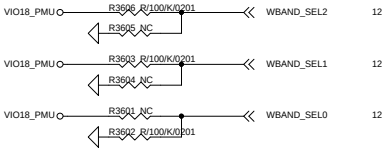






3G TX Control

MEDIATEK		
Title: 32_RF_WCDMA		
Size: A2	Document Number: MT6580 PHONE	Rev: V1.0
Date: Monday, June 25, 2018	Sheet of	



Band Select logic table

	SEL2	SEL1	SEL0	
B1/3/8	0	0	0	**R3605 , R3604, R3602**
B2/5	0	0	1	**R3605 , R3604, R3601**
B1/5	0	1	0	**R3605 , R3603, R3602**
B1/8	0	1	1	**R3605 , R3603, R3601**
B1/2/5	1	0	0	**R3606 , R3604, R3602**
B2/4/5	1	0	1	**R3606 , R3604, R3601**
B1/2/5/8	1	1	0	**R3606 , R3603, R3602**
B2/4/5/8	1	1	1	**R3606 , R3603, R3601**

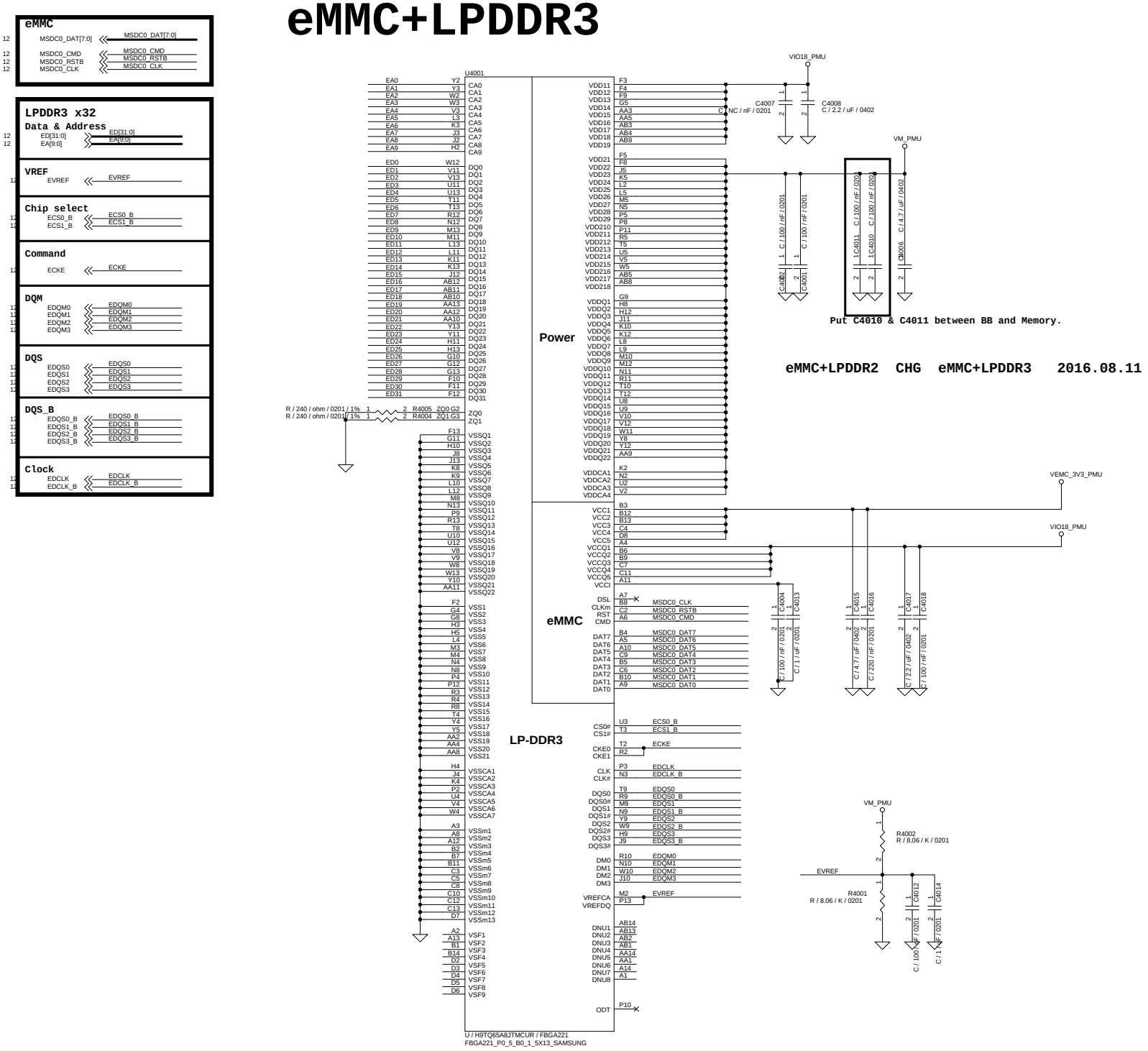
MEDIATEK

Title: 36_RF_AT

Size: C | Document Number: MT6580 PHONE | Rev: V1.0

Date: Monday, June 25, 2018 | Sheet of

eMMC+LPDDR3



Title	40_Memory_eMMC_LPDDR3
Size	C
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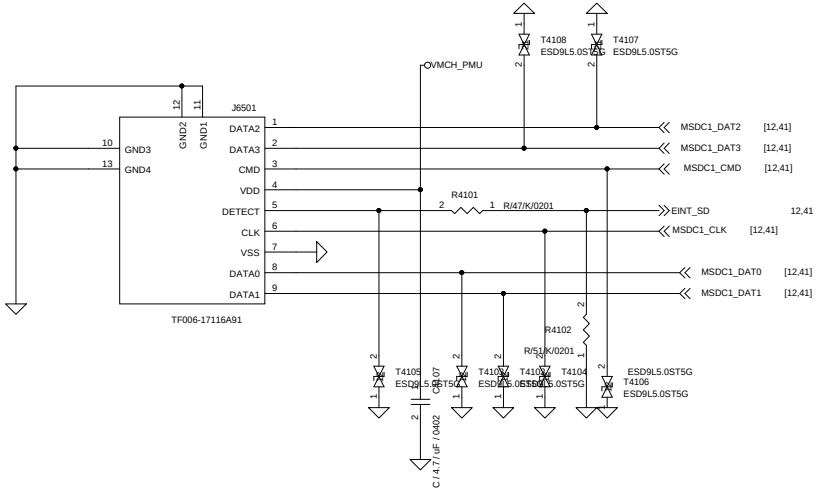
MSDC

MSDC1_CLK
MSDC1_CMD
MSDC1_DAT0
MSDC1_DAT1
MSDC1_DAT2
MSDC1_DAT3

Card detect

SD CARD



Schematic design notice of "41_MEMORY_SD Card" page.

Note 41-1:	The equivalent capacitance of ESD protection device must be <=1pF -- otherwise it will result in NFC card mode function fail.
Note 41-2:	Depends on system design to add ESD protection componemt or not.

MEDIATEK

Title: 41_MEMORY_SD Card

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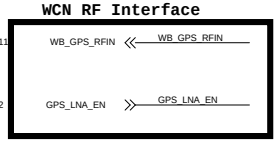
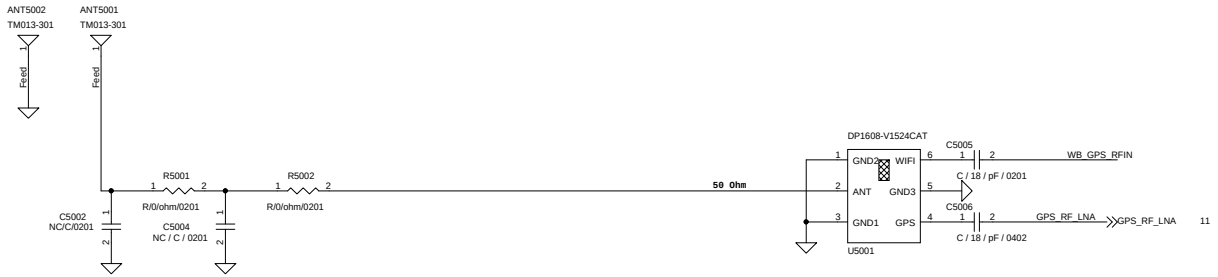
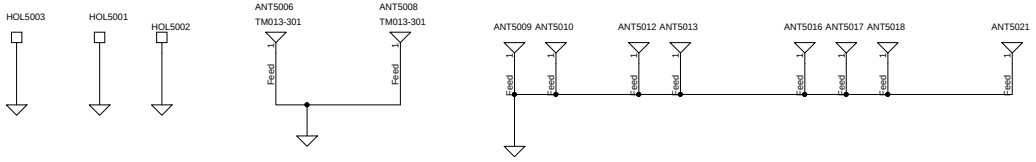
WCN RF Interface







MEDIATEK

Title: 50_BT_FM_GPS_WiFi		
Size: C	Document Number: MT6580 PHONE	Rev: V1.0
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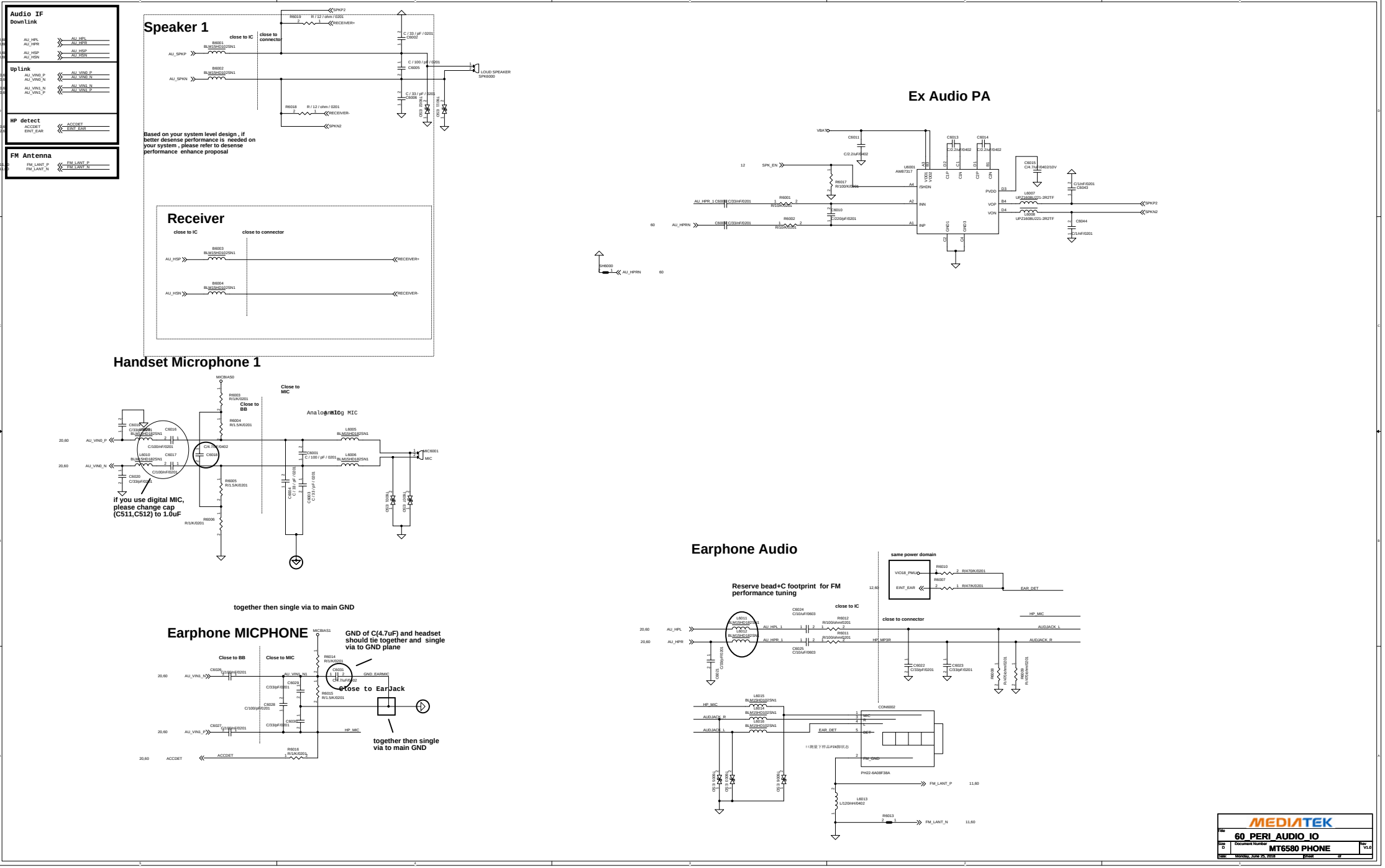
Audio IF
Downlink
AU_HPL
AU_HPR
AU_HSP
AU_HSN
Uplink
AU_VIN0_P
AU_VIN0_N
AU_VIN1_N
AU_VIN1_P
HP detect
ACCDET
EINT_EAR
FM Antenna
FM_LANT_P
FM_LANT_N
Speaker 1
close to IC
close to connector
AU_SPKP
AU_SPKN
R6001
R6002
BLM15HD102SN1
C6002
C6005
C6006
LOUD SPEAKER
SPK6000
R6018
R / 12 / ohm / 0201
RECEIVER-
SPKN2
SPKP2
RECEIVER+
Based on your system level design , if better desense performance is needed on your system , please refer to desense performance enhance proposal
Receiver
close to IC
close to connector
AU_HSP
AU_HSN
B6003
B6004
BLM15HD102SN1
RECEIVER+
RECEIVER-
Ex Audio PA
VBAT
C6011
C/2.2uF/0402
C6013
C6014
C/2.2nF/0402
U6001
AW87317
SPK_EN
R6017
R/100K/0402
SHDN
R6001
R6002
R/100K/0201
C6009
C/33nF/0201
C6010
C/220pF/0201
AU_HPR_1
AU_HPRN
VOUT1
VOUT2
VOUT3
CLP
CLN
CPP
CPN
GND1
GND2
GND3
PVDD
VOP
VON
L6007
L6008
UPZ1608U221-2R2TF
C6015
C/4.7uF/0402/10V
C6043
C6044
C/1nF/0201
SH6000
Handset Microphone 1
MICBIAS0
Close to MIC
Close to BB
Analog MIC
R6003
R/1K/0201
R6004
R/1.5K/0201
R6005
R6006
C6016
C6017
C6018
C6019
C6020
C6021
C/100nF/0201
C6601
C/ 100 / pF / 0201
C6602
C/ 33 / pF / 0201
C6603
C/ 33 / pF / 0201
L6005
L6006
BLM15HD102SN1
MIC6001
MIC
D3
D4
if you use digital MIC, please change cap (C511,C512) to 1.0uF
together then single via to main GND
Earphone MICPHONE
MICBIAS1
Close to BB
Close to MIC
GND of C(4.7uF) and headset should tie together and single via to GND plane
Close to EarJack
R6014
R/1K/0201
C6031
C/4.7uF/0402
GND_EARMIC
C6026
C6027
C6028
C6029
C/33pF/0201
C/100pF/0201
R6015
R/1.5K/0201
AU_VIN1_N
AU_VIN1_P
HP_MIC
ACCDET
R6016
R/1K/0201
together then single via to main GND
Earphone Audio
same power domain
Reserve bead+C footprint for FM performance tuning
close to IC
close to connector
VIO18_PMU
EINT_EAR
R6010
R/470K/0201
R6007
R/470K/0201
EAR_DET
C6024
C/10uF/0603
C6025
C/10uF/0603
R6012
R6011
R/100ohm/0201
HP_MP3R
AU_HPL_1
AU_HPR_1
AUDJACK_L
AUDJACK_R
C6022
C6023
C/33pF/0201
R6038
R6039
R/470ohm/0201
L6011
L6012
L6013
L6014
L6015
L6016
CON6002
MIC
L
R
DET
GND
PH22-6A08F38A
FM_GND
FM_LANT_P
FM_LANT_N
L6013
L/120ohm/0402
R6013
MEDIATEK
File
60_PERI_AUDIO_IO
Size
D
Document Number
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of

LCD/Speaker/ MIC/VIBR CONNECTOR

删除跳线电阻
20130517

LCD interface
MIPI-DSI
TCN / TCP
TDN0 / TDP0
TDN1 / TDP1
TDN2 / TDP2

Control interface
LCM_RST
DSI_TE

CTP IF
SCL1 / SDA1
EINT_CTP / CTP_RSTB

20 VGP1_PMU — C6101 C/1uF/0201
VIO18_PMU — R6103 NC/R/10K/0201
12,61 SCL1
12,61 SDA1
12,61 EINT_CTP
12,61 CTP_RSTB

TP6107
TP6112 TP6111 TP6110 TP6109 TP6108
CON6101
12,61 CTP_RSTB
12,61 EINT_CTP
12,61 SDA1
12,61 SCL1
VGP1_PMU
GND2 GND3
FH34SRJ-6S-0.5SH
T6112 ESD9L5.0ST5G
T6114 ESD9L5.0ST5G
T6115 ESD9L5.0ST5G
T6116 ESD9L5.0ST5G
T6117 ESD9L5.0ST5G

11,20,63 VIO28_PMU — C6104 C/4.7uF/0402
VIO18_PMU — C6105 C/1uF/0201
100-ohm differential

11,20,63 VIO28_PMU — C6102 C/4.7uF/0402
VIO18_PMU — C6103 C/1uF/0201
100-ohm differential

Main LCM

100-ohm differential

12,61 TCP — DSI_TCP
12,61 TCN — DSI_TCN
12,61 TDP0 — DSI_TDP0
12,61 TDN0 — DSI_TDN0
12,61 TDP1 — DSI_TDP1
12,61 TDN1 — DSI_TDN1
12,61 TDP2 — DSI_TDP2
12,61 TDN2 — DSI_TDN2

CON6102
VLED_N
[12,61] LCM_RST — R6106 R/0/ohm/0201
[12,61] TDP2 — R6104 R/0/ohm/0201 TDN2 [12,61]
[12,61] TCN — R6107 R/0/ohm/0201 R6113 R/0/ohm/0201 TCP [12,61]
[12,61] TDP1 — R6108 R/0/ohm/0201 R6112 R/0/ohm/0201 TDN1 [12,61]
[12,61] TDN0 — R6109 R/0/ohm/0201 R6110 R/0/ohm/0201 TDP0 [12,61]
IOVCC VIO18_PMU
VLED_P
DSI_TE [12]
EINT3_LCD_ID
VIO28_PMU VCI
ZIF_FH26W_39S_HRS

ADD 2016.01.19

J6101
VLED_P
VIO18_PMU
12,61 TDP1 — R6116 R/0/ohm/0201
12,61 TCN — R6115 R/0/ohm/0201
12,61 TDP0 — R6114 R/0/ohm/0201
12,61 TDN2 — R6105 R/0/ohm/0201
12,61 DSI_TE
12,61 LCM_RST
VLED_N
VIO28_PMU
R6117 R/0/ohm/0201 TDN1 12,61
R6118 R/0/ohm/0201 TCP 12,61
R6119 R/0/ohm/0201 TDN0 12,61
R6120 R/0/ohm/0201 TDP2 12,61
R6111 R / 100 / K / 0201 LCD_ID — EINT3_LCD_ID 12,61
ZIF_FH26W_2SS_HRS

MEDIATEK			
Title: 61_PERI_LCD_CTP			
Size: C	Document Number: MT6580 PHONE		Rev: V1.0
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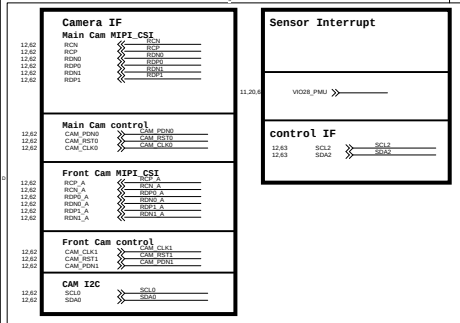
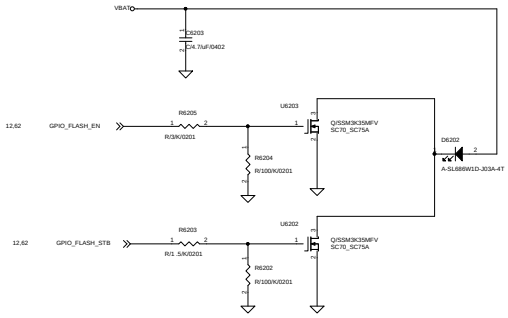
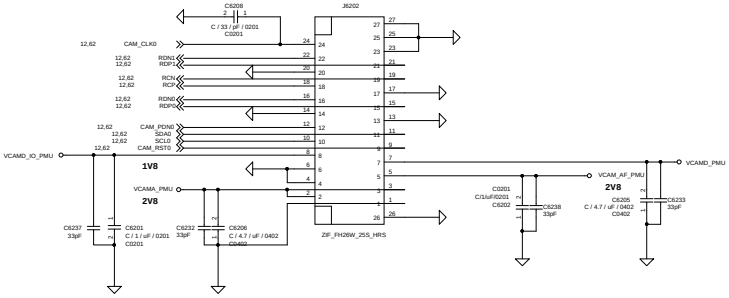
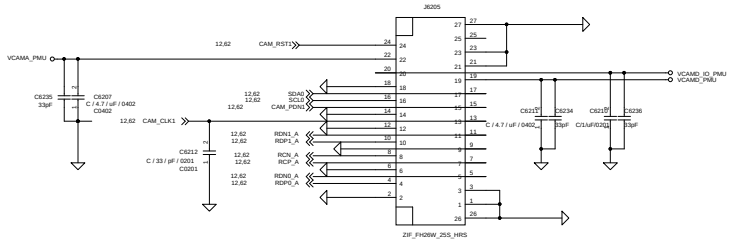


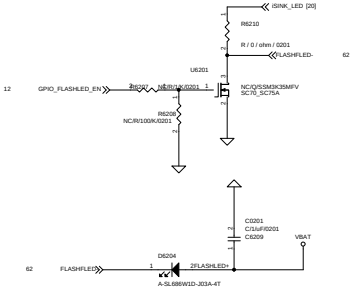


Main Camera



Front Camera





MEDIATEK		
Title	62_PERI_CAMERA	
Size: D	Document Number: MT6580 PHONE	Rev: V1.0
Date: Monday, June 25, 2018	Sheet of	

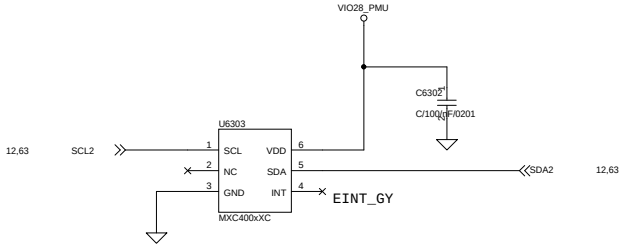
Sensor Interrupt

1,20,6 VIO28_PMU

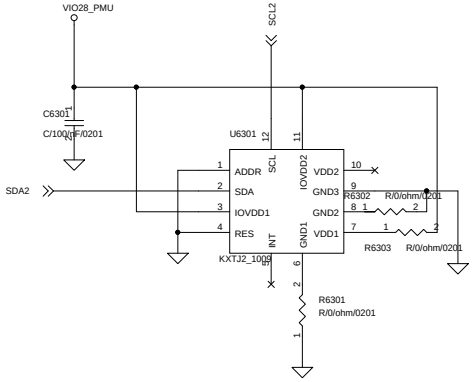
control IF

12,63 SCL2
12,63 SDA2

G-Sensor



G-Sensor



MEDIATEK

Title: 63_PERI_SENSORS		
Size: C	Document Number: MT6580 PHONE	Rev: V1.0
Date: Monday, June 25, 2018	Sheet of	

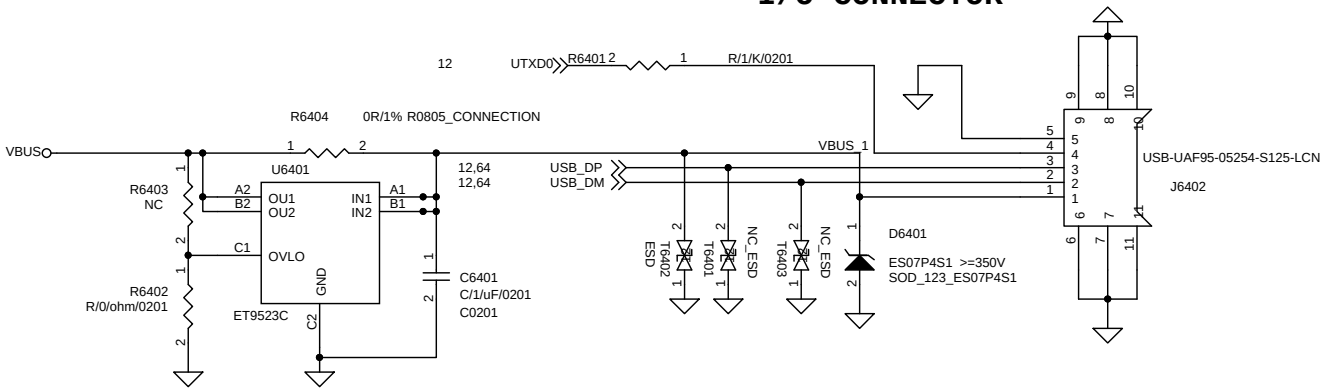
USB IF

12,64 USB_DP — USB_DP
12,64 USB_DM — USB_DM

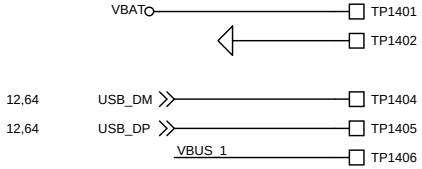
USB HS IF

Delete UTXD0 T6404
20160826

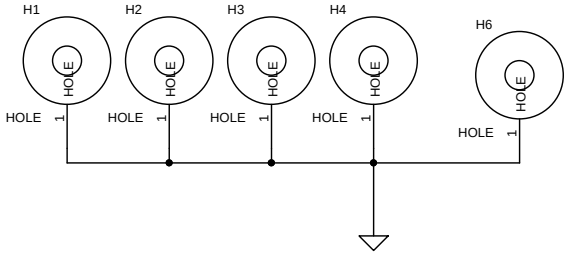
I/O CONNECTOR



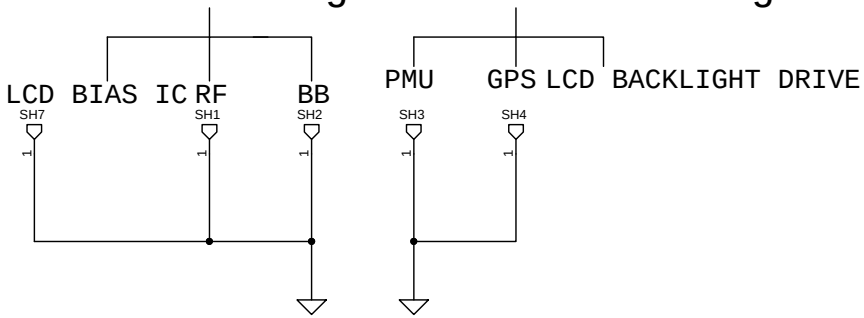
DOWNLOAD PAD



HOLE



TOP shielding BOTTOM shielding



Add LCD BIAS IC shielding SH6
20161022

MEDIATEK		
Title: 64_PERI_USB_MHL		
Size: B	Document Number: MT6580 PHONE	Rev: V1.0
Date: Monday, June 25, 2018	Sheet of	

SIM Card IF

SIM1

20,65 SCLK1 — SCLK1
20,65 SIO1 — SIO1
20,65 SRST1 — SRST1

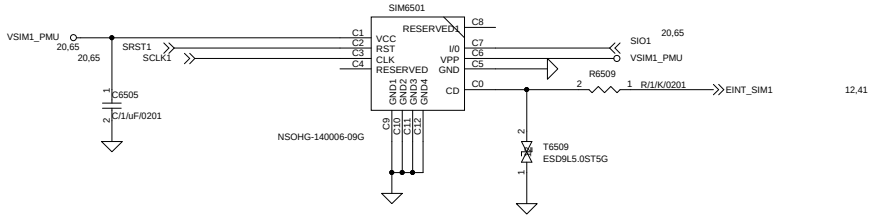
SIM2

20,65 SCLK2 — SCLK2
20,65 SIO2 — SIO2
20,65 SRST2 — SRST2

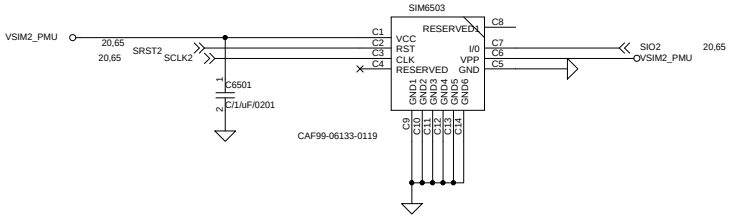
KP IF

20,65 PWRKEY — PWRKEY
12,65 KPCOL0 — KPCOL0
12,65 KPROW0 — KPROW0
12,65 KPROW1 — KPROW1

SIM1 NANO SIM CARD



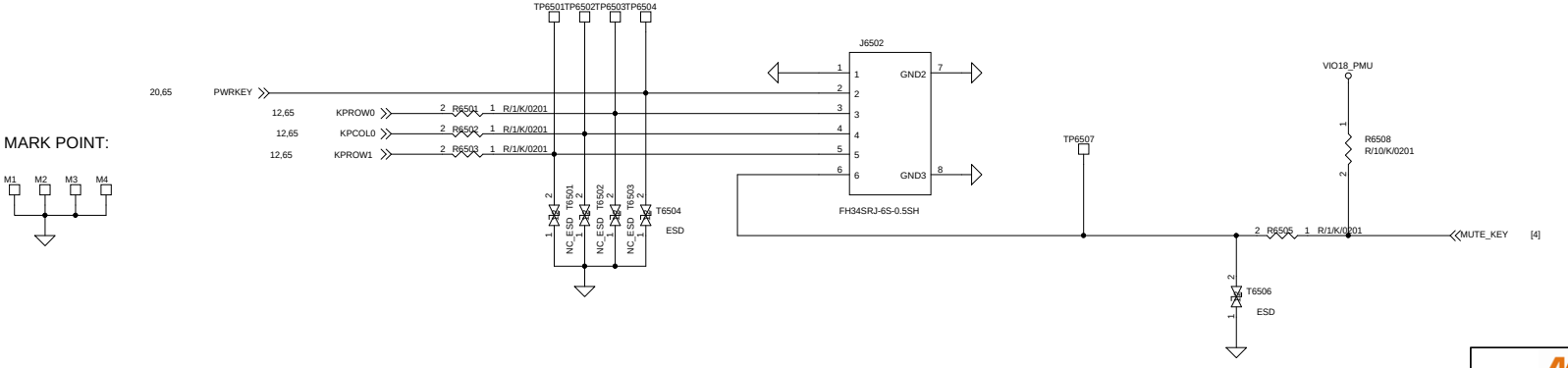
SIM2 Micro SIM CARD



Reseve Reset schematic
20160826

Volume Up

Volume Down



MARK POINT:

MEDIATEK

Title	65_PERI_Dual_SIM_ICUSB_KEYPAD	
Size C	Document Number MT6580 PHONE	Rev V1.0
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