

1

2

3

4

A

A

B

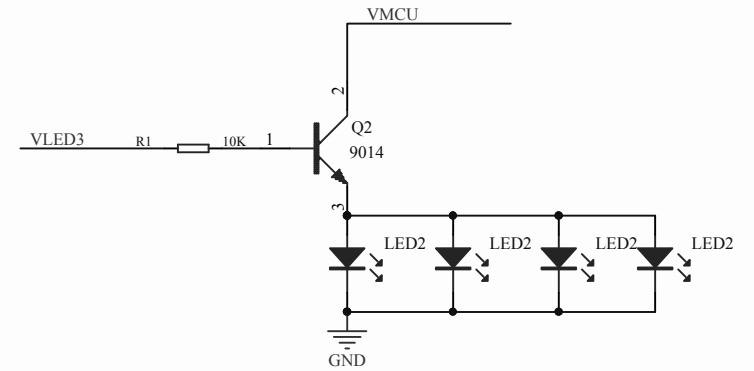
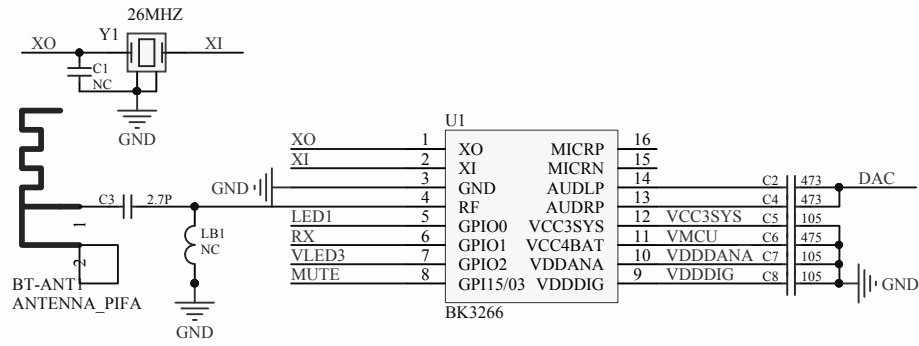
B

C

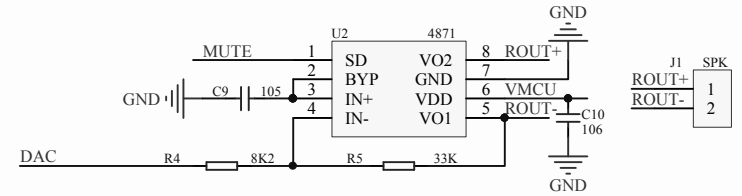
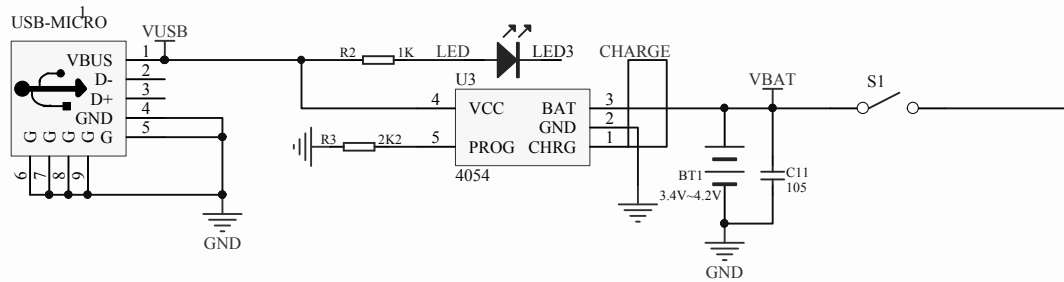
C

D

D



VMCU



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D

D