

EM4 Wake sources  
 PA0  
 PC9  
 PF1  
 PF2  
 PE13

GPIO Alternate Functions

ADC0\_CH0 PE12 Analog to digital converter ADC0, input channel number 0.  
 ADC0\_CH1 PE13 Analog to digital converter ADC0, input channel number 1.  
 ADC0\_CH4 PD4 Analog to digital converter ADC0, input channel number 4.  
 ADC0\_CH5 PD5 Analog to digital converter ADC0, input channel number 5.  
 ADC0\_CH6 PD6 Analog to digital converter ADC0, input channel number 6.  
 ADC0\_CH7 PD7 Analog to digital converter ADC0, input channel number 7.

BOOT\_RX PD6 Bootloader RX.  
 BOOT\_TX PD7 Bootloader TX.

CMU\_CLK0 PD7 PF2 Clock Management Unit, clock output number 0.  
 CMU\_CLK1 PA1 PE12 PB11 Clock Management Unit, clock output number 1.

DBG\_SWCLK PF0 Debug-interface Serial Wire clock input. Note that this function is enabled to pin out of reset, and has a built-in pull down.  
 DBG\_SWDIO PF1 Debug-interface Serial Wire data input / output. Note that this function is enabled to pin out of reset, and has a built-in pull up.

GPIO\_EM4WU0 PA0 Pin can be used to wake the system up from EM4.  
 GPIO\_EM4WU2 PC9 Pin can be used to wake the system up from EM4.  
 GPIO\_EM4WU3 PF1 Pin can be used to wake the system up from EM4.  
 GPIO\_EM4WU4 PF2 Pin can be used to wake the system up from EM4.  
 GPIO\_EM4WU5 PE13 Pin can be used to wake the system up from EM4.

HFXTAL\_N PB14 High Frequency Crystal negative pin. Also used as external optional clock input pin.  
 HFXTAL\_P PB13 High Frequency Crystal positive pin.

I2C0\_SCL PA1 PD7 PF1 PE13 I2C0 Serial Clock Line input / output.  
 I2C0\_SDA PA0 PD6 PF0 PE12 I2C0 Serial Data input / output.

IDAC0\_OUT PB11 IDAC0 output.

LEU0\_RX PD5 PB14 PF1 PA0 PC15 LEUART0 Receive input.  
 LEU0\_TX PD4 PB13 PF0 PC14 LEUART0 Transmit output. Also used as receive input in half duplex communication.

LFXTAL\_N PB8 Low Frequency Crystal (typically 32.768 kHz) negative pin. Also used as an optional external clock input pin.  
 LFXTAL\_P PB7 Low Frequency Crystal (typically 32.768 kHz) positive pin.

PCNT0\_S0IN PC13 PD6 PA0 Pulse Counter PCNT0 input number 0.  
 PCNT0\_S1IN PC14 PD7 PB11 Pulse Counter PCNT0 input number 1.

PRS\_CH0 PA0 PF3 PC14 PF2 Peripheral Reflex System PRS, channel 0.  
 PRS\_CH1 PA1 PF4 PC13 PE12 Peripheral Reflex System PRS, channel 1.  
 PRS\_CH2 PE10 PE13 Peripheral Reflex System PRS, channel 2.  
 PRS\_CH3 PE11 PA0 Peripheral Reflex System PRS, channel 3.

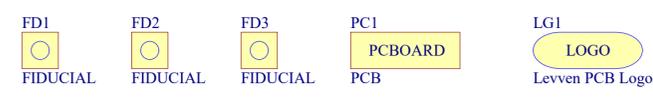
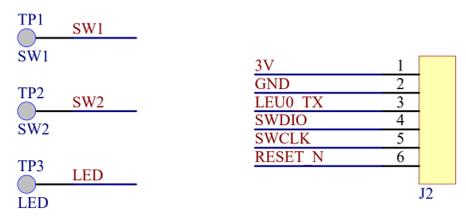
TIM0\_CC0 PA0 PD9 PF0 PA1 Timer 0 Capture Compare input / output channel 0.  
 TIM0\_CC1 PA1 PA1 PF1 PA0 Timer 0 Capture Compare input / output channel 1.  
 TIM0\_CC2 PF2 PF2 Timer 0 Capture Compare input / output channel 2.  
 TIM0\_CDT0 PC13 PF3 PC13 Timer 0 Complimentary Dead Time Insertion channel 0.  
 TIM0\_CDT1 PC14 PF4 PC14 Timer 0 Complimentary Dead Time Insertion channel 1.  
 TIM0\_CDT2 PC15 PC15 Timer 0 Complimentary Dead Time Insertion channel 2.

TIM1\_CC0 PC13 PE10 PB7 PD6 Timer 1 Capture Compare input / output channel 0.  
 TIM1\_CC1 PC14 PE11 PB8 PD7 Timer 1 Capture Compare input / output channel 1.  
 TIM1\_CC2 PC15 PE12 PB11 PC13 Timer 1 Capture Compare input / output channel 2.

TIM2\_CC0 PC8 PF2 Timer 2 Capture Compare input / output channel 0.  
 TIM2\_CC1 PC9 PE12 Timer 2 Capture Compare input / output channel 1.  
 TIM2\_CC2 PC10 PE13 Timer 2 Capture Compare input / output channel 2.

US0\_CLK PE12 PC9 PC15 PB13 PB13 PE12 USART0 clock input / output.  
 US0\_CS PE13 PC8 PC14 PB14 PE13 USART0 chip select input / output.  
 US0\_RX PE11 PC10 PE12 PB8 USART0 Asynchronous Receive. USART0 Synchronous mode Master Input / Slave Output (MISO).  
 US0\_TX PE10 PC11 PE13 PB7 USART0 Asynchronous Transmit. Also used as receive input in half duplex communication. USART0 Synchronous mode Master Output / Slave Input (MOSI).

USRF1\_RX PD6 USARTRF1 Asynchronous Receive. USARTRF1 Synchronous mode Master Input / Slave Output (MISO).  
 USRF1\_TX PD7 USARTRF1 Asynchronous Transmit. Synchronous mode Master Output / Slave Input (MOSI).



Matching Network Based on SiLabs Application Note AN627 (915MHz SWC DT)  
 Antenna Based on SiLabs Application Note AN848 (Small Sized Printed ILA)

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