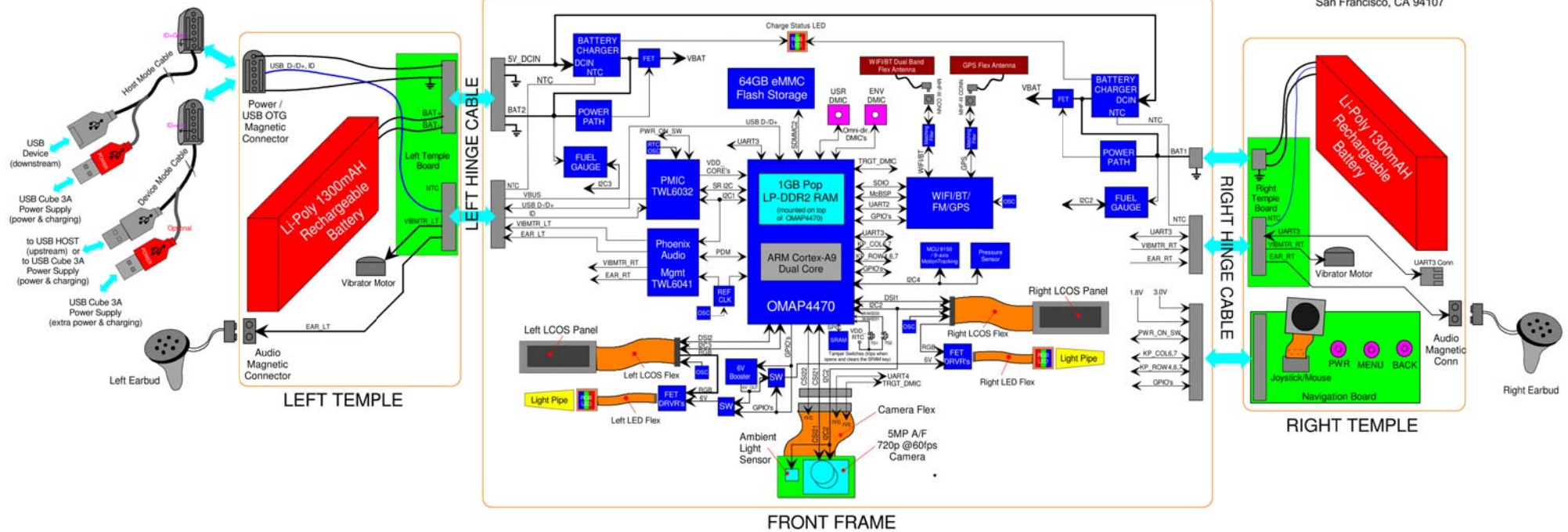


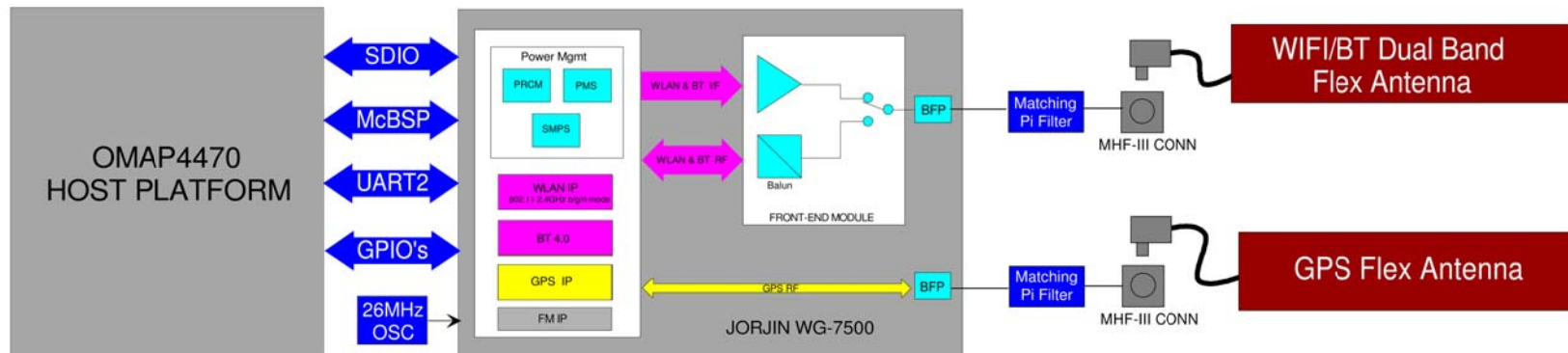
I. SYSTEM BLOCK DIAGRAM



ODG

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II. RF SUBSYSTEM BLOCK DIAGRAM



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