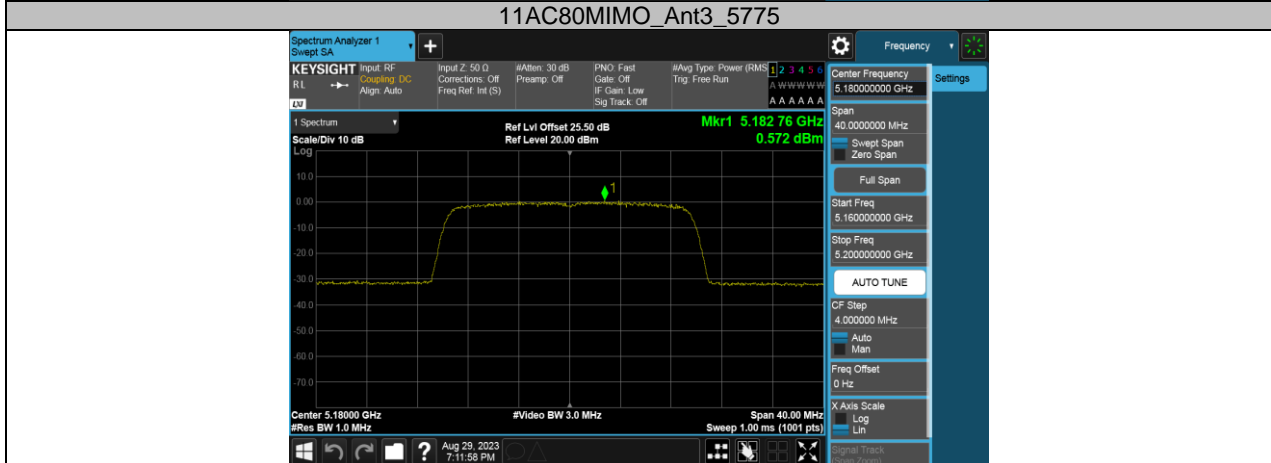
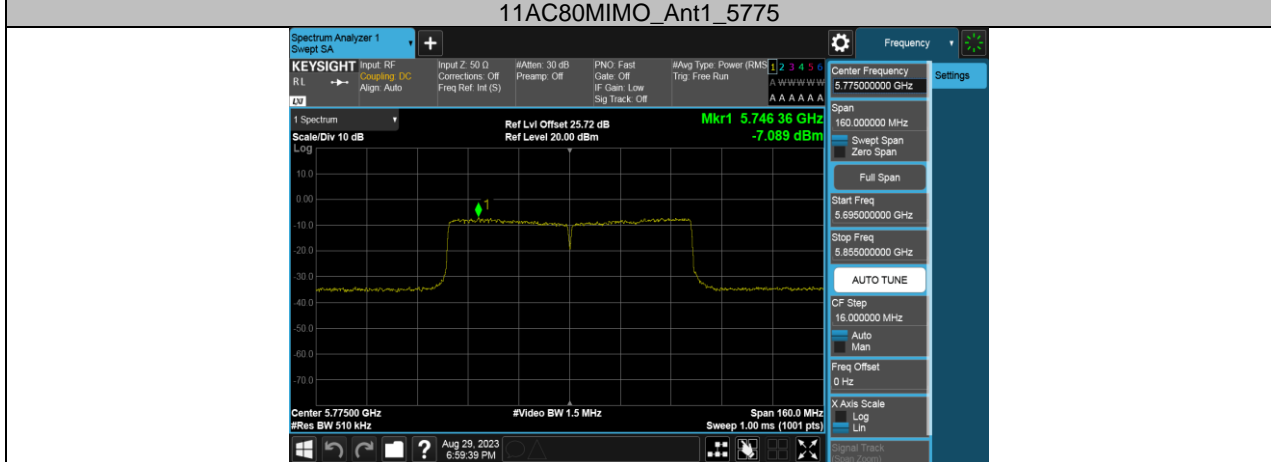
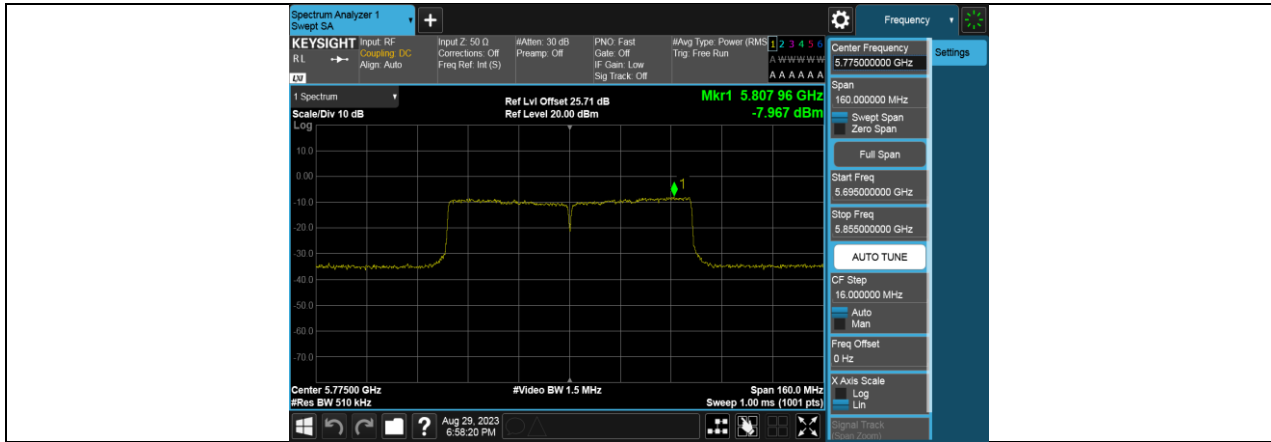
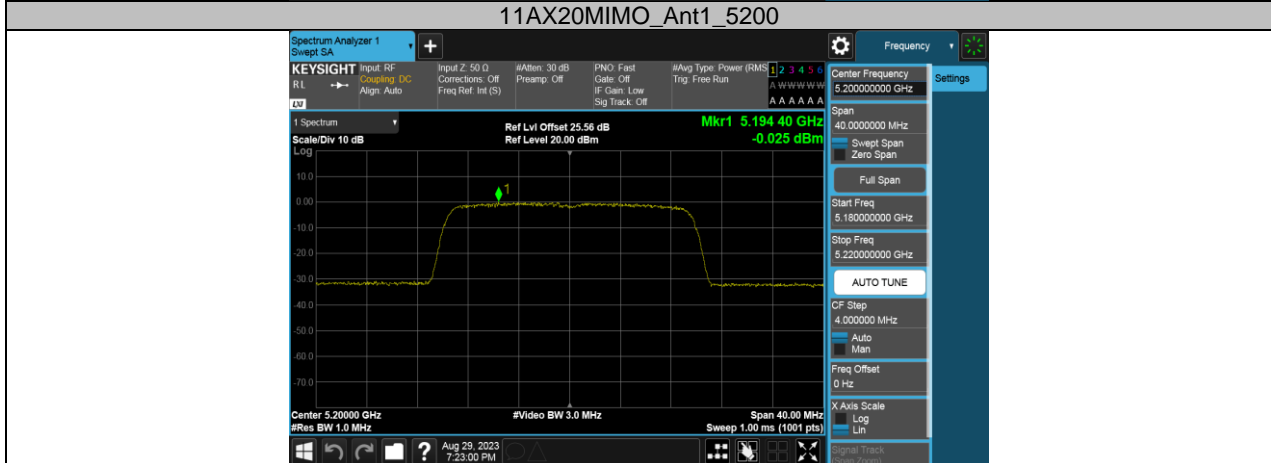
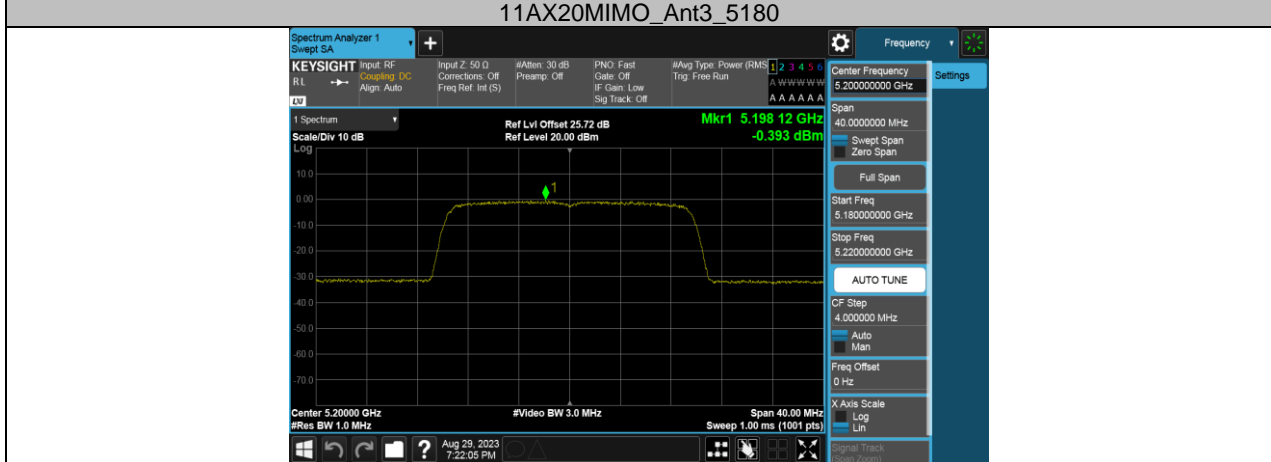
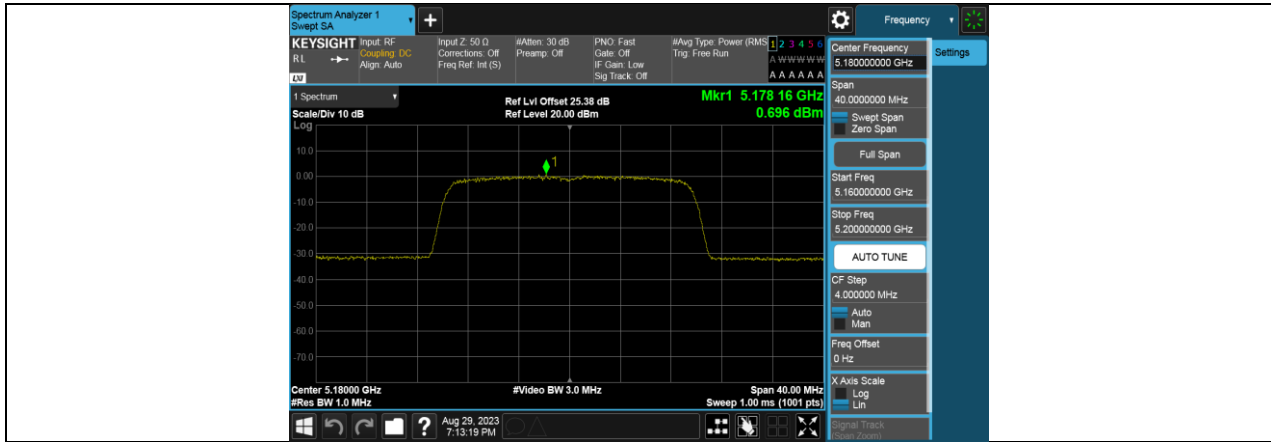


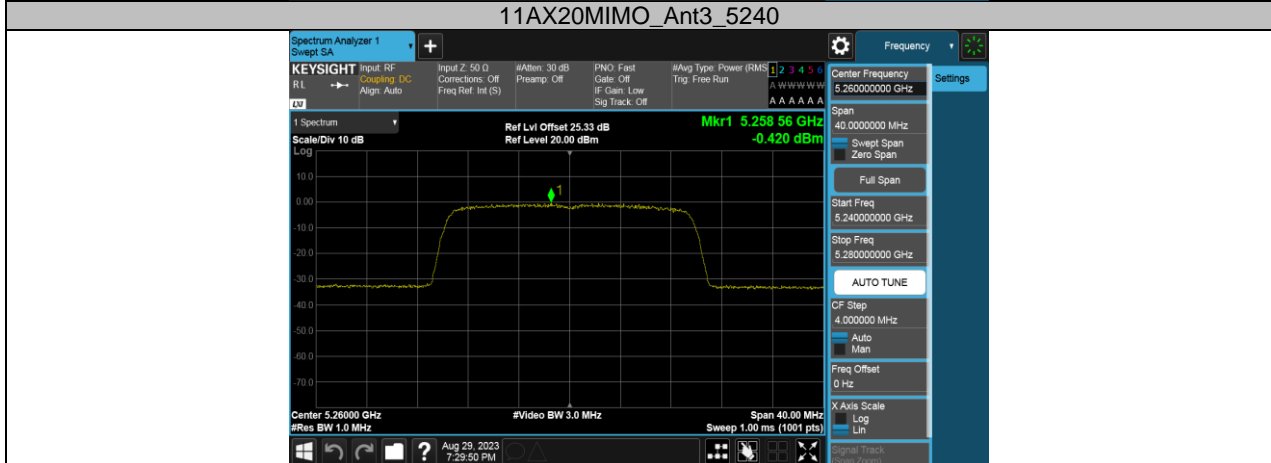
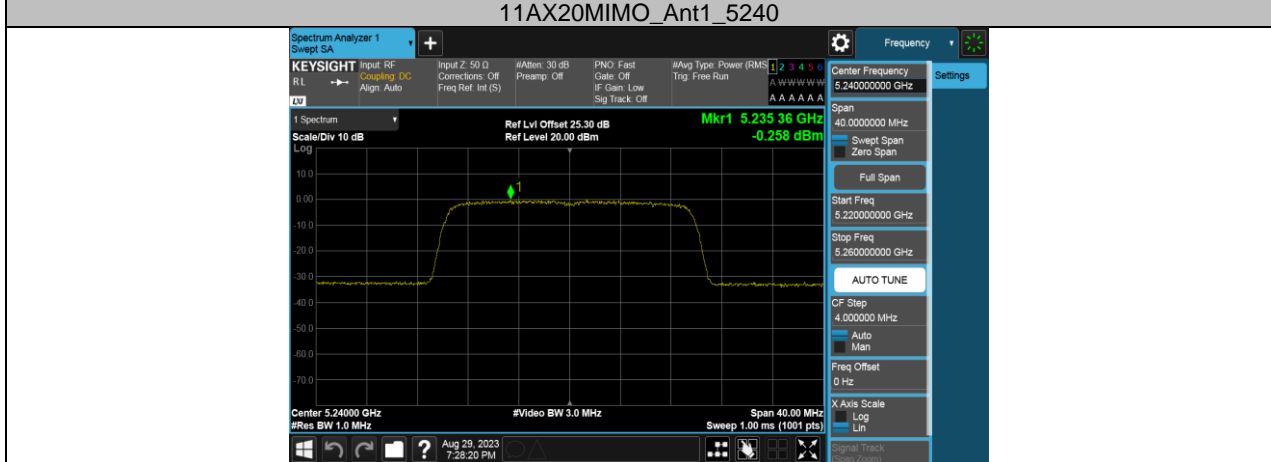
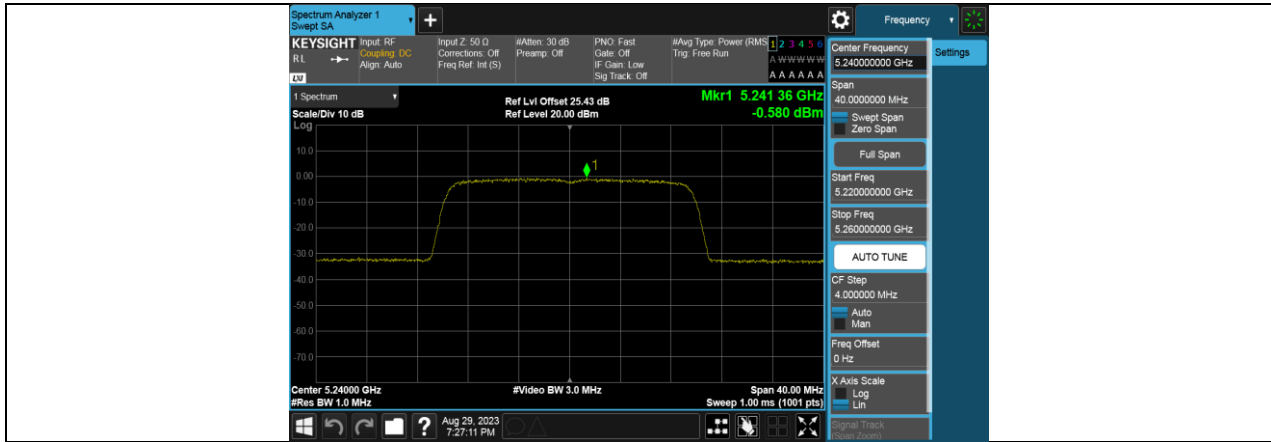
**11AC80MIMO\_Ant3\_5690\_UNII-3**



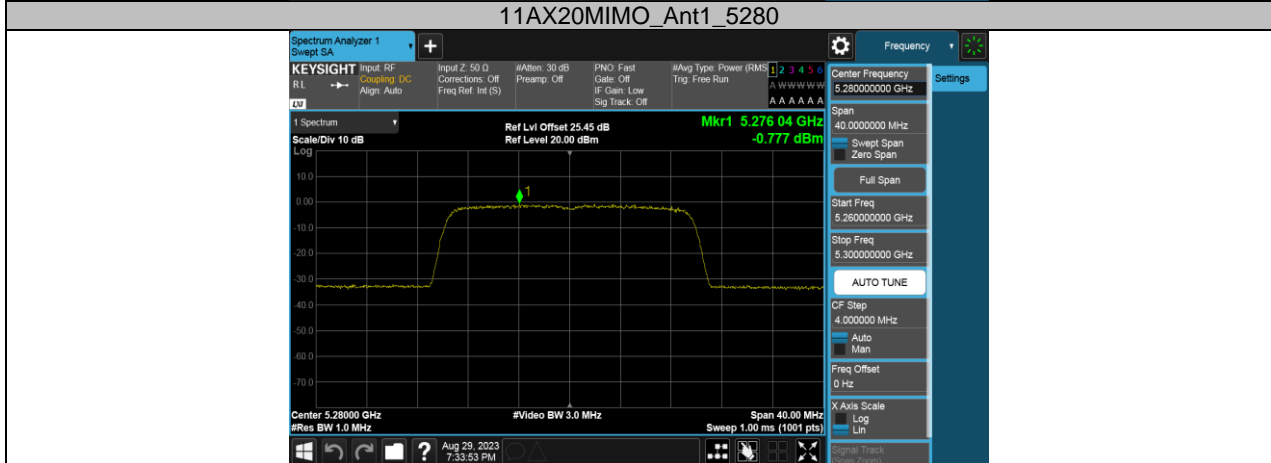
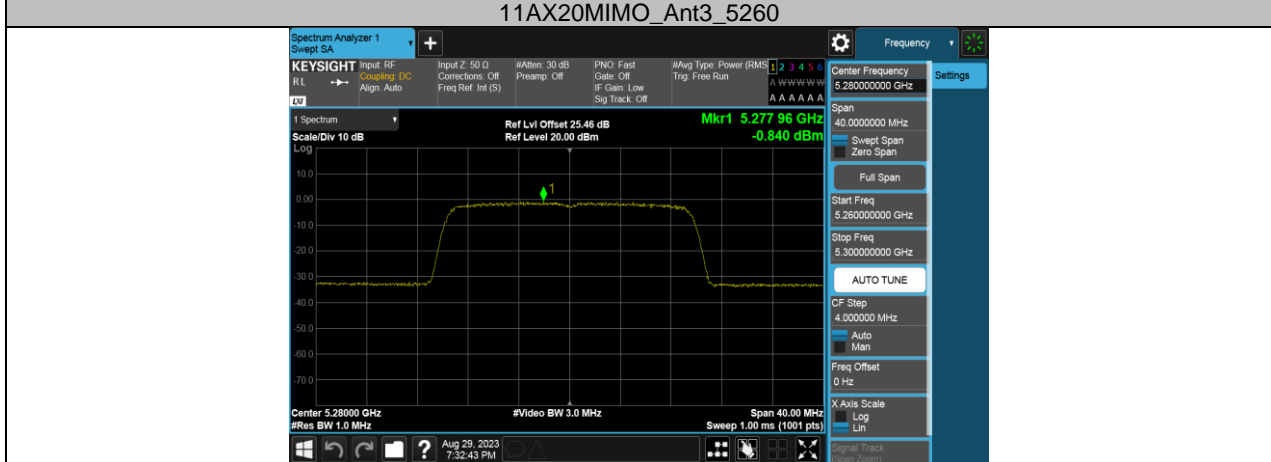
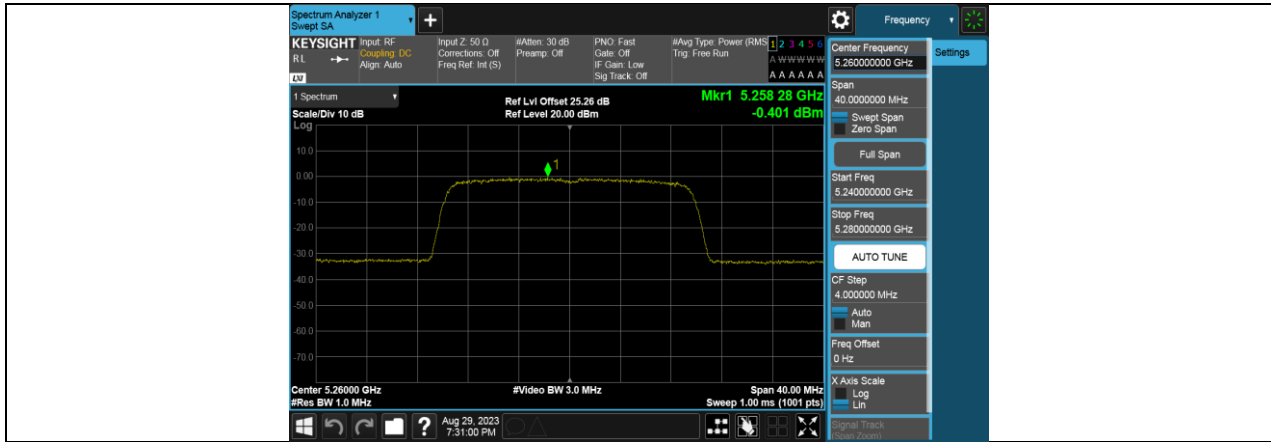
**11AX20MIMO\_Ant1\_5180**



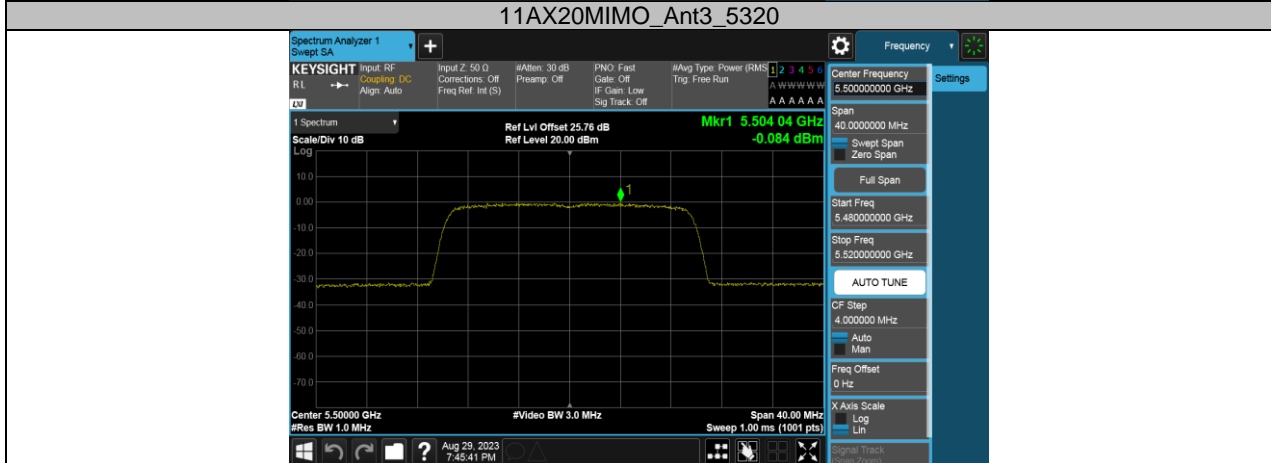
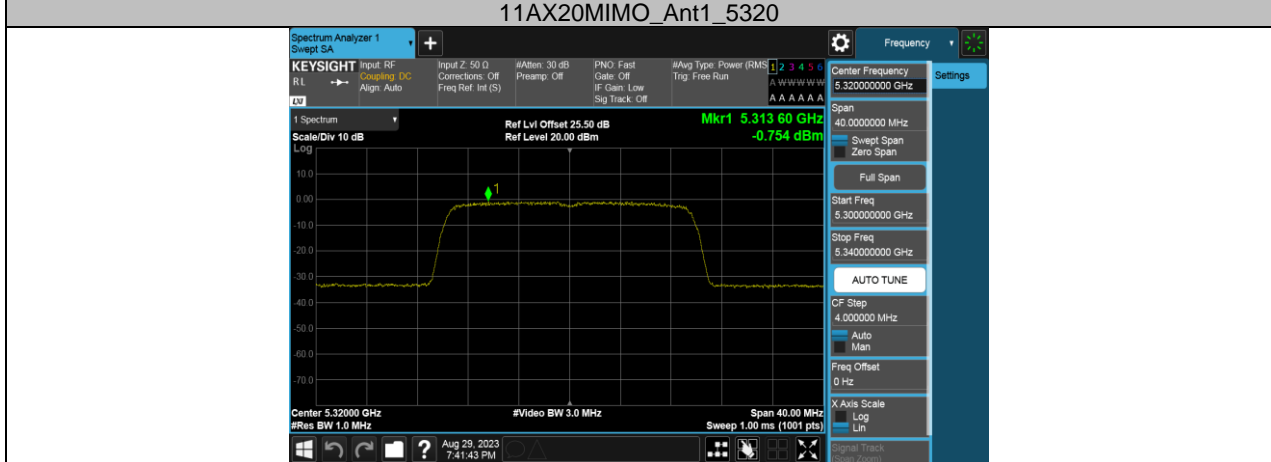
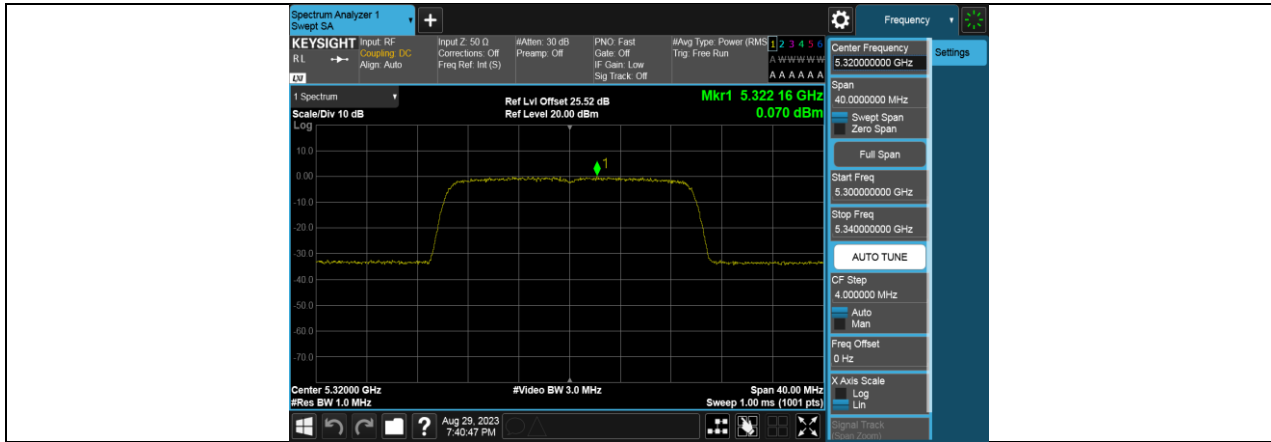
**11AX20MIMO\_Ant3\_5200**



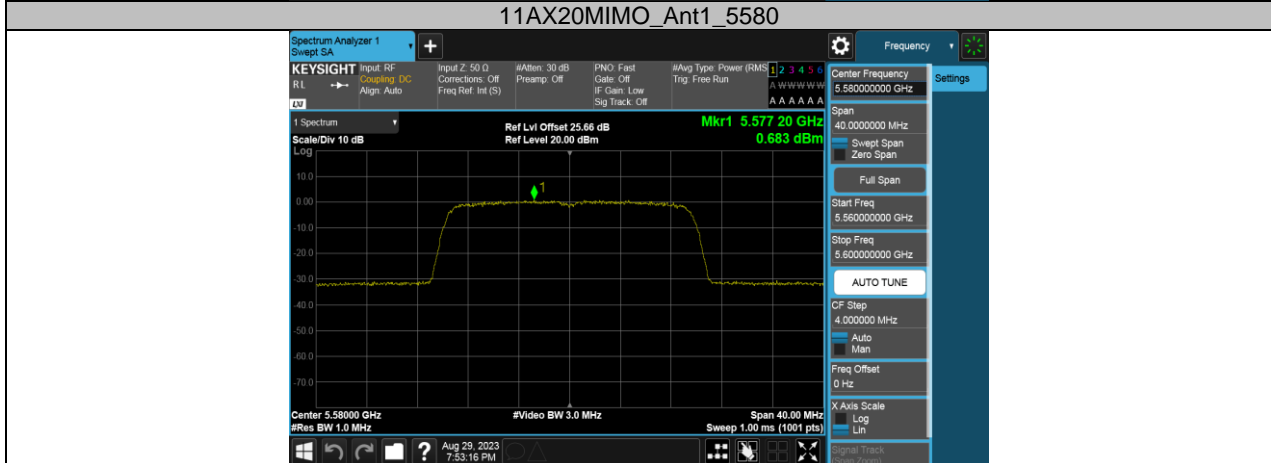
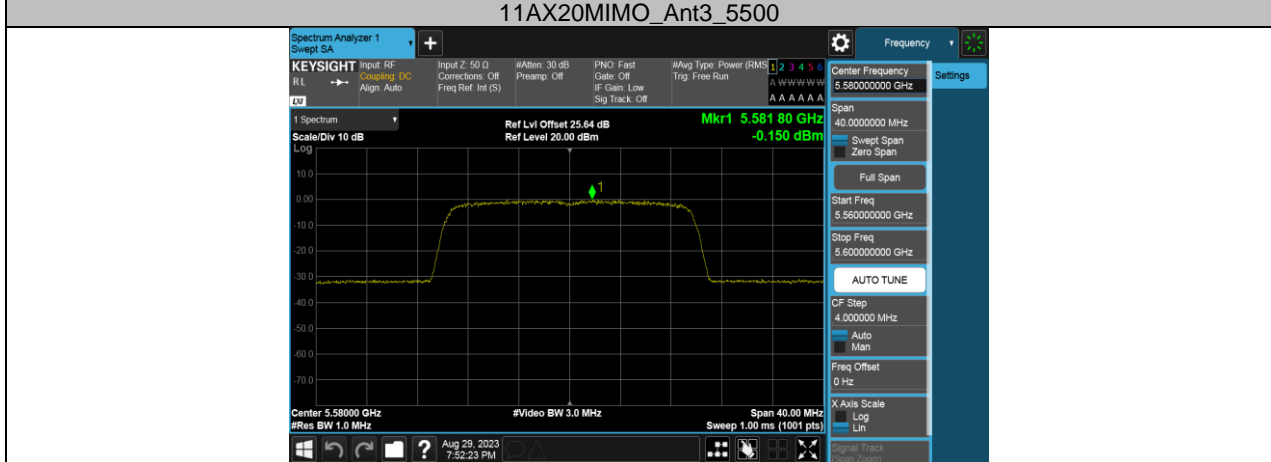
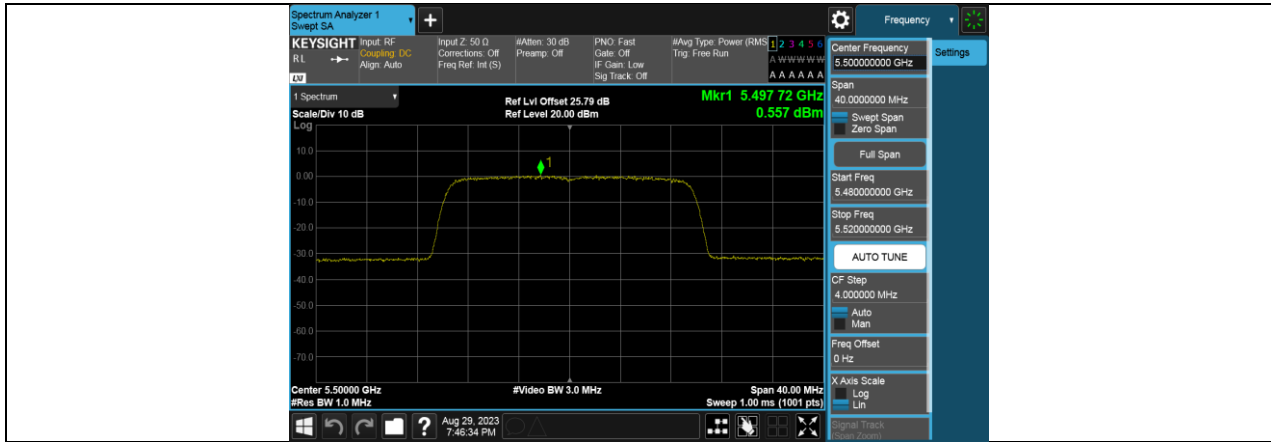
**11AX20MIMO\_Ant1\_5260**



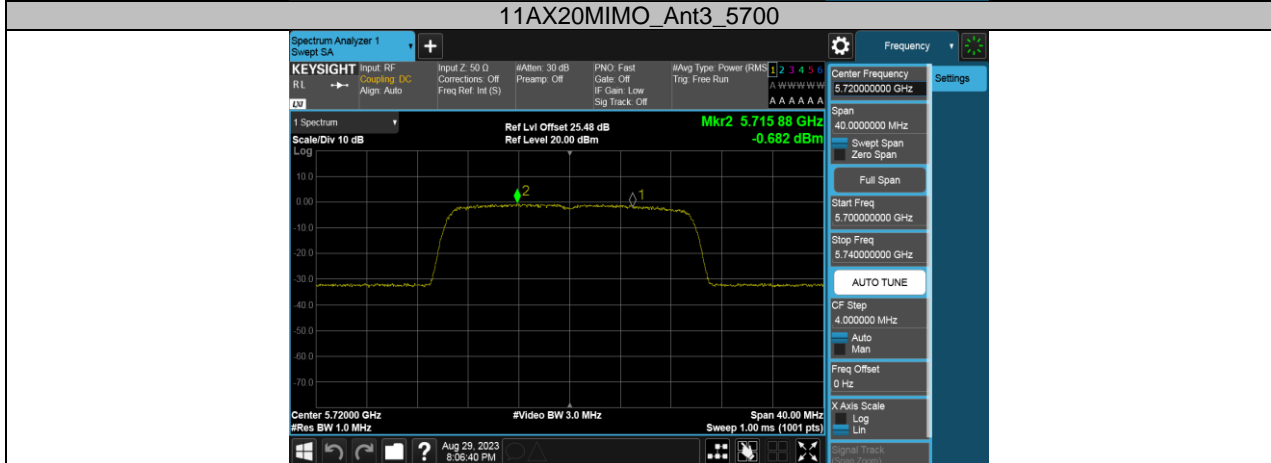
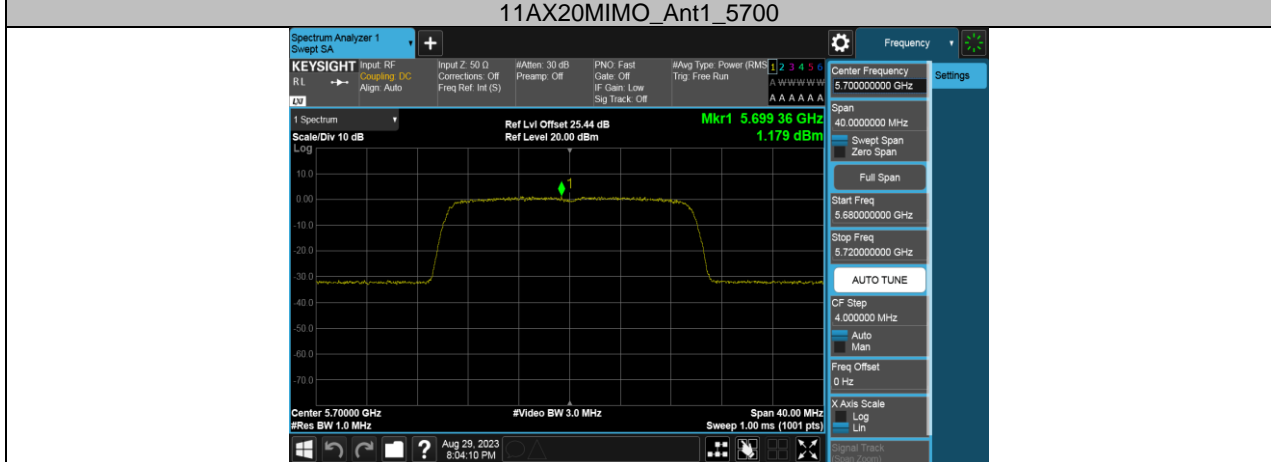
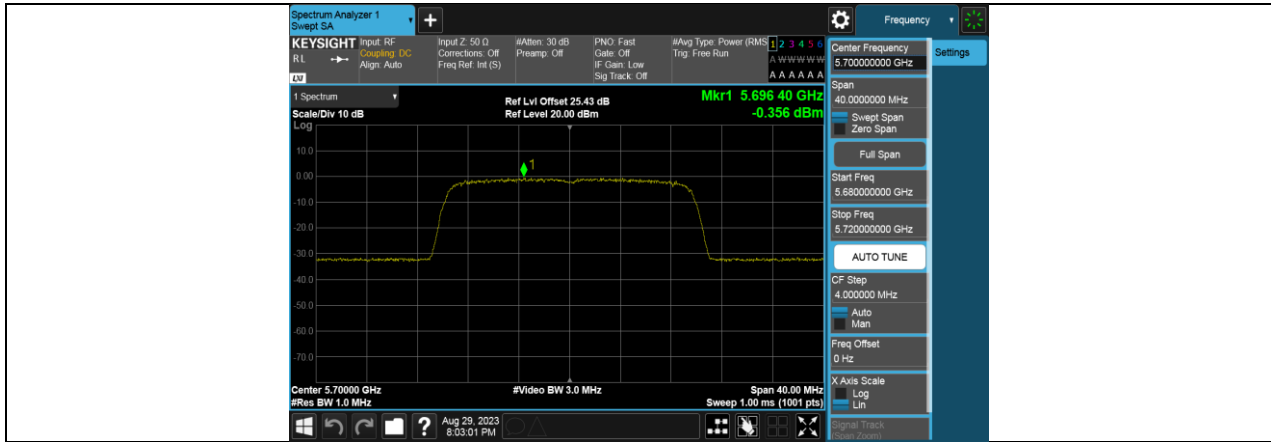
**11AX20MIMO\_Ant3\_5280**



**11AX20MIMO\_Ant1\_5500**

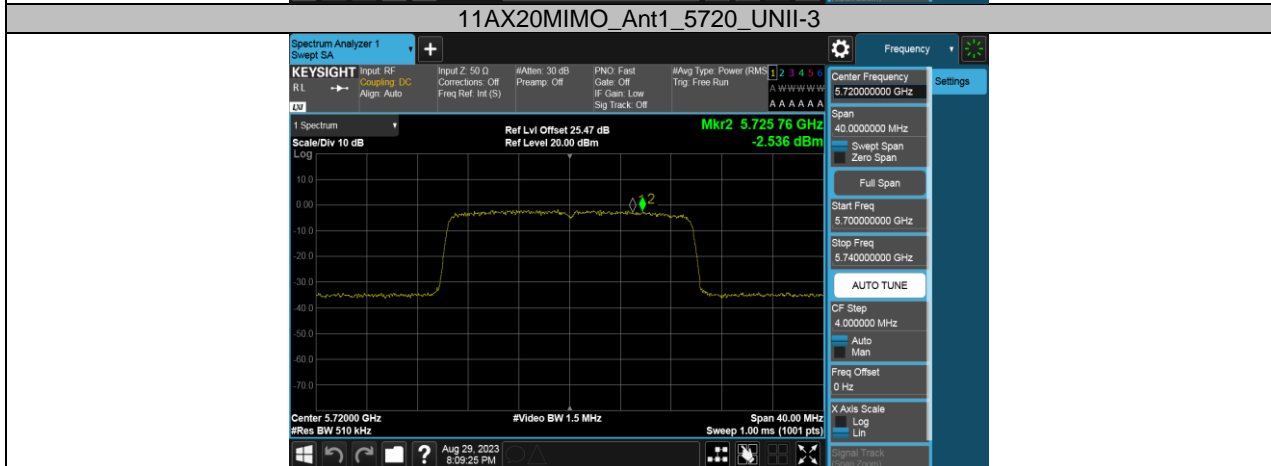
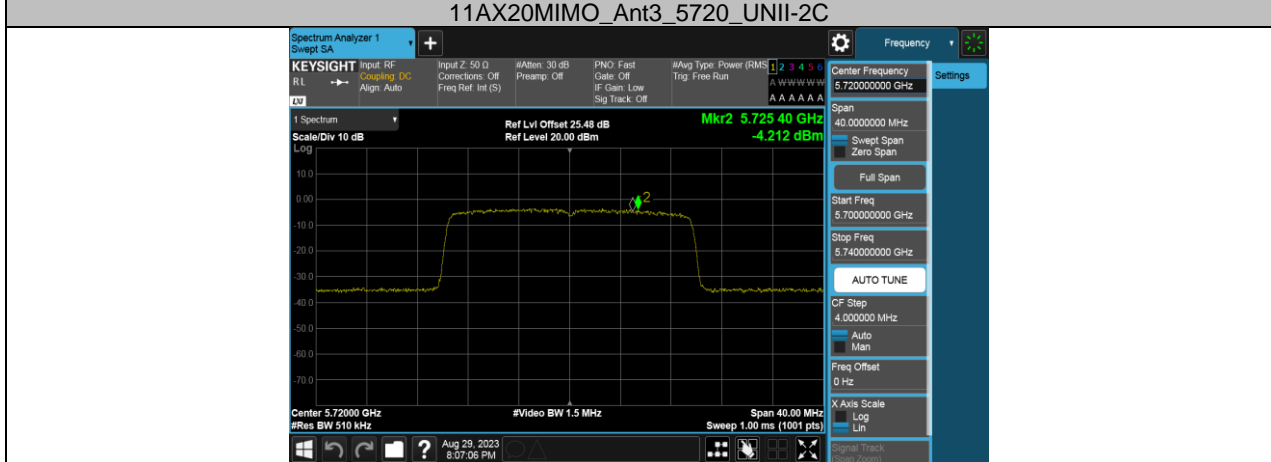
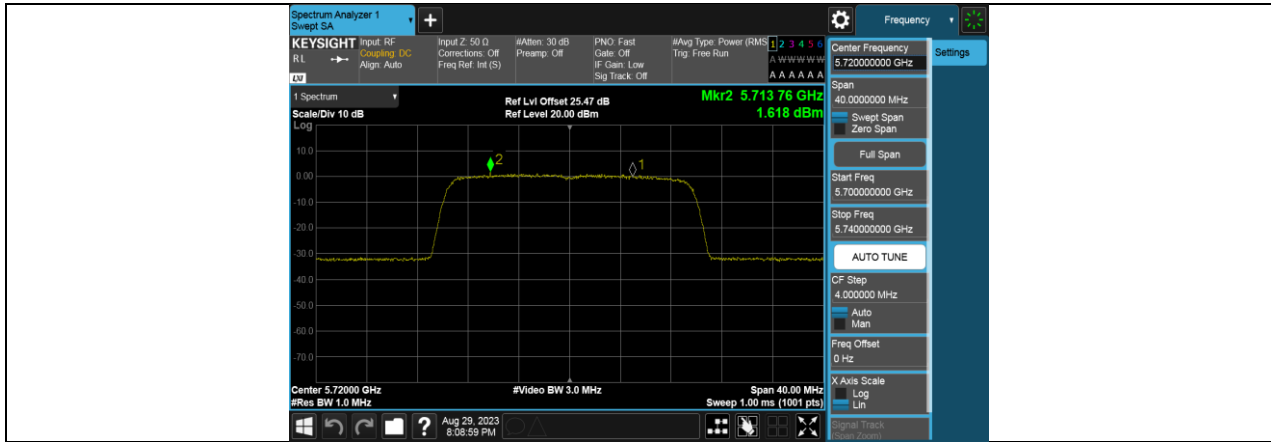


**11AX20MIMO\_Ant3\_5580**

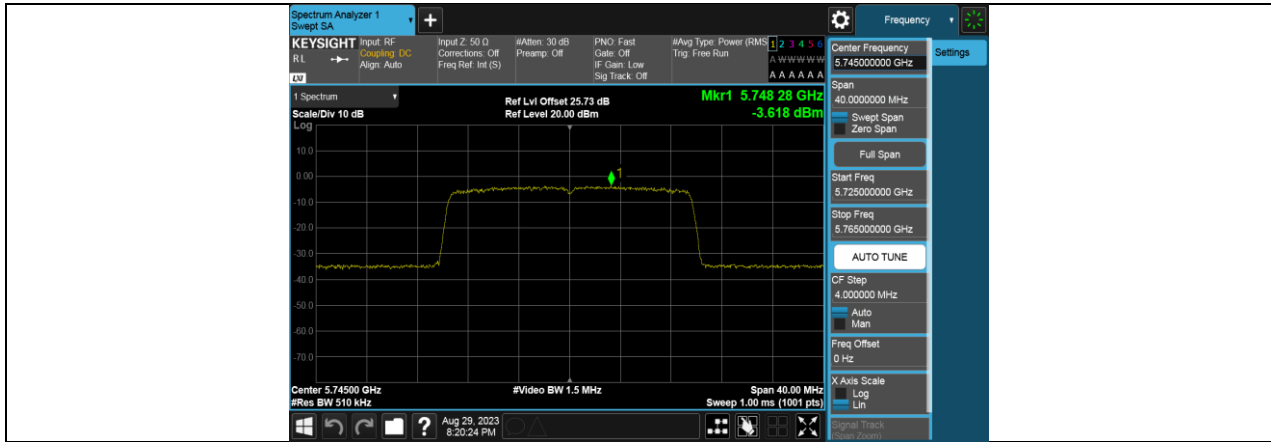


**11AX20MIMO\_Ant1\_5720\_UNII-2C**

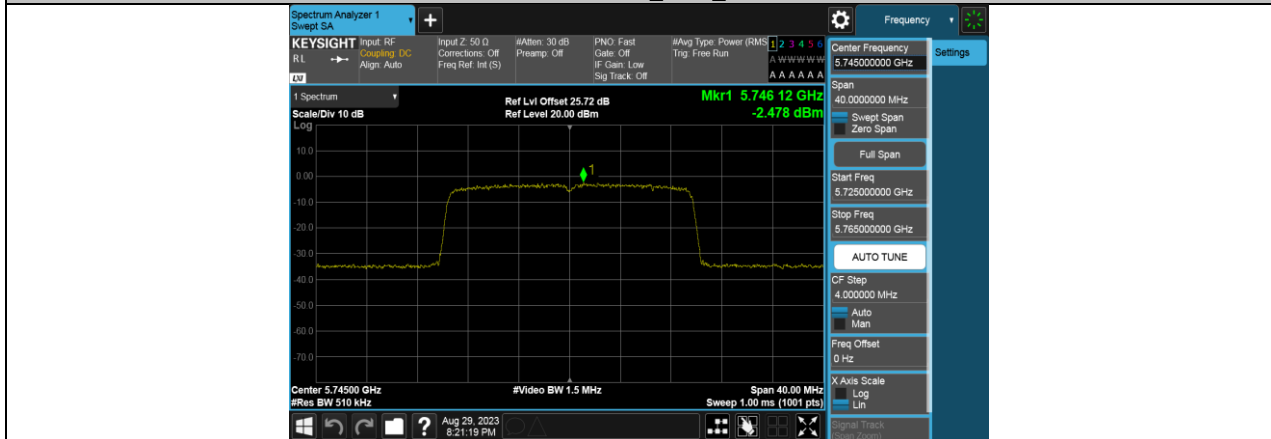




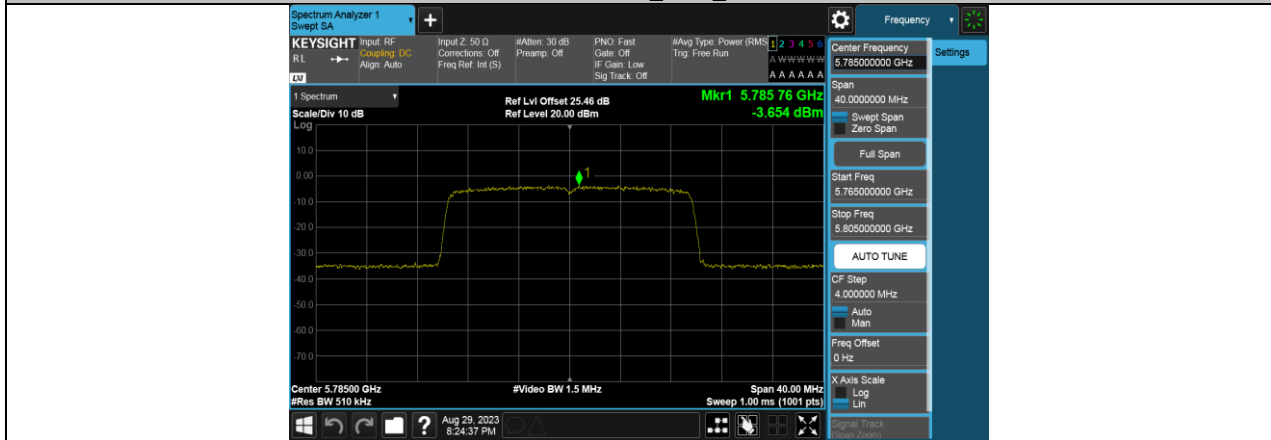
**11AX20MIMO\_Ant3\_5720\_UNII-3**



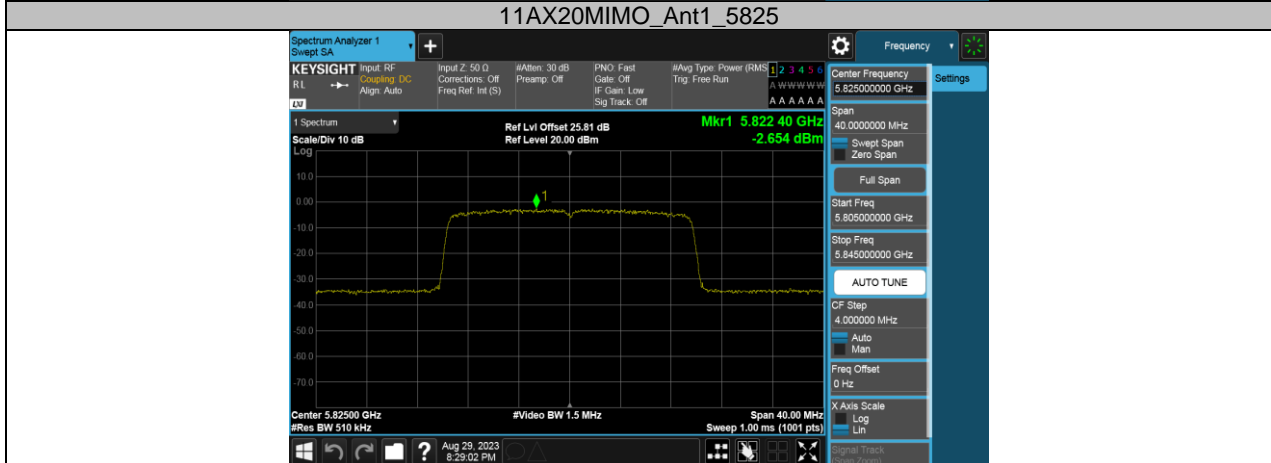
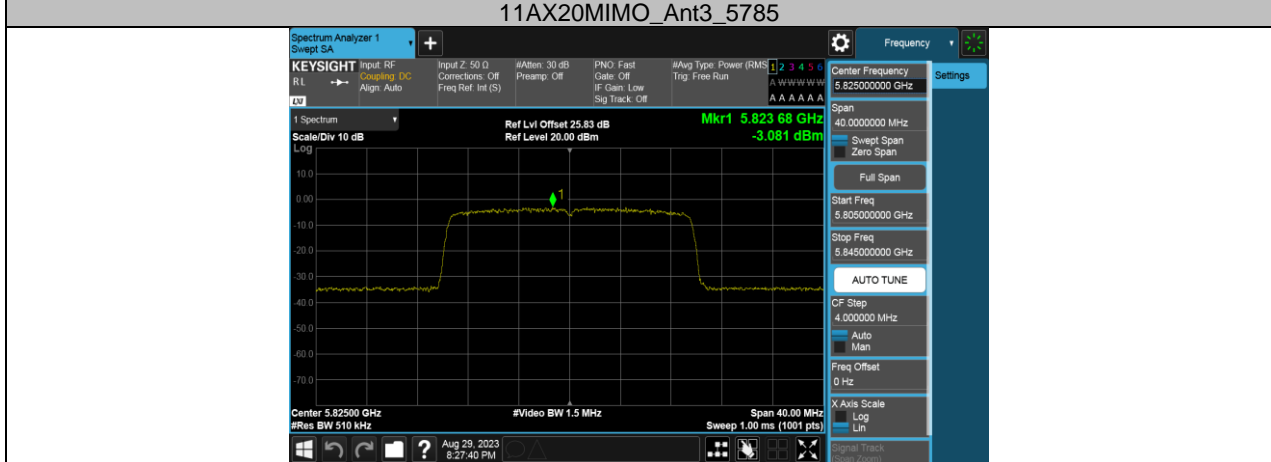
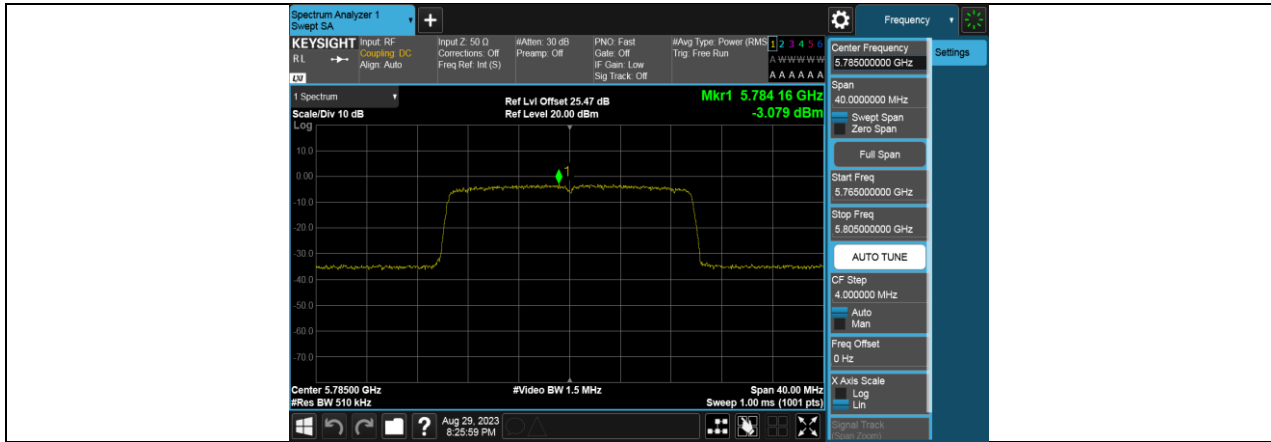
11AX20MIMO\_Ant1\_5745



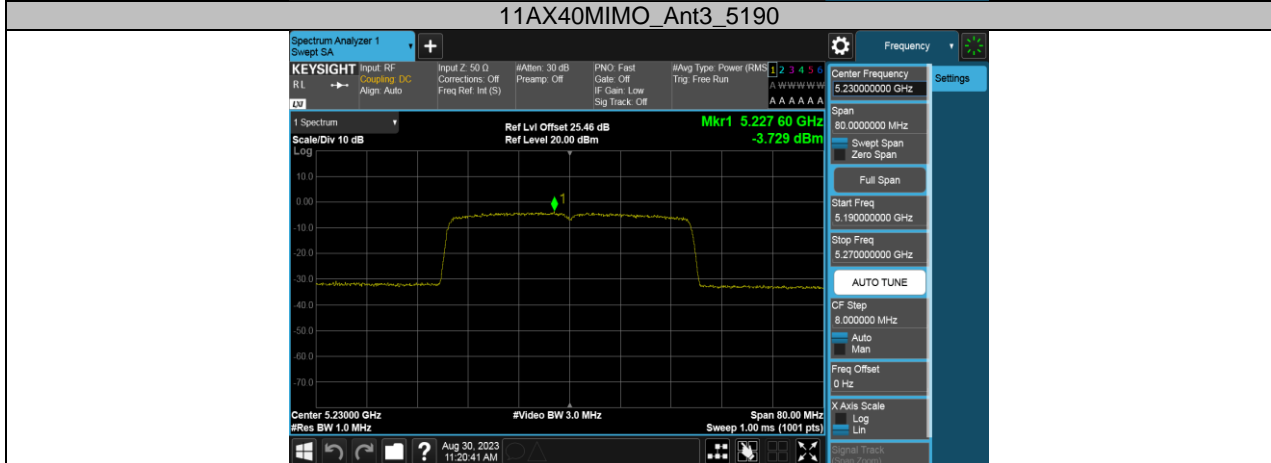
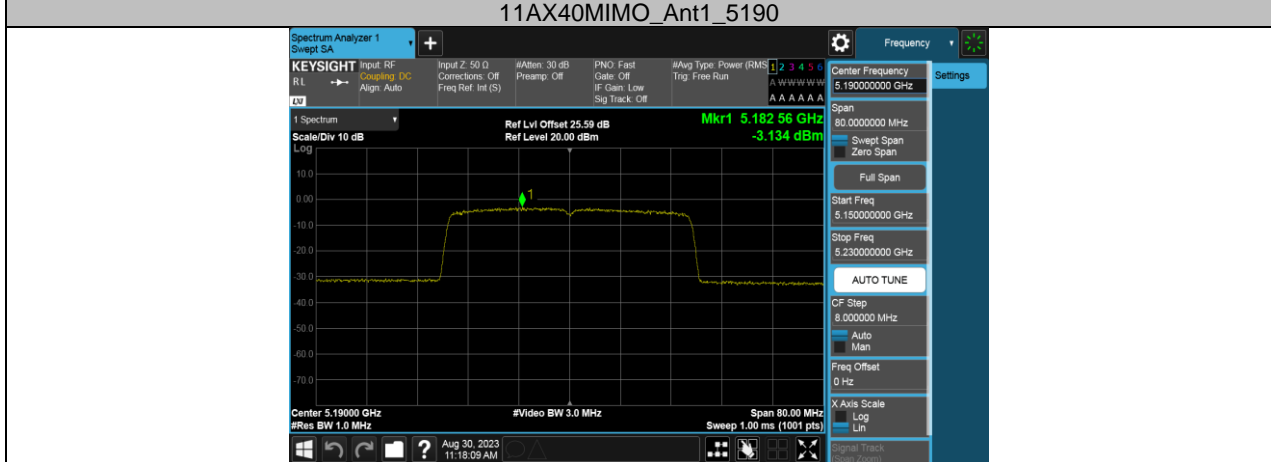
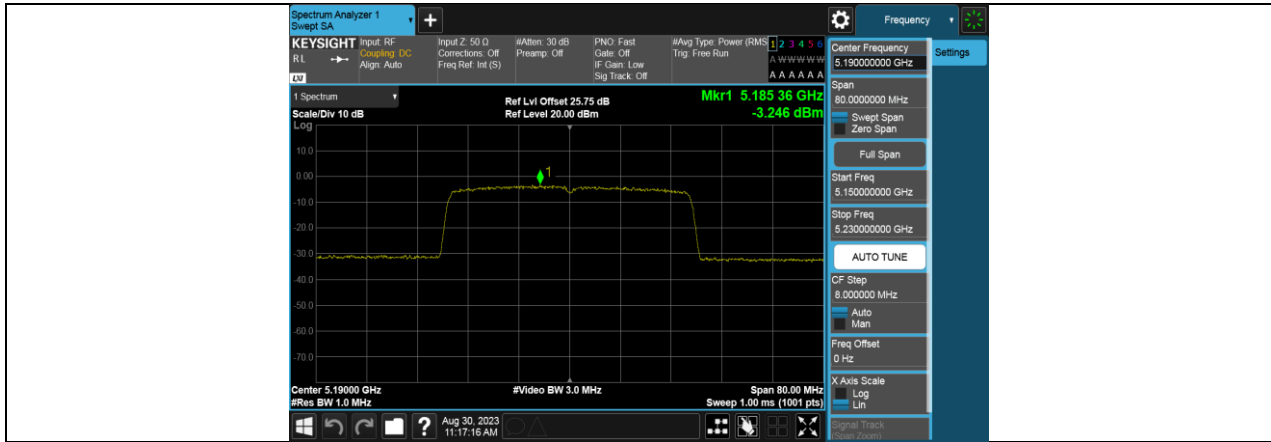
11AX20MIMO\_Ant3\_5745



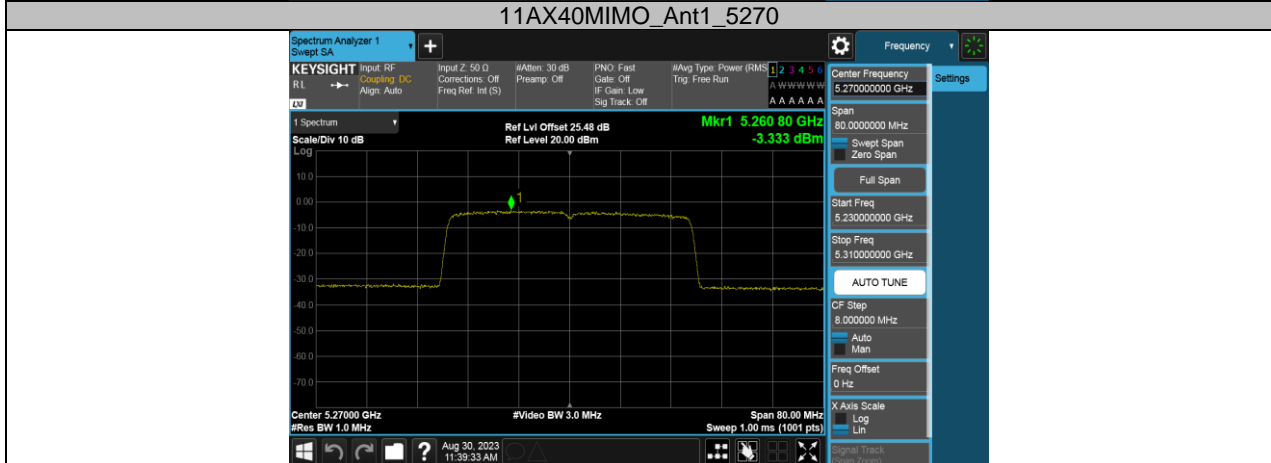
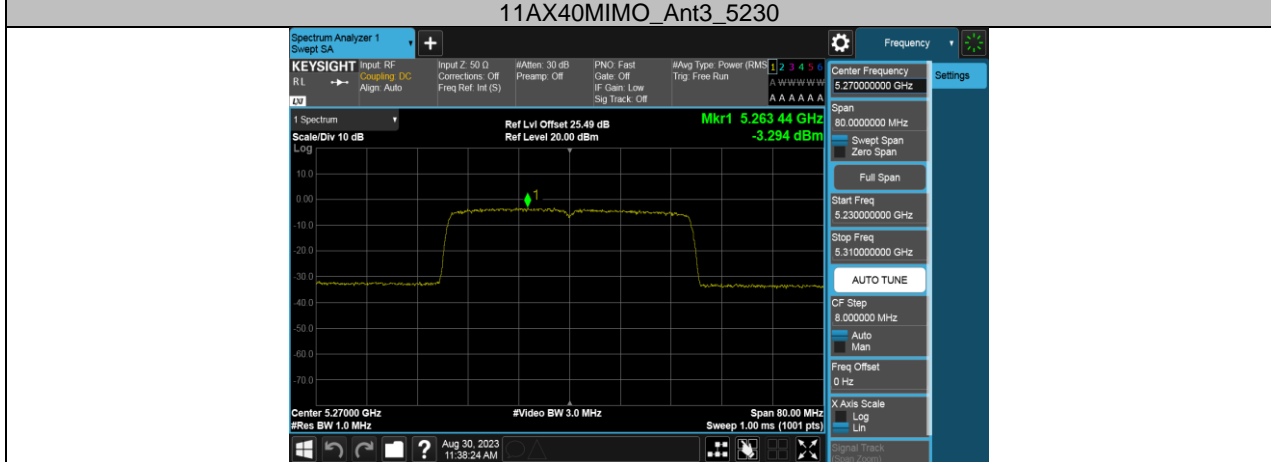
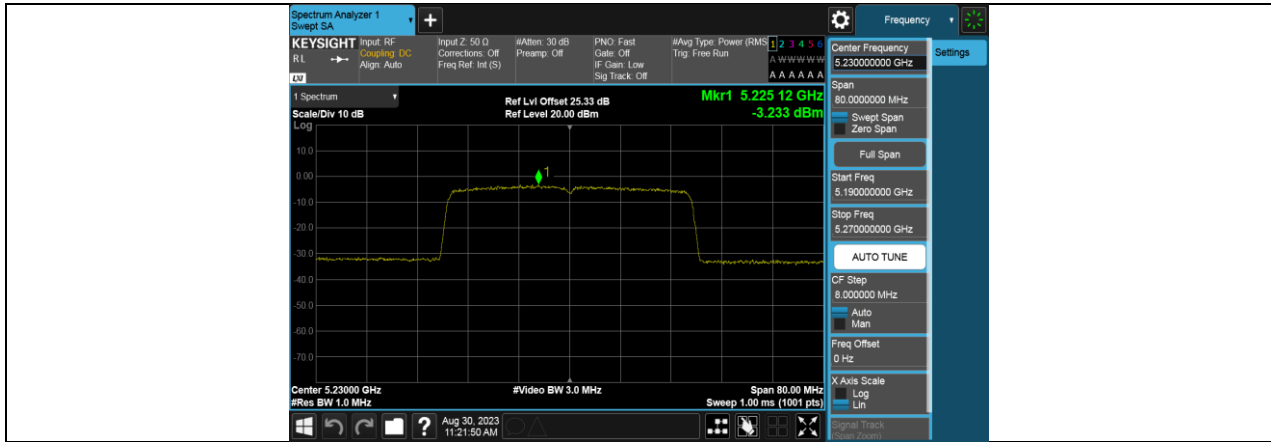
11AX20MIMO\_Ant1\_5785



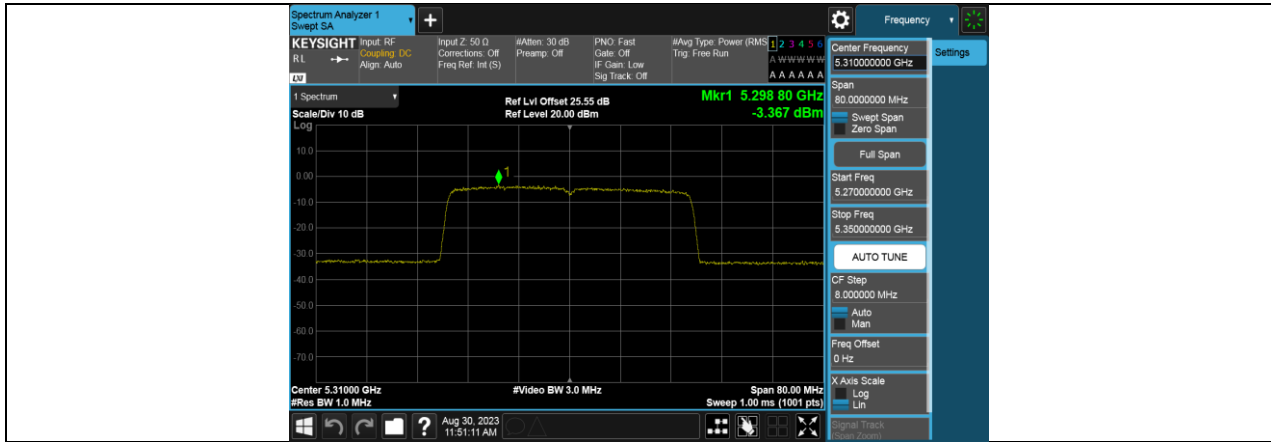
**11AX20MIMO\_Ant3\_5825**



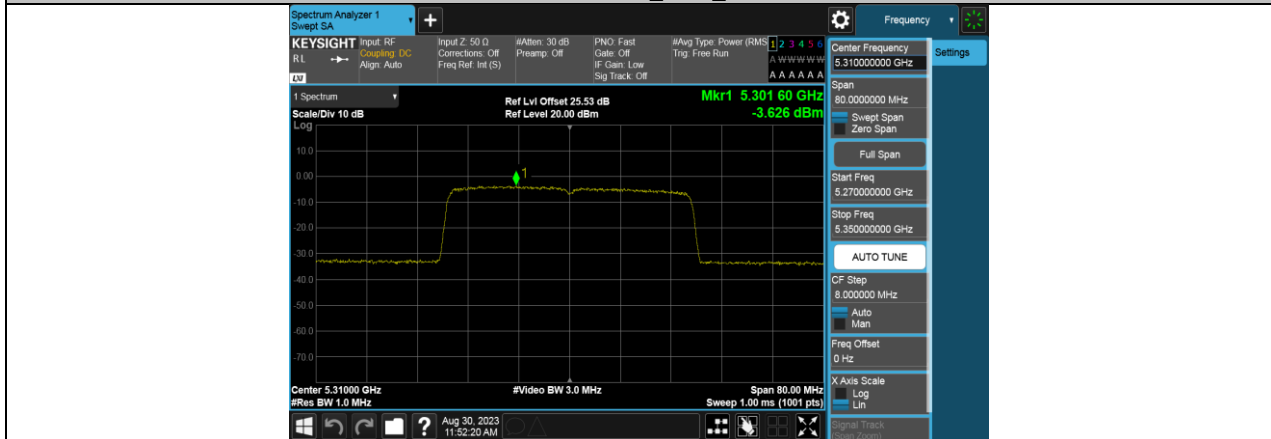
**11AX40MIMO\_Ant1\_5230**



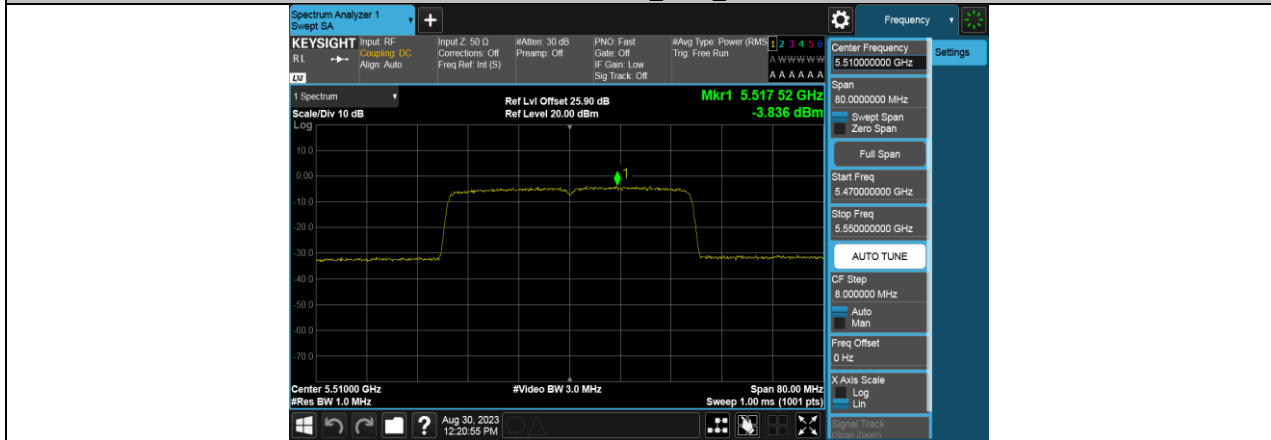
**11AX40MIMO\_Ant3\_5270**



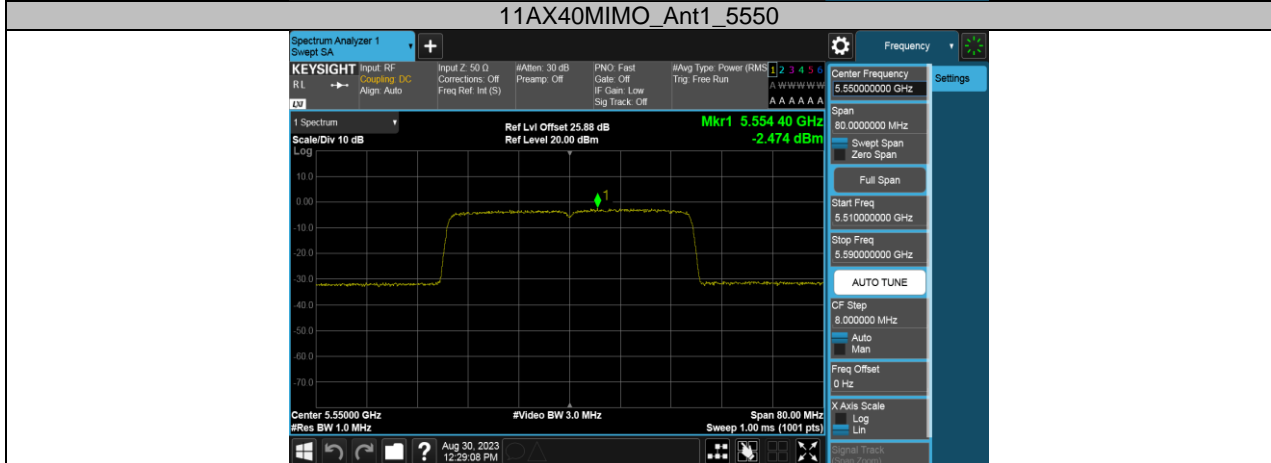
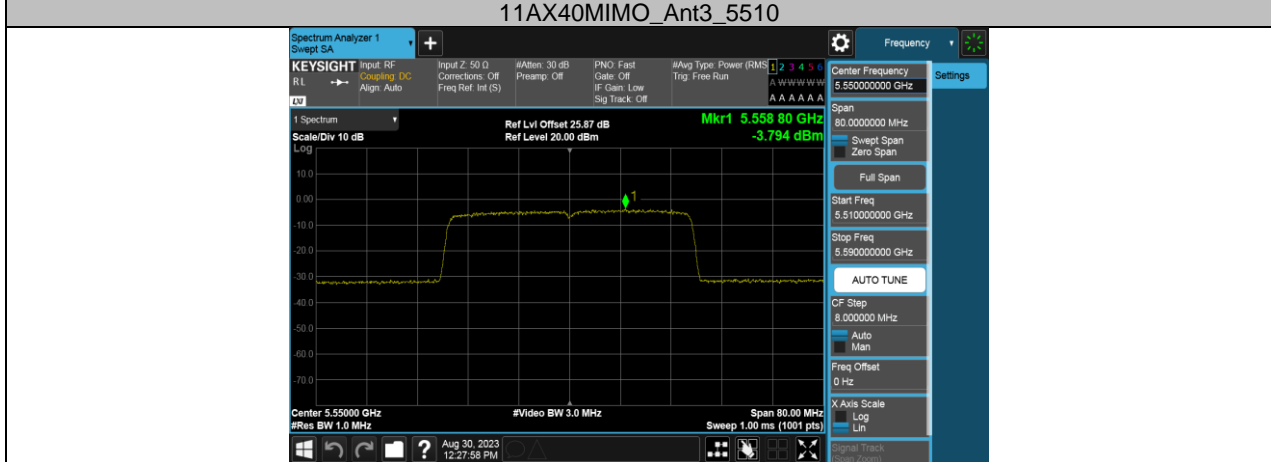
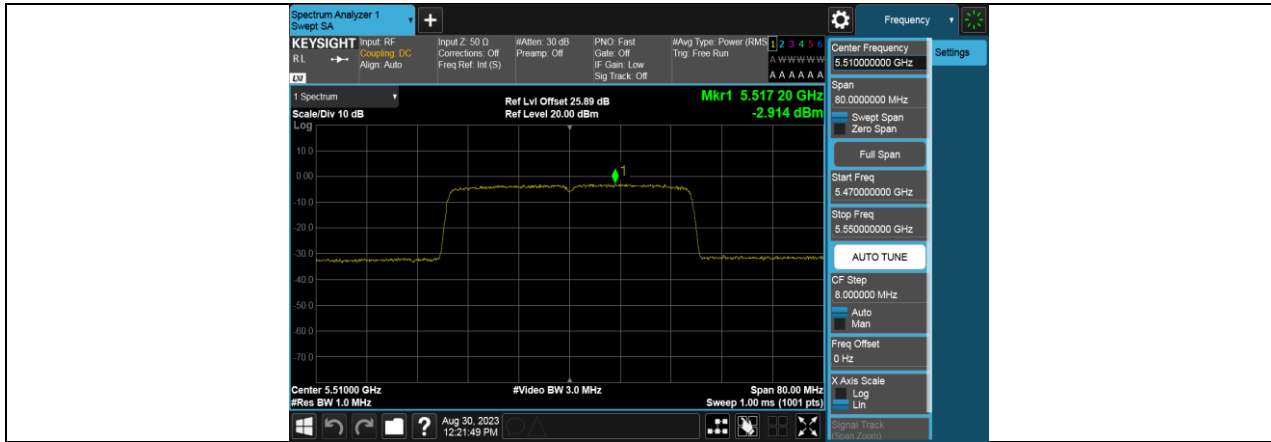
11AX40MIMO\_Ant1\_5310



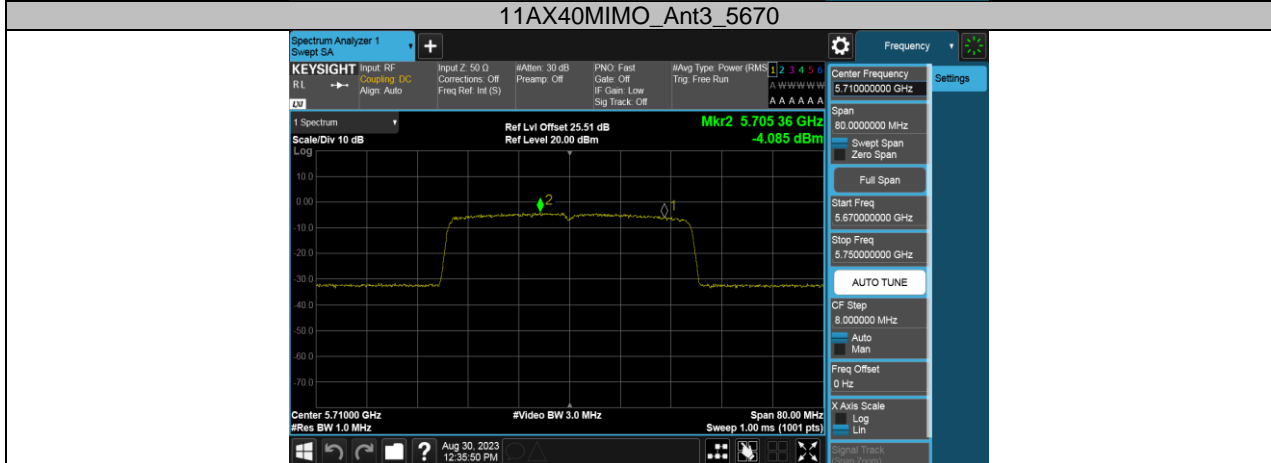
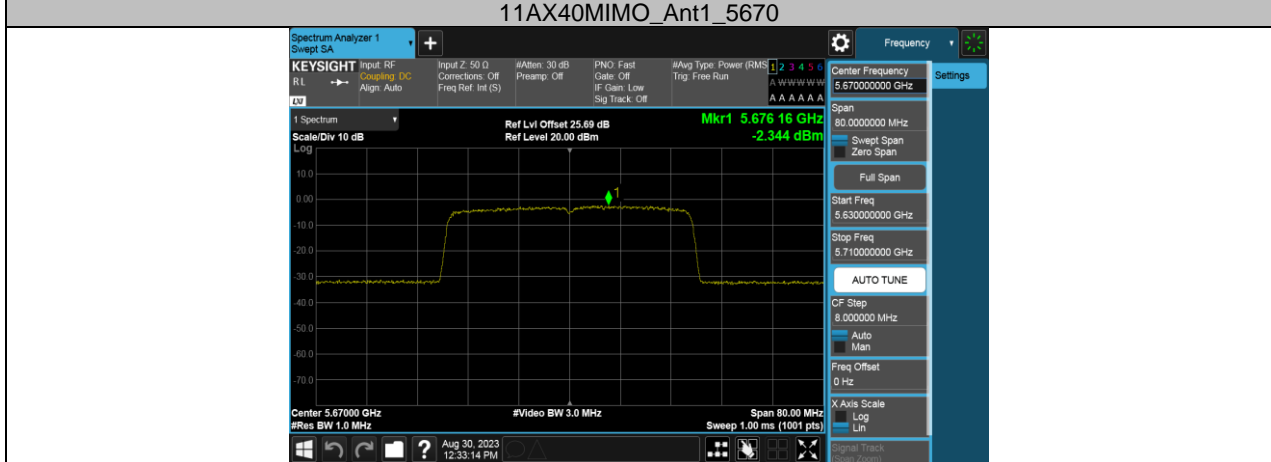
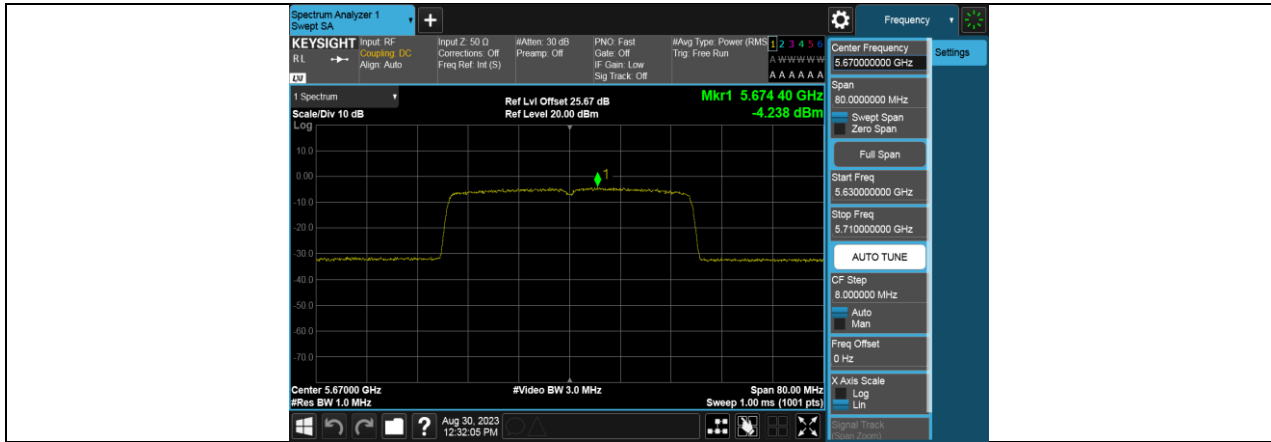
11AX40MIMO\_Ant3\_5310



11AX40MIMO\_Ant1\_5510

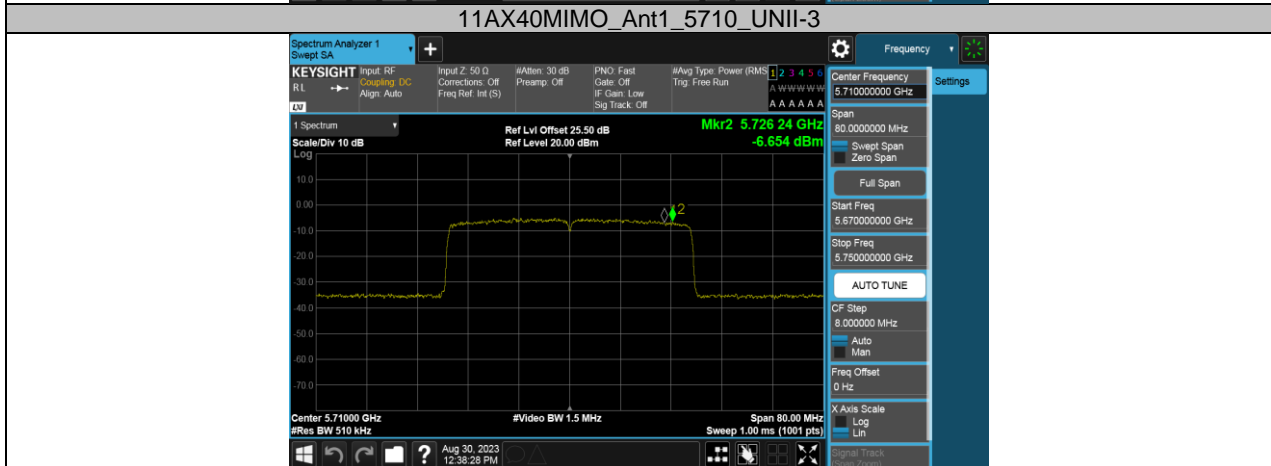
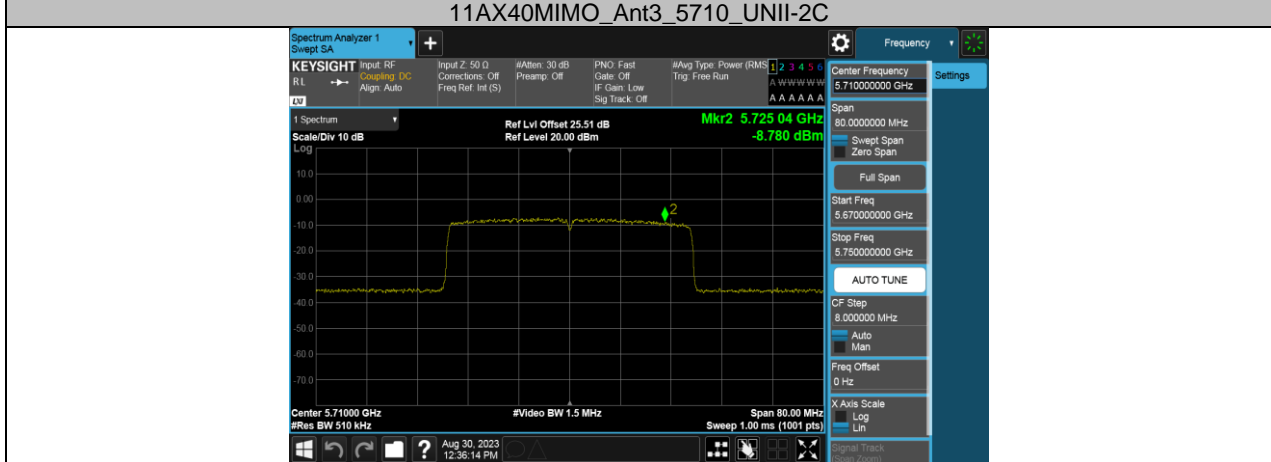
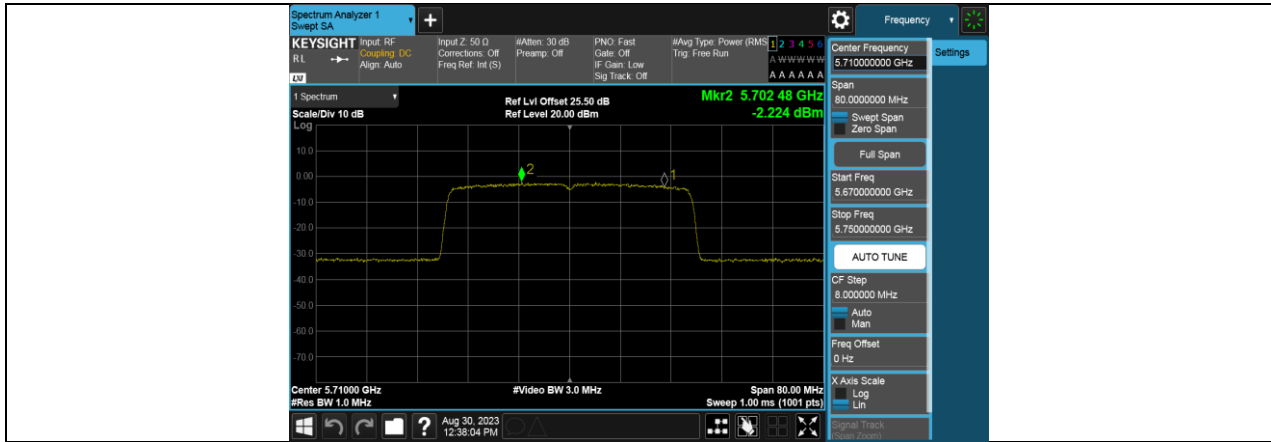


**11AX40MIMO\_Ant3\_5550**

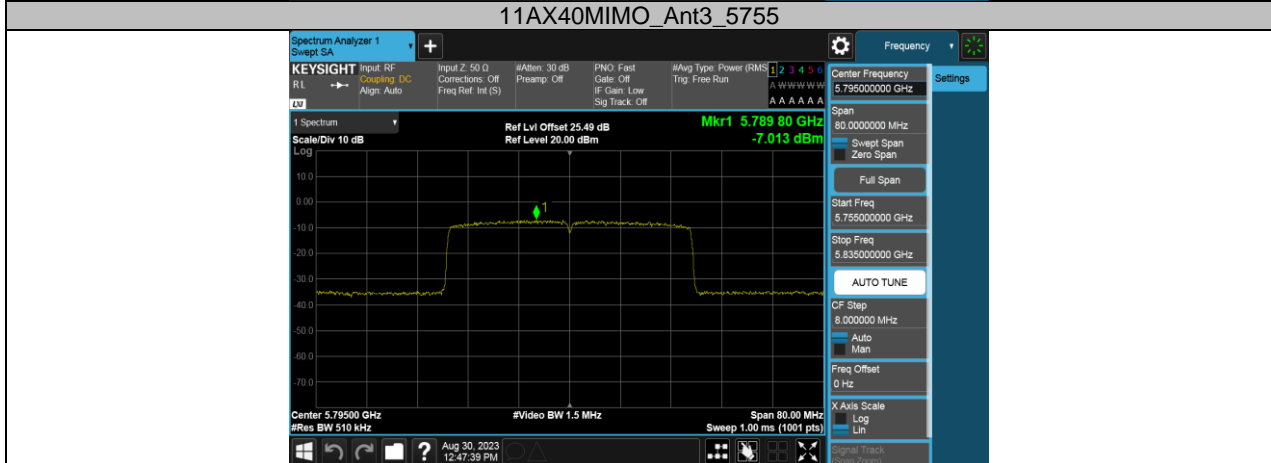
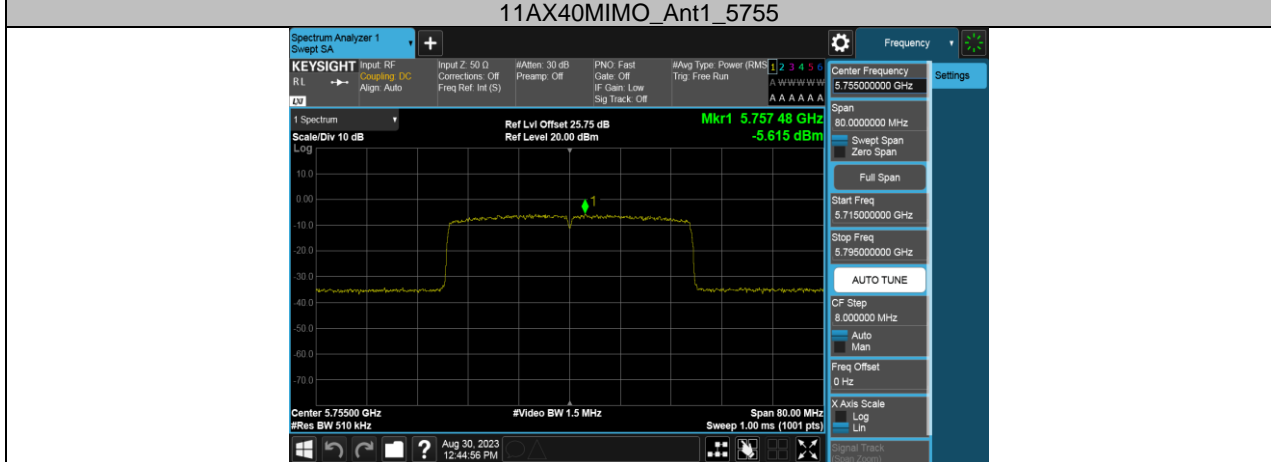
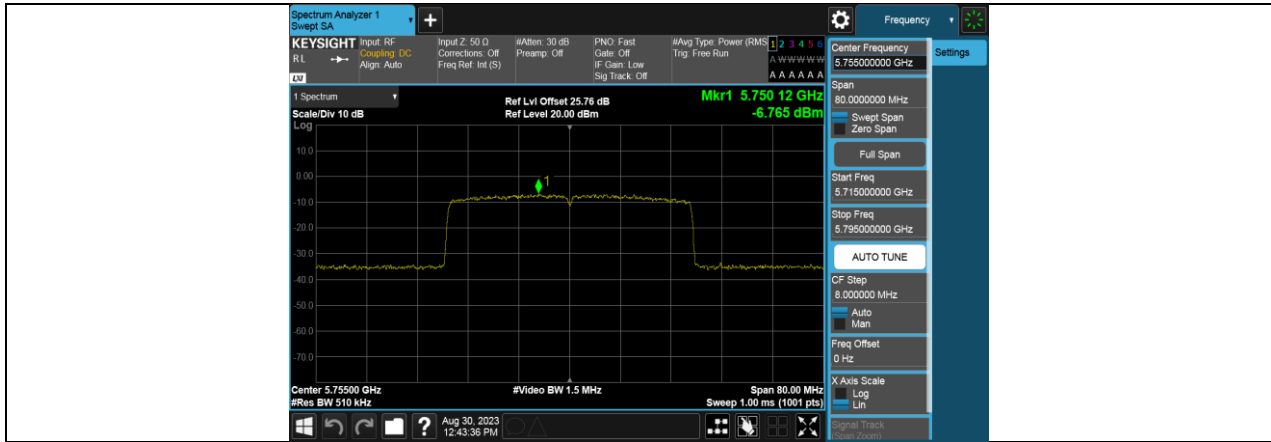


**11AX40MIMO\_Ant1\_5710\_UNII-2C**

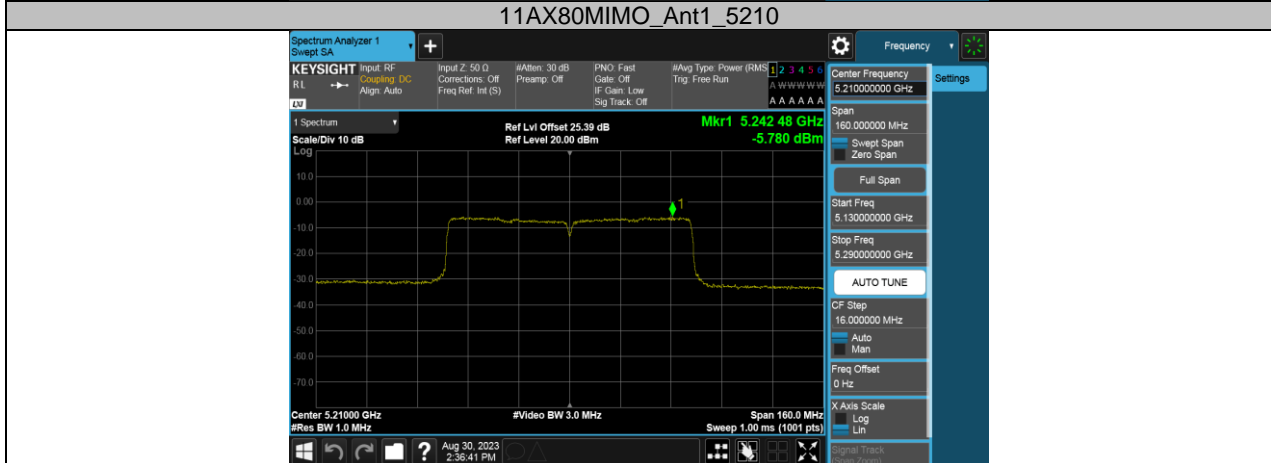
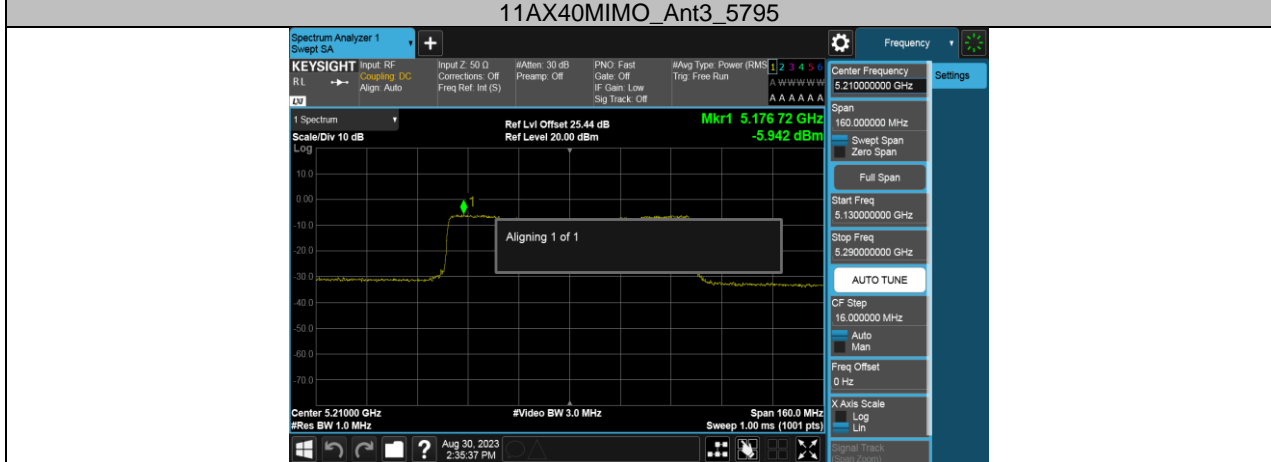
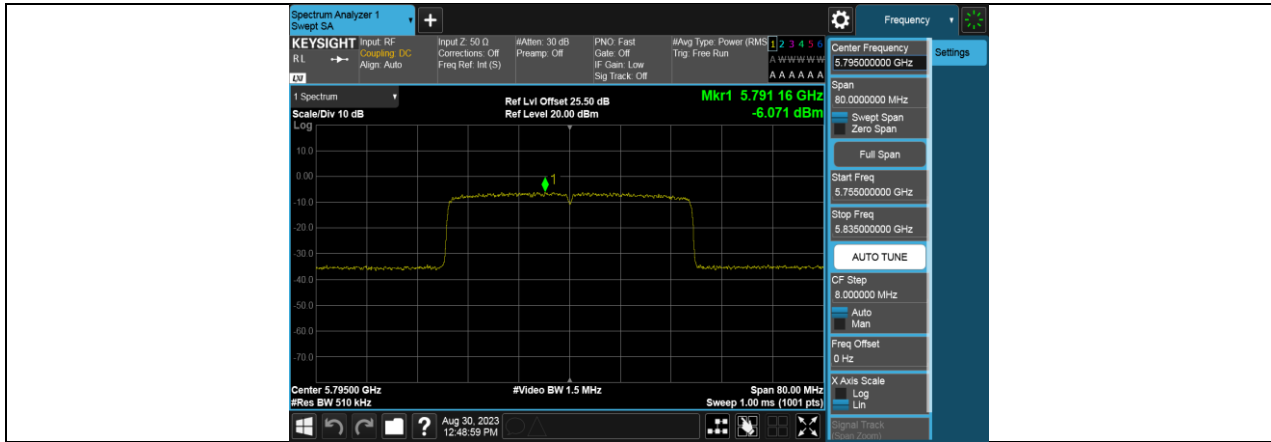


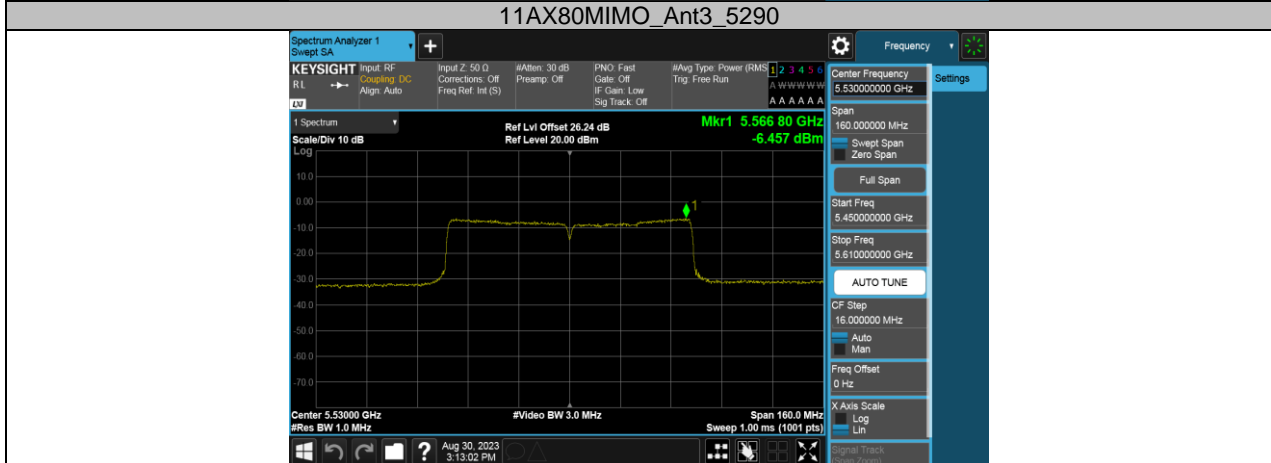
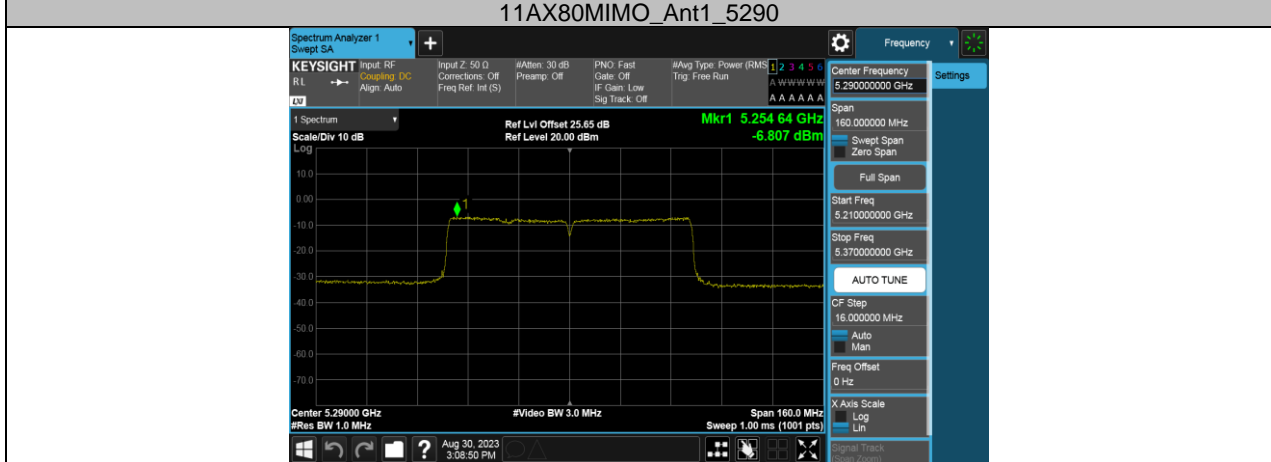
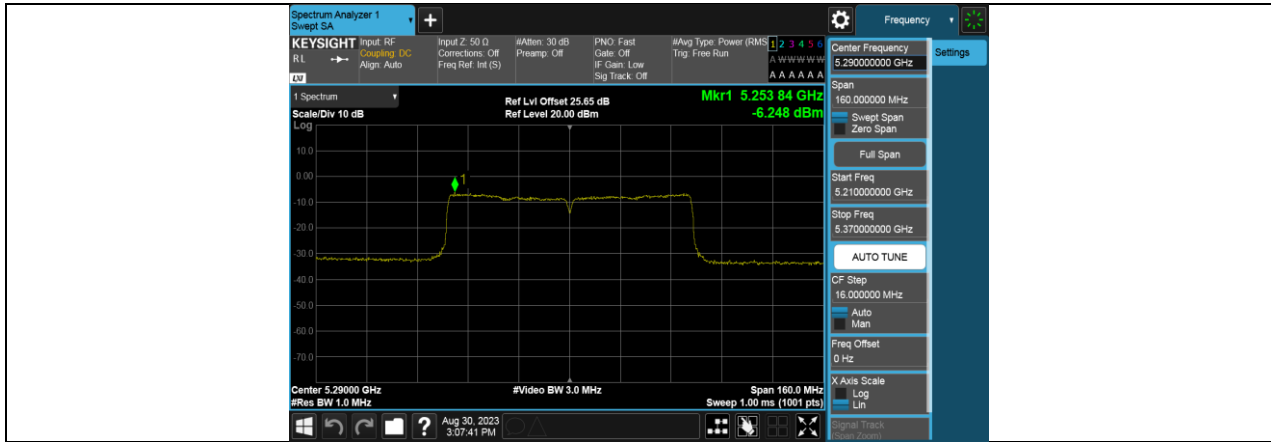


**11AX40MIMO\_Ant3\_5710\_UNII-3**



**11AX40MIMO\_Ant1\_5795**





**11AX80MIMO\_Ant1\_5530**

