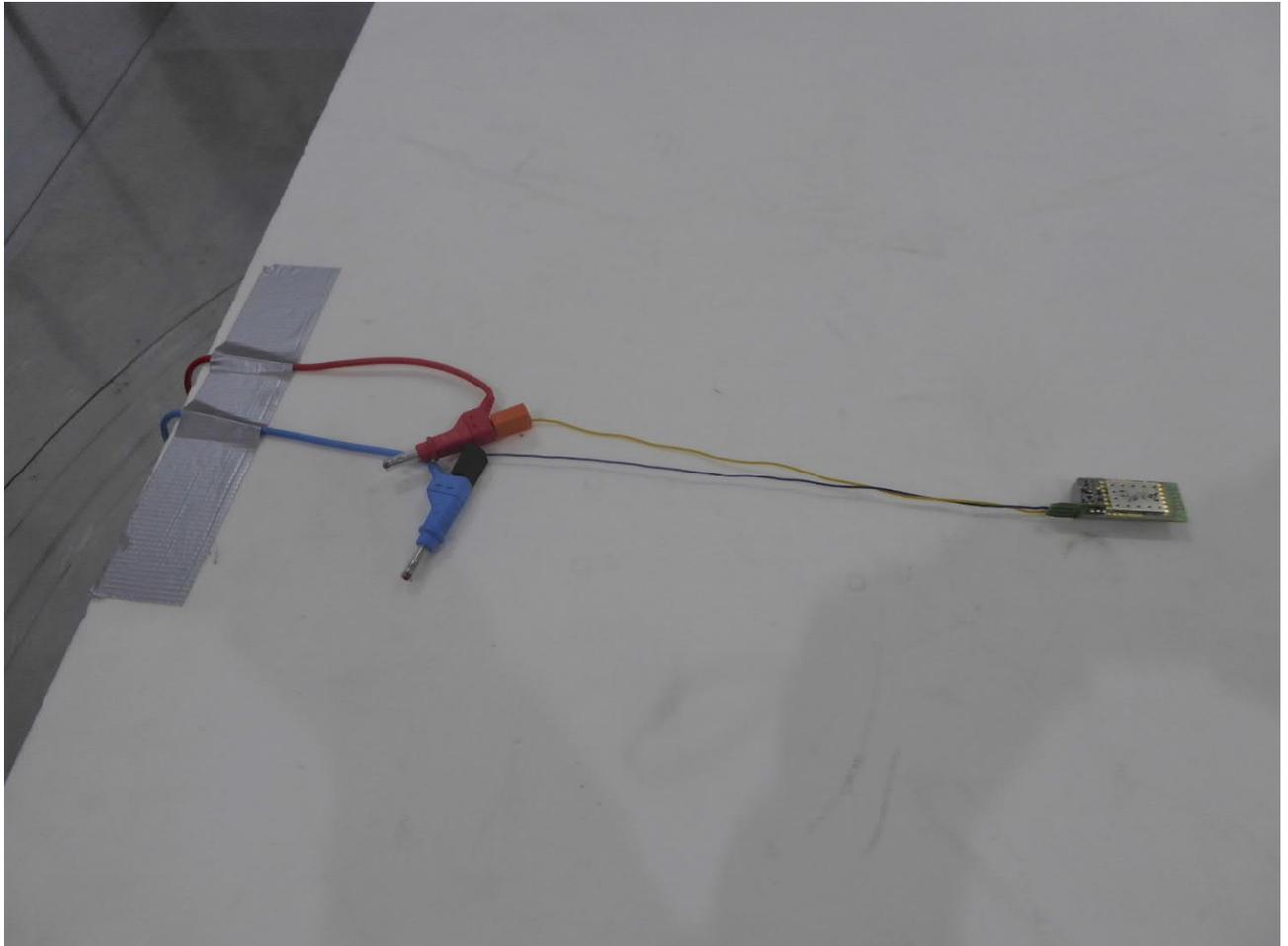


Annex A

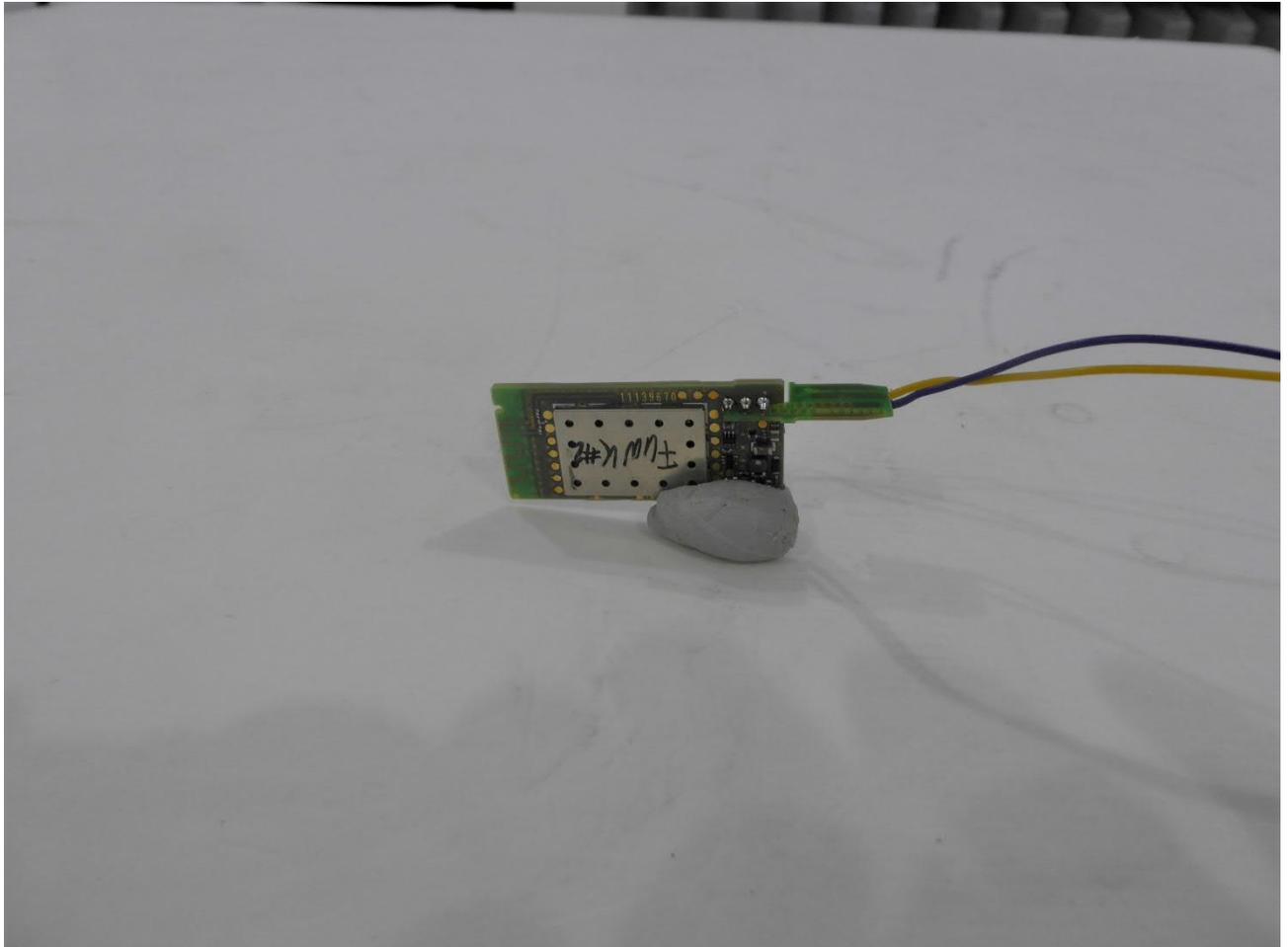
Test Setup Photos



Test setup semi anechoic chamber

Annex A

Test Setup Photos



Test setup semi anechoic chamber

Annex A

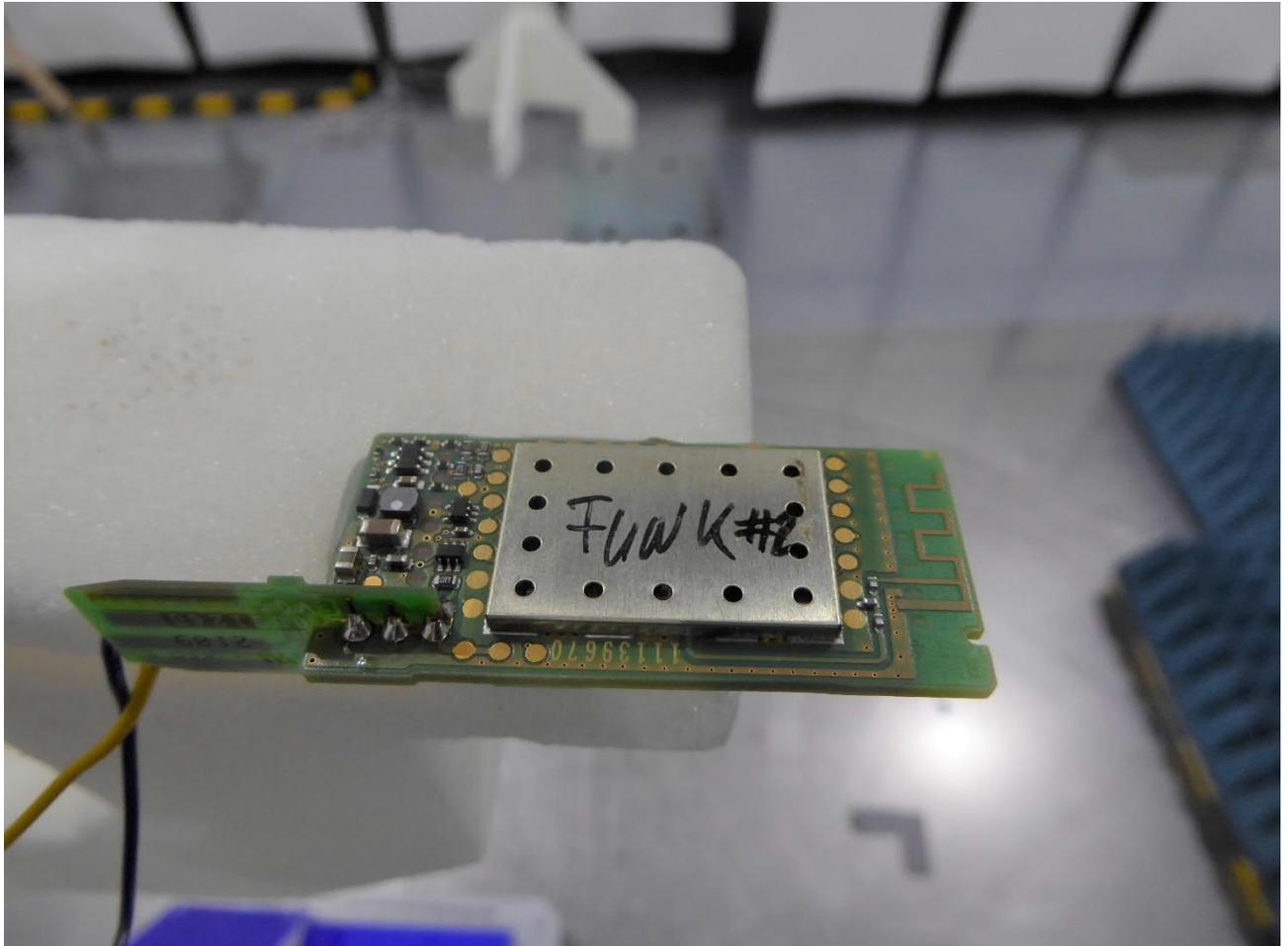
Test Setup Photos



Test setup semi anechoic chamber

Annex A

Test Setup Photos



Test setup semi anechoic chamber

Annex A

Test Setup Photos



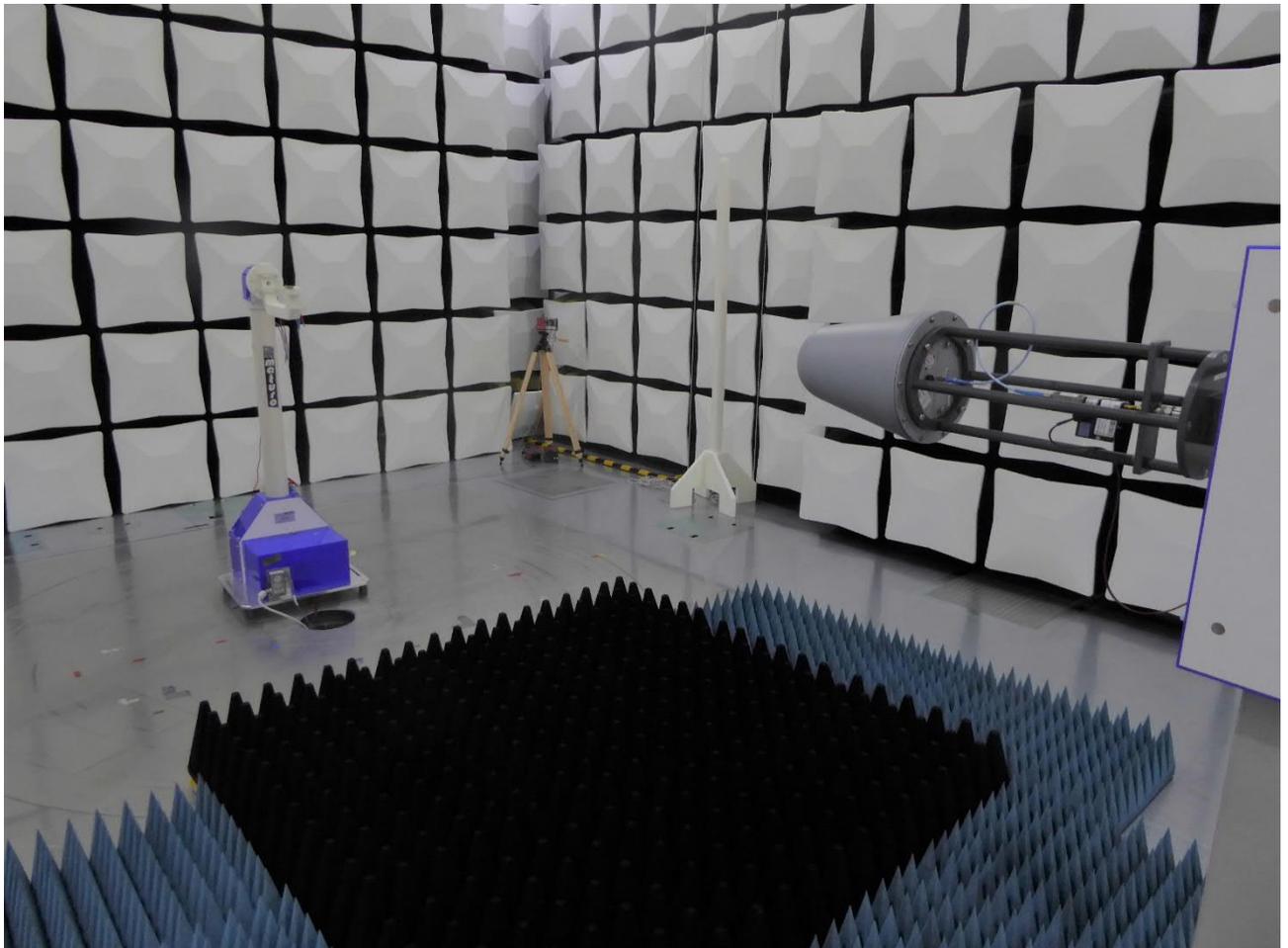
Test setup semi anechoic chamber

Annex A Test Setup Photos



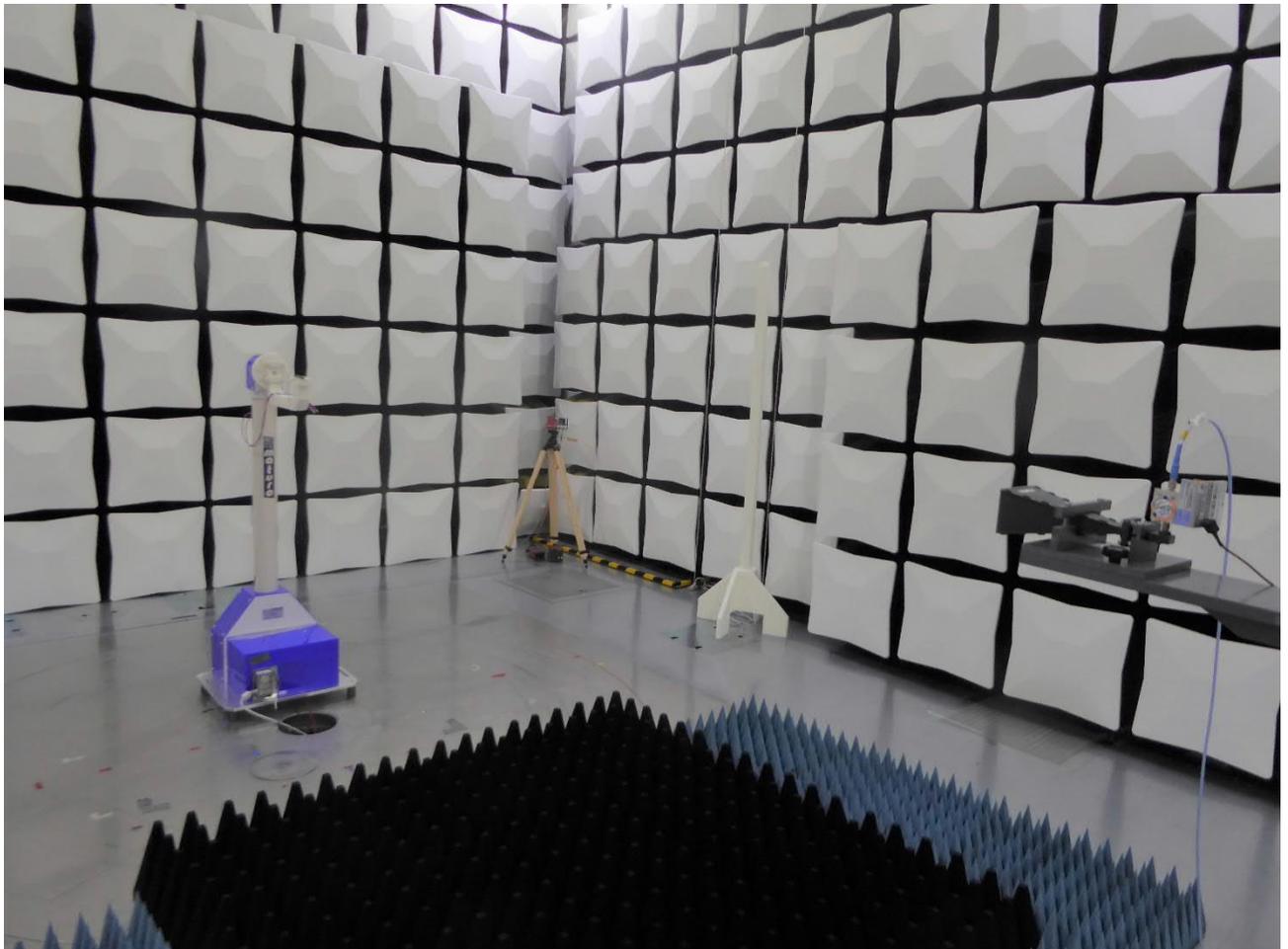
Test setup semi anechoic chamber

Annex A Test Setup Photos



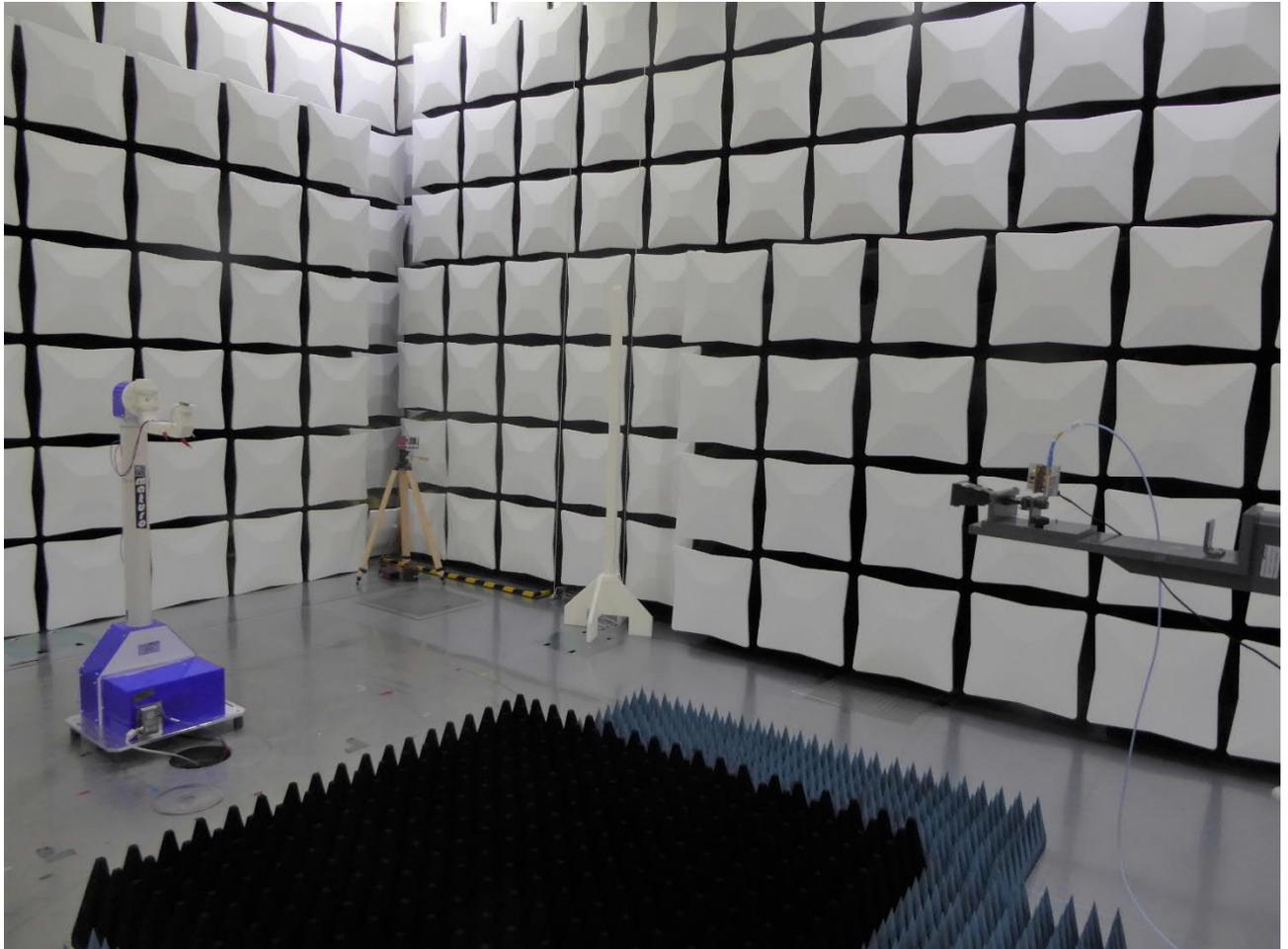
Test setup semi anechoic chamber

Annex A Test Setup Photos



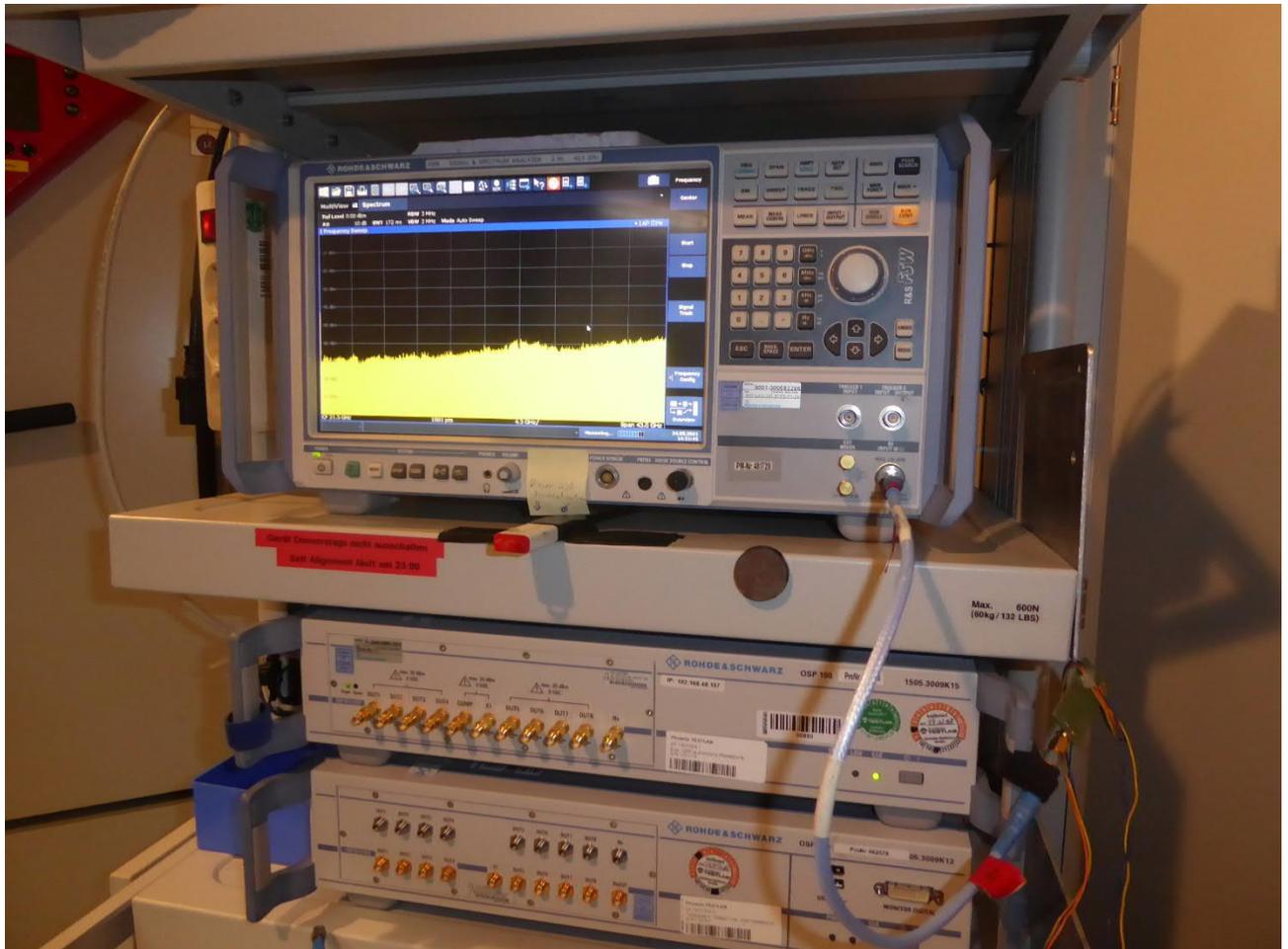
Test setup semi anechoic chamber

Annex A Test Setup Photos



Test setup semi anechoic chamber

Annex A Test Setup Photos



Test setup antenna port conducted tests

Annex A

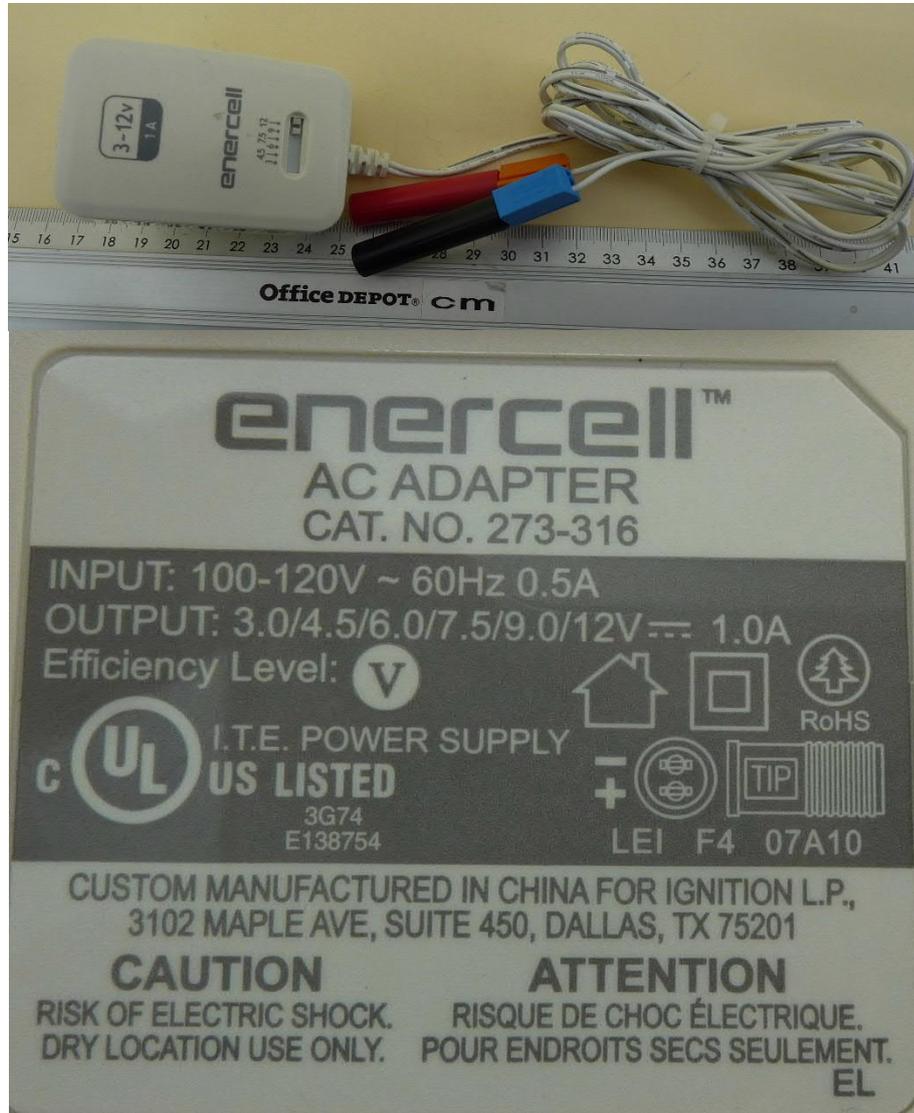
Test Setup Photos



Test setup power line conducted emissions

Annex A

Test Setup Photos



Ancillary power supply

Annex A Test Setup Photos



TTL UART to USB cable