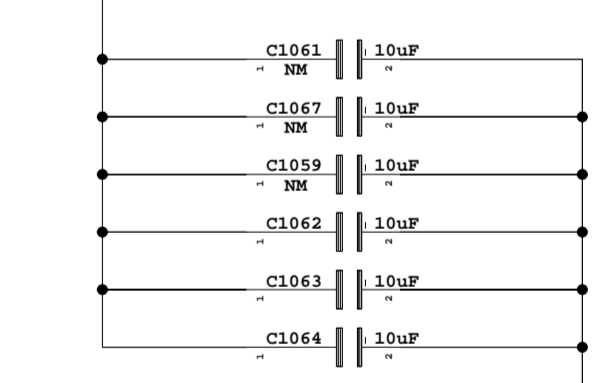
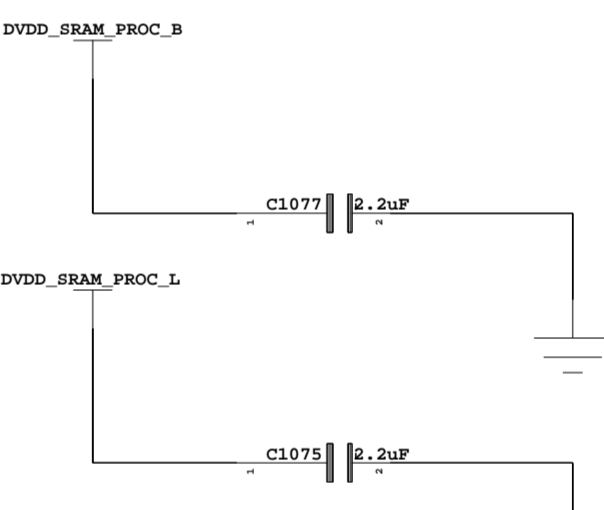
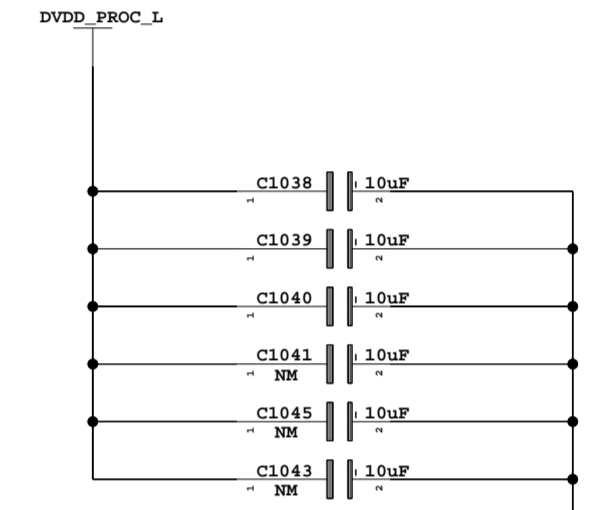
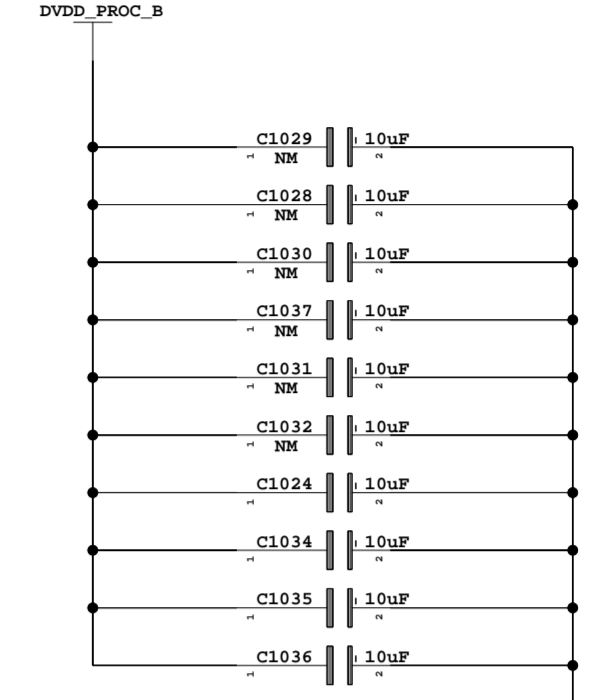
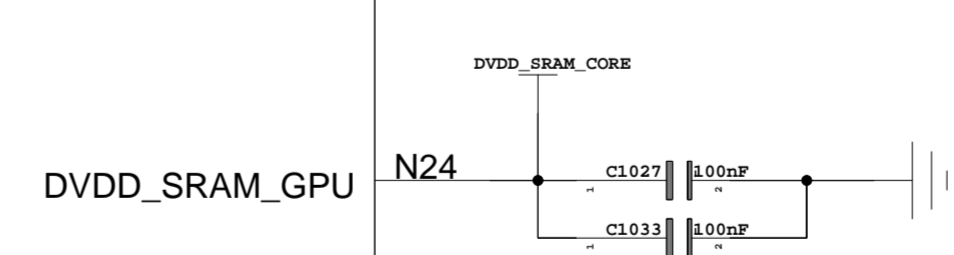
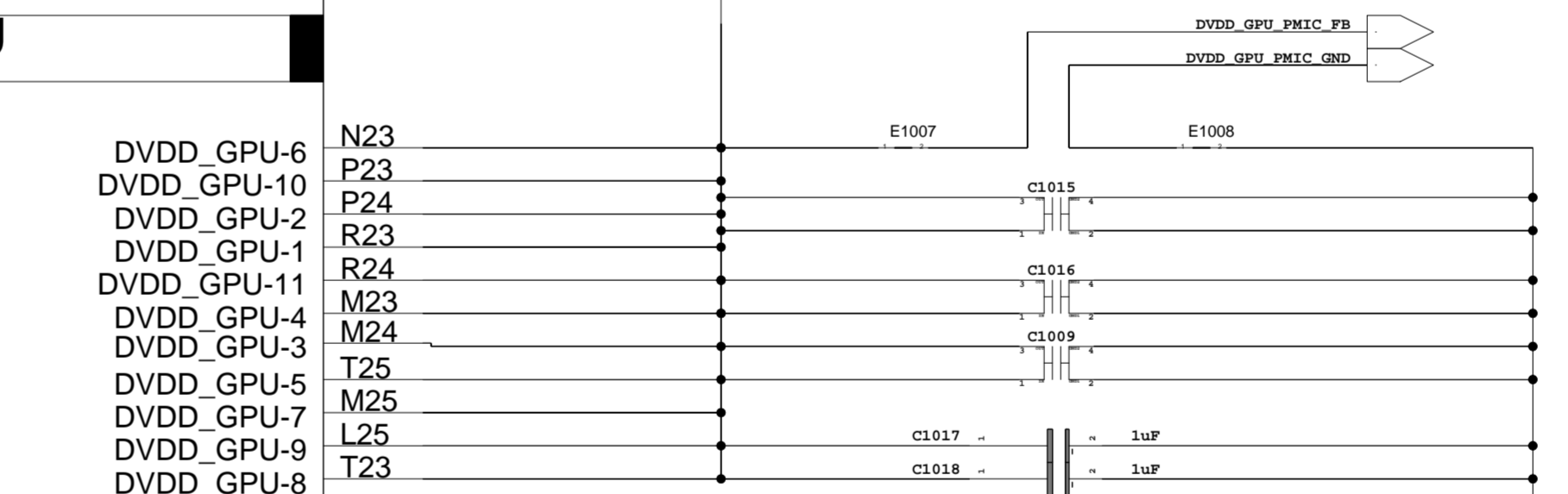
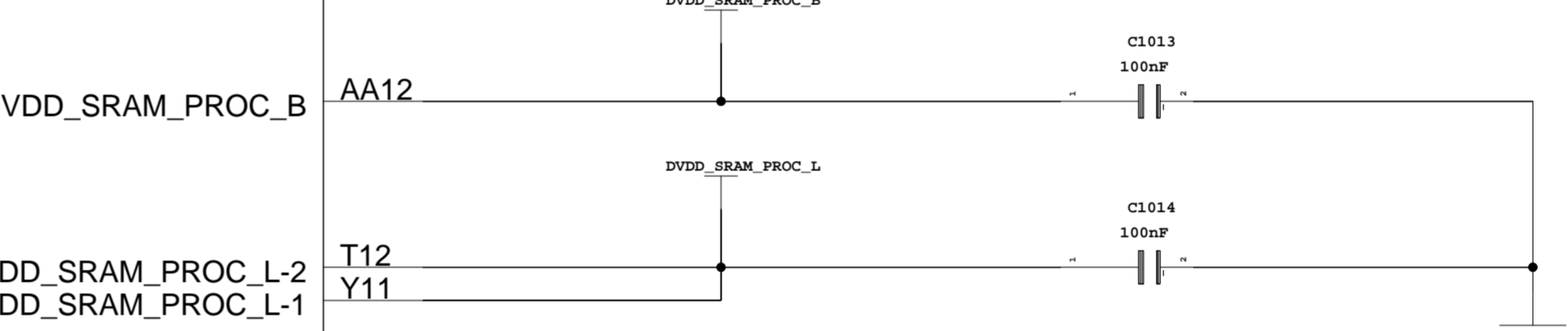
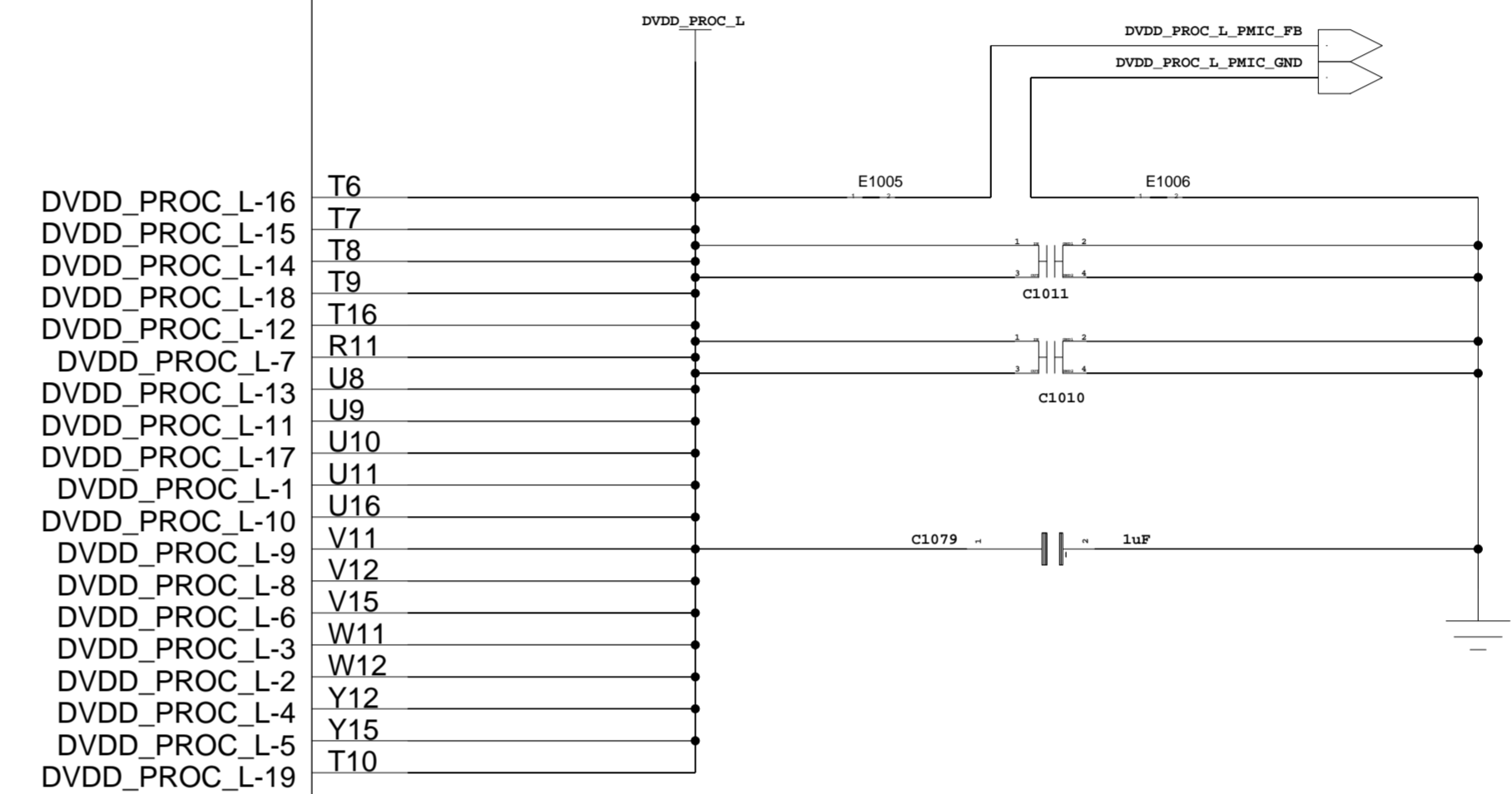
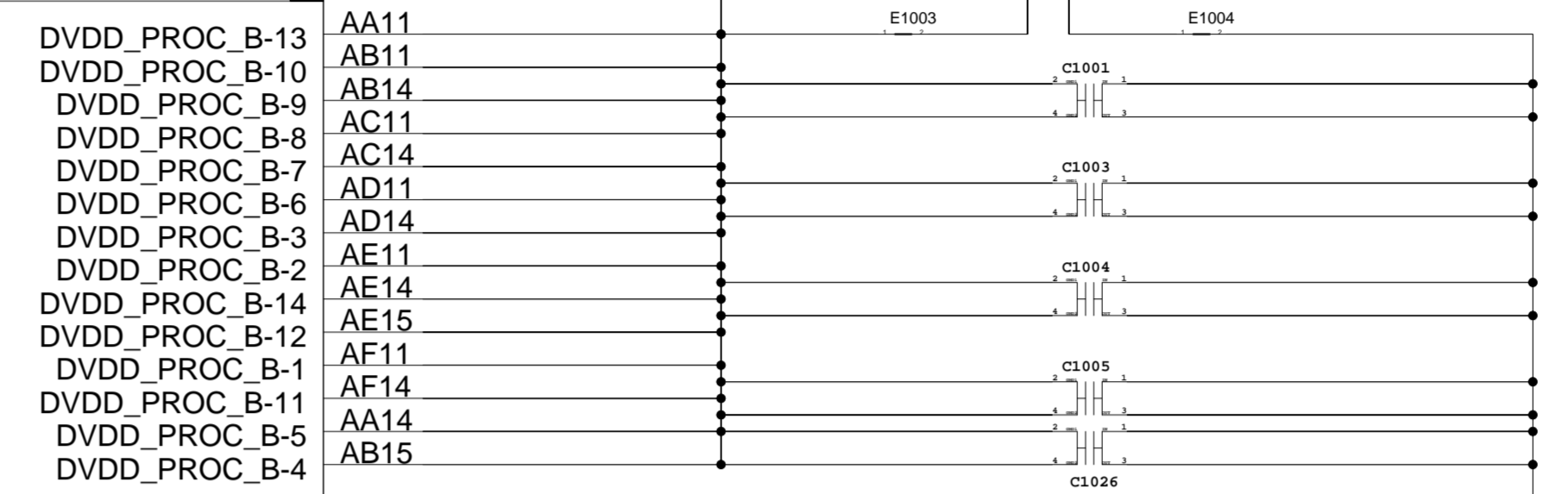
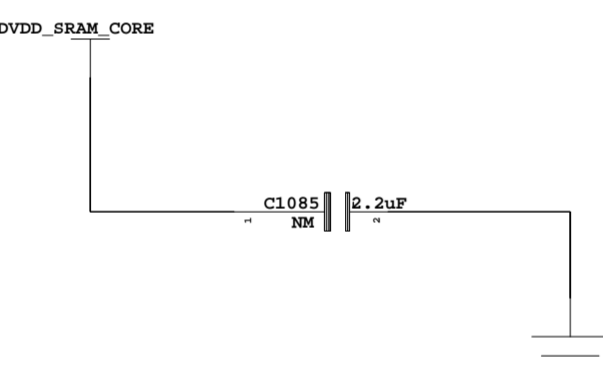
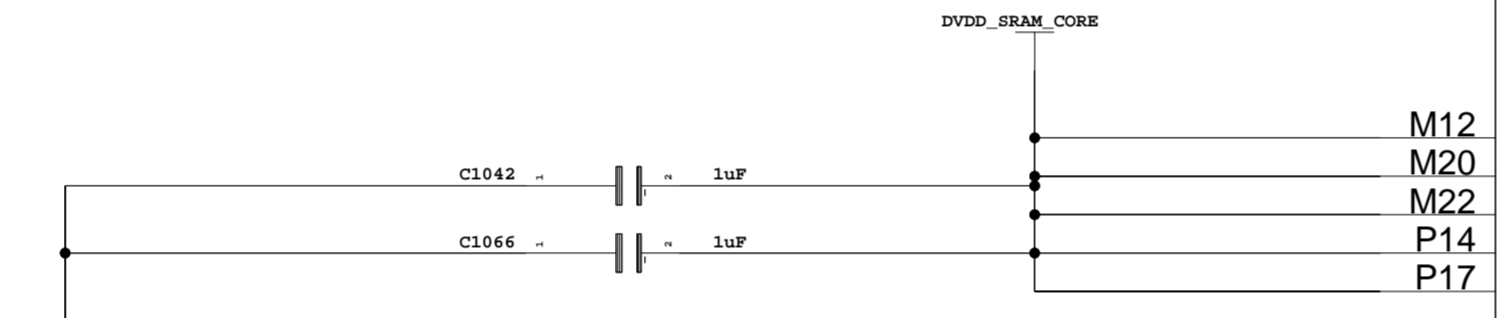
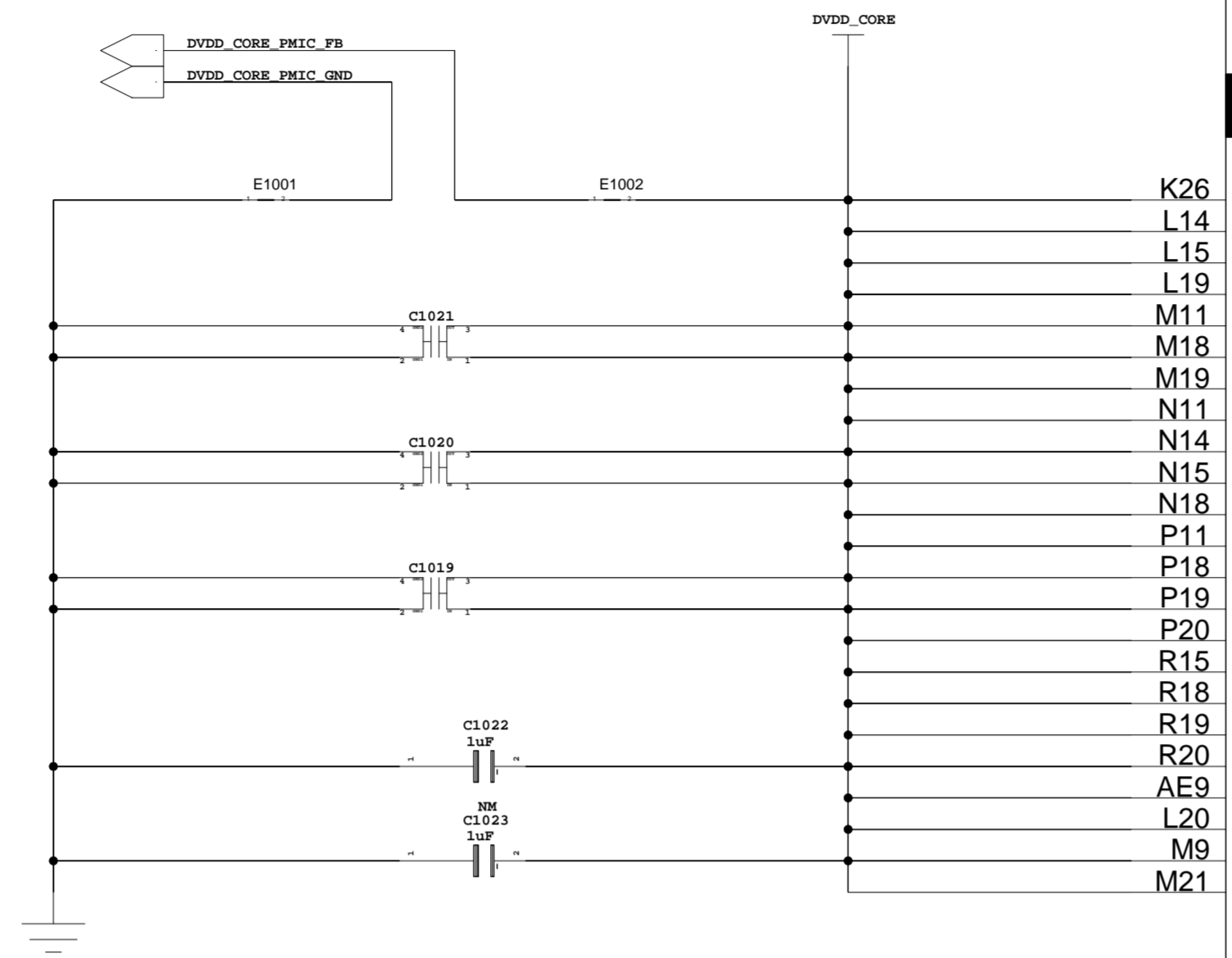
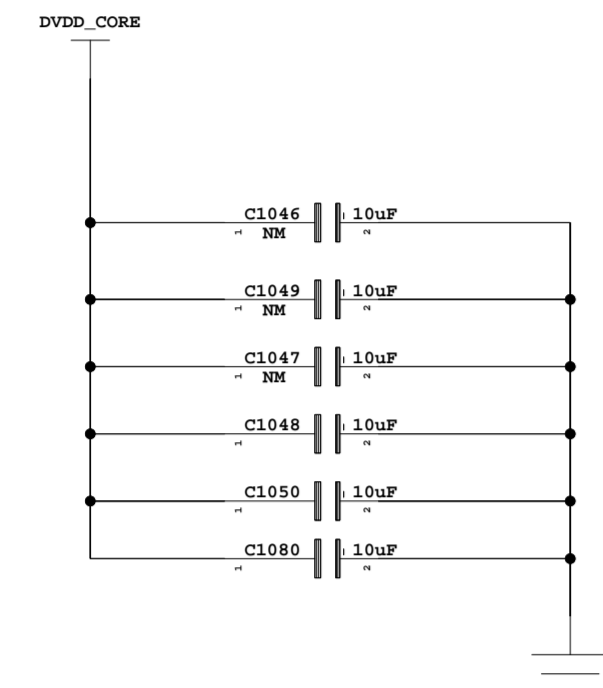


MT6835-MT6755

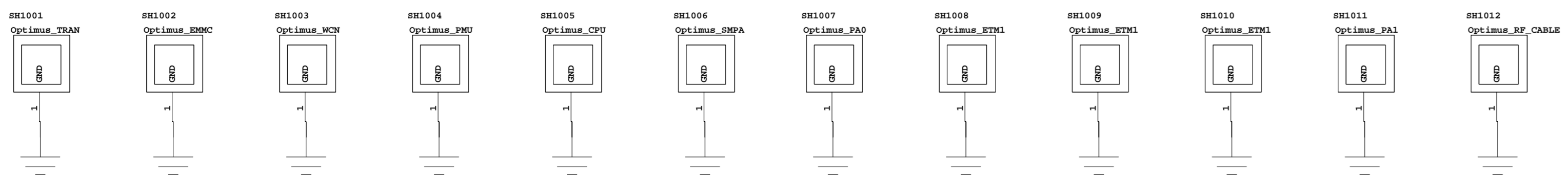
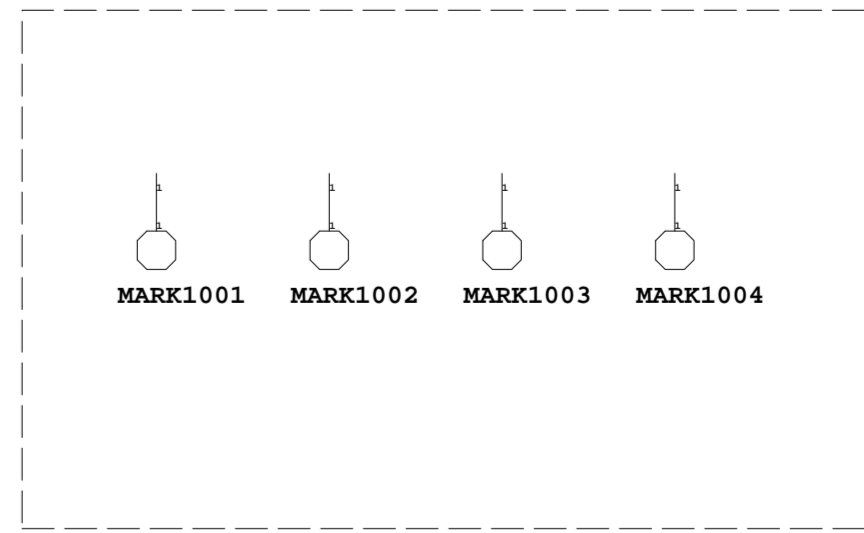
U1001
MT6835V/ZA

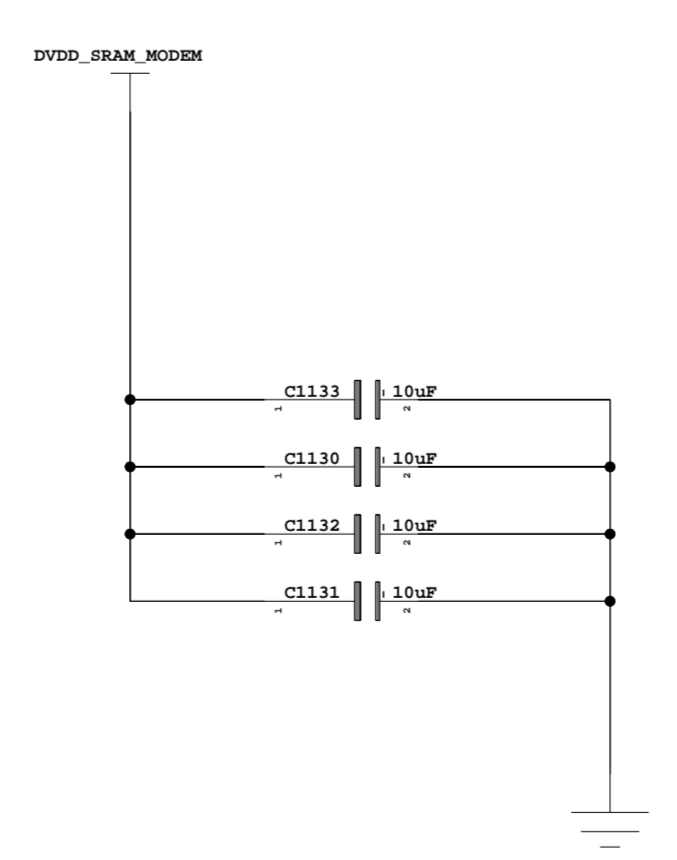
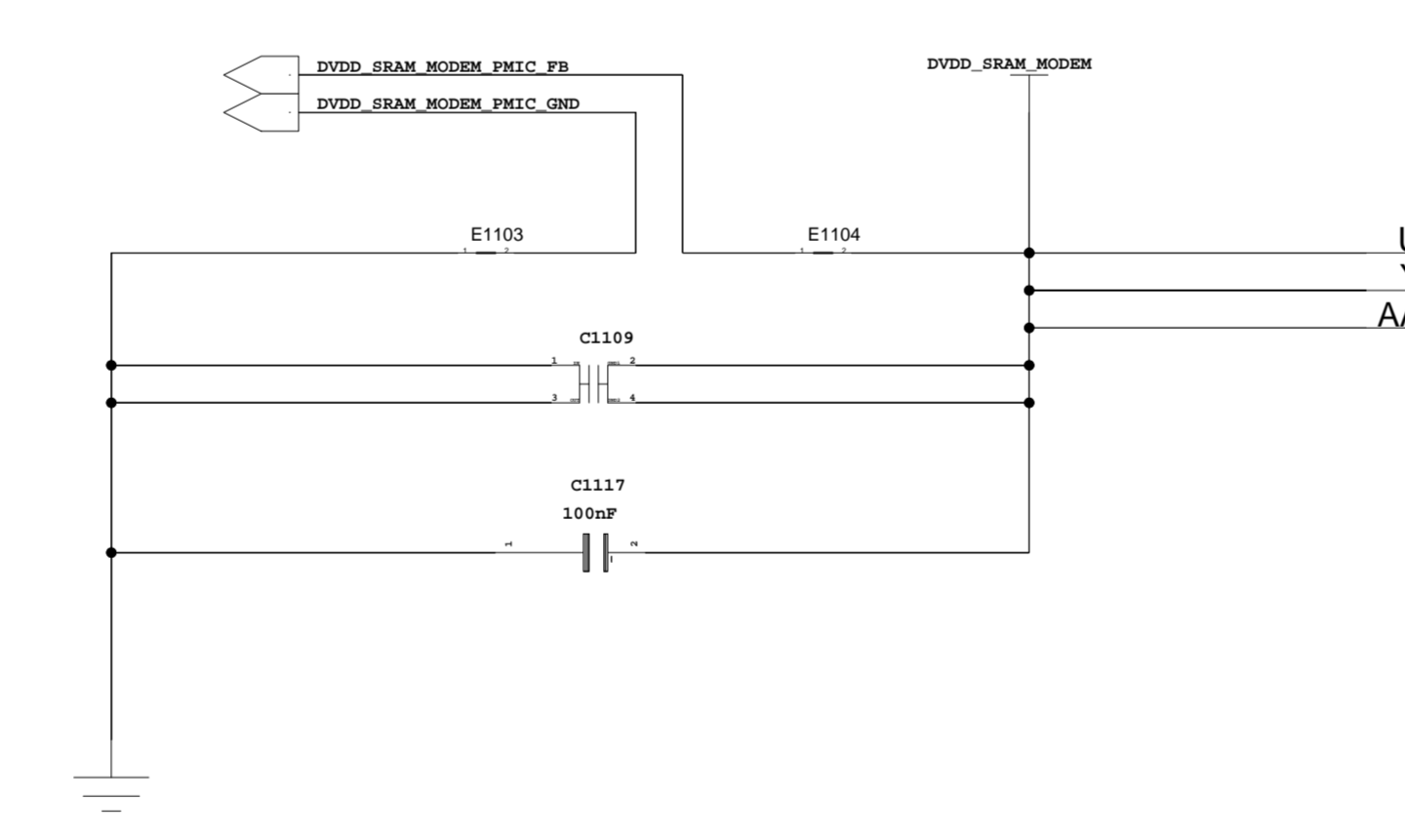
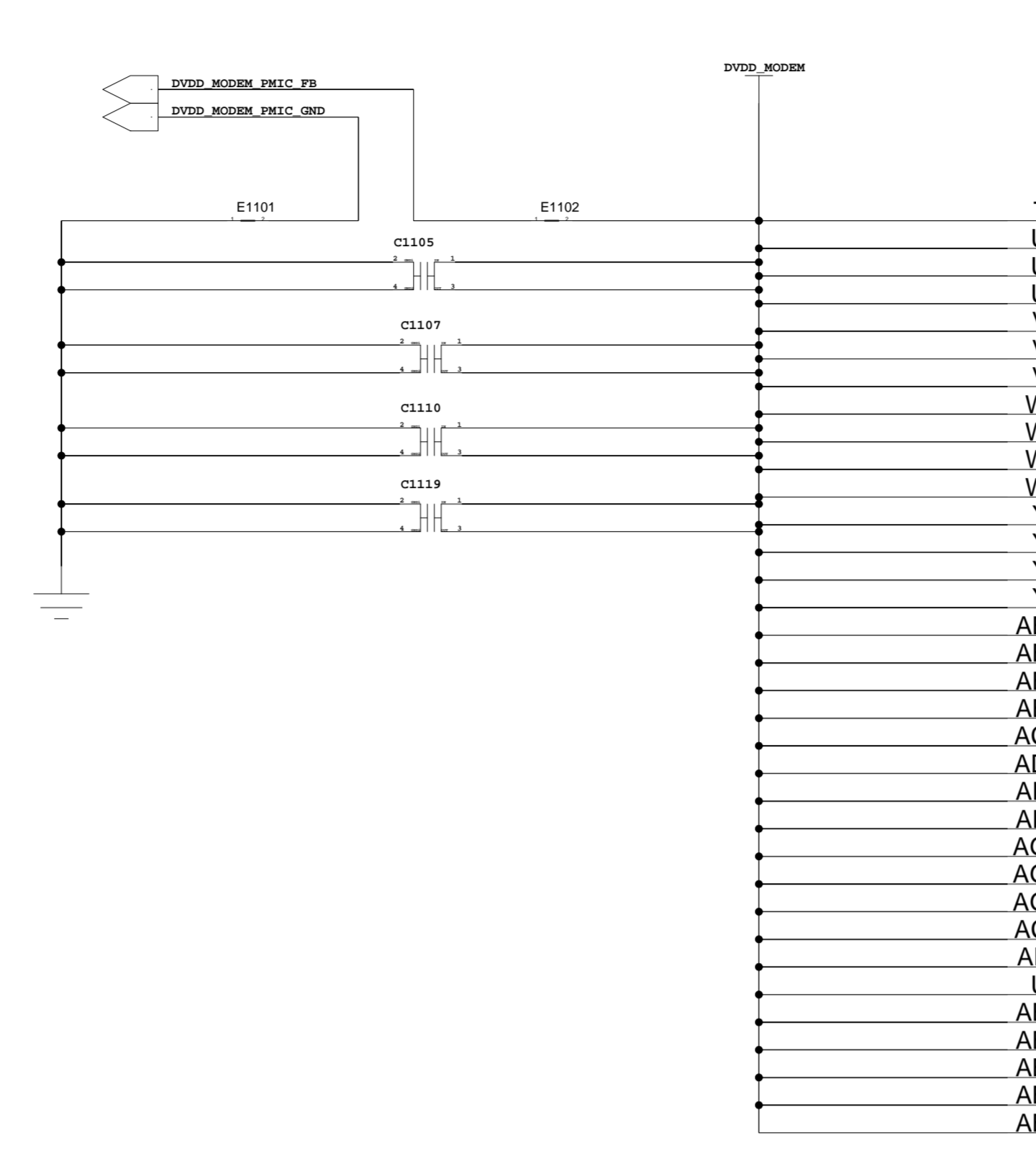
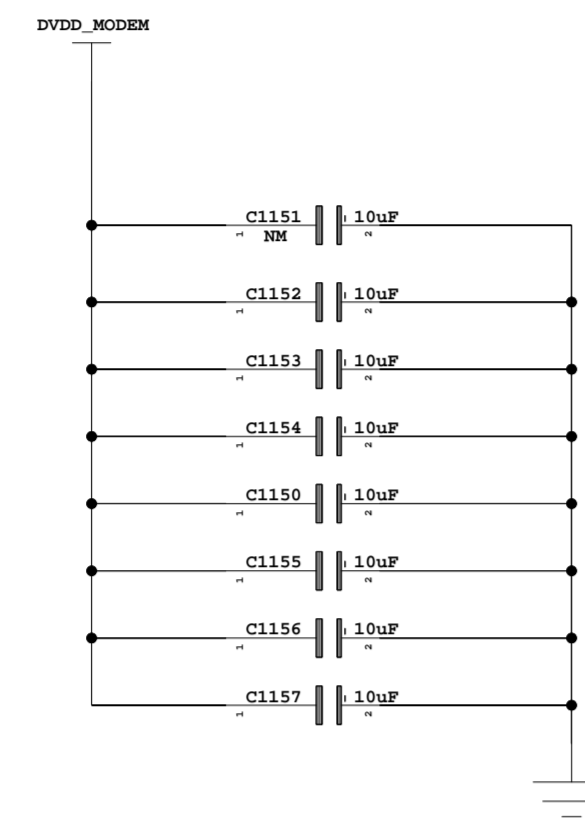
MT6835

CORE PROC_B/L

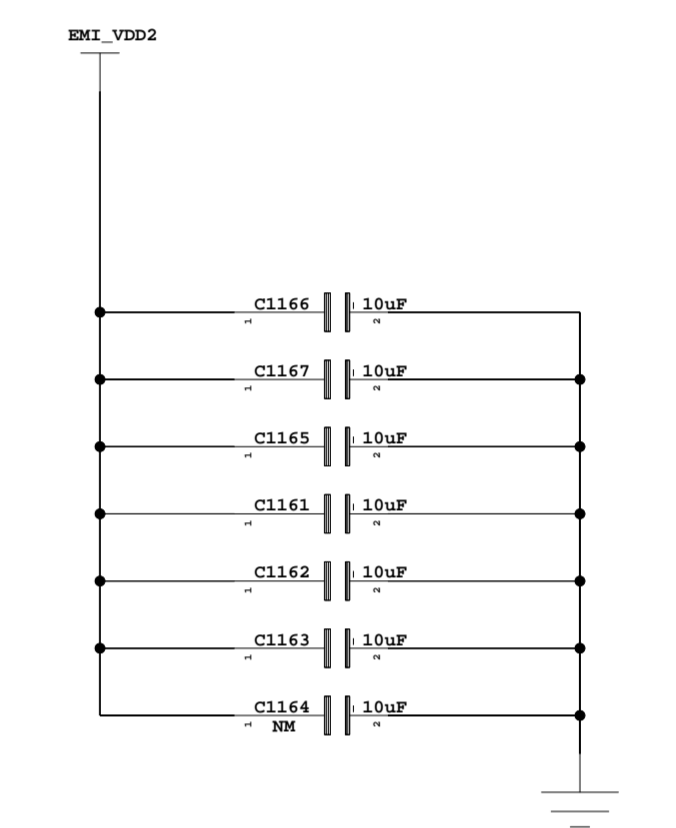
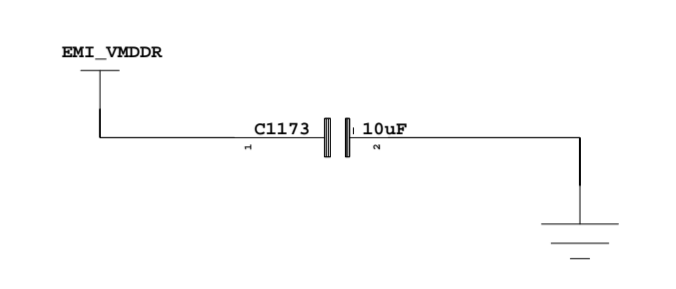
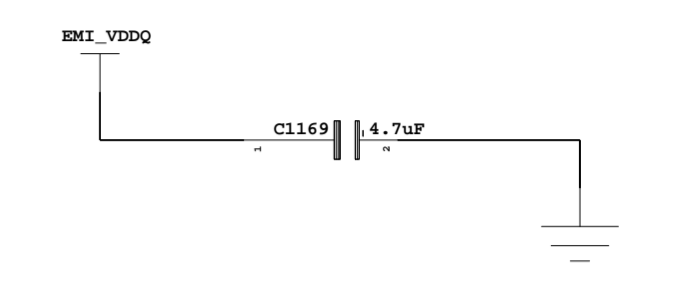
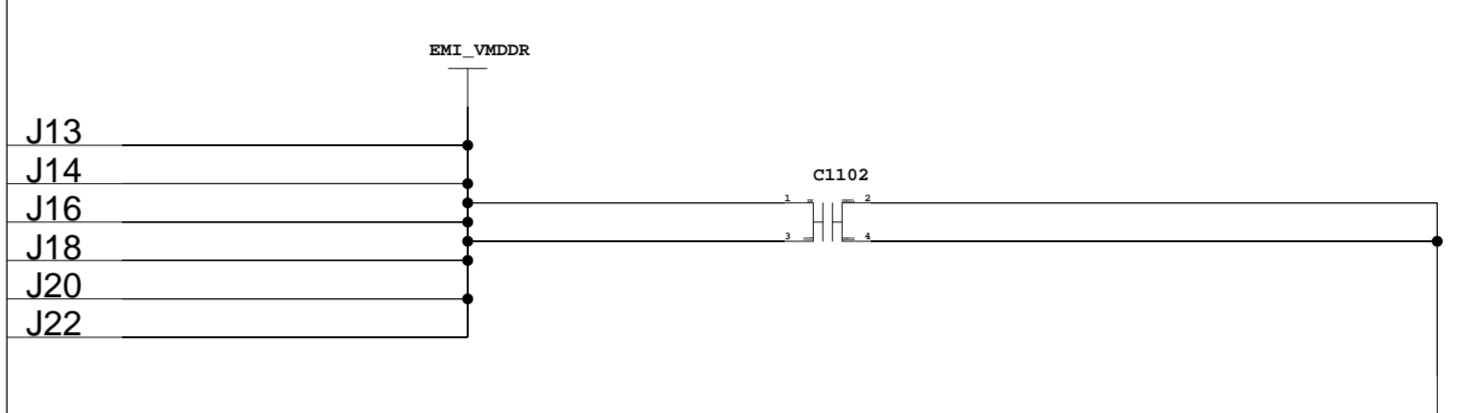
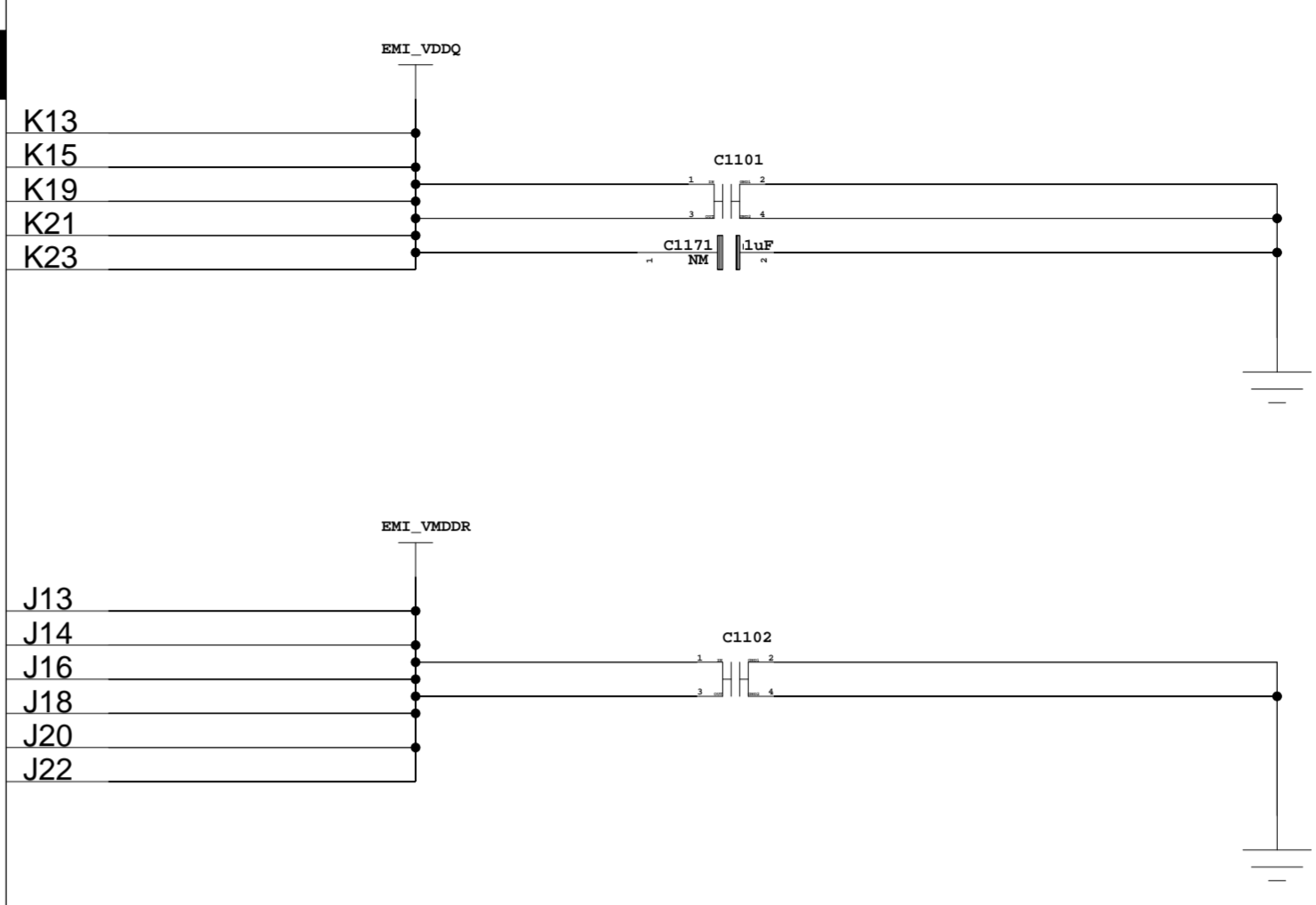


MARK

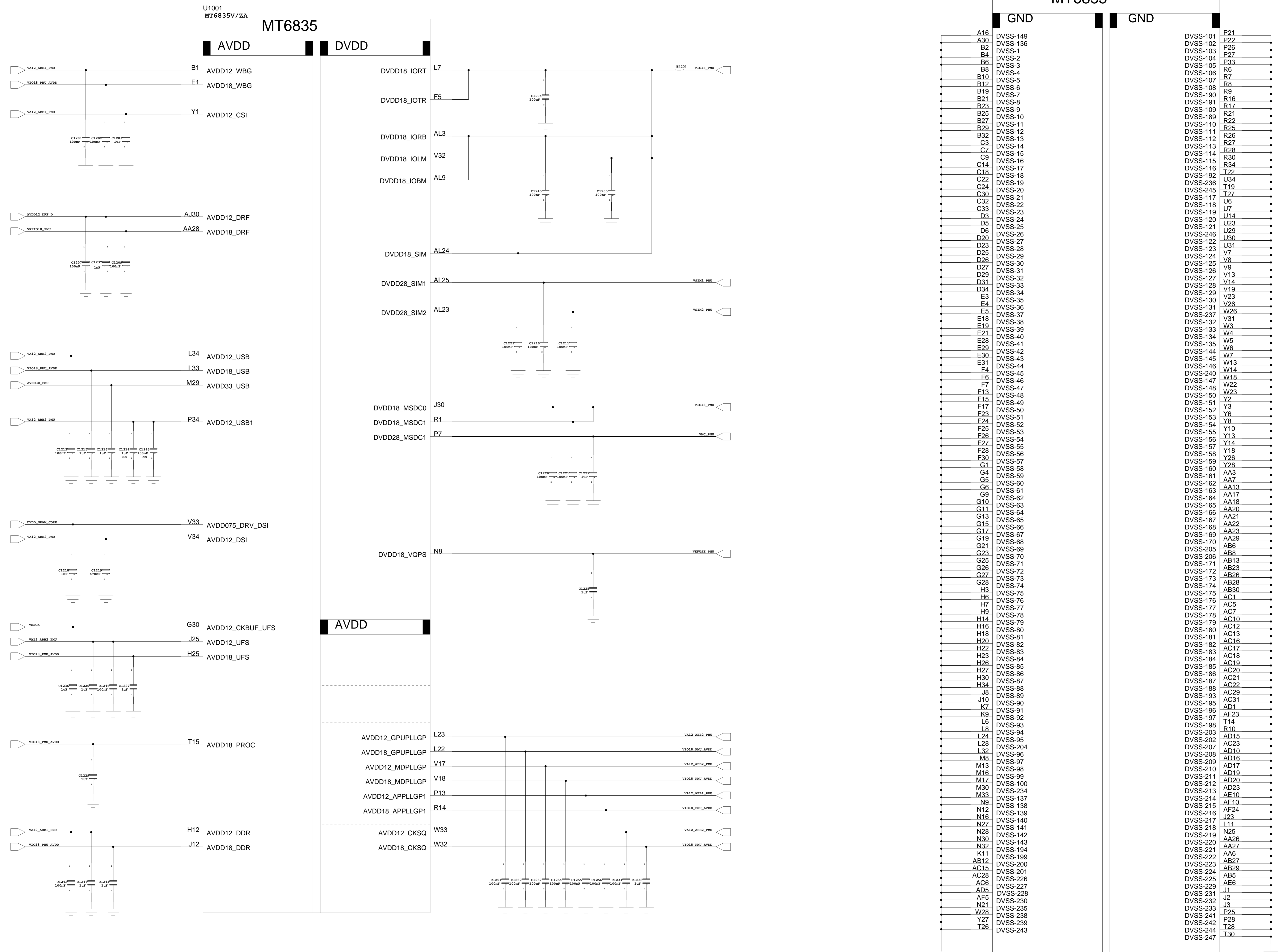




U1001
MT6835V/ZA

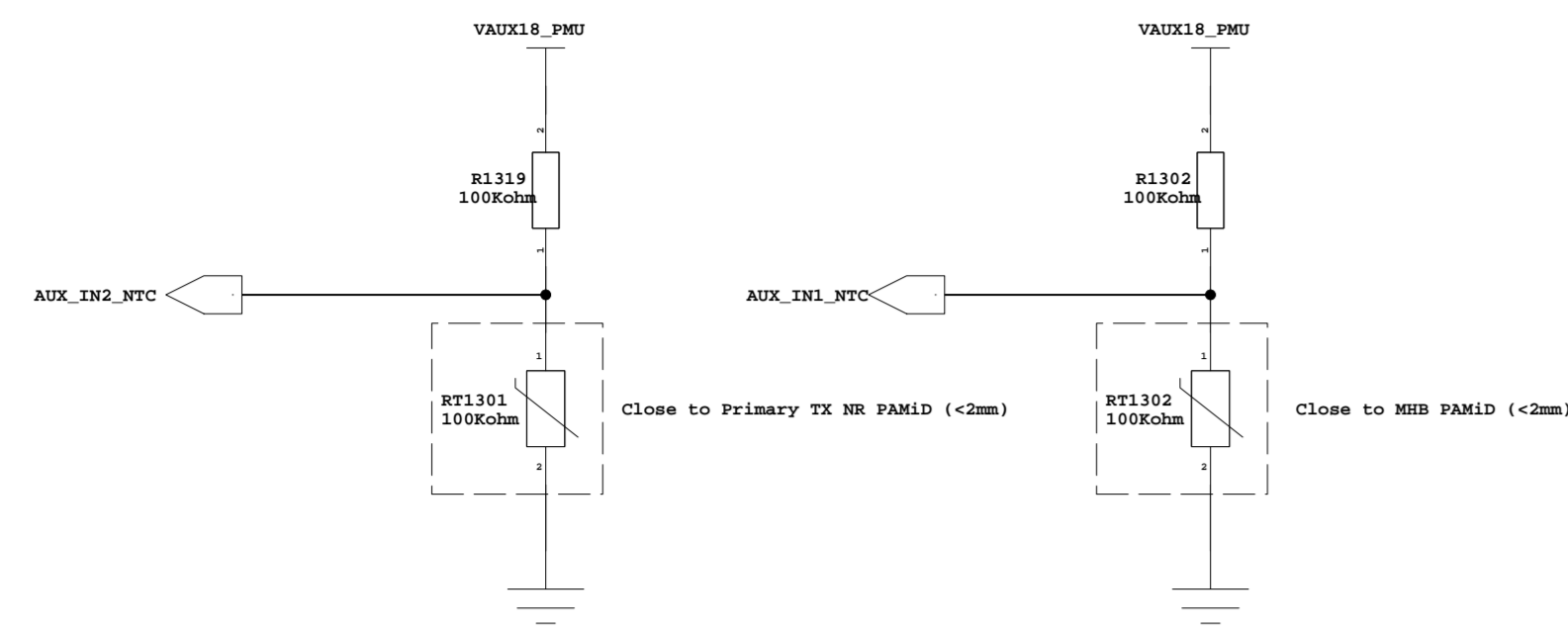


GND

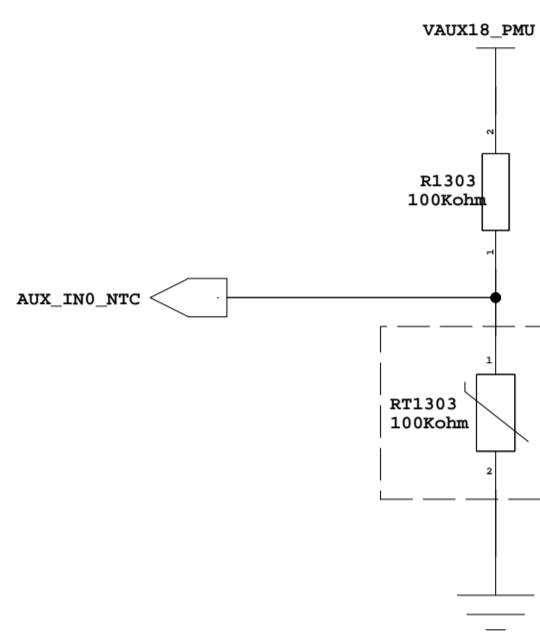


GND		GND	
A16	DVSS-149	P21	DVSS-101
A30	DVSS-136	P22	DVSS-102
B2	DVSS-103	P26	DVSS-103
B4	DVSS-2	P27	DVSS-104
B6	DVSS-3	P33	DVSS-105
B8	DVSS-4	R6	DVSS-106
B10	DVSS-5	R7	DVSS-107
B12	DVSS-6	R8	DVSS-108
B19	DVSS-7	R9	DVSS-109
B21	DVSS-8	R16	DVSS-110
B23	DVSS-9	R17	DVSS-111
B25	DVSS-10	R21	DVSS-112
B27	DVSS-11	R22	DVSS-113
B29	DVSS-12	R25	DVSS-114
B32	DVSS-13	R26	DVSS-115
C3	DVSS-14	R27	DVSS-116
C7	DVSS-15	R28	DVSS-117
C9	DVSS-16	R30	DVSS-118
C14	DVSS-17	R34	DVSS-119
C18	DVSS-18	T22	DVSS-120
C22	DVSS-19	T27	DVSS-121
C24	DVSS-20	T28	DVSS-122
C30	DVSS-21	T29	DVSS-123
C32	DVSS-22	U6	DVSS-124
C33	DVSS-23	U7	DVSS-125
D3	DVSS-24	U14	DVSS-126
D5	DVSS-25	U23	DVSS-127
D6	DVSS-26	U29	DVSS-128
D20	DVSS-27	U30	DVSS-129
D23	DVSS-28	U31	DVSS-130
D25	DVSS-29	V7	DVSS-131
D26	DVSS-30	V8	DVSS-132
D27	DVSS-31	V9	DVSS-133
D29	DVSS-32	V13	DVSS-134
D31	DVSS-33	V14	DVSS-135
D34	DVSS-34	V23	DVSS-136
E3	DVSS-35	V26	DVSS-137
E4	DVSS-36	V26	DVSS-138
E5	DVSS-37	W26	DVSS-139
E18	DVSS-38	V31	DVSS-140
E19	DVSS-39	W3	DVSS-141
E21	DVSS-40	W4	DVSS-142
E28	DVSS-41	W5	DVSS-143
E29	DVSS-42	W6	DVSS-144
E30	DVSS-43	W7	DVSS-145
E31	DVSS-44	W13	DVSS-146
F4	DVSS-45	W14	DVSS-147
F6	DVSS-46	W18	DVSS-148
F7	DVSS-47	W22	DVSS-149
F13	DVSS-48	Y2	DVSS-150
F15	DVSS-49	Y3	DVSS-151
F17	DVSS-50	Y6	DVSS-152
F23	DVSS-51	Y8	DVSS-153
F24	DVSS-52	Y10	DVSS-154
F25	DVSS-53	Y14	DVSS-155
F26	DVSS-54	Y18	DVSS-156
F27	DVSS-55	Y26	DVSS-157
F28	DVSS-56	Y28	DVSS-158
F30	DVSS-57	Y28	DVSS-159
G1	DVSS-58	AA3	DVSS-160
G4	DVSS-59	AA7	DVSS-161
G5	DVSS-60	AA13	DVSS-162
G6	DVSS-61	AA17	DVSS-163
G9	DVSS-62	AA18	DVSS-164
G10	DVSS-63	AA20	DVSS-165
G11	DVSS-64	AA21	DVSS-166
G13	DVSS-65	AA22	DVSS-167
G15	DVSS-66	AA23	DVSS-168
G17	DVSS-67	AA29	DVSS-169
G19	DVSS-68	AB6	DVSS-170
G21	DVSS-69	AB8	DVSS-171
G23	DVSS-70	AB13	DVSS-172
G25	DVSS-71	AB23	DVSS-173
G26	DVSS-72	AB28	DVSS-174
G27	DVSS-73	AB30	DVSS-175
G28	DVSS-74	AC1	DVSS-176
H3	DVSS-75	AC5	DVSS-177
H6	DVSS-76	AC7	DVSS-178
H7	DVSS-77	AC10	DVSS-179
H9	DVSS-78	AC12	DVSS-180
H14	DVSS-79	AC13	DVSS-181
H16	DVSS-80	AC16	DVSS-182
H18	DVSS-81	AC17	DVSS-183
H20	DVSS-82	AC18	DVSS-184
H22	DVSS-83	AC19	DVSS-185
H23	DVSS-84	AC20	DVSS-186
H26	DVSS-85	AC21	DVSS-187
H27	DVSS-86	AC22	DVSS-188
H30	DVSS-87	AD1	DVSS-189
H34	DVSS-88	AD19	DVSS-190
J8	DVSS-89	AD20	DVSS-191
J10	DVSS-90	AD23	DVSS-192
K7	DVSS-91	AD26	DVSS-193
K9	DVSS-92	AD27	DVSS-194
L6	DVSS-93	AD29	DVSS-195
L8	DVSS-94	AD31	DVSS-196
L24	DVSS-95	AD33	DVSS-197
L28	DVSS-96	AD35	DVSS-198
M8	DVSS-97	AD37	DVSS-199
M13	DVSS-98	AD39	DVSS-200
M16	DVSS-99	AD41	DVSS-201
M17	DVSS-100	AD43	DVSS-202
M30	DVSS-234	AD45	DVSS-203
M33	DVSS-137	AD47	DVSS-204
N9	DVSS-138	AD49	DVSS-205
N12	DVSS-139	AD51	DVSS-206
N16	DVSS-140	AD53	DVSS-207
N27	DVSS-141	AD55	DVSS-208
N28	DVSS-142	AD57	DVSS-209
N30	DVSS-143	AD59	DVSS-210
N32	DVSS-144	AD61	DVSS-211
N21	DVSS-145	AD63	DVSS-212
K11	DVSS-199	AD65	DVSS-213
AB12	DVSS-200	AD67	DVSS-214
AC15	DVSS-201	AD69	DVSS-215
AC28	DVSS-202	AD71	DVSS-216
AC6	DVSS-226	AD73	DVSS-217
AD5	DVSS-228	AD75	DVSS-218
AF5	DVSS-230	AD77	DVSS-219
N21	DVSS-233	AD79	DVSS-220
W28	DVSS-235	AD81	DVSS-221
Y27	DVSS-239	AD83	DVSS-222
T26	DVSS-243	AD85	DVSS-223
		AD87	DVSS-224
		AD89	DVSS-225
		AD91	DVSS-226
		AD93	DVSS-227
		AD95	DVSS-228
		AD97	DVSS-229
		AD99	DVSS-230
		AD101	DVSS-231
		AD103	DVSS-232
		AD105	DVSS-233
		AD107	DVSS-234
		AD109	DVSS-235
		AD111	DVSS-236
		AD113	DVSS-237
		AD115	DVSS-238
		AD117	DVSS-239
		AD119	DVSS-240
		AD121	DVSS-241
		AD123	DVSS-242
		AD125	DVSS-243

Thermistor to sense RF PA temperature



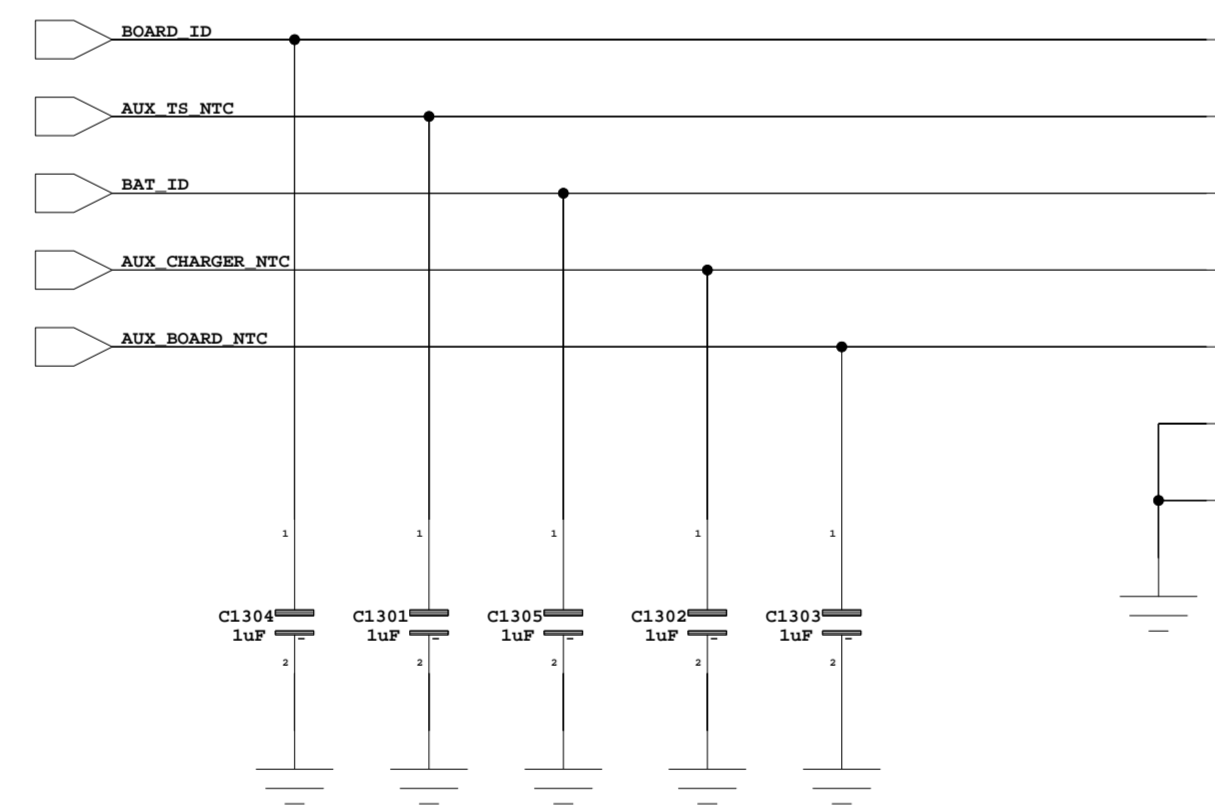
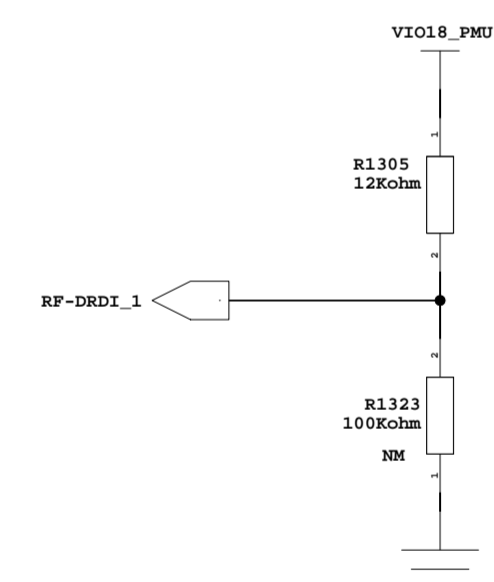
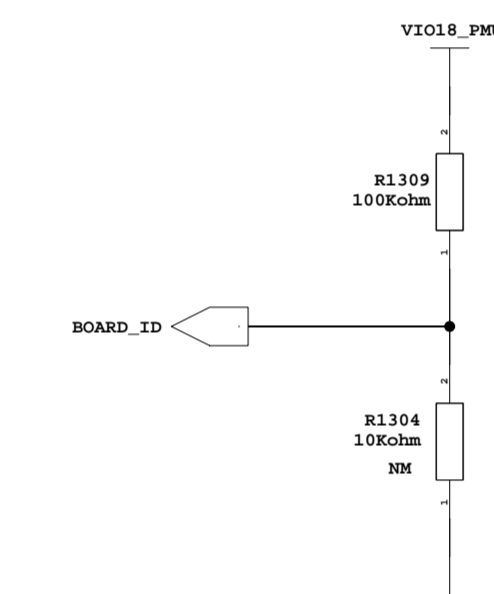
Thermistor to sense AP temperature



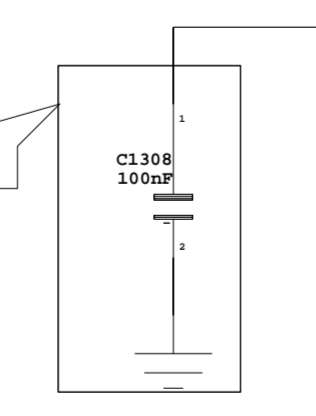
1. RT1303 must keep a distance about 6-8 mm away from AP and far from other heat sources 10 mm at least.
2. The distance is the shortest distance from package edge to edge.

Note 17-1: SW Default Configuration is as follows
 AUX_TX1_NTC is for AP (MT6835).
 AUX_TX1_NTC is for RF PA (MT6835).
 AUX_TX1_NTC is for RX PENCIL.
 AUX_TX1_NTC is for RX PENCIL.
 Note 17-2: If your design or placement is different with SW Default Config, please refer to "RT1303" in "Design Manual.docx" and modify SW setting.

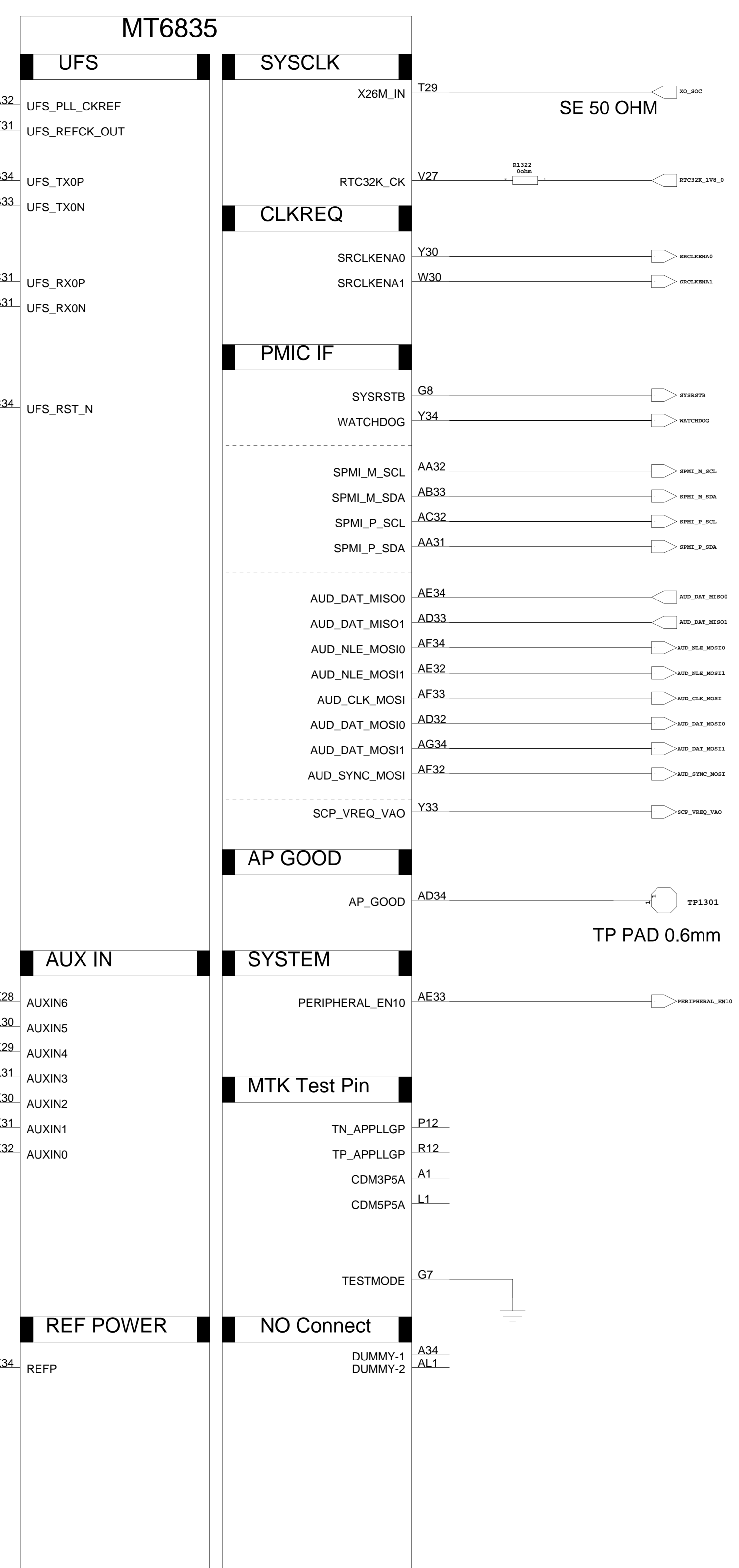
EVB	SKU1	RF-CRDL_1 Voltage	R1303	R1304
Phone	SKU1	LOW	0.05	NC
	SKU2	High	1.75	100K
	SKU3	High	0.21	750K
	SKU4	High	0.42	330K
	SKU5	High	0.62	191K
	SKU6	High	0.82	120K
	SKU7	High	1.04	100K
	SKU8	High	1.24	220K



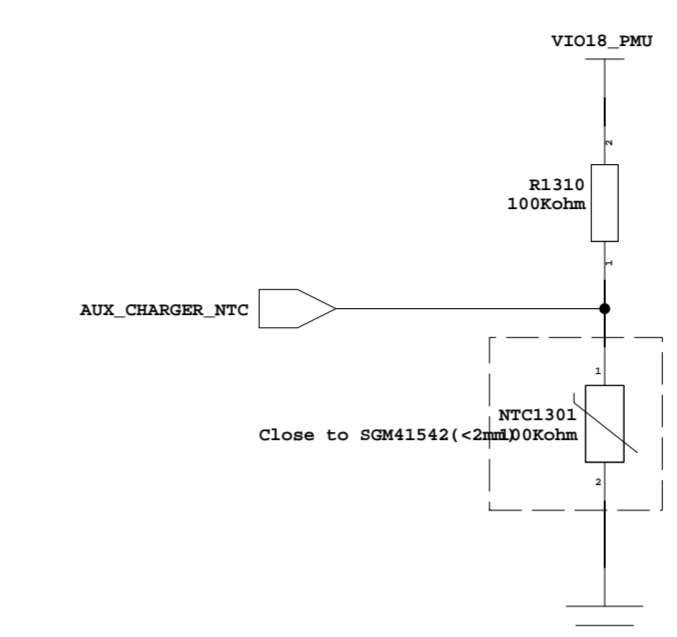
Note: 13-1



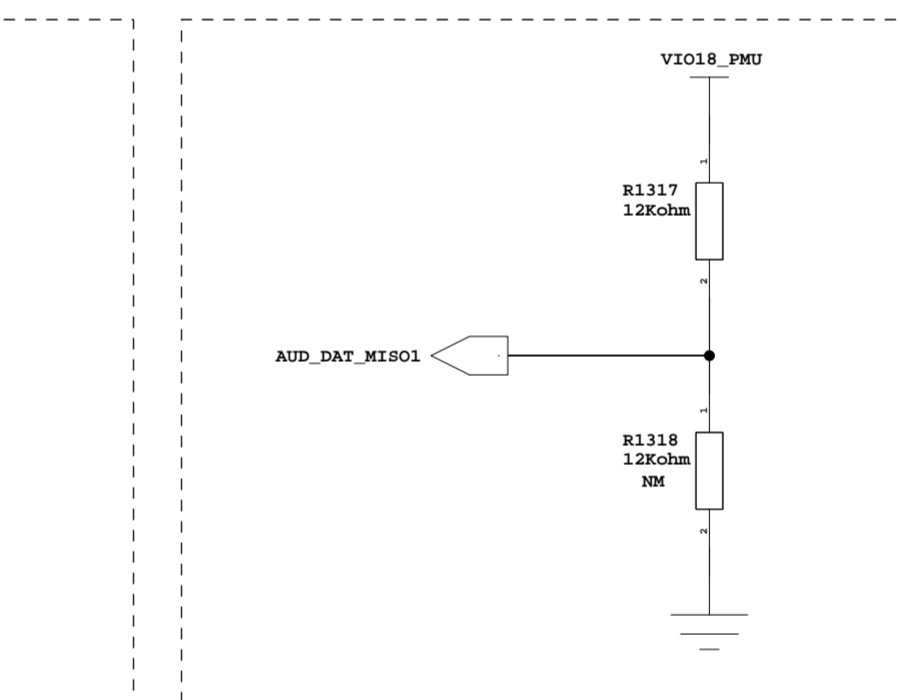
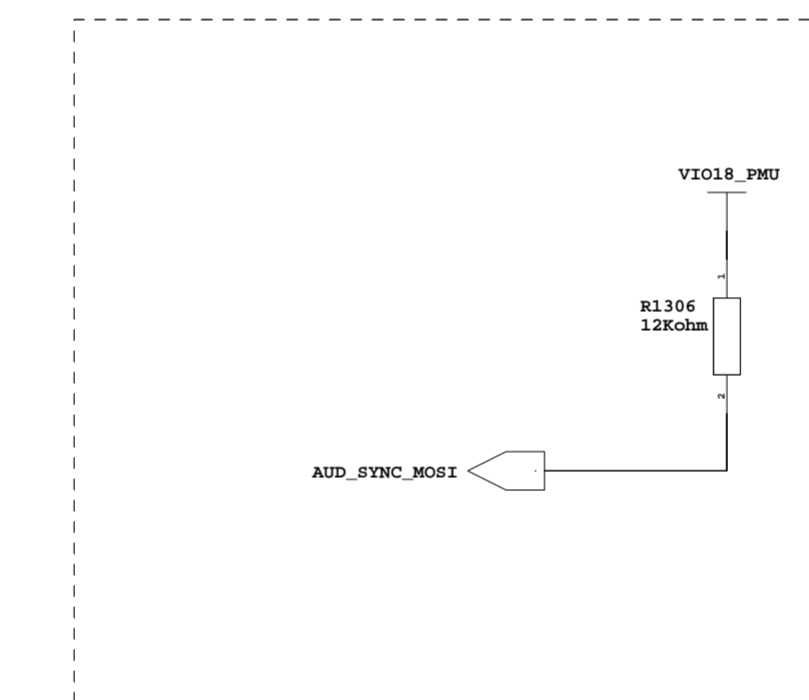
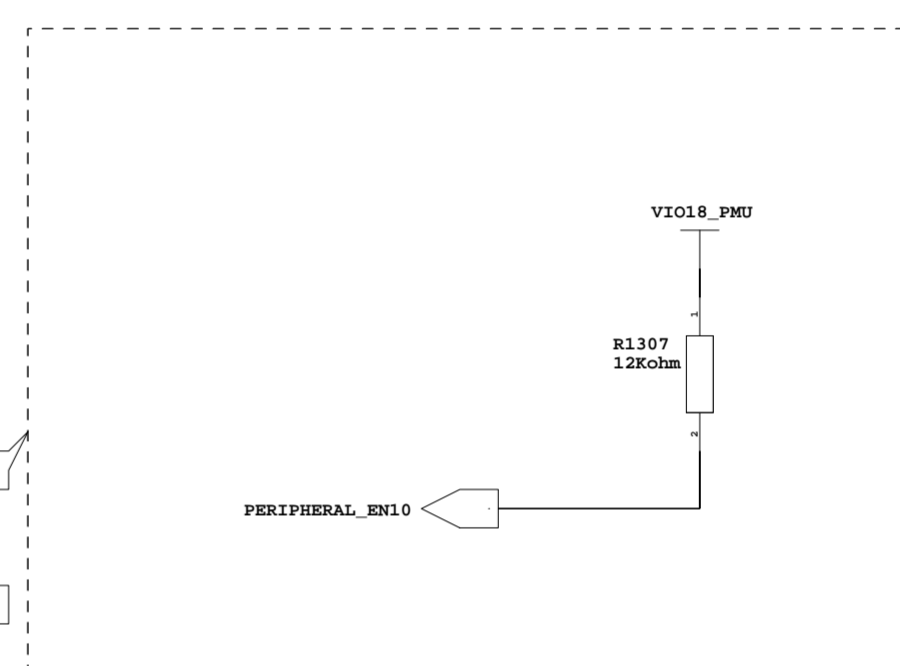
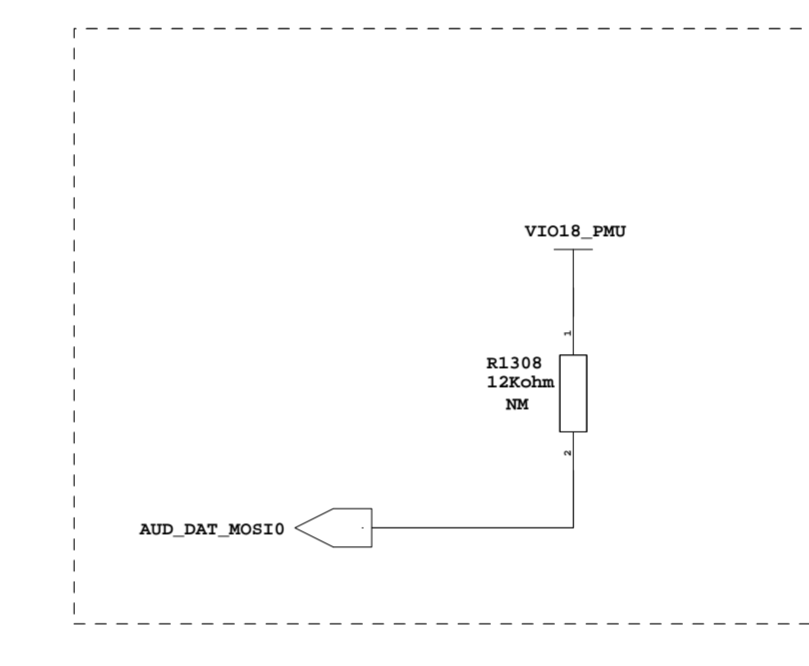
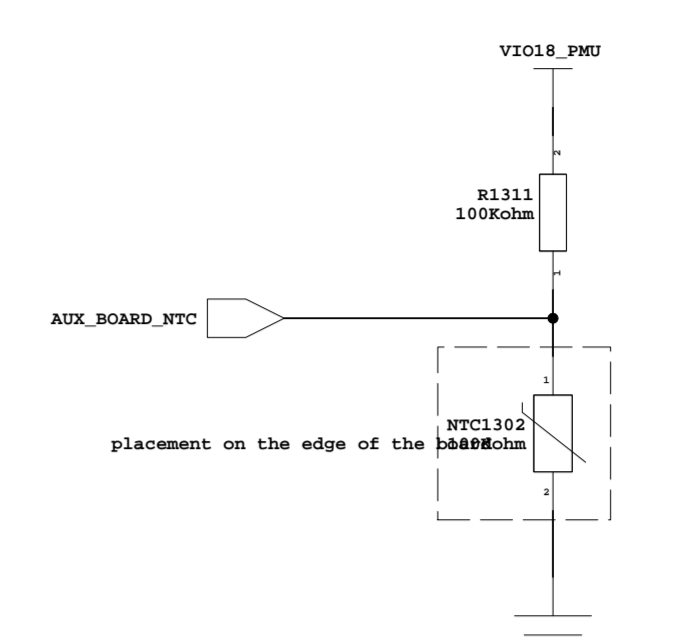
U1001
MT6835V/ZA



Thermistor to sense CHARGE temperature



Thermistor to sense BOARD temperature



Note 13-1: The load cap. have to be placed as close to REFP ball as possible.

Note 13-2: 'AUD_DAT_MOSI0' and 'AUD_DAT_MOSI1' pin features in strapping pin to enable JTAG.

AUD_DAT_MOSI0	AUD_DAT_MOSI1	AP JTAG	IO JTAG	DAP_SONIC/DAP_MD32
L (Default)	L (Default)	N/A	N/A	N/A
L (Default)	H (by external PU)	SPD, CSR, SPD, CLK, KPCOL1	N/A	N/A
H (by external PU)	L	SPD, CSR, SPD, CLK, KPCOL1	EINT[8-12]	N/A
H (by external PU)	H (by external PU)	N/A	N/A	N/A

Note 13-3: 'EINT1' and 'PERIPHERAL_EN10' pin features in strapping pin to booting (eMMC/UFS/Reserved).

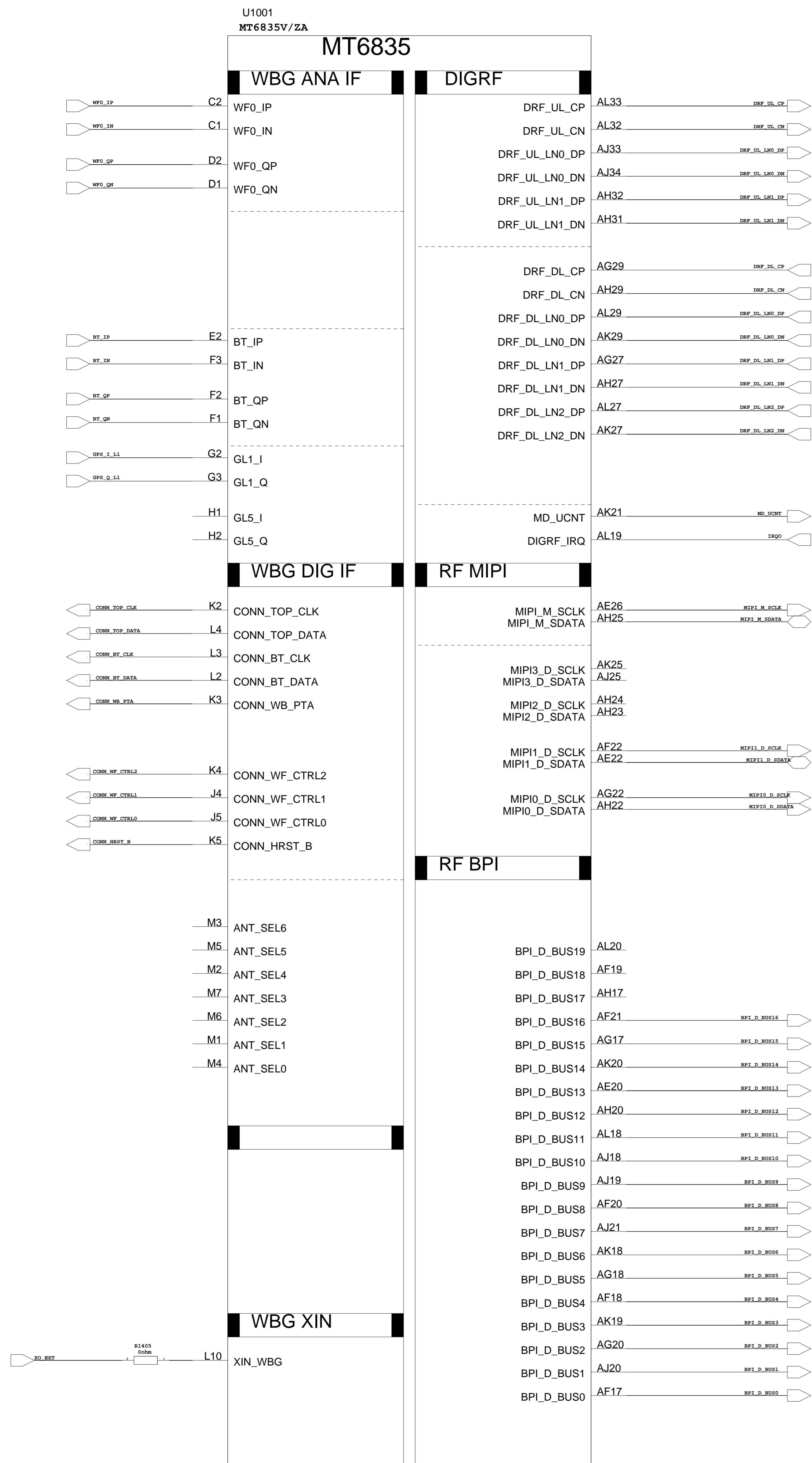
EINT1	PERIPHERAL_EN10	Storage Booting
L (Default)	L (Default)	Only UFS boot
L (Default)	H (by external PU)	Only eMMC boot
H (by external PU)	H (by external PU)	Reserved

Note 13-4: 'AUD_SYNC_MOSI' and HW strapping pin to enable DDR.

AUD_SYNC_MOSI	DDR
L (Default)	MCP
H (by external PU)	DSC

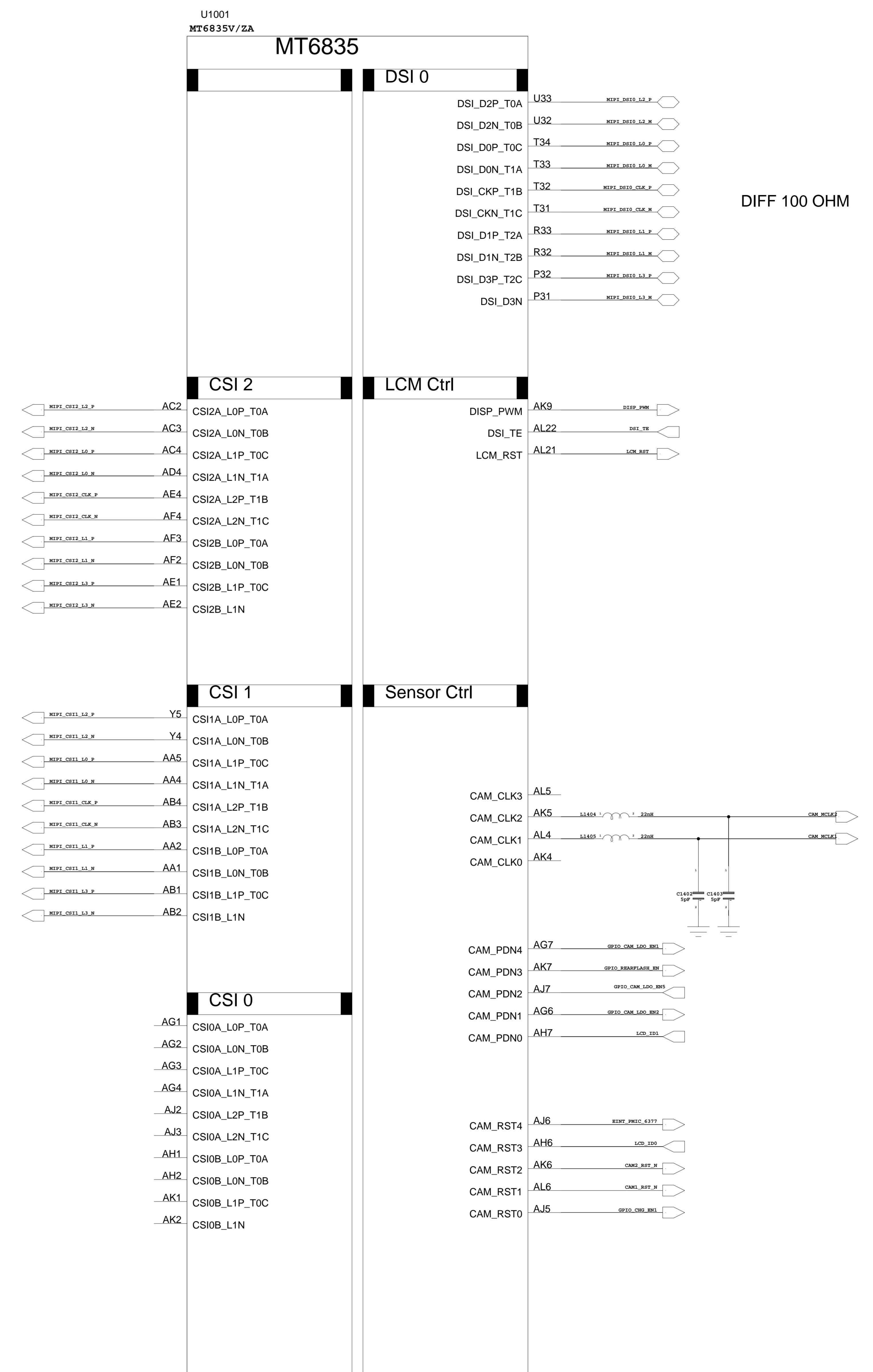
Note 13-5: 'AUD_DAT_MISO1' is MT6837 strapping pin to select VEMC Voltage.

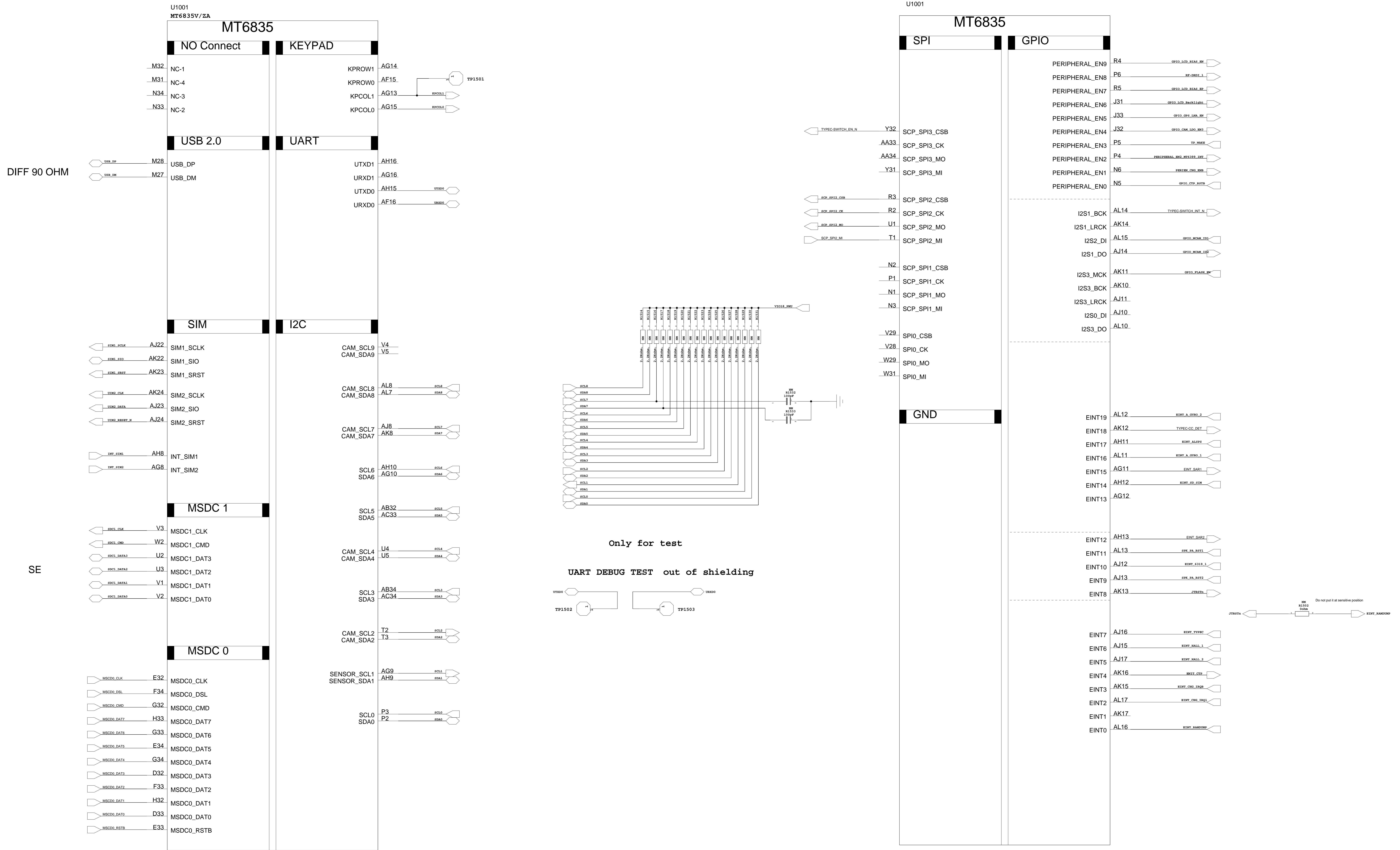
AUD_DAT_MISO1	VEMC	Application
L	2.5xV	UFS 3.1
H (by ext. PU)	3.0V	UFS 2.x



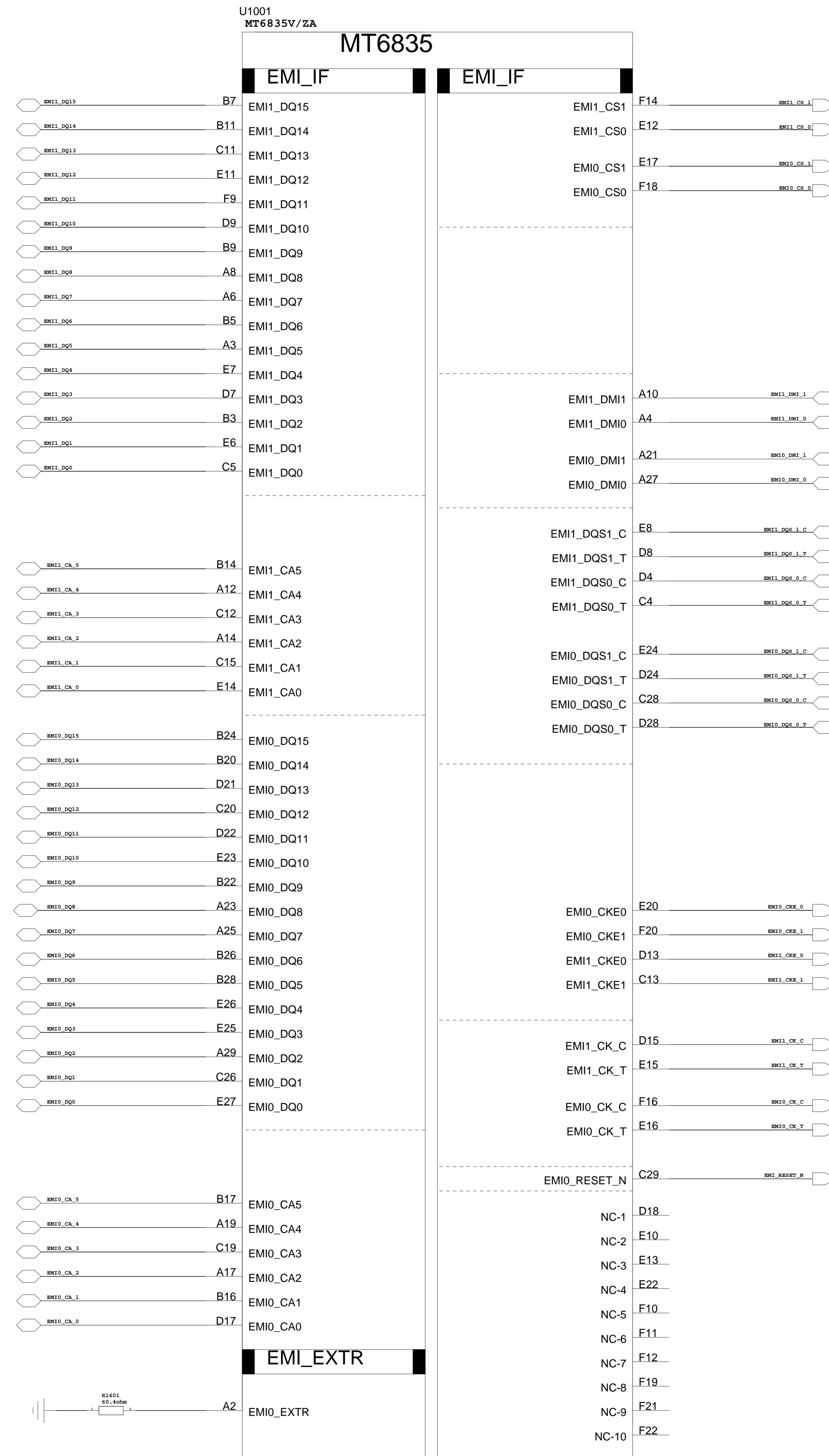
8M FRONT
DIFF90 OHM

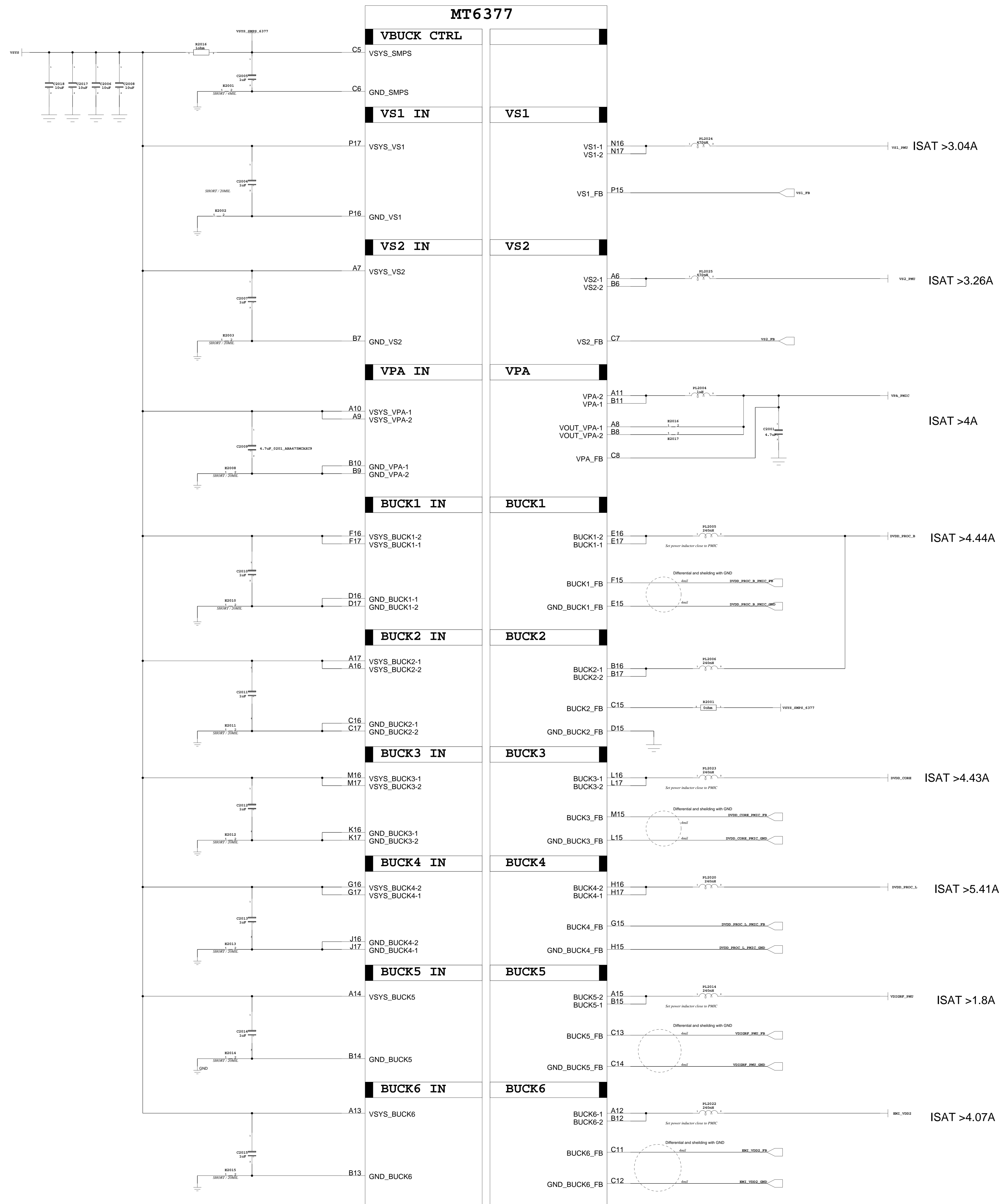
8M MAIN
DIFF90 OHM





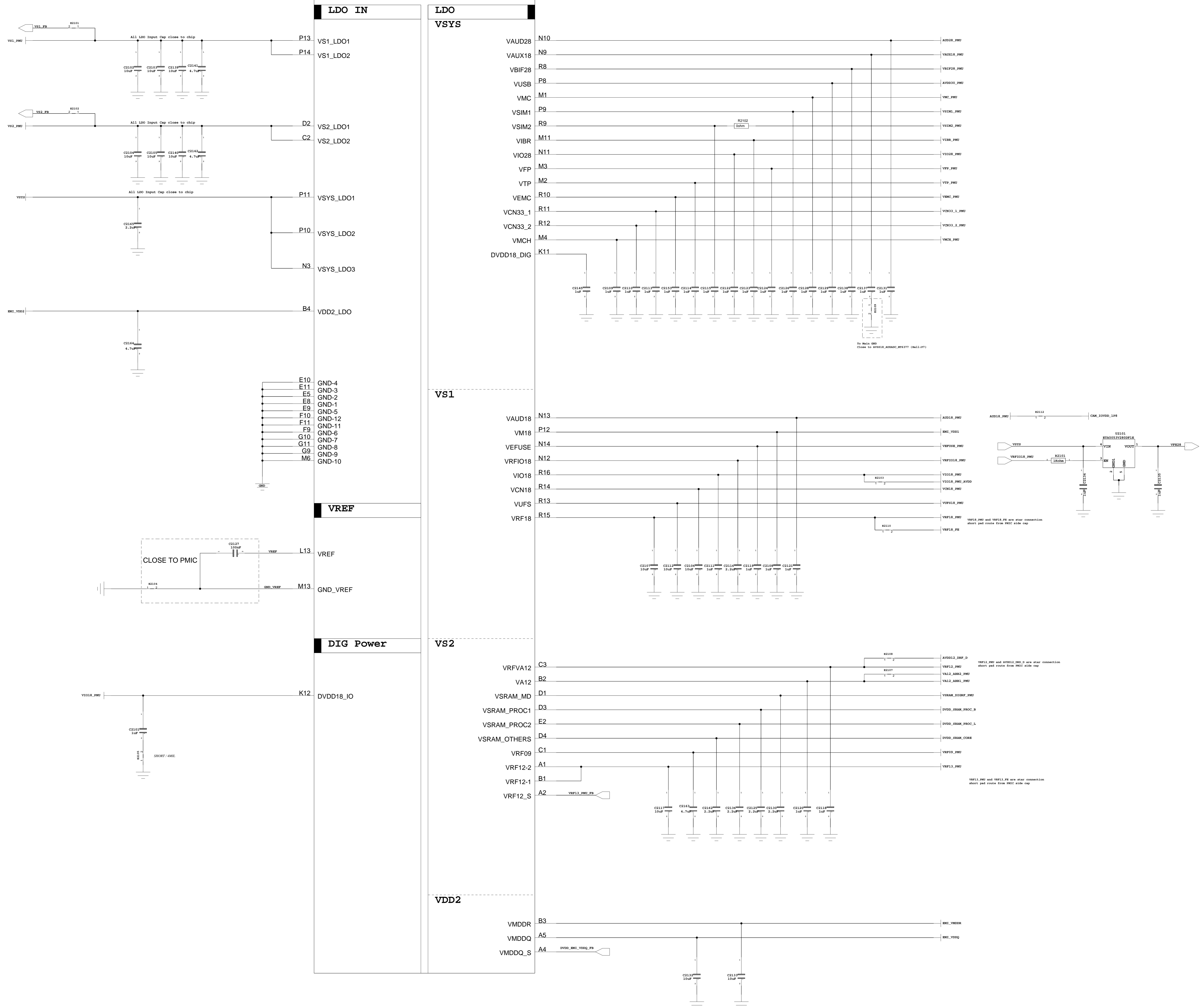
DSC memory	AMA0001209C1	next-c+
uMCP memory	AMA0001208C1	next-c



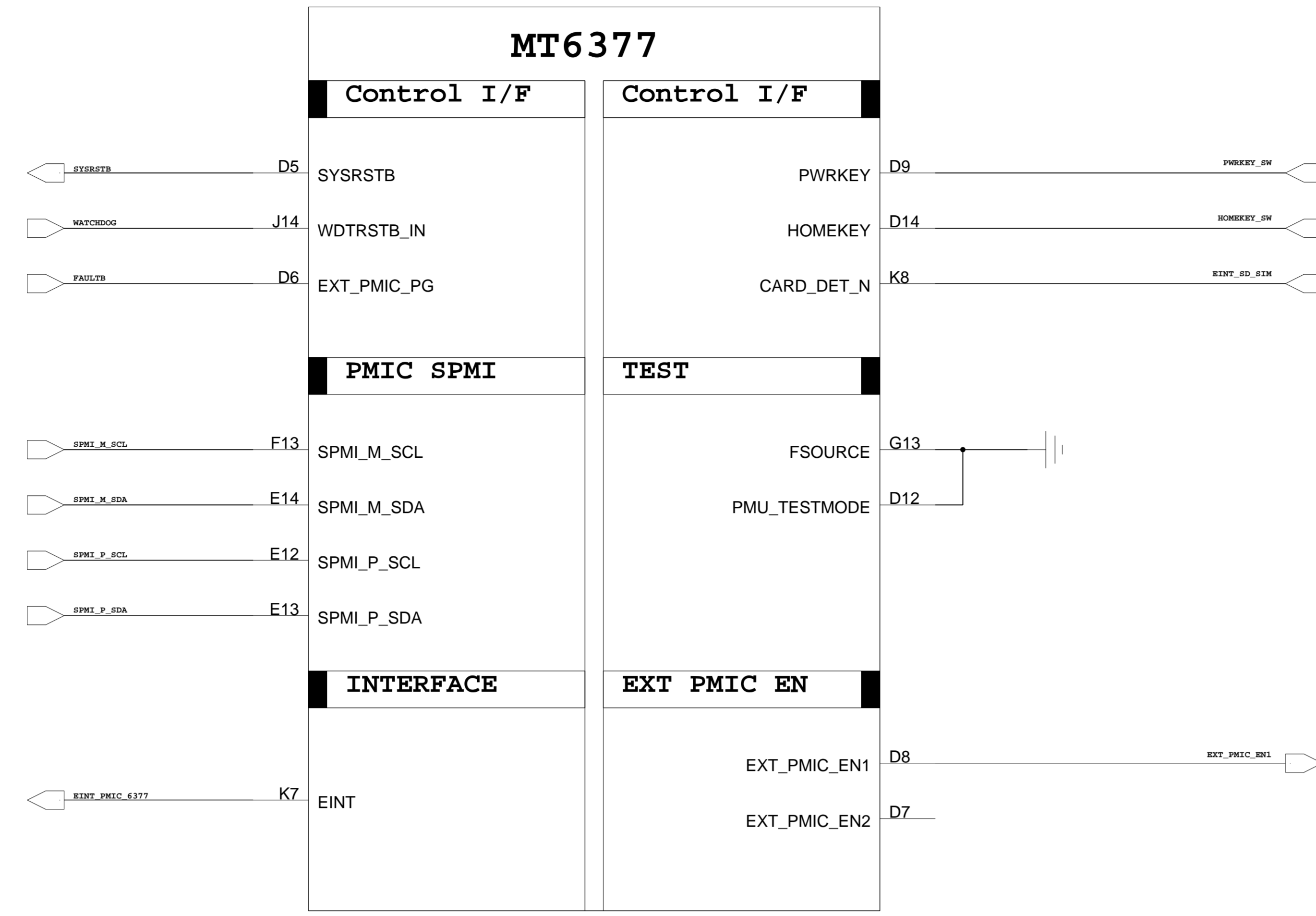


U2001
MT6377W/A

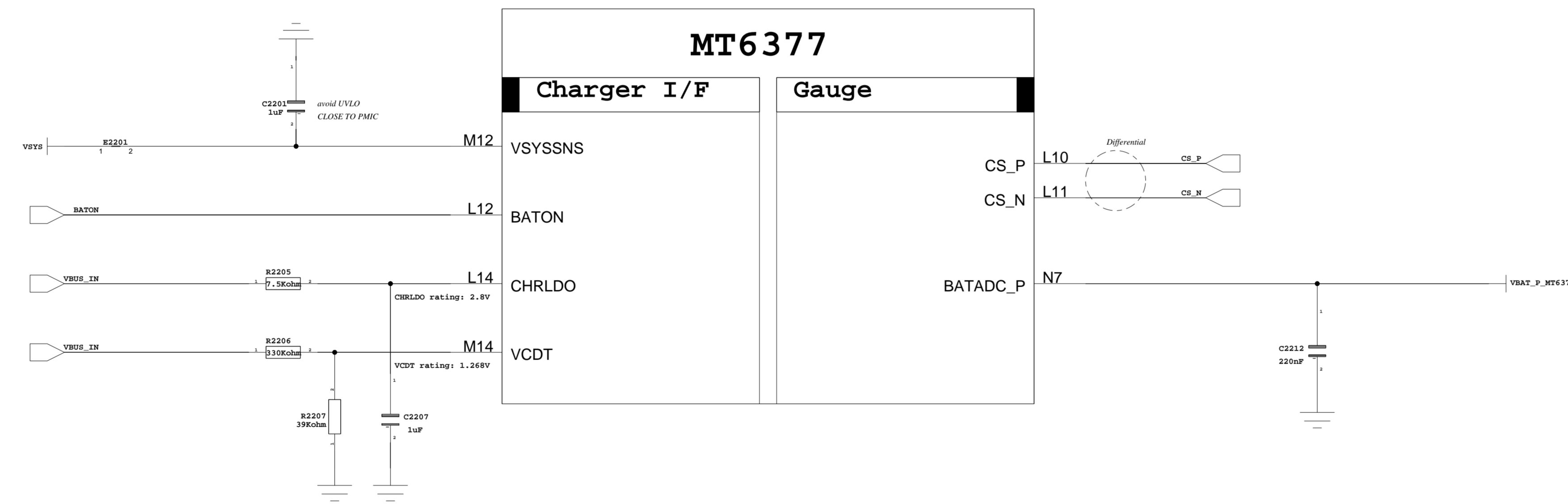
MT6377

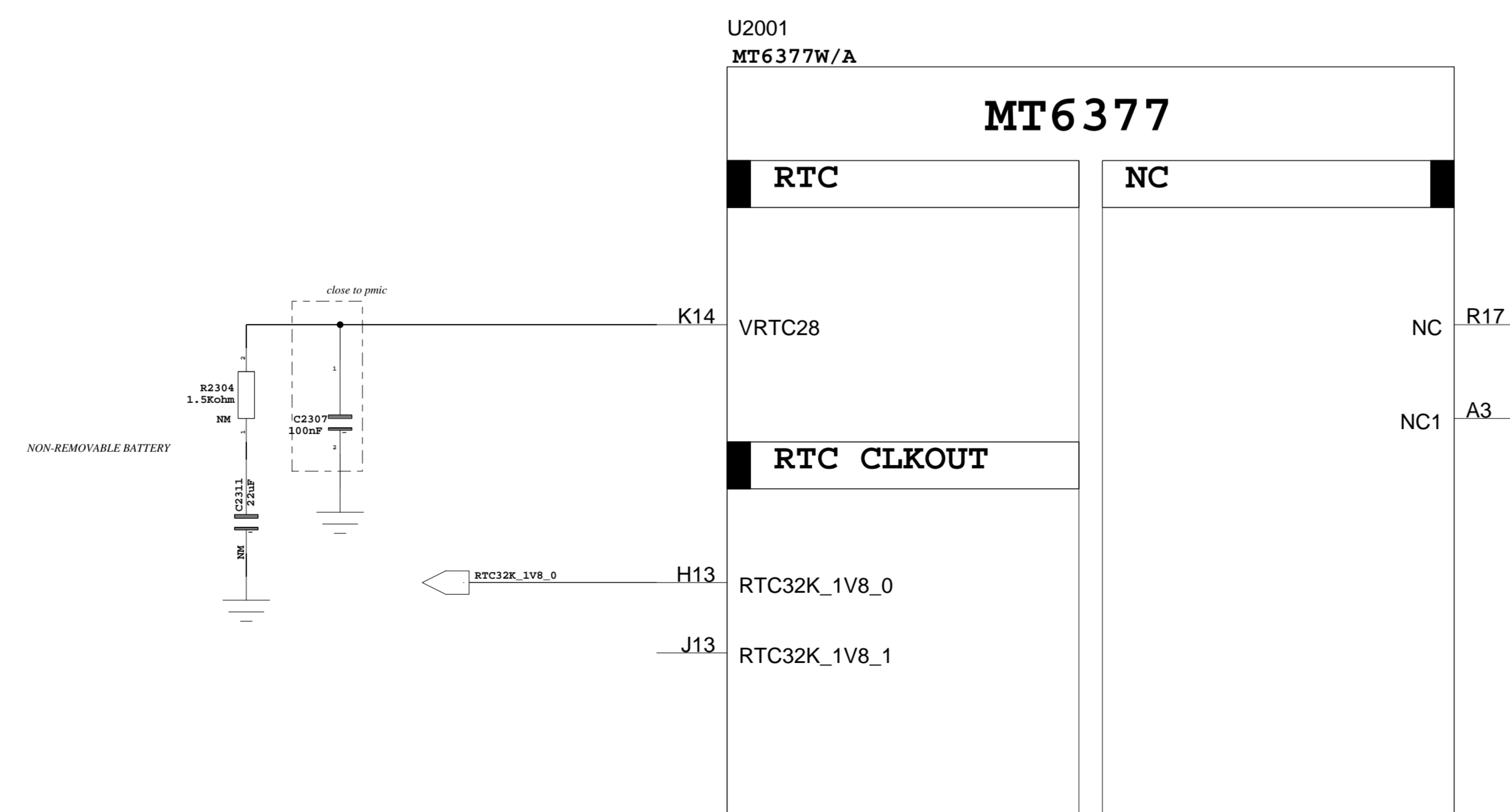
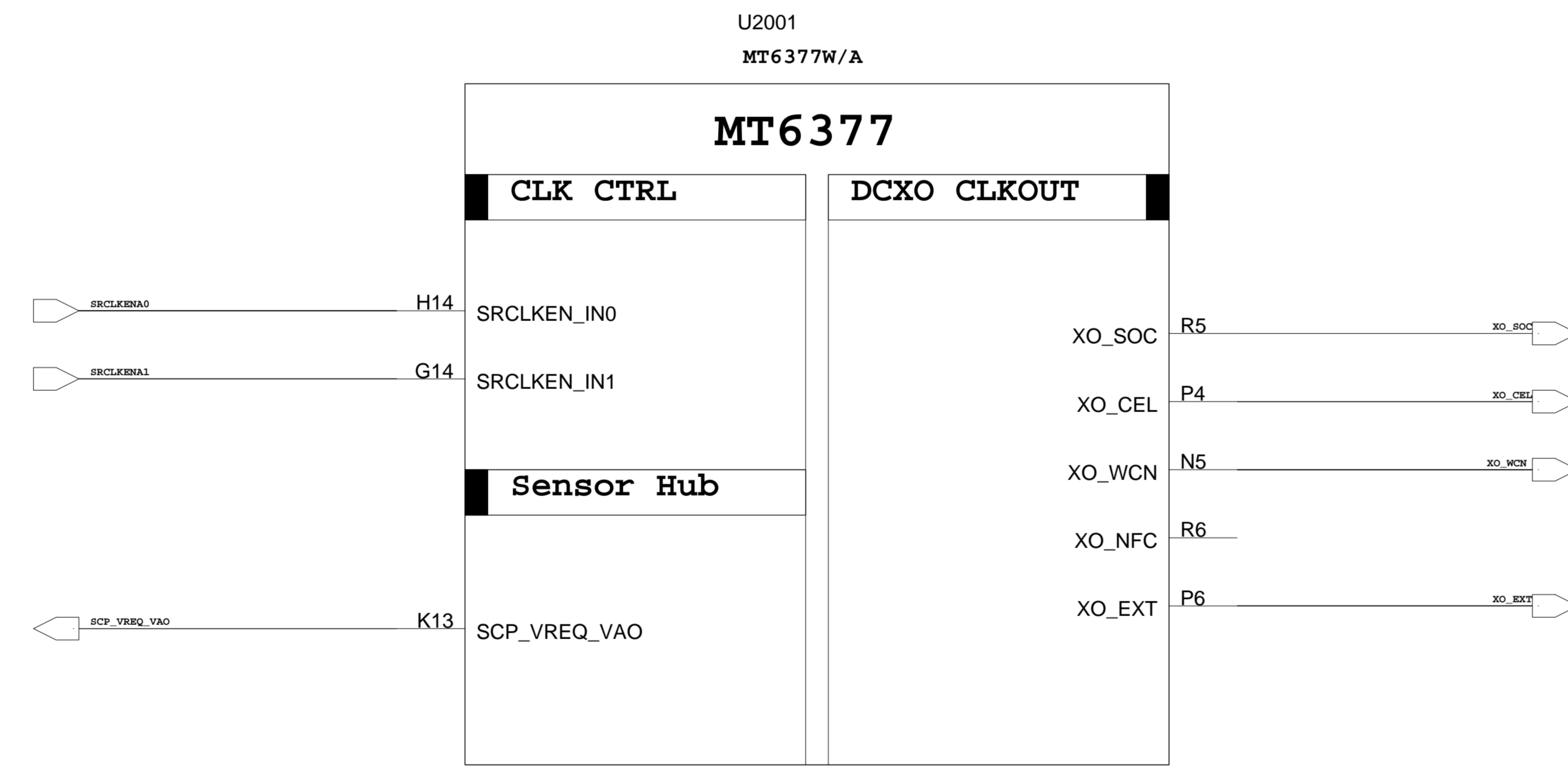
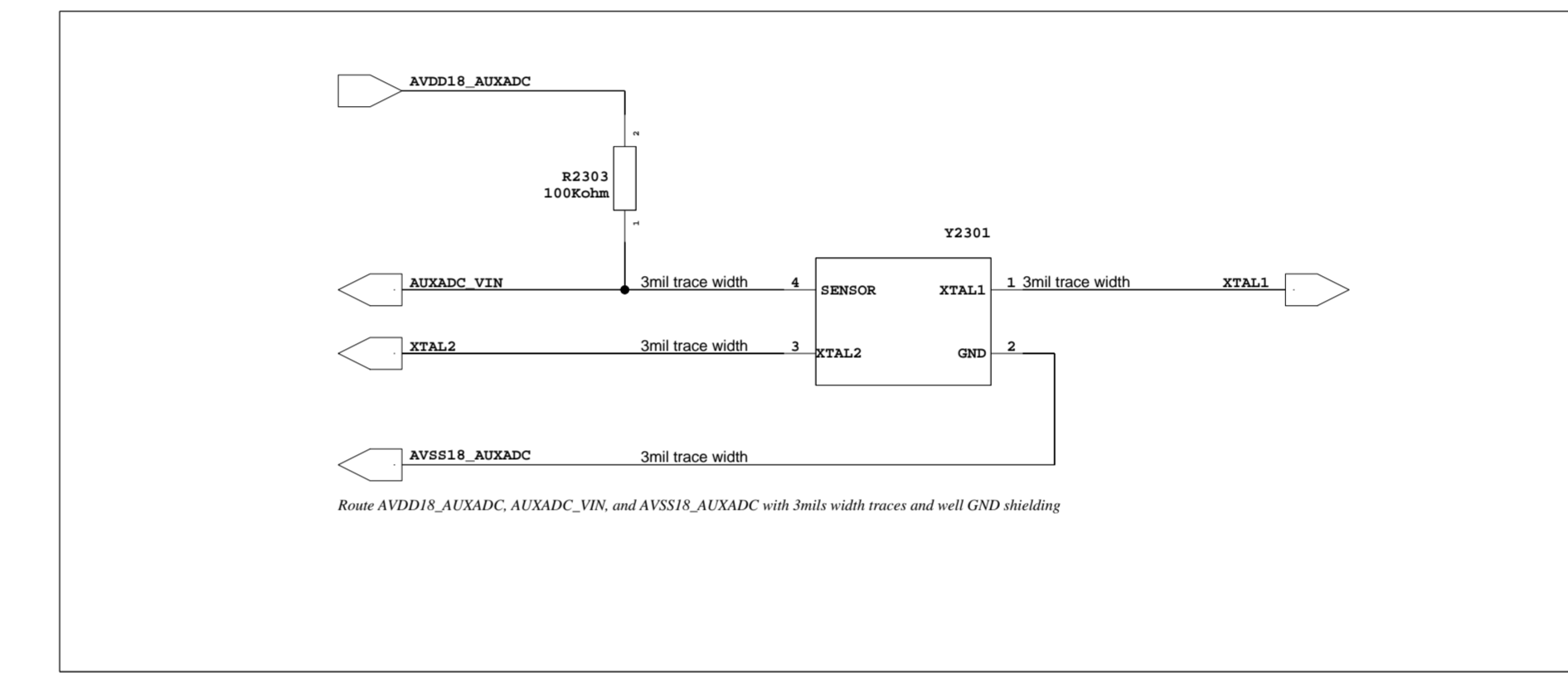
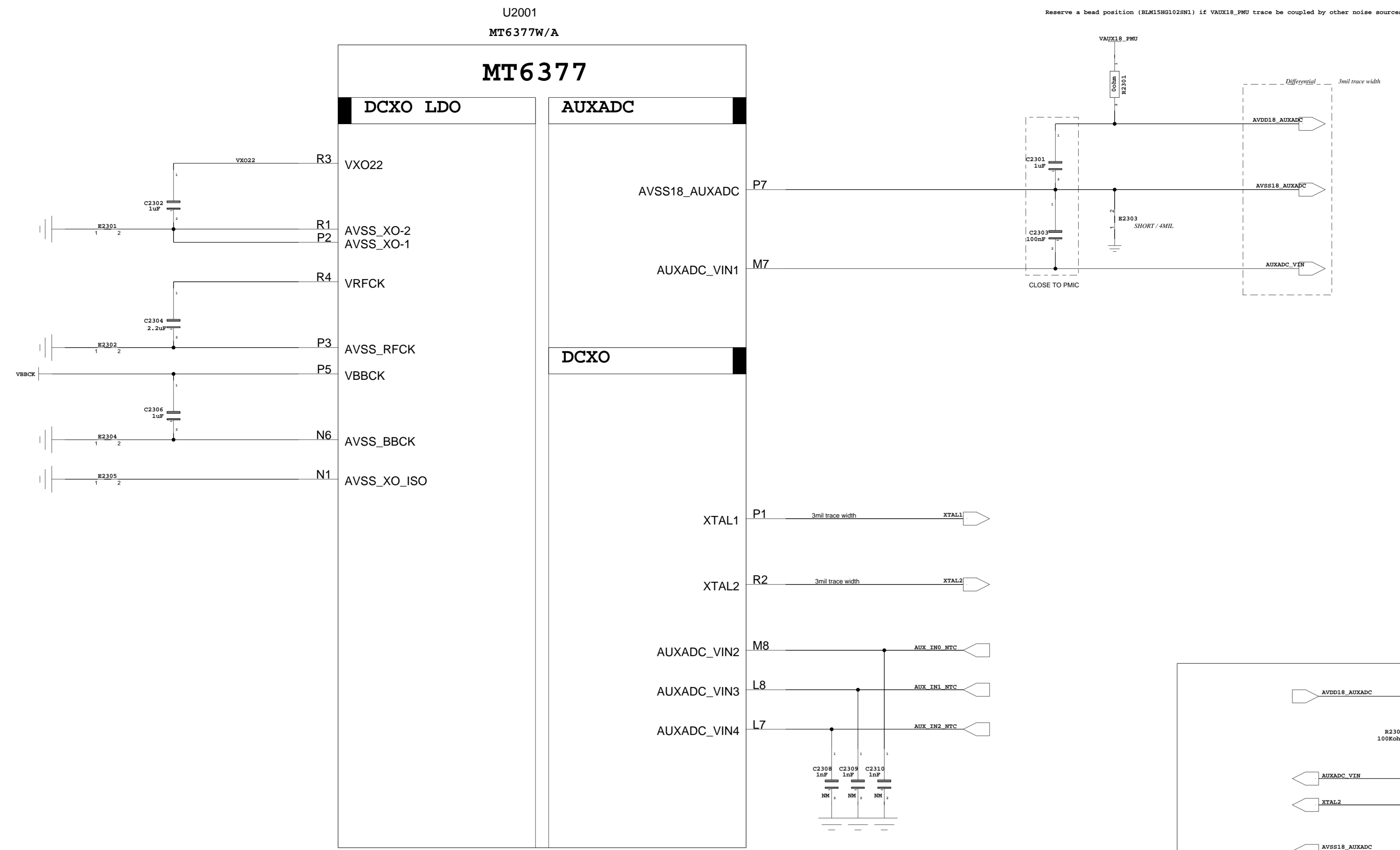


U2001
MT6377W/A



U2001
MT6377W/A

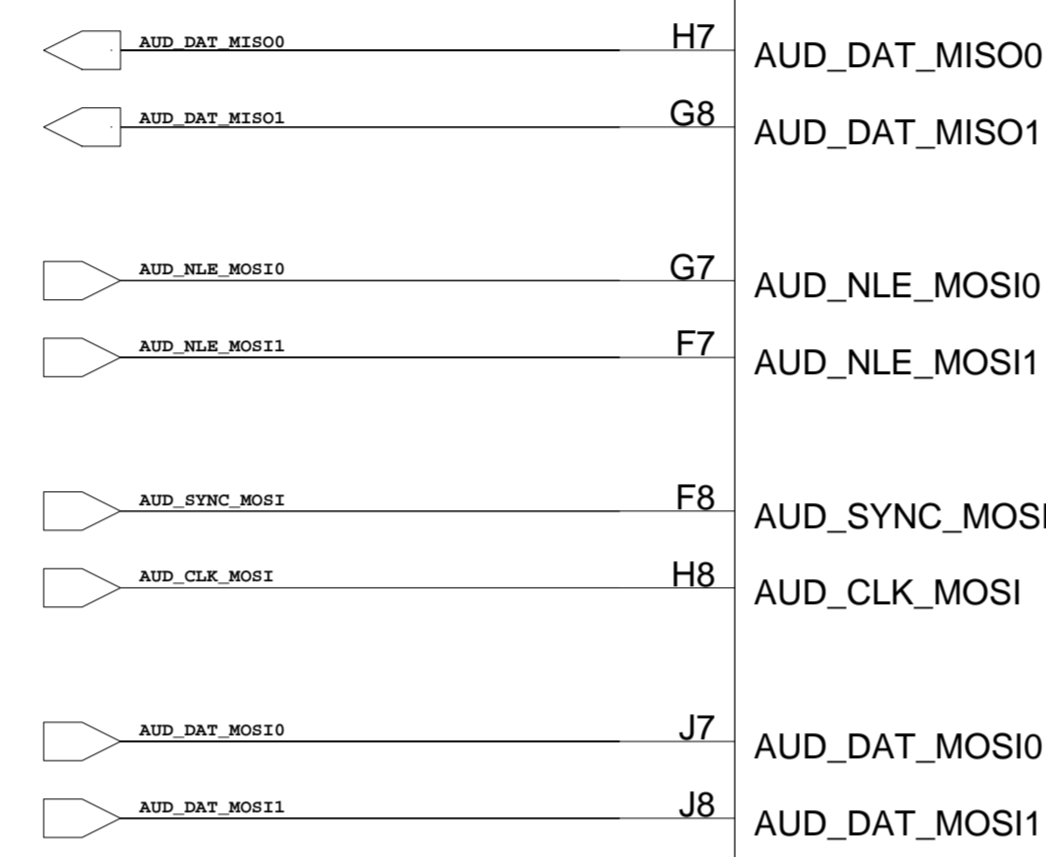




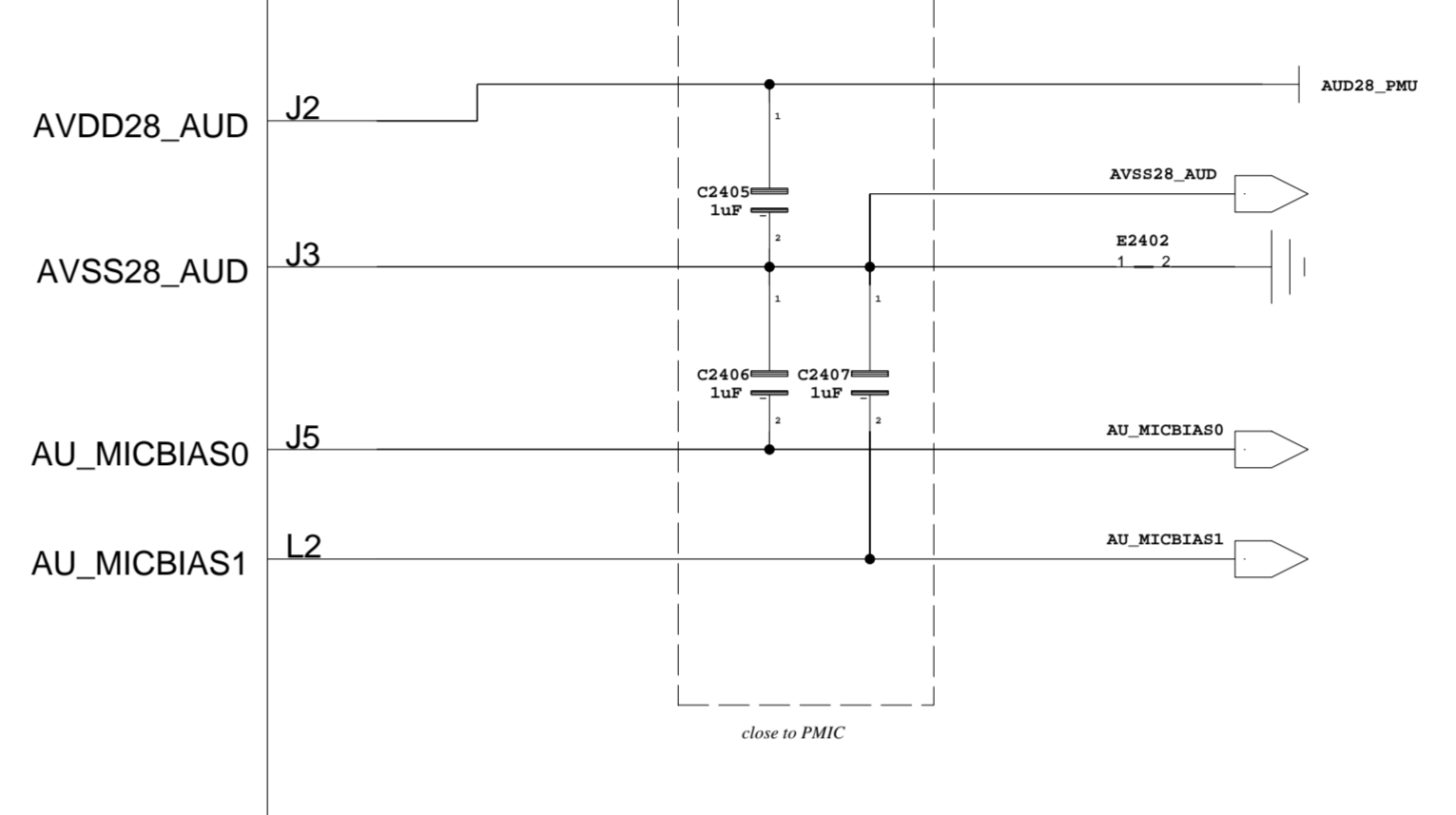
U2001
MT6377W/A

MT6377

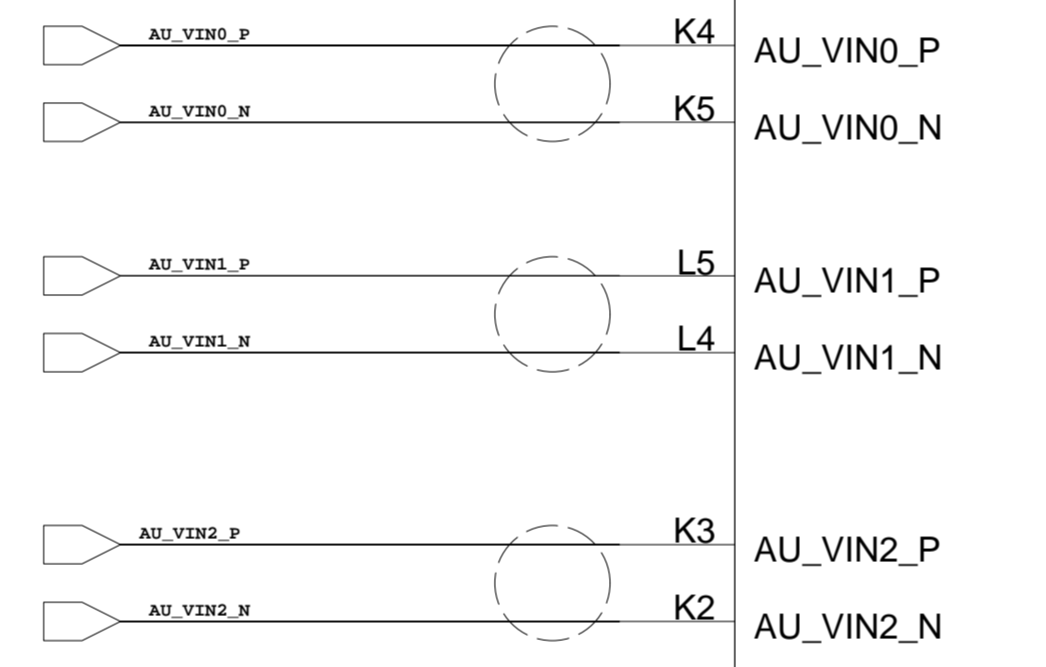
AUDIO IF



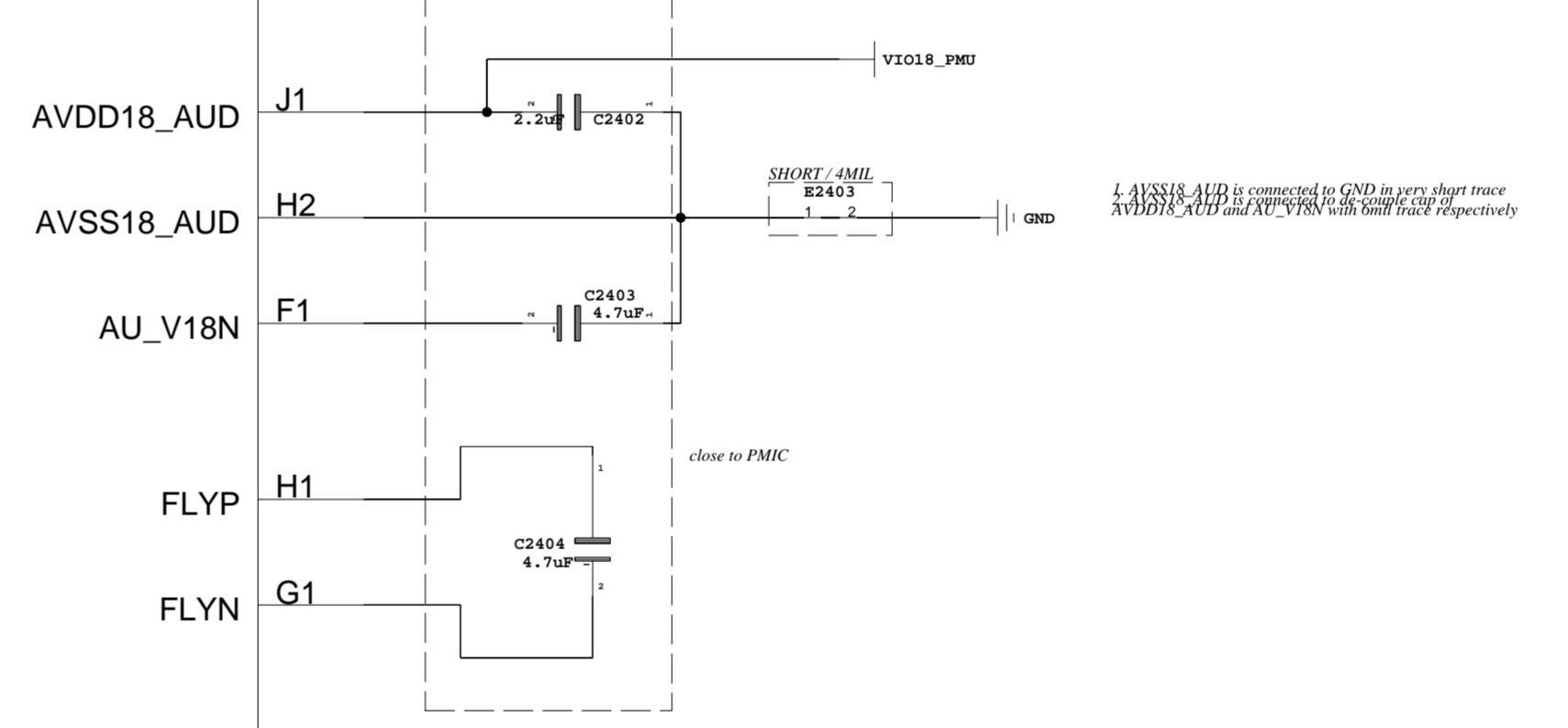
UL POWER



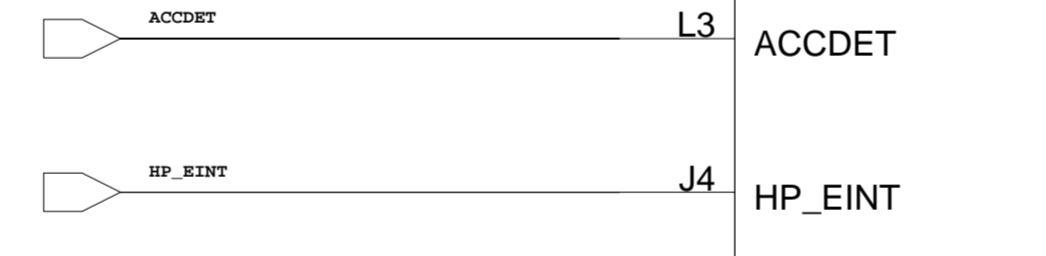
AUDIO INPUT



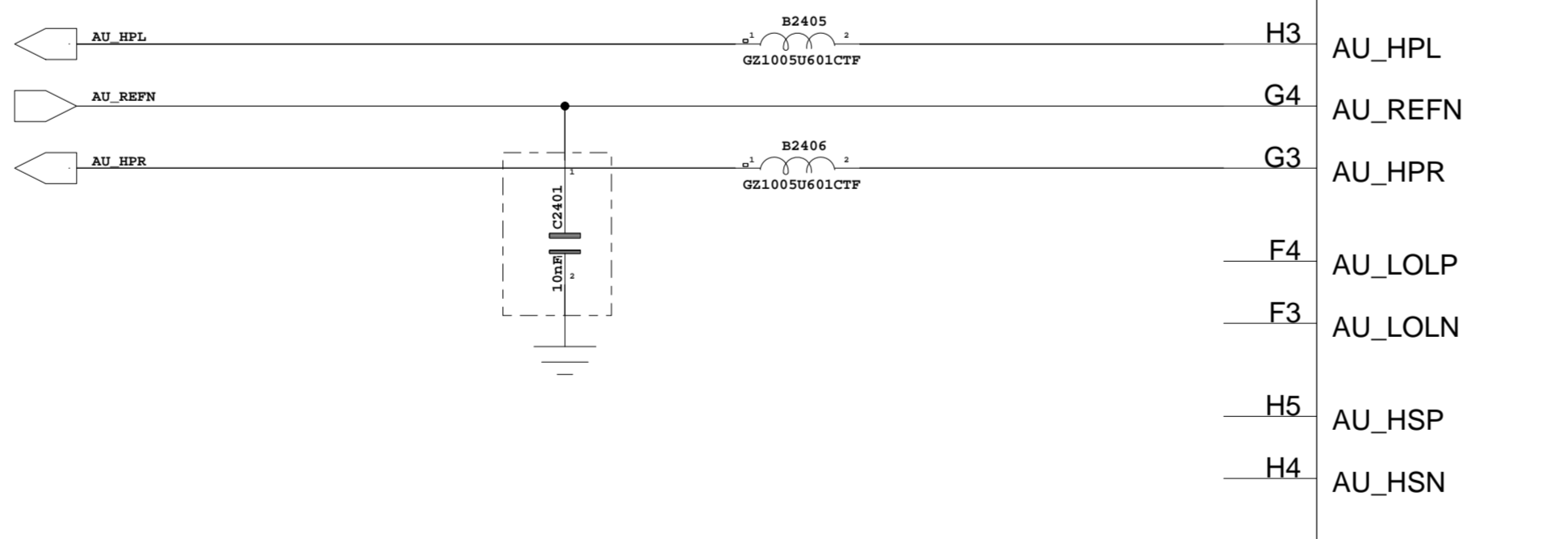
CHARGE PUMP



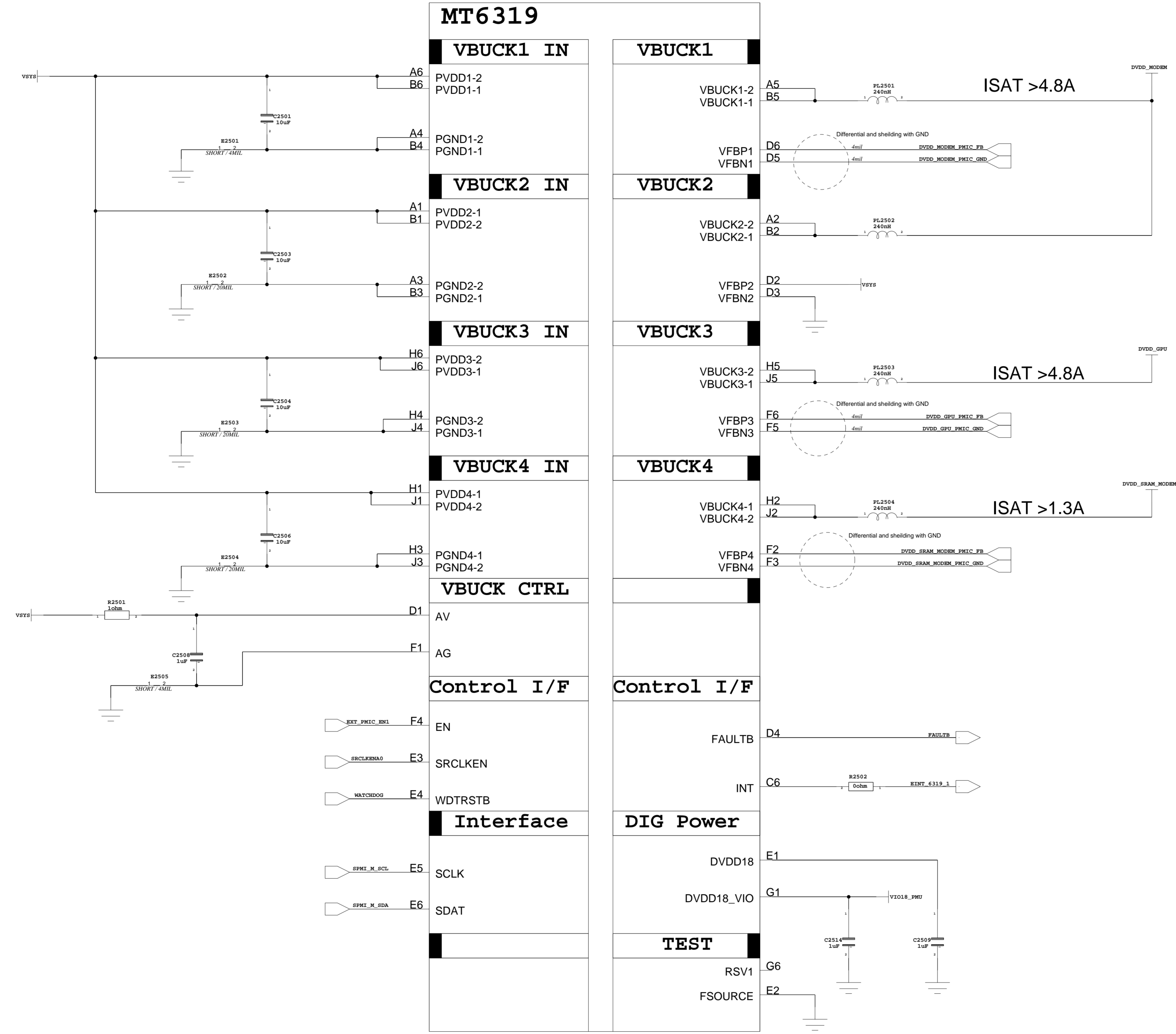
ACCDET



AUDIO OUTPUT

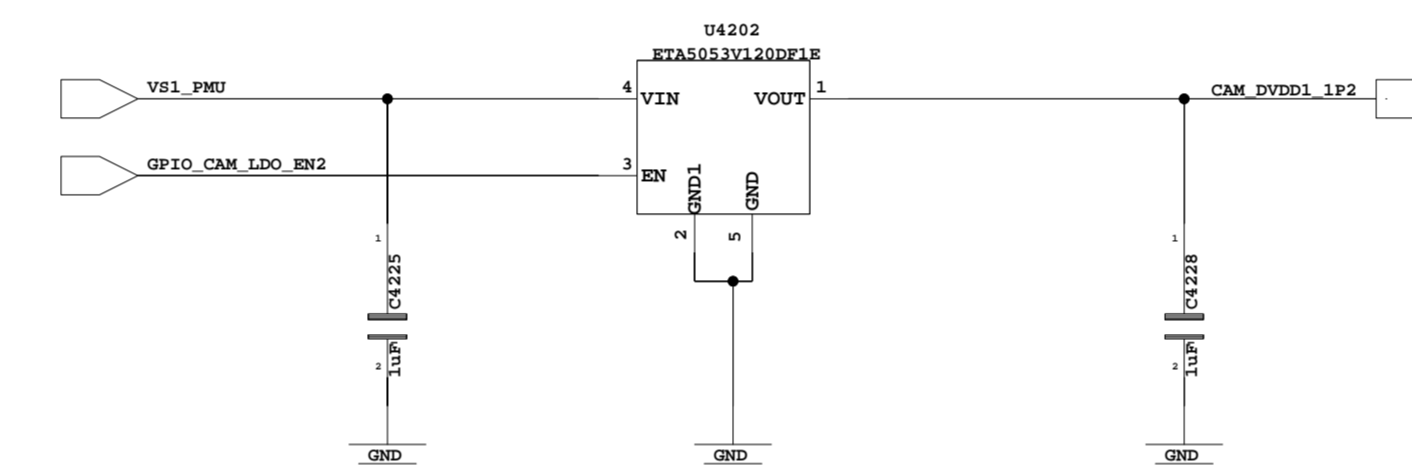
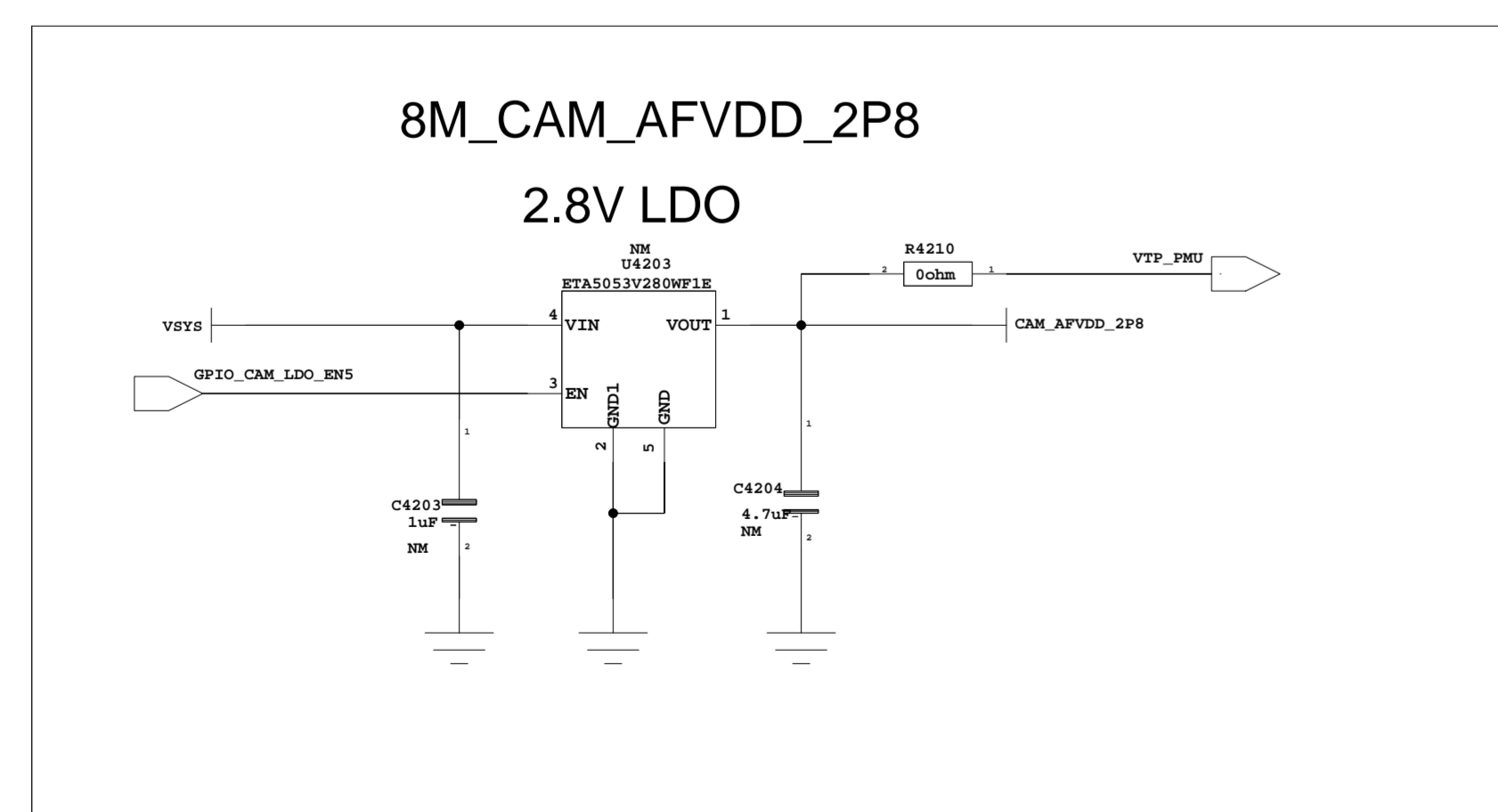
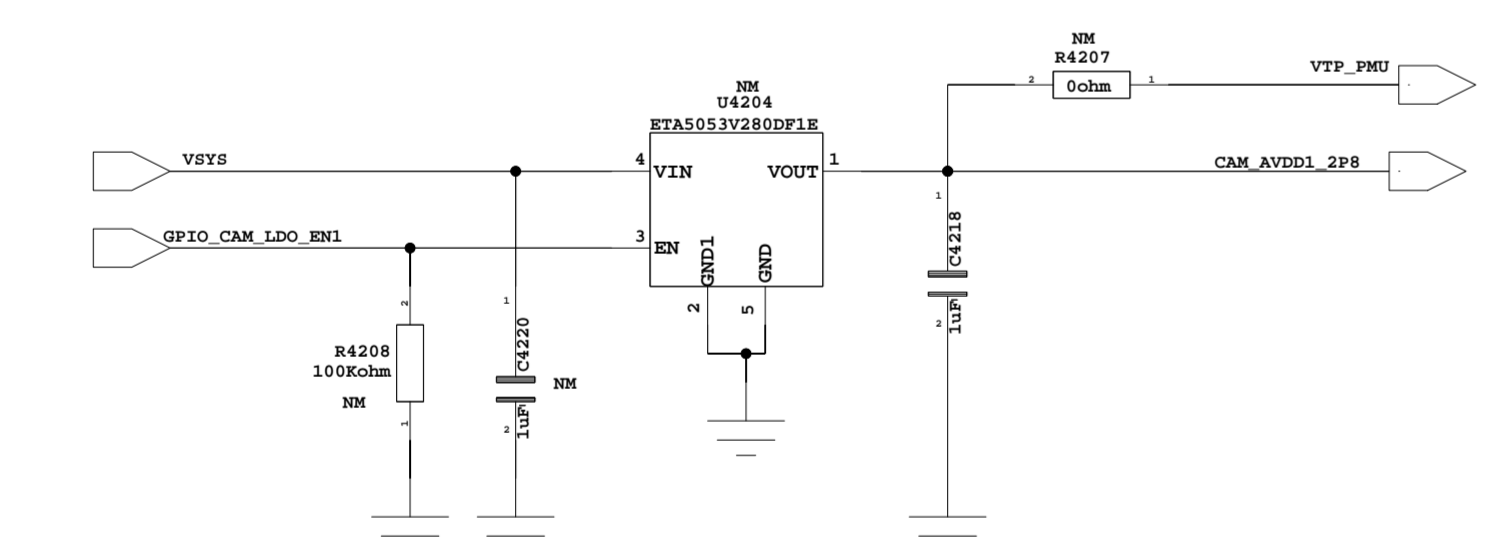
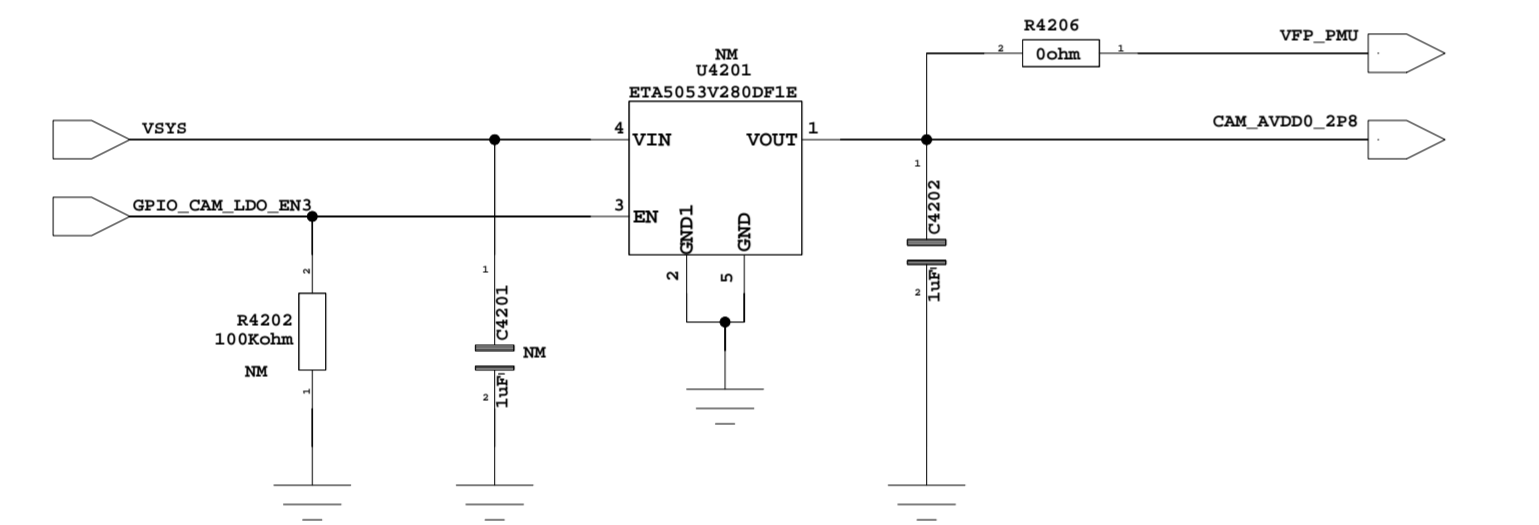
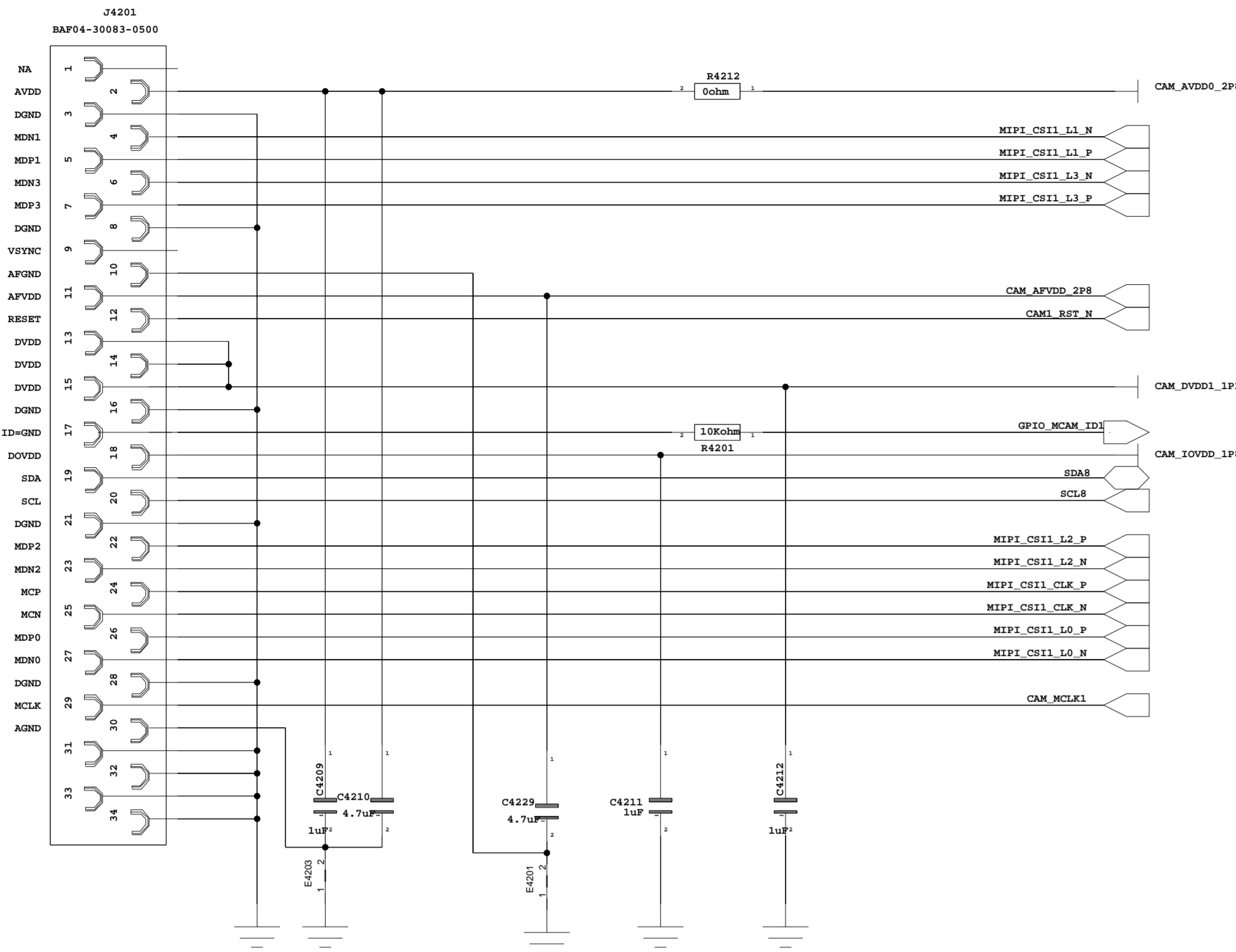


U2501
MT6319NP/A



8M MAIN CAMERA

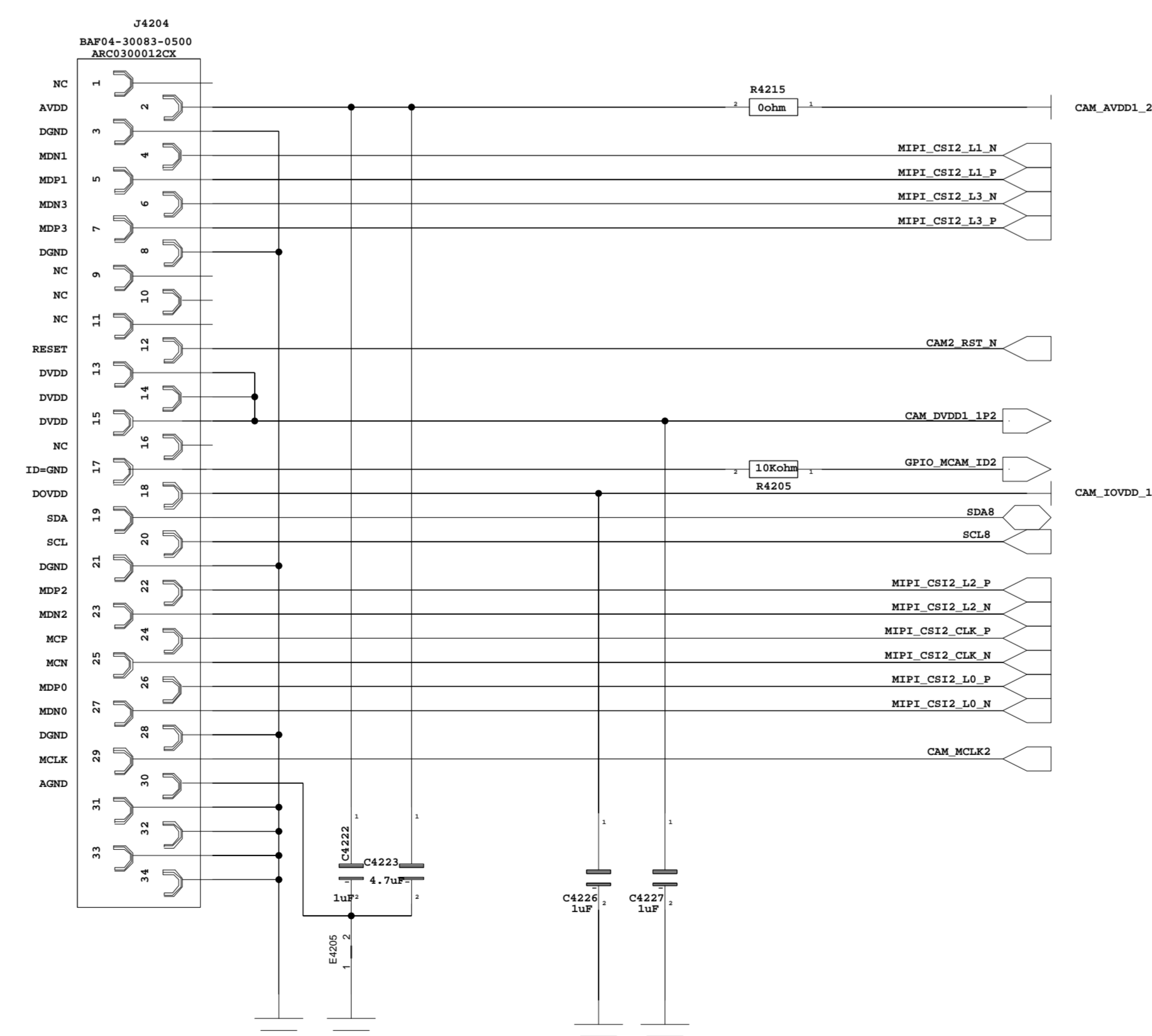
DVDD 1.2V 67mA SENSOR I2C Address:0x46(W)/0x47(R)
 AVDD 2.8V 42mA EEPROM I2C Address:NA
 DOVDD 1.8V 2mA DRIVER I2C Address:0x18(W)/0x19(R)



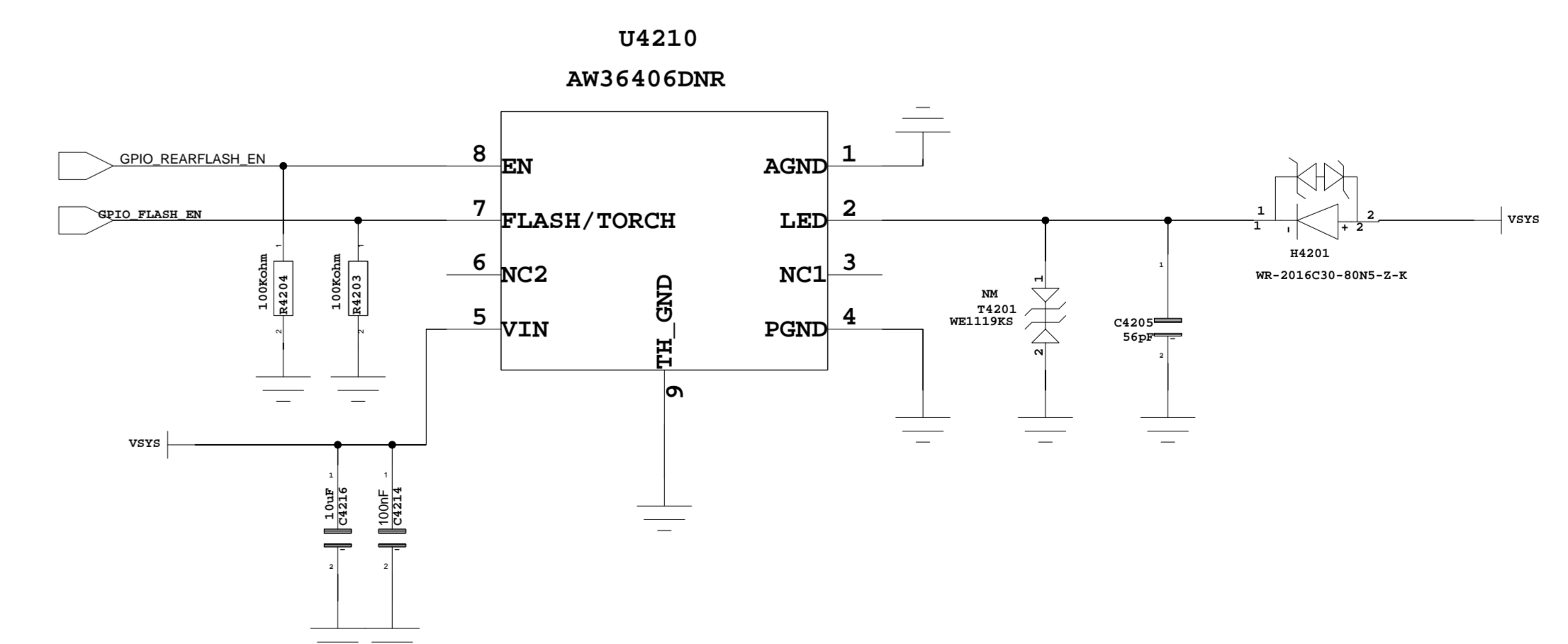
8M front camera

8M
 SENSOR I2C Address:0x40(W)/0x41(R)
 DRIVER I2C Address:NA

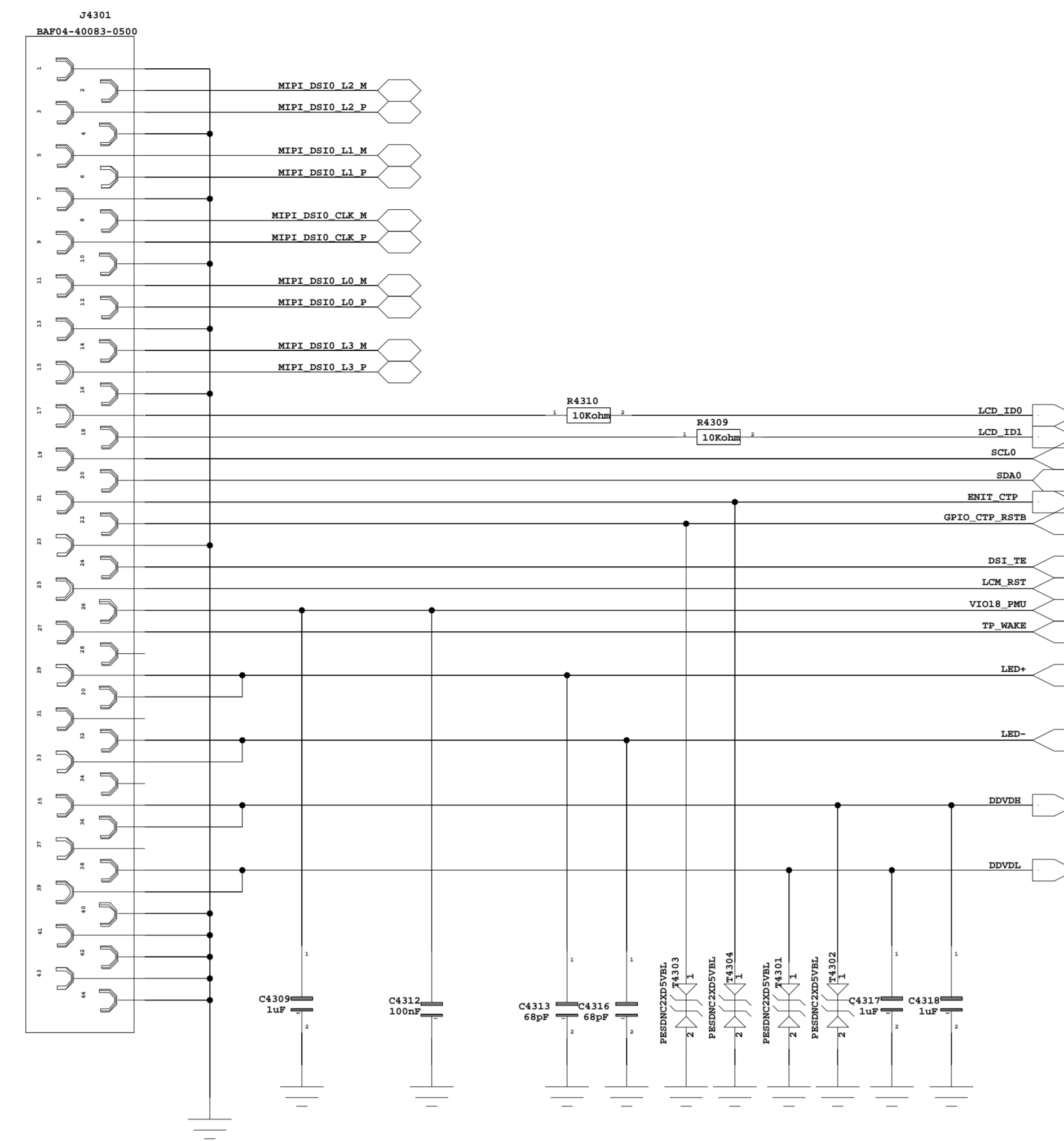
8M
 DVDD 1.2V 67mA
 AVDD 2.8V 42mA
 DOVDD 1.8V 2mA



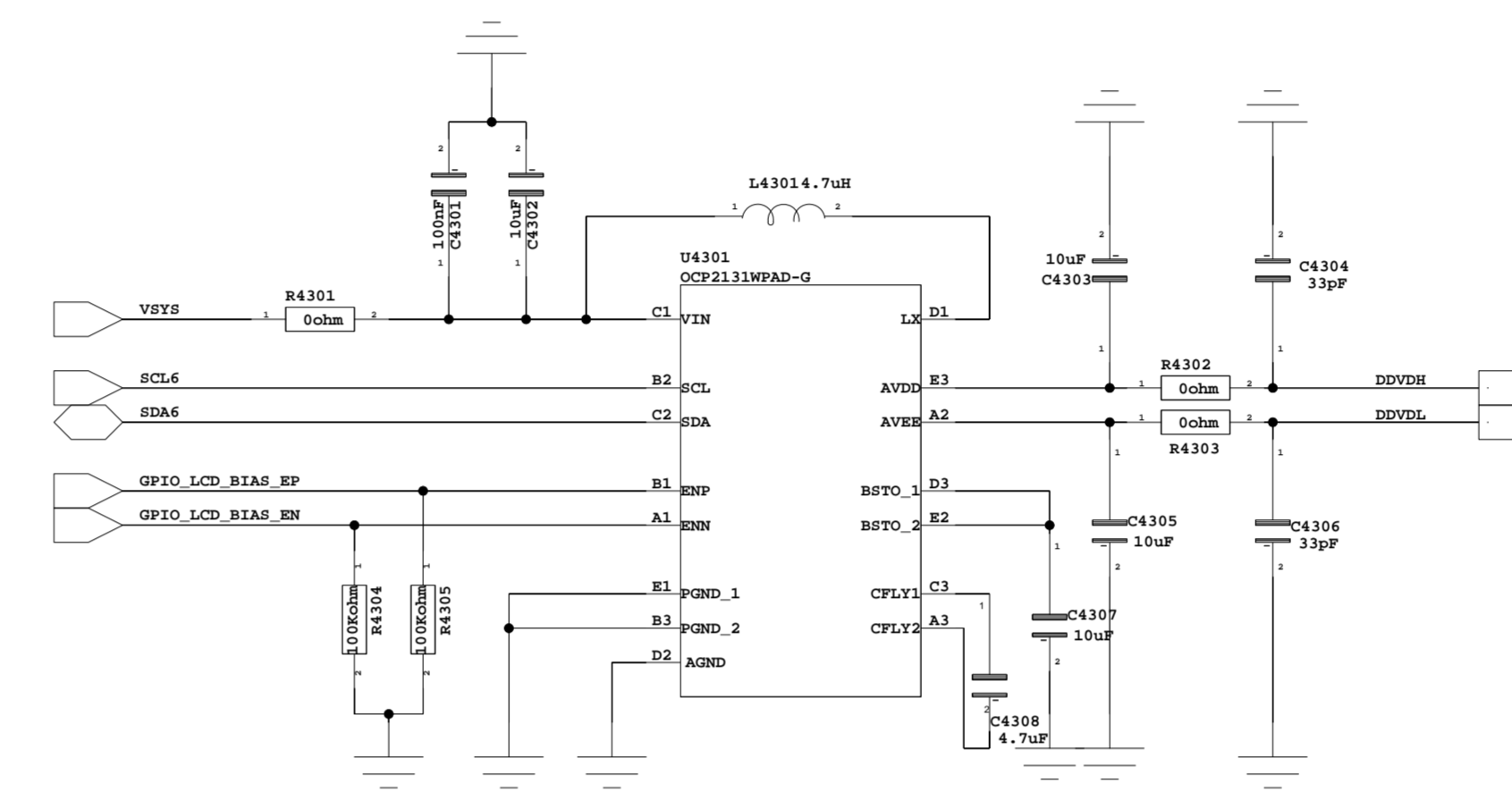
REAR FLASH



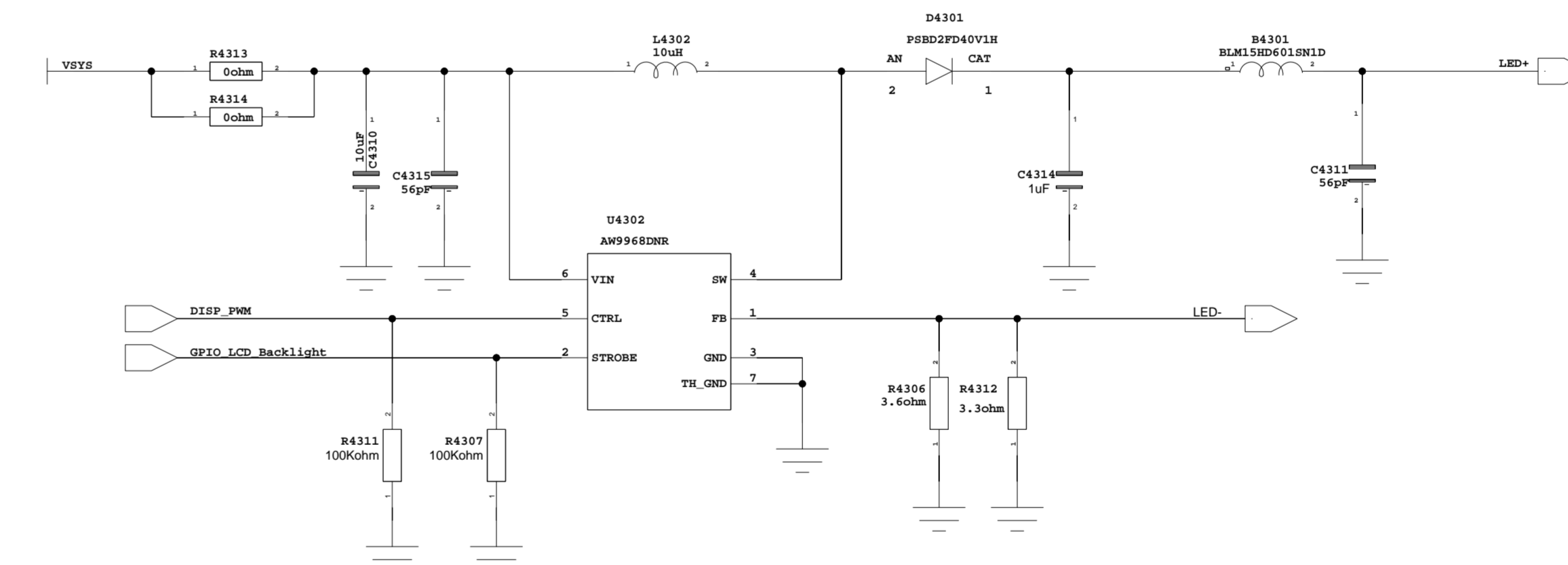
DISPLAY LCD_ID0=0
LCD_ID1=1



ADDRESS : 0X3E

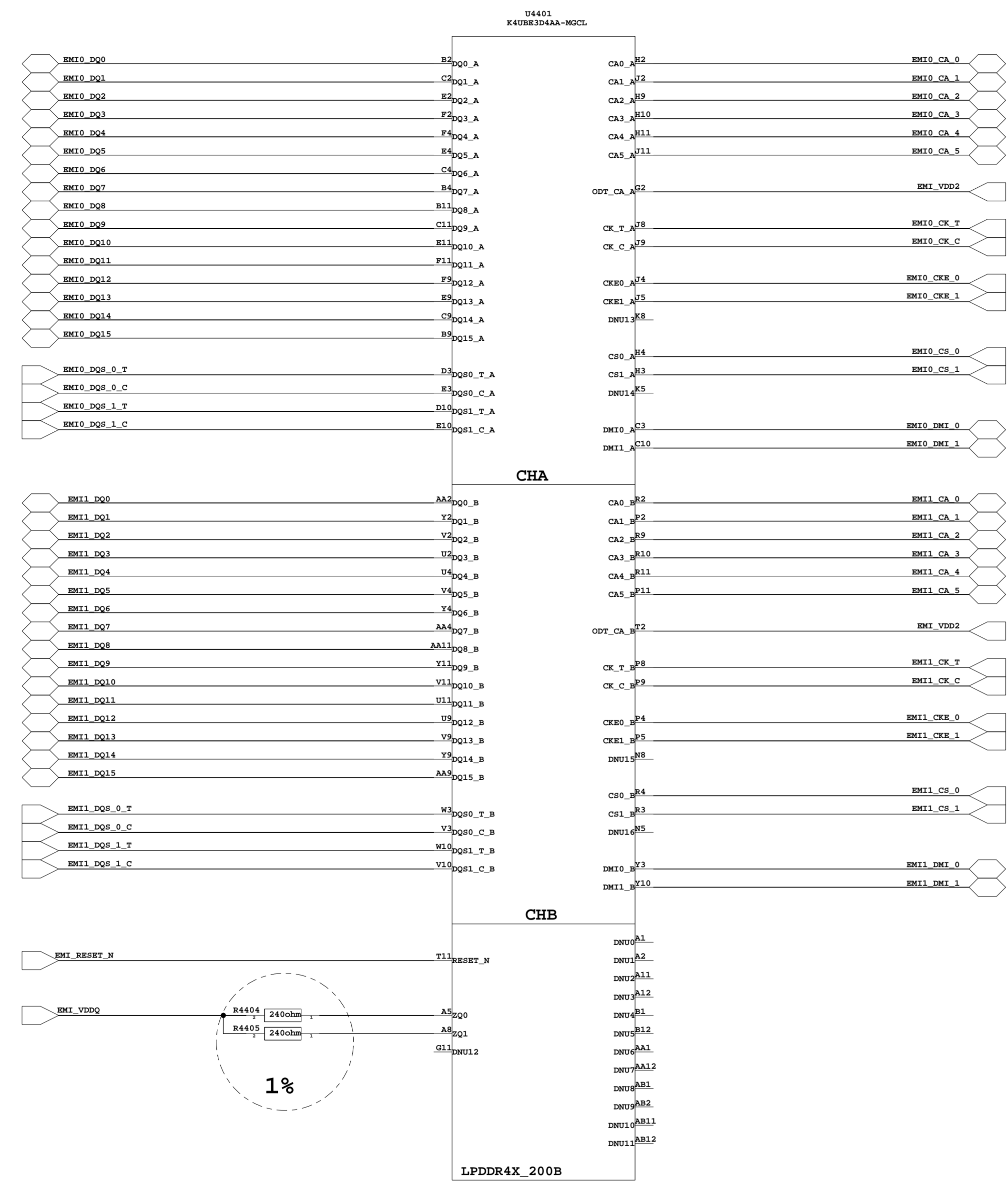


BL Power Supply

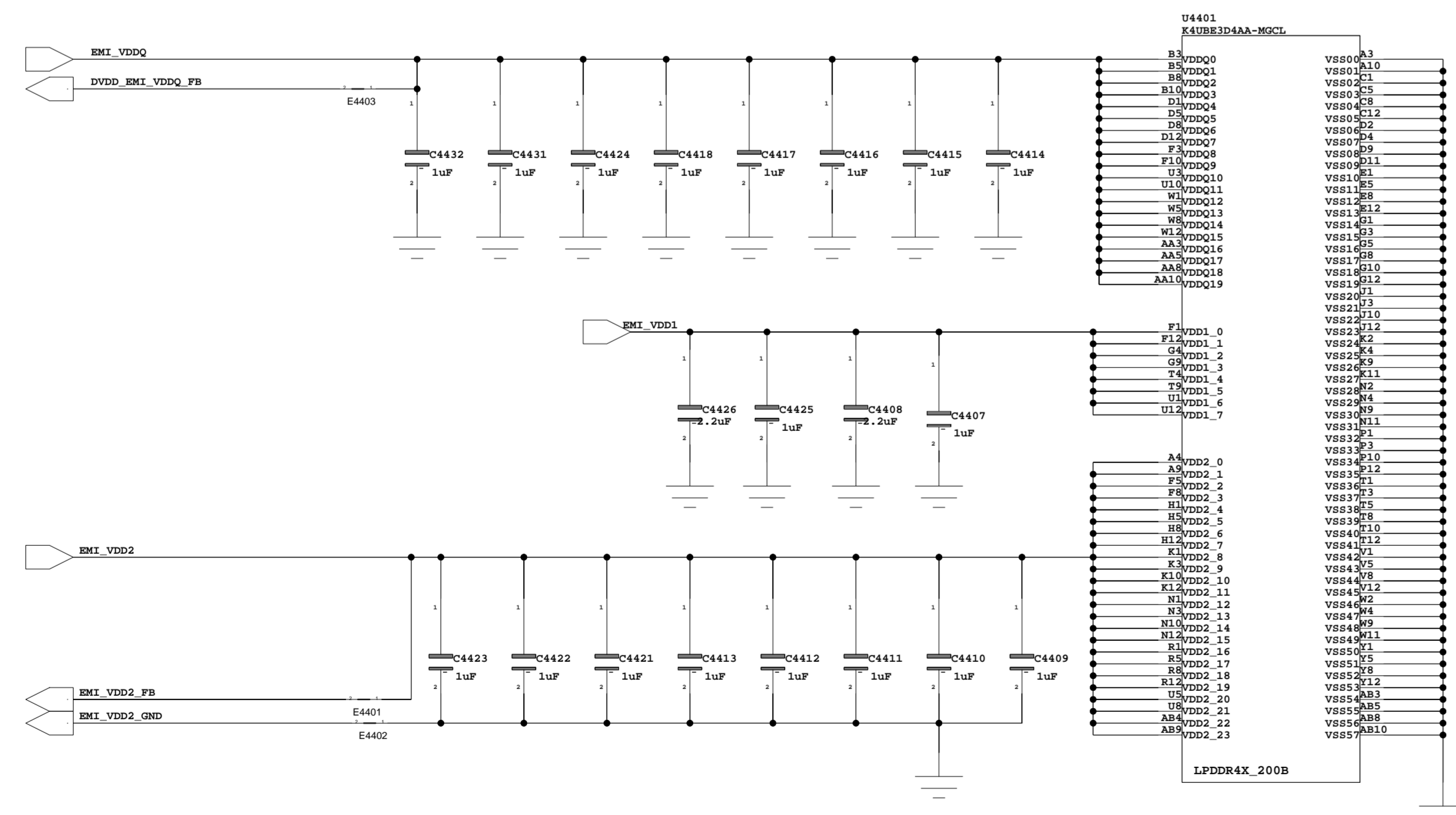


UFS:64GB/128 GB
LPDDR4X: 4GB/6GB

LPDDR4X

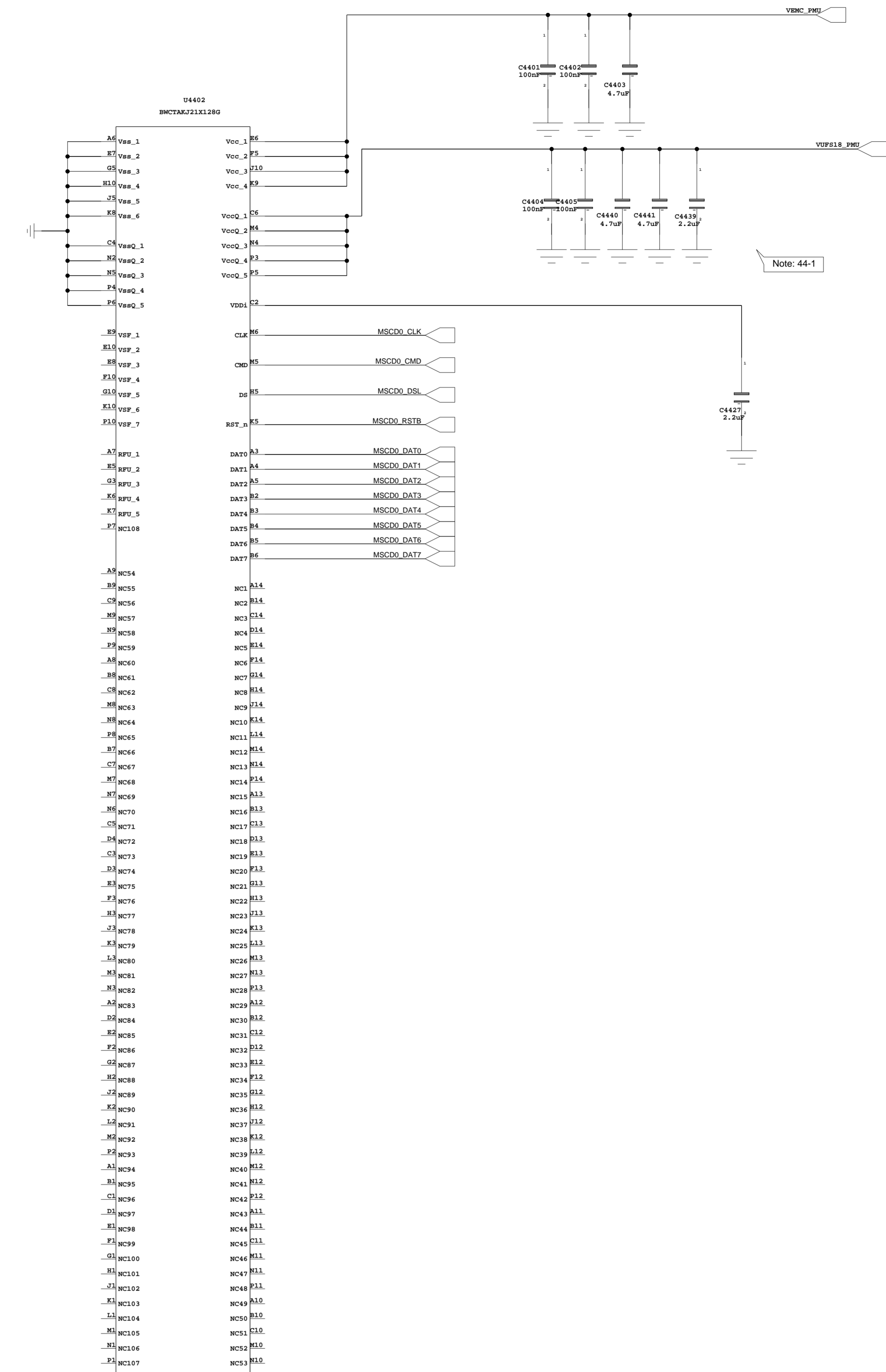


Note 5-2: resistor = 240ohm (1%) that must be connected to VDDQ(VDD2)



Note 5-1: VDDQ decoupling cap: closed to DRAM ball. For other caps: please refer to BMD and layout guides for placement.

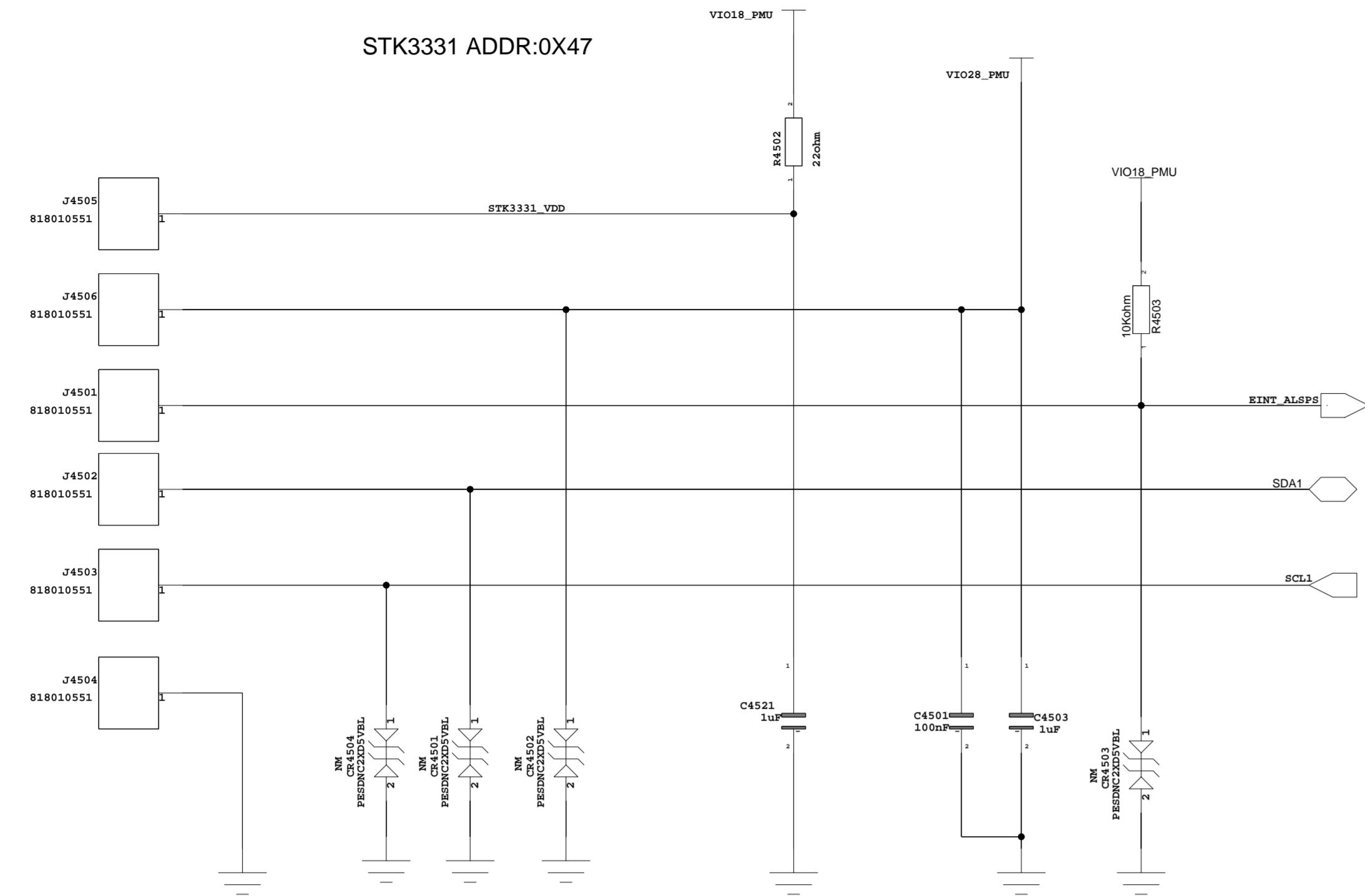
EMMC



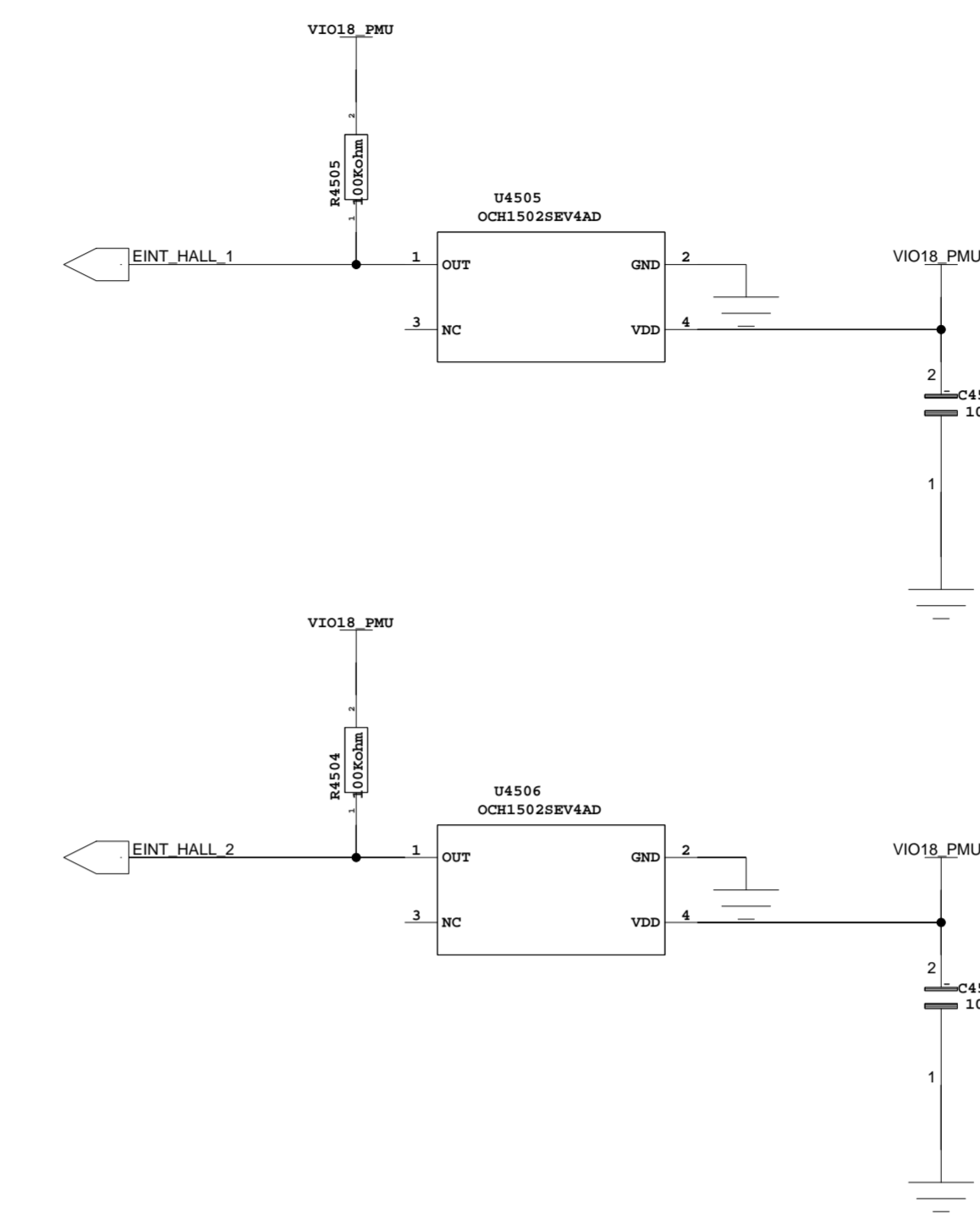
Schematic design notice of "44_Memory_UFS" page.

Note 44-1: Please refer to UFS vendor's datasheet or MTK common design notice to get therecommendation bypass cap. value for VCC/VCCQ/VDDI power domains of UFS.

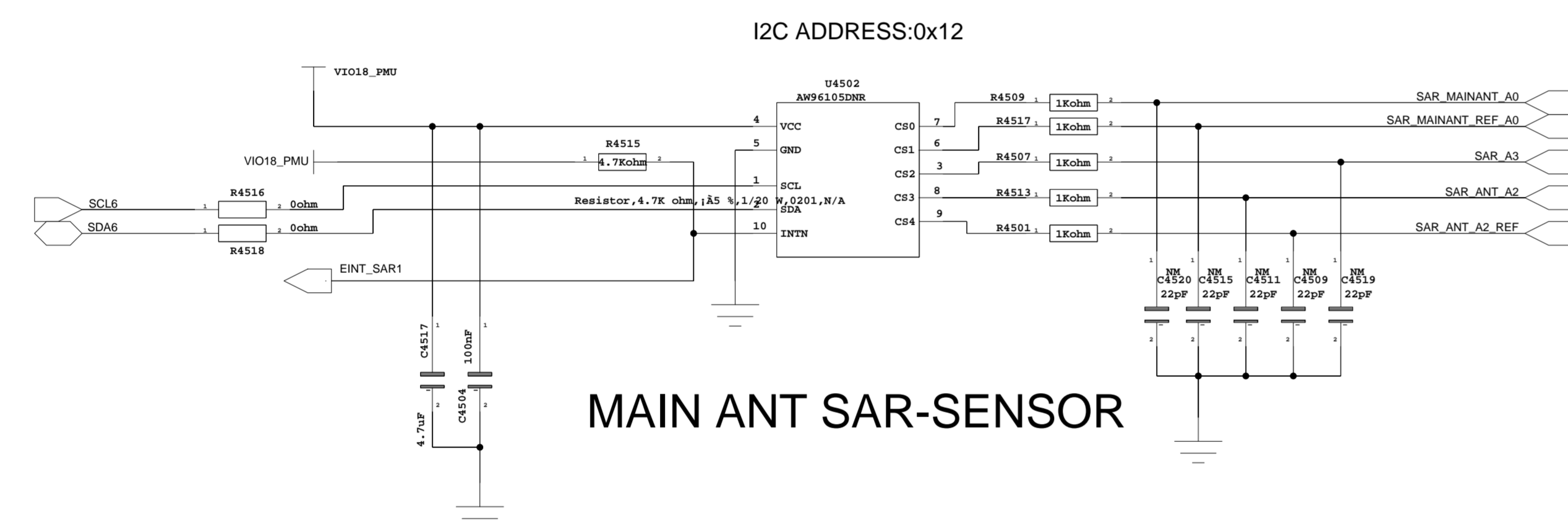
Ambient Light+Proximity Sensor



HALL

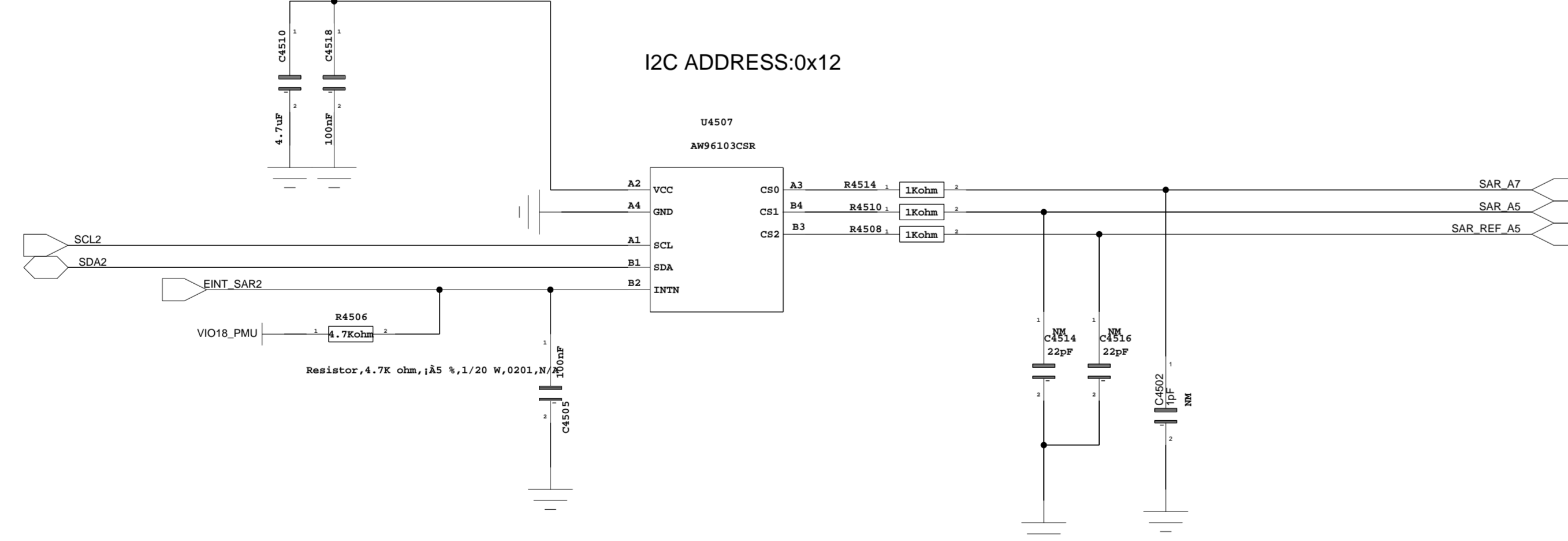


RESERVE SAR-SENSOR0(A2/A0/A3)

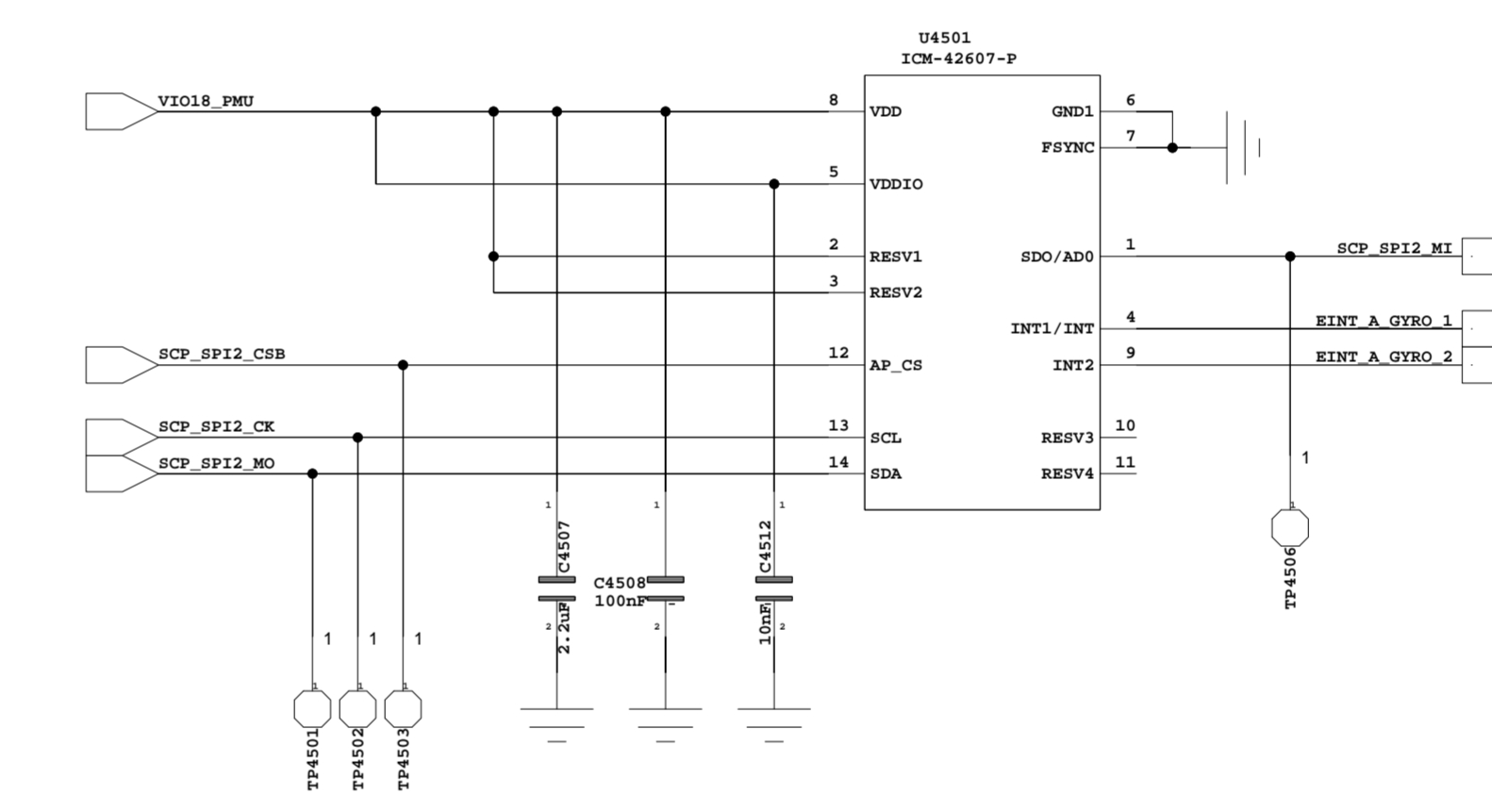


MAIN ANT SAR-SENSOR

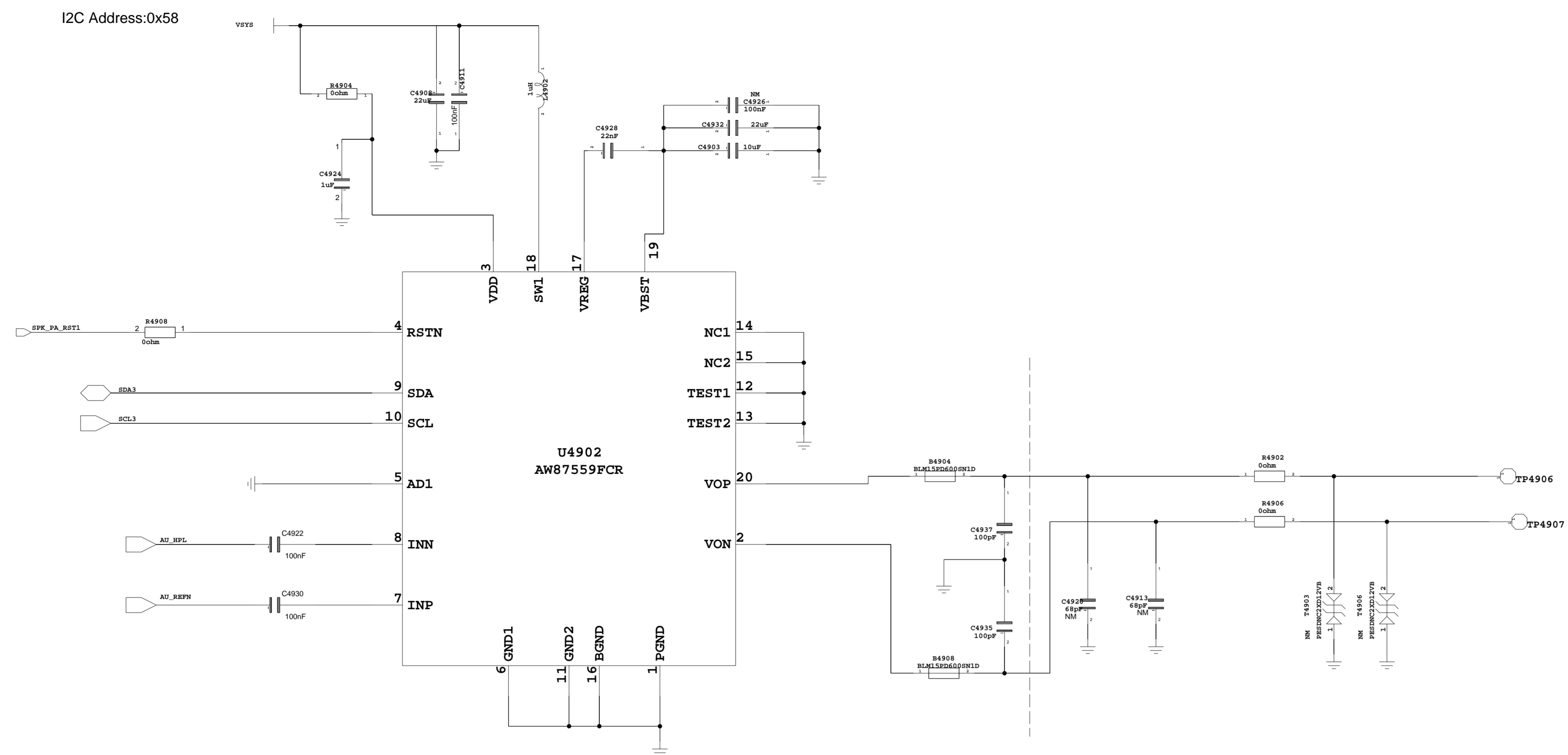
MAIN ANT RESERVE SAR-SENSOR1(A5/A7)



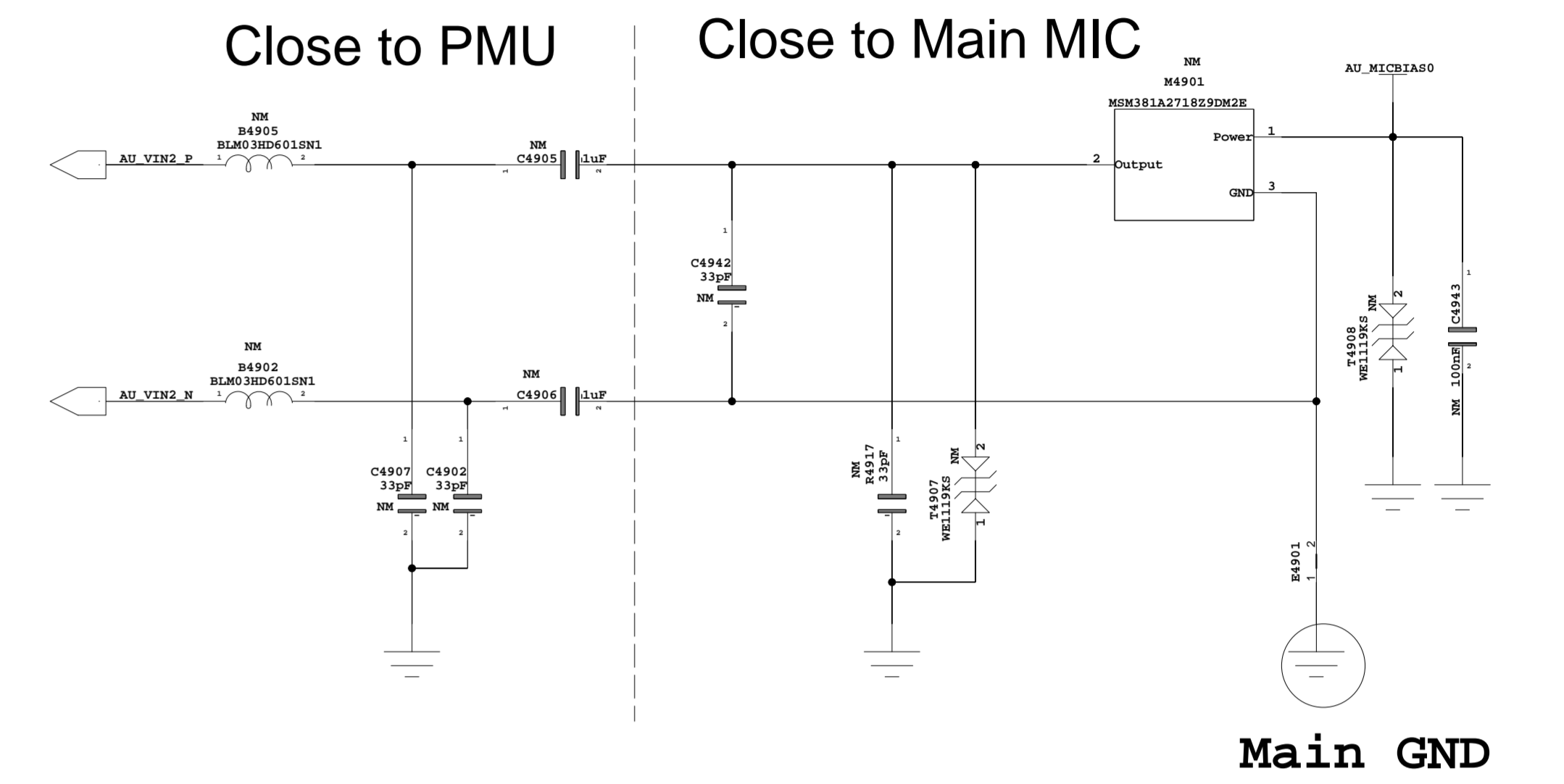
GYRO



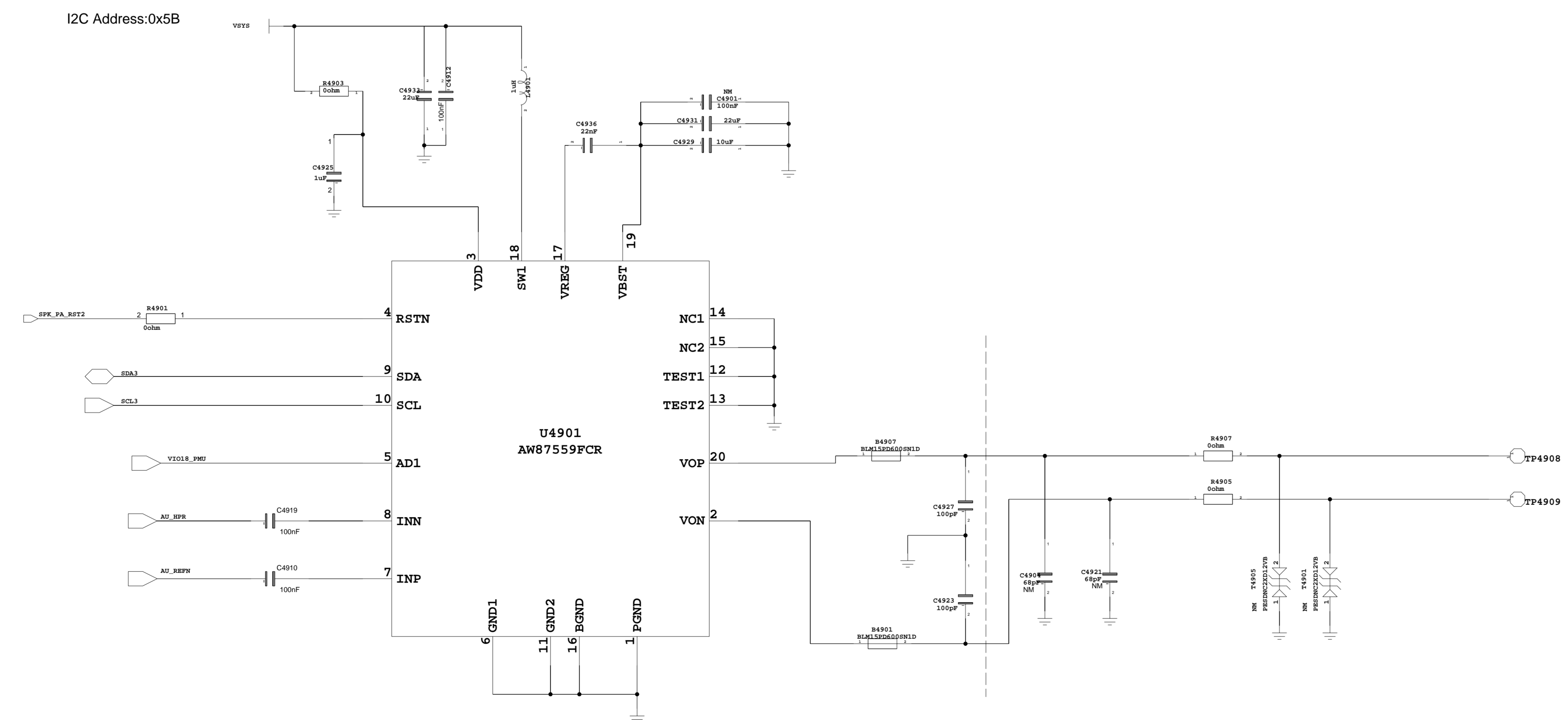
LEFT Speaker



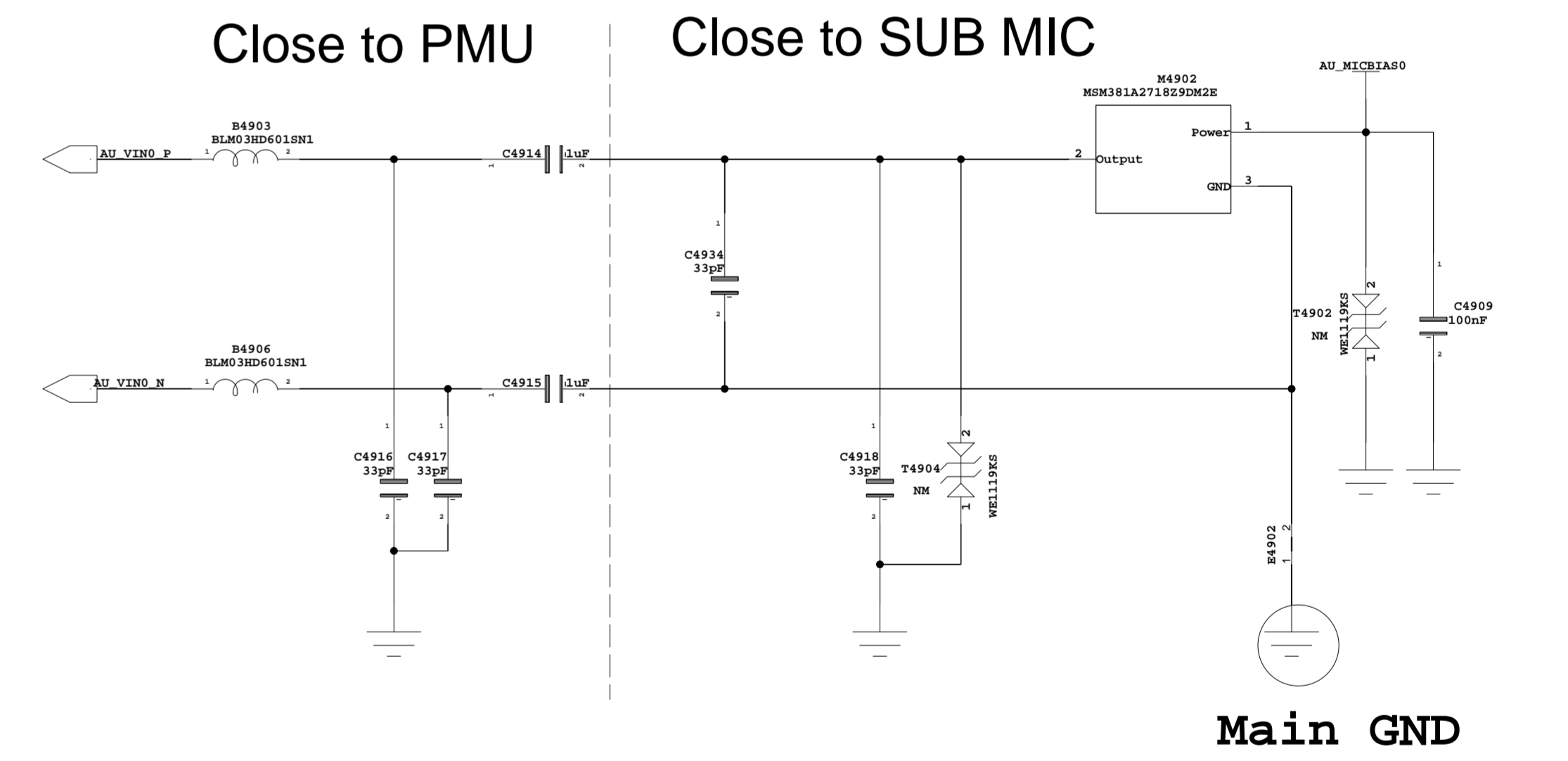
MIC1



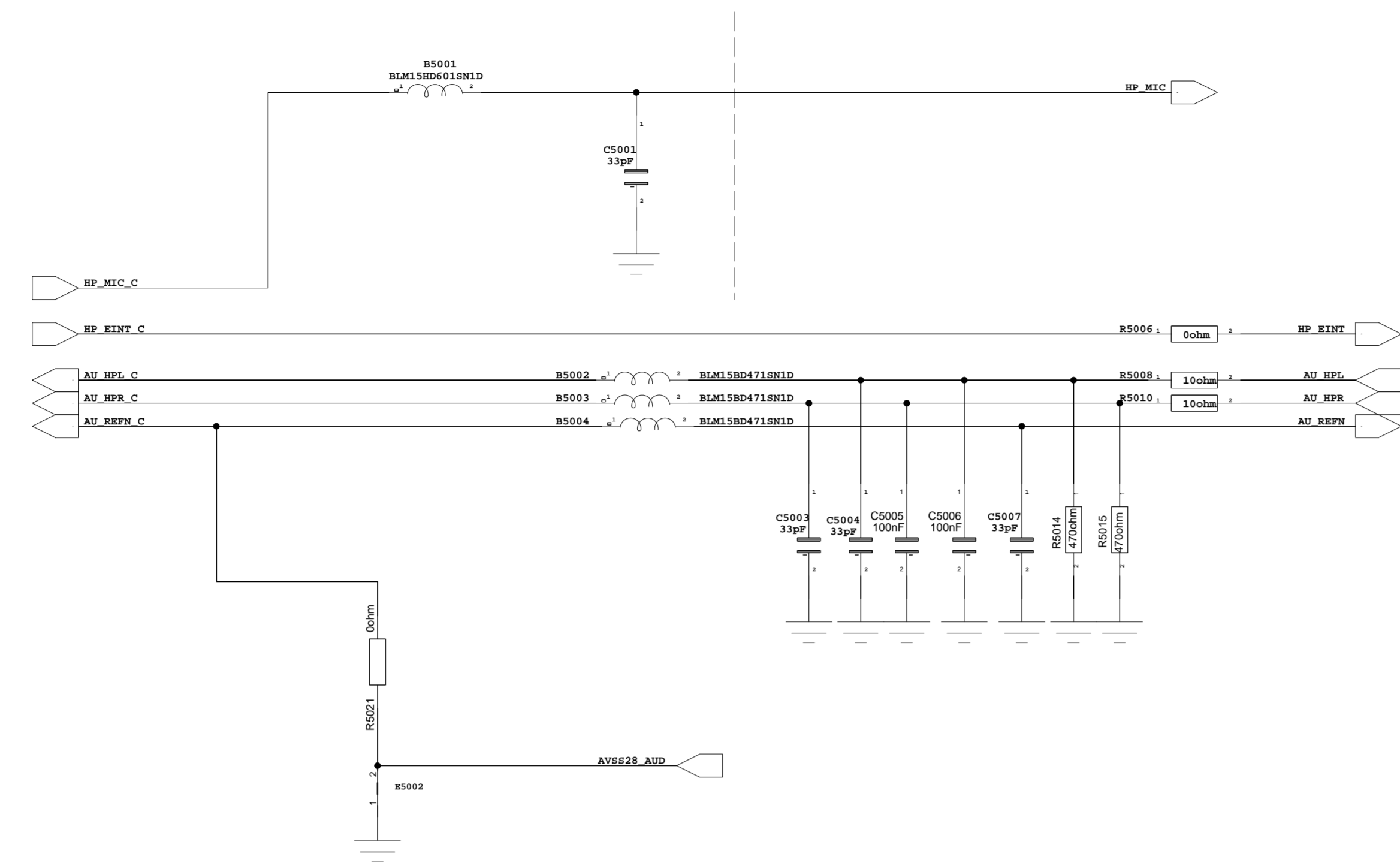
RIGHT Speaker



MIC2

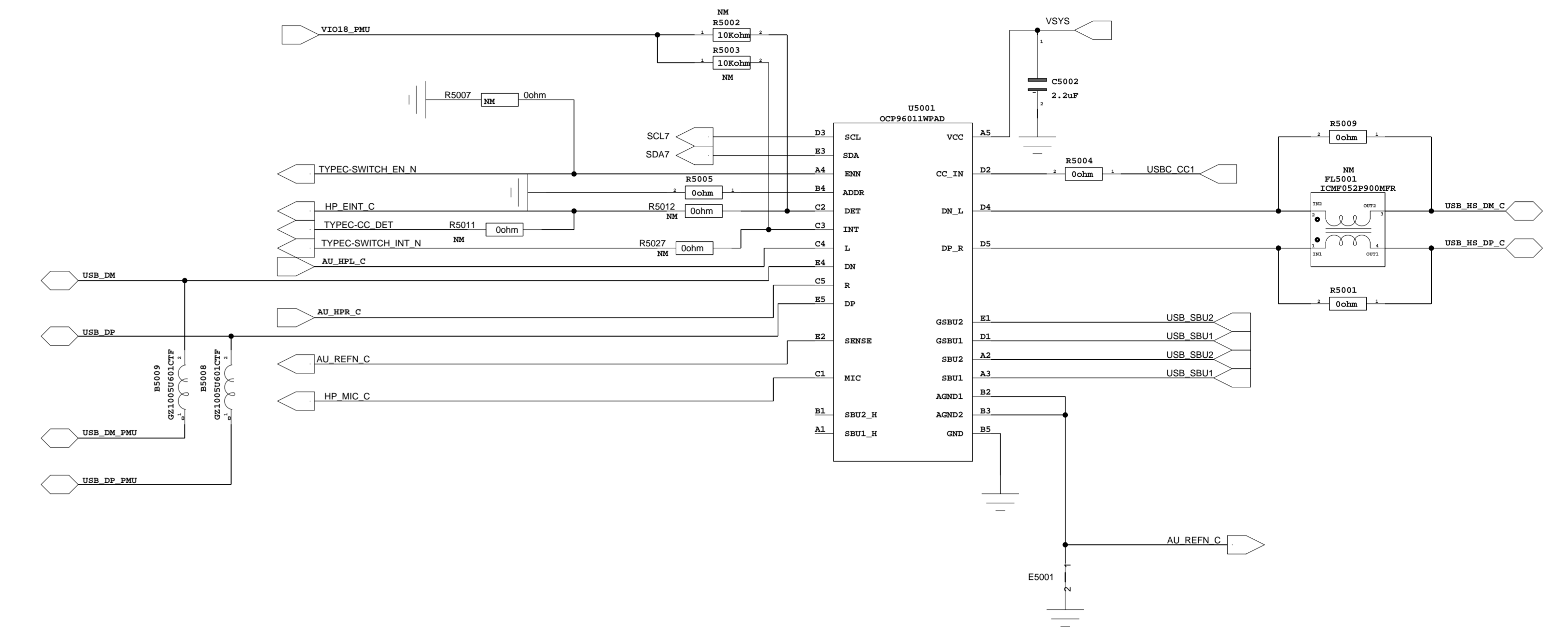


HEADSET

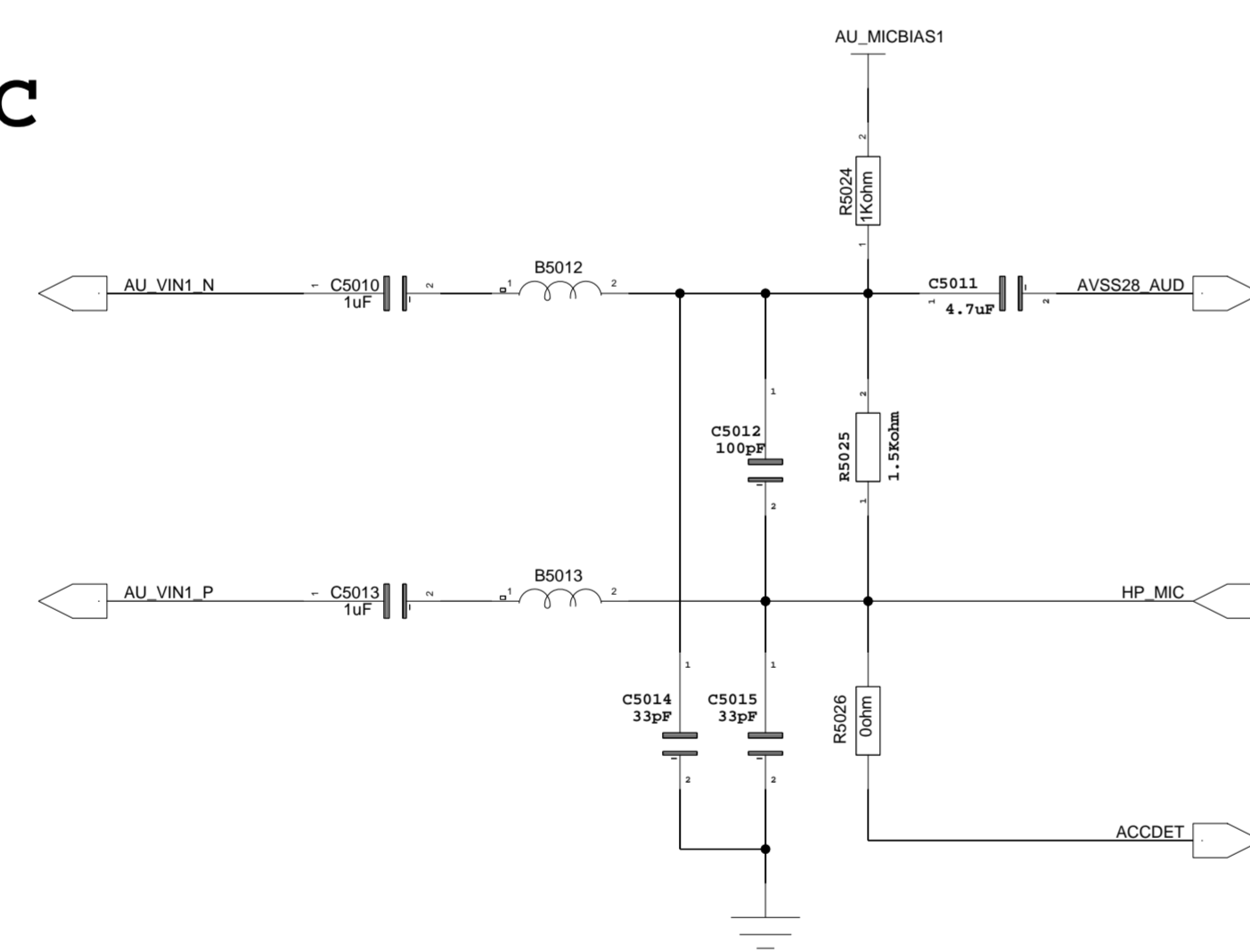


TYPEC HEADSET

I2C address: 0x42 (7-bit address)



EARPHONE MIC

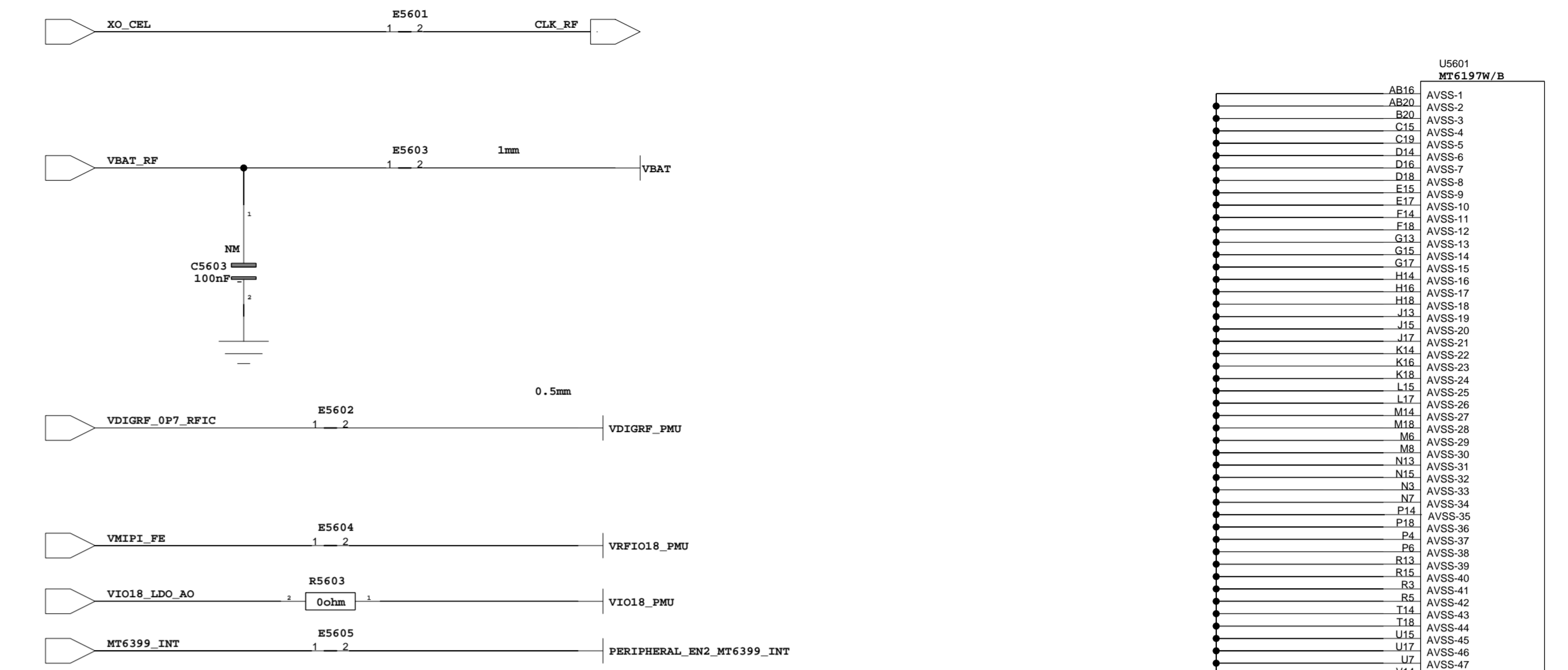


U5601
MT6197W/AMB-H

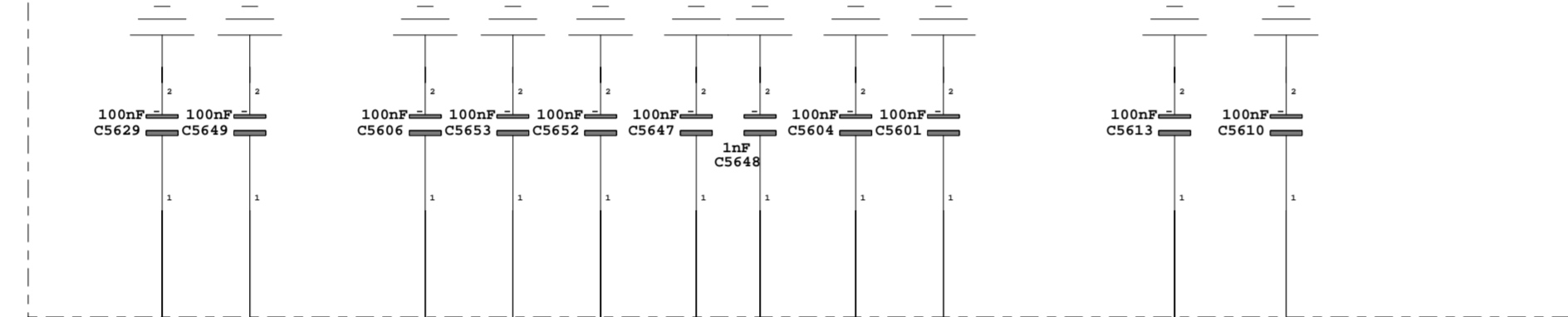
DRP_SCL_CN	D4	DRF_DL_CN
DRP_SCL_CP	E5	DRF_DL_CP
DRP_UL_LN0_DP	D2	DRF_DL_LN0_DN
DRP_UL_LN0_DP	E1	DRF_DL_LN0_DP
DRP_UL_LN1_DP	B2	DRF_DL_LN1_DN
DRP_UL_LN1_DP	C1	DRF_DL_LN1_DP
DRP_UL_LN2_DP	B4	DRF_DL_LN2_DN
DRP_UL_LN2_DP	A5	DRF_DL_LN2_DP
DRP_UL_LN3_DP	G3	DRF_DL_LN3_DN
DRP_UL_LN3_DP	A3	DRF_DL_LN3_DP
DRP_UL_CN	F4	DRF_UL_CN
DRP_UL_CP	G4	DRF_UL_CP
DRP_UL_LN0_DP	J1	DRF_UL_LN0_DN
DRP_UL_LN0_DP	H2	DRF_UL_LN0_DP
DRP_UL_LN1_DP	J3	DRF_UL_LN1_DN
DRP_UL_LN1_DP	H4	DRF_UL_LN1_DP

U5601
MT6197W/B

MIPI_S_SCLK	L5	MIPI_S_SCLK
MIPI_S_SDATA	K6	MIPI_S_SDATA
MIPI0_A_SCLK	C9	MIPI0_A_SCLK
MIPI0_A_SDATA	B10	MIPI0_A_SDATA
MIPI1_A_SCLK	C7	MIPI1_A_SCLK
MIPI1_A_SDATA	B8	MIPI1_A_SDATA
MIPI2_A_SCLK	F8	MIPI2_A_SCLK
MIPI2_A_SDATA	E9	MIPI2_A_SDATA
MIPI3_A_SCLK	G9	MIPI3_A_SCLK
MIPI3_A_SDATA	F10	MIPI3_A_SDATA
MIPI4_A_SCLK	J11	MIPI4_A_SCLK
MIPI4_A_SDATA	H12	MIPI4_A_SDATA
MIPI5_A_SCLK	M12	MIPI5_A_SCLK
MIPI5_A_SDATA	L11	MIPI5_A_SDATA
AVSS3	W13	AVSS3
BPI_BUS0	G7	BPI_BUS0
BPI_BUS1	A16	BPI_BUS1
BPI_BUS2	A13	BPI_BUS2
BPI_BUS3	B14	BPI_BUS3
BPI_BUS4	C13	BPI_BUS4
BPI_BUS5	N9	BPI_BUS5
BPI_BUS6	N11	BPI_BUS6
BPI_BUS7	P12	BPI_BUS7
UCNT	R11	UCNT
DVSS1	G7	DVSS1
AVSS1	L9	AVSS1
AVSS2	A17	AVSS2
	B16	AVSS2



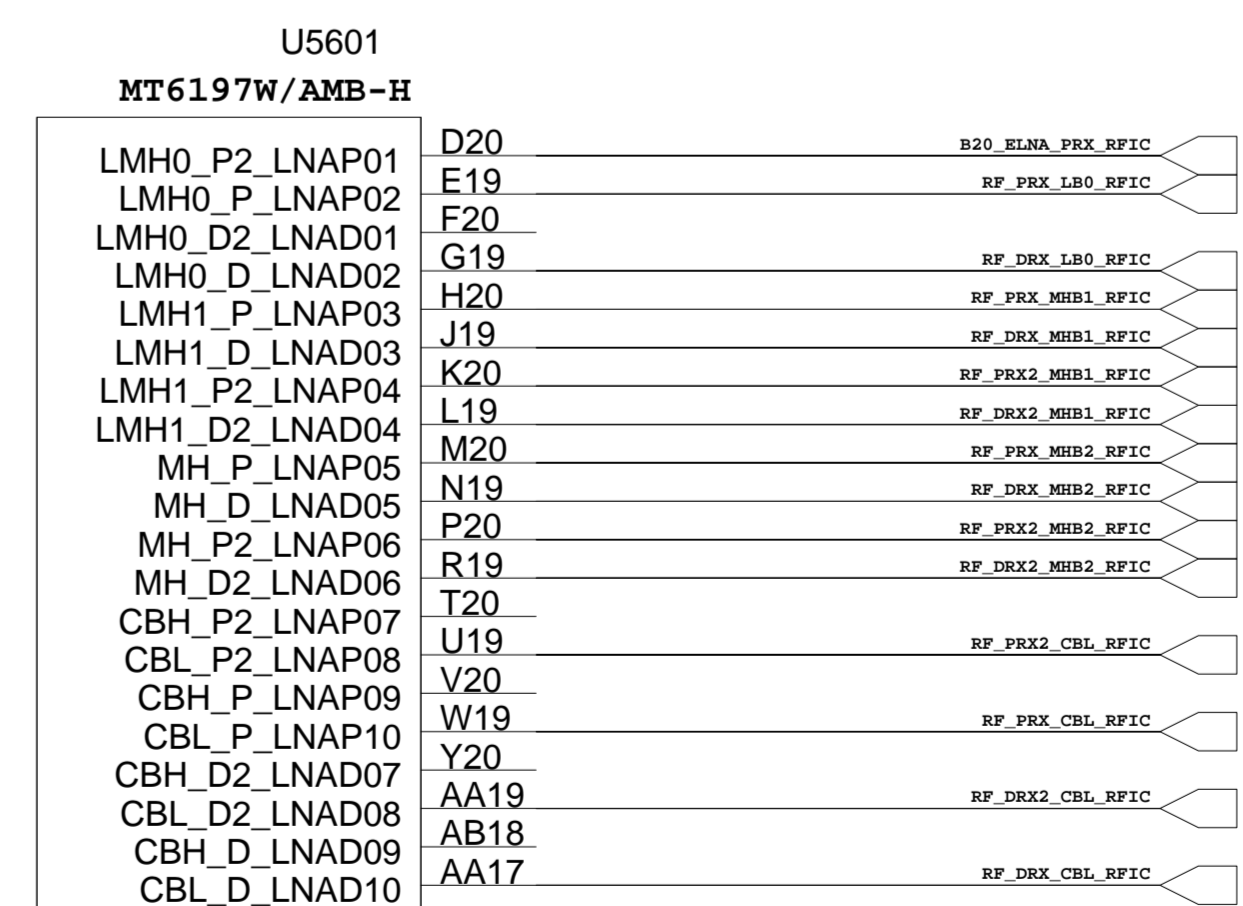
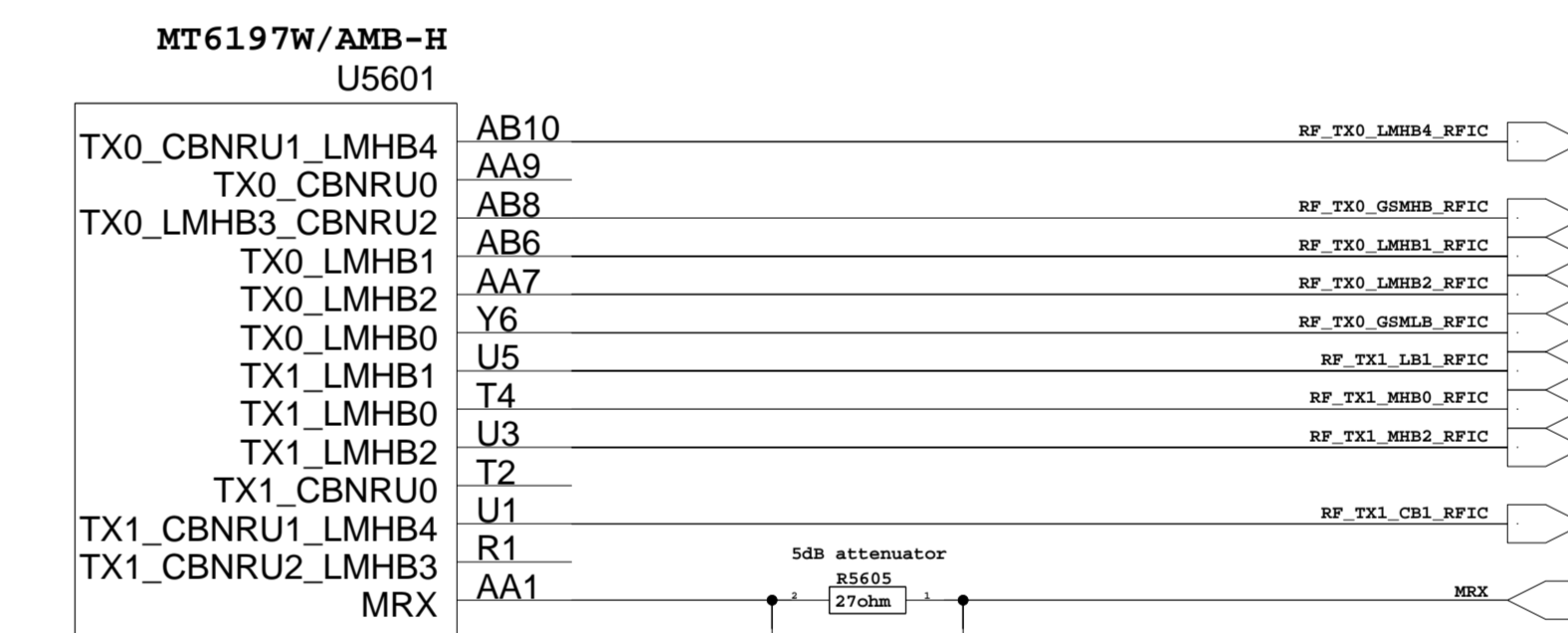
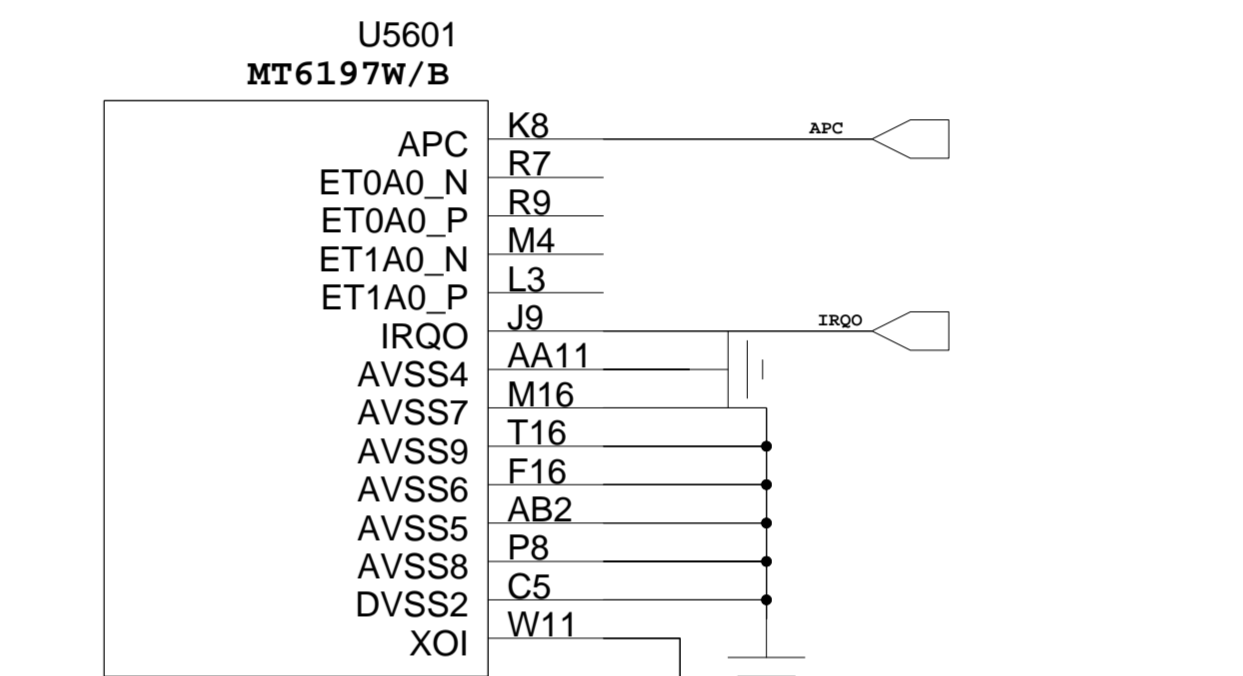
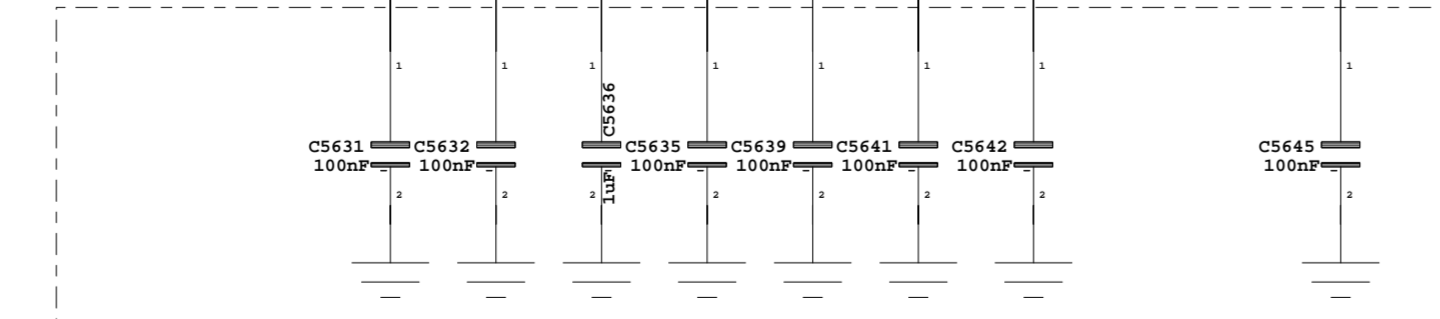
Capacitor CLOSE TO RFIC PIN



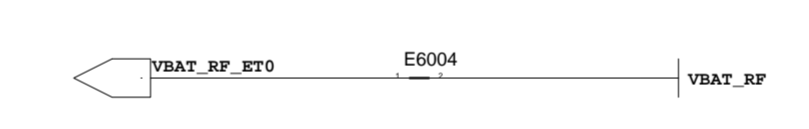
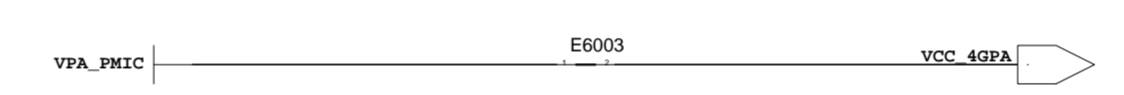
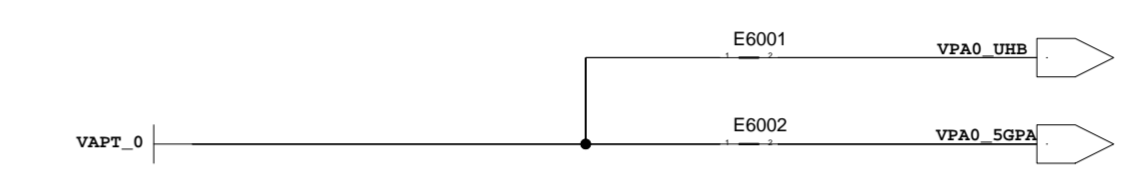
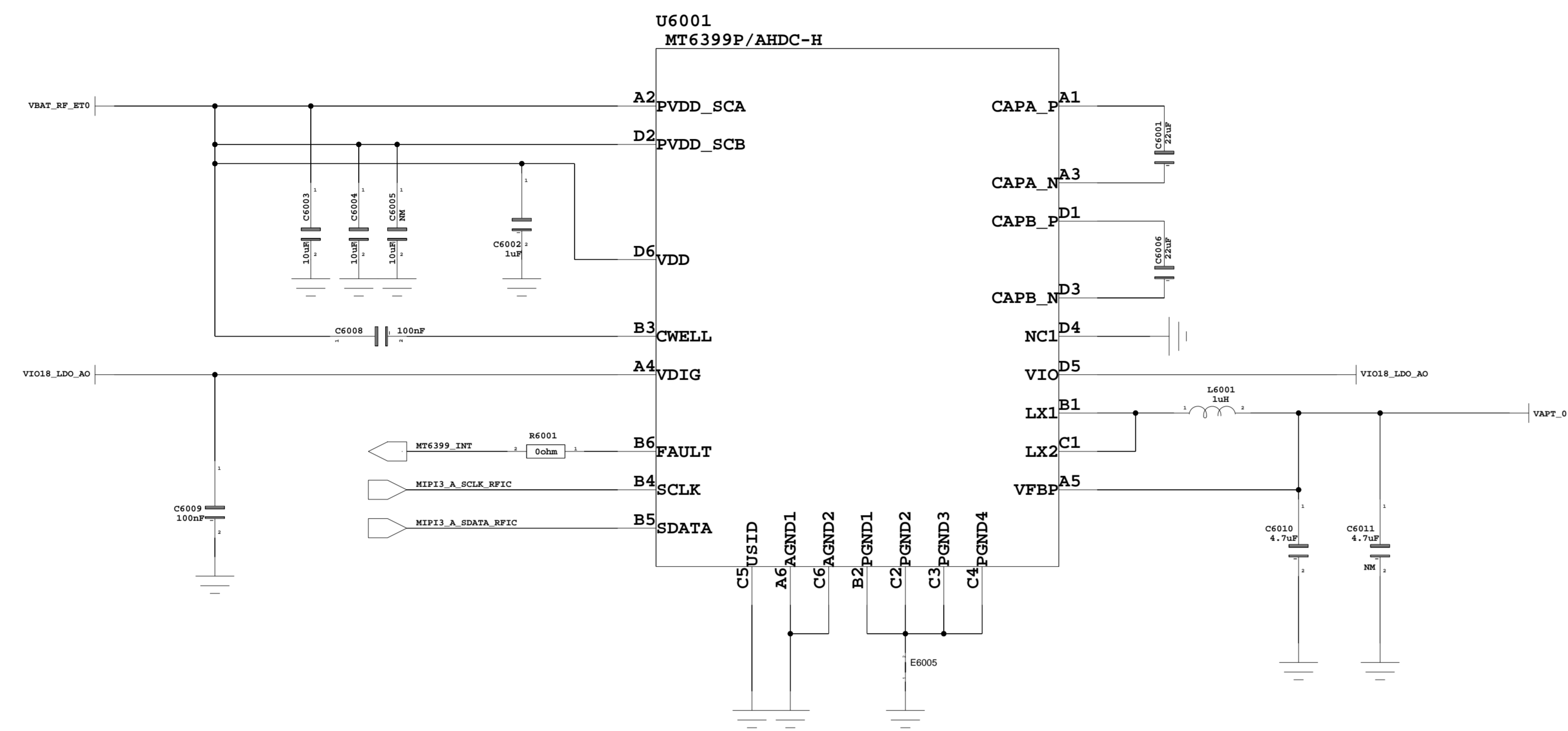
U5601
MT6197W/AMB-H

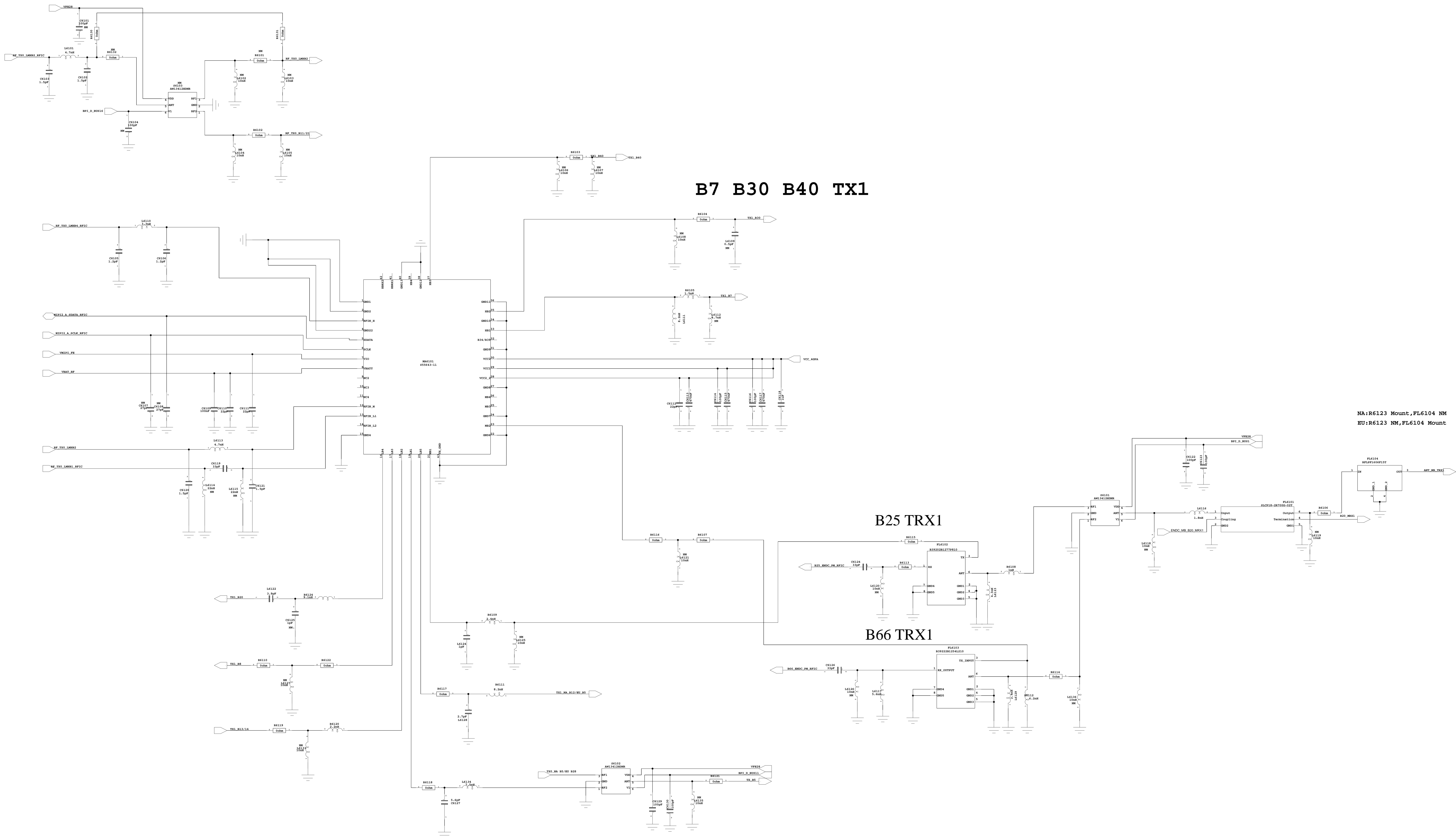
W17	AVDD09_RX_L0
A19	AVDD09_RX_U0
N17	AVDD09_RX_U1
L13	AVDD09_SX0_LF
U13	AVDD09_SX1_LF
E13	AVDD09_SX2_LF
AB4	AVDD09_SX6_LF
N5	AVDD09_SX7_LF
AA3	AVDD09_TX0
M2	AVDD09_TX1
G1	AVDD12_DRF
AA15	AVDD12_RX_L0
P16	AVDD12_RX_L1
B18	AVDD12_RX_U0
T6	AVDD12_TX0
P2	AVDD12_TX1
F2	AVDD18_DRF
R17	AVDD18_RX_L0
C17	AVDD18_RX_L1
AB14	AVDD18_TOP
AA5	AVDD18_TX0
N1	AVDD18_TX1
A7	DVDD07_DRF-1
E7	DVDD07_DRF-2
J7	DVDD07_DRF-3
M10	DVDD07_DRF-4
B6	DVDD0P85_MEM
A11	DVDD0P9_AFE
B12	DVDD12_AFE
AA13	DVDD18_AFE
A9	DVDDIO_DRF_0
L1	DVDDIO_DRF_1
AB12	VIO18

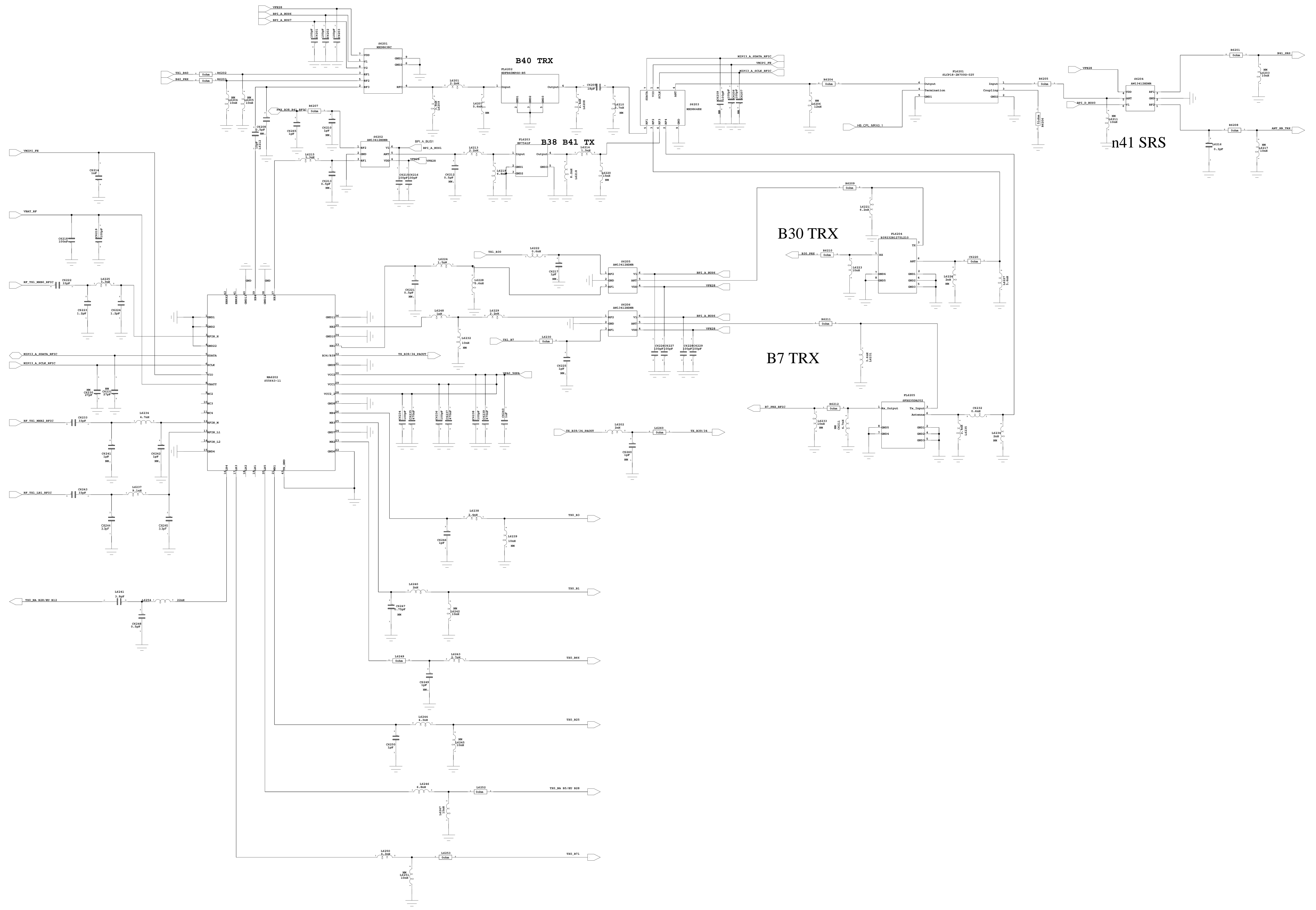
Capacitor CLOSE TO RFIC PIN



MT6399P_0







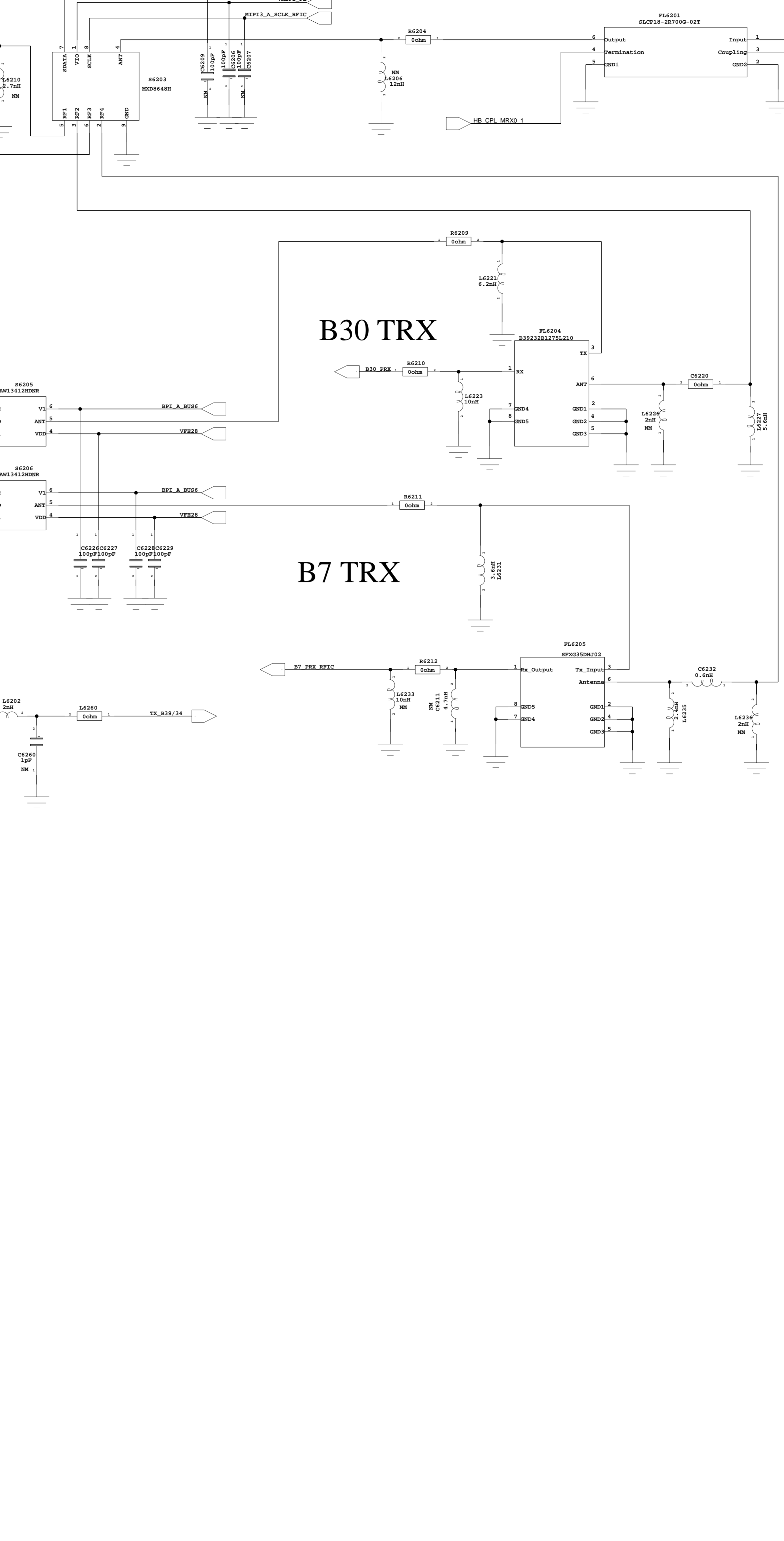
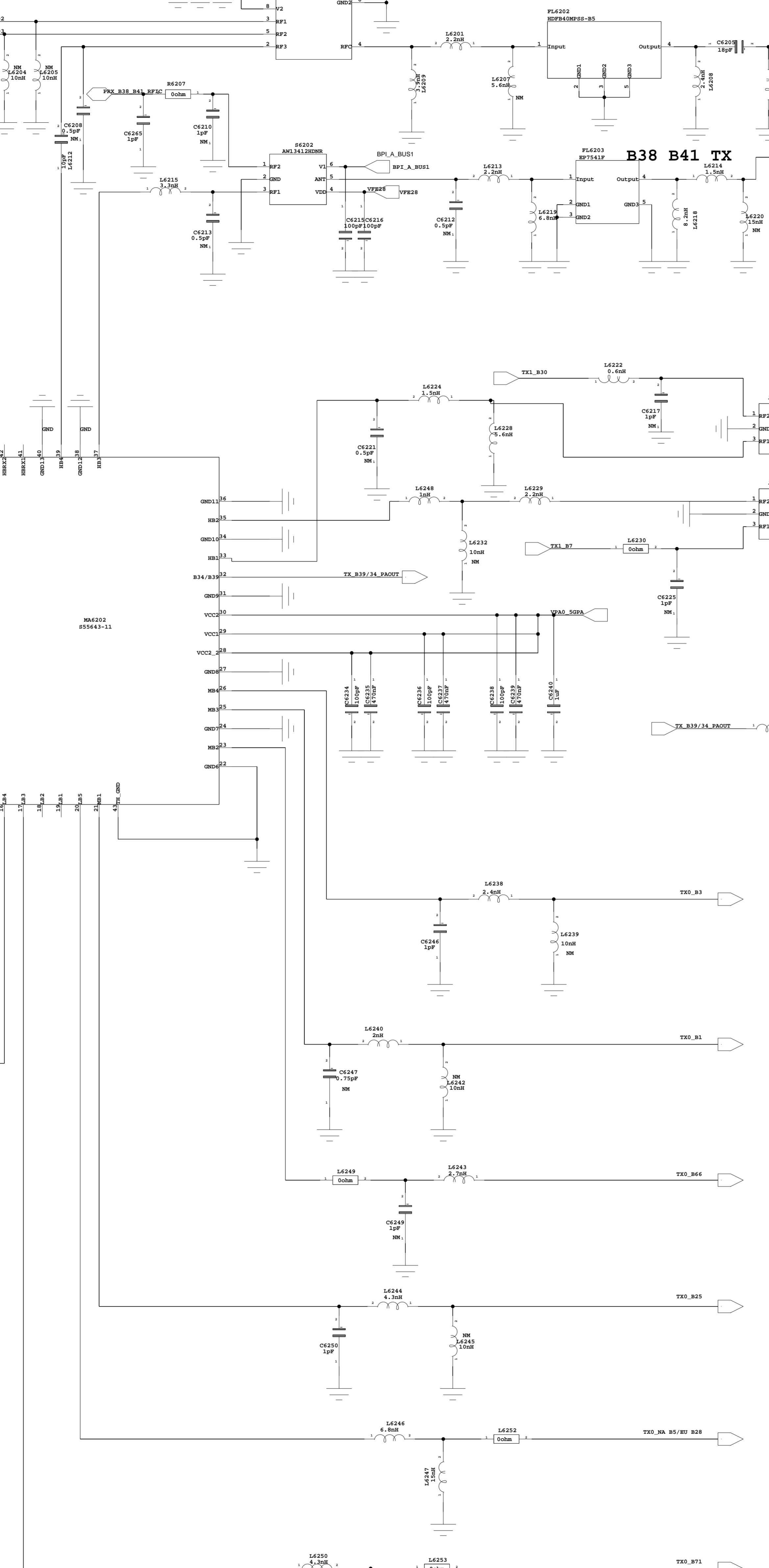
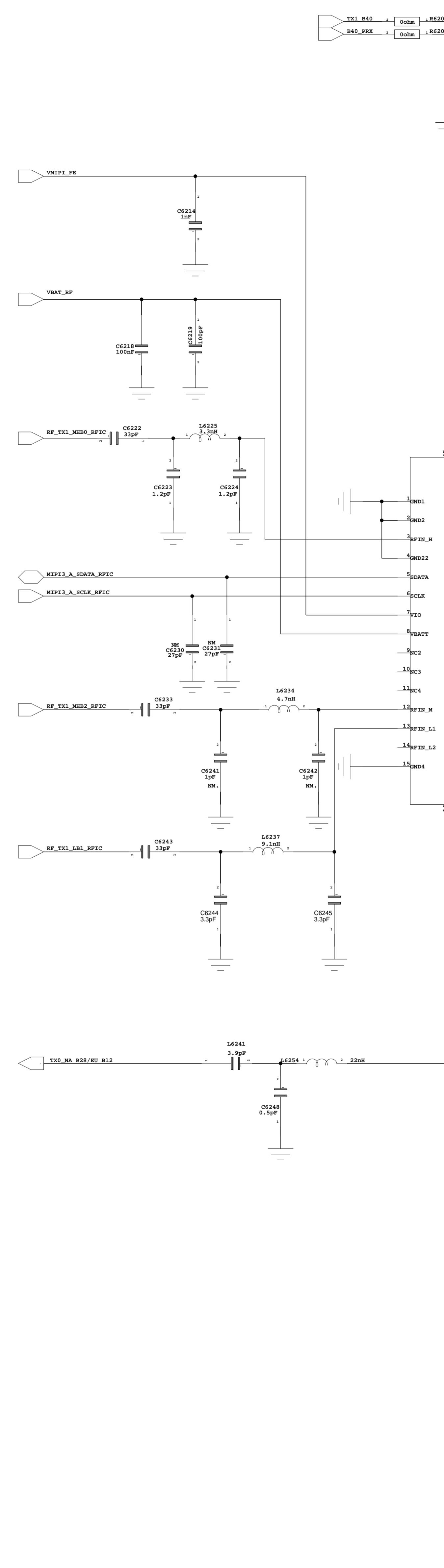
B40 TRX

B38 B41 TX

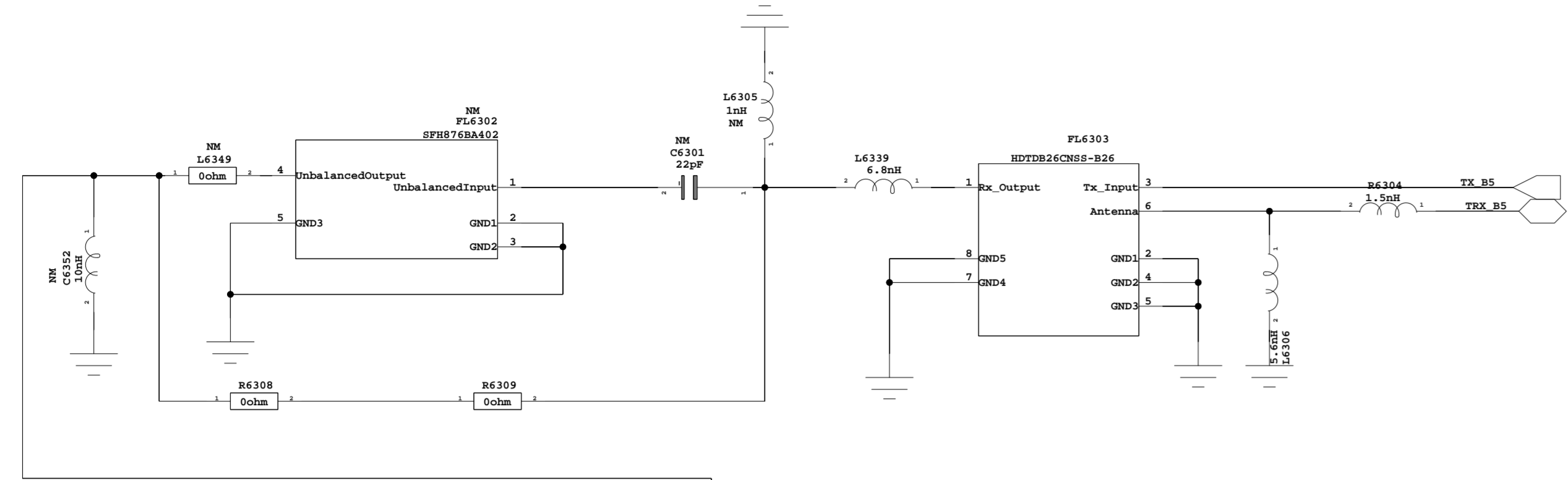
B30 TRX

B7 TRX

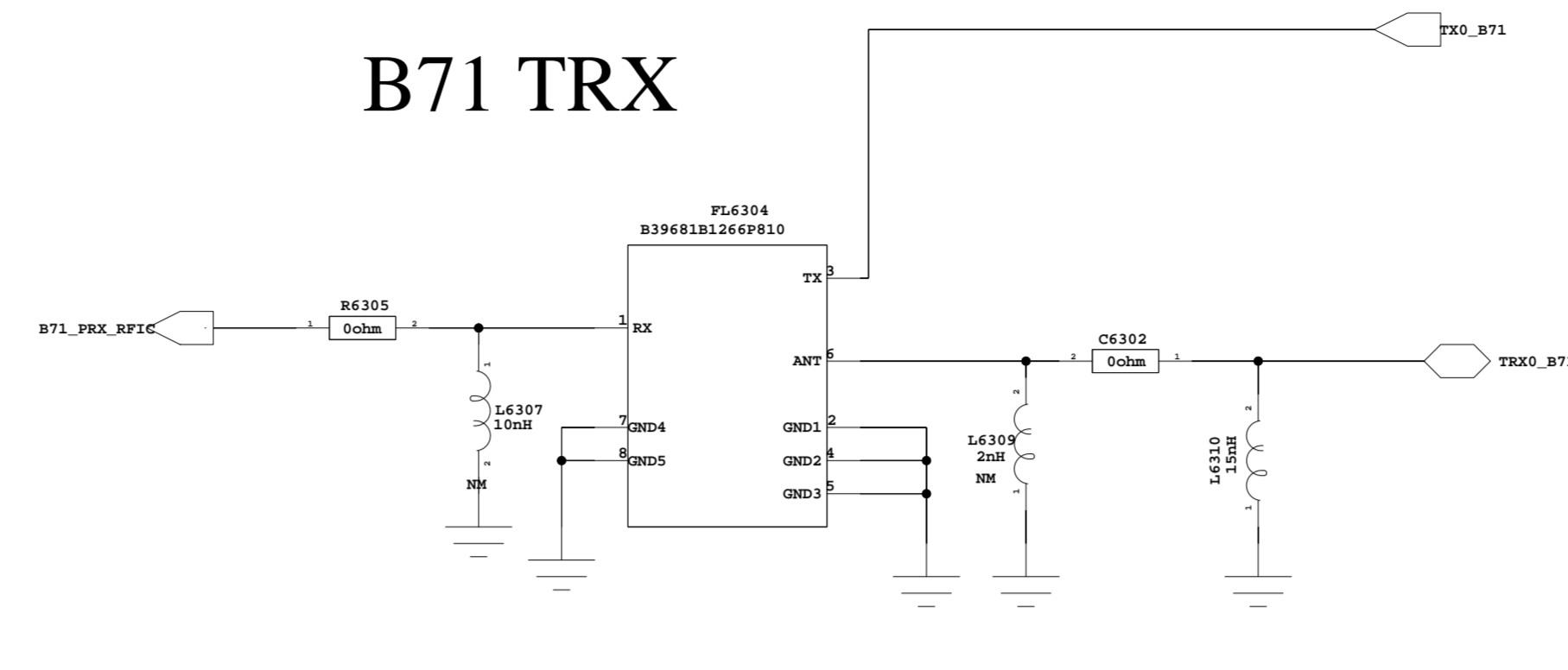
n41 SRS



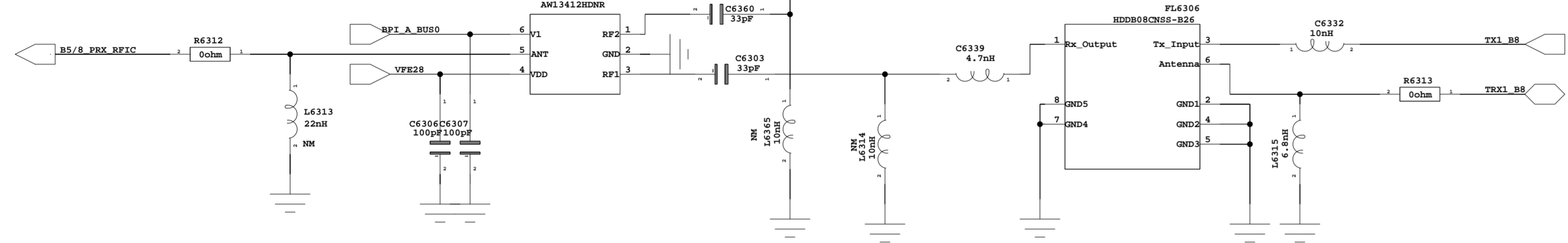
B5/B26 TRX



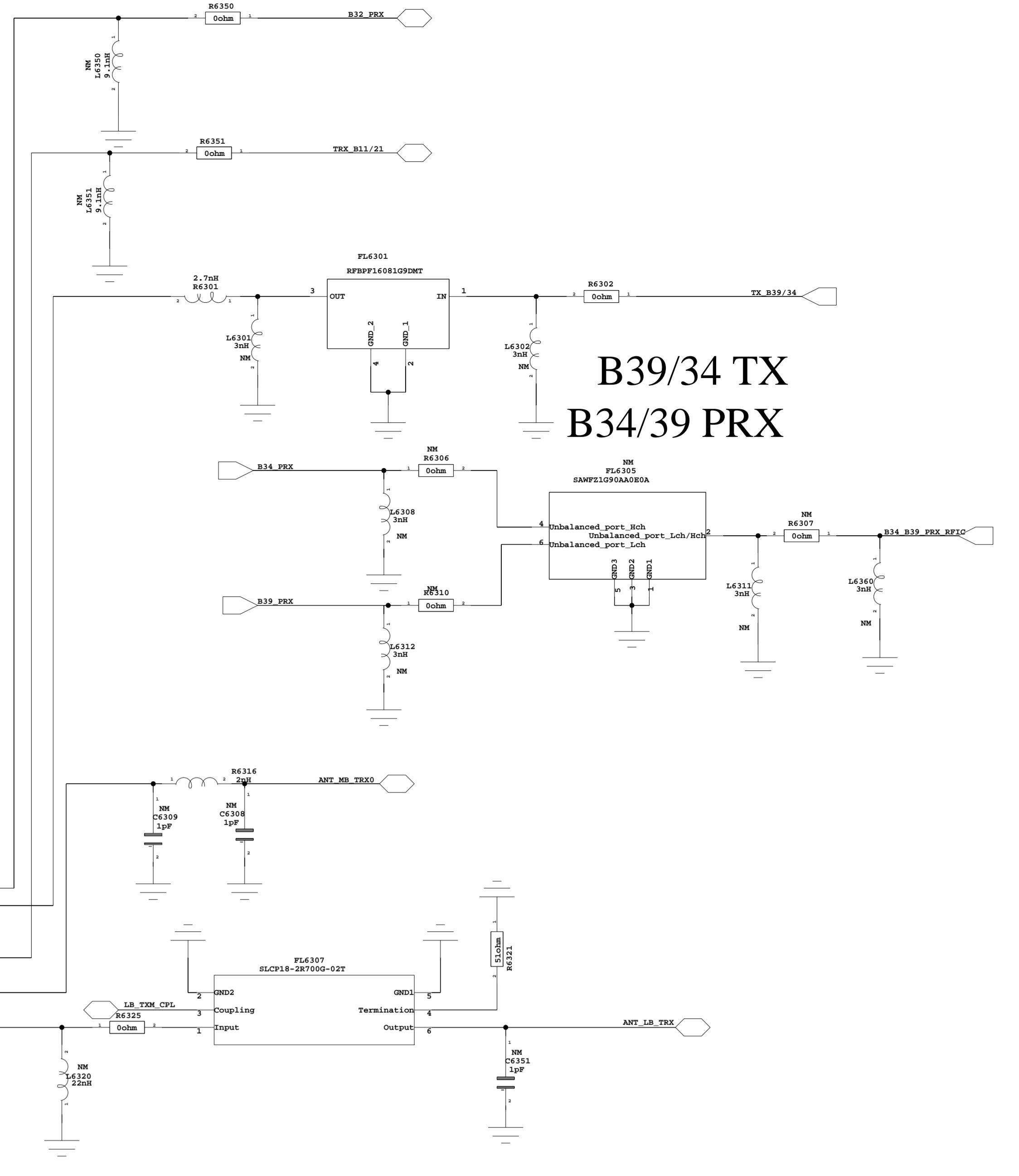
B71 TRX



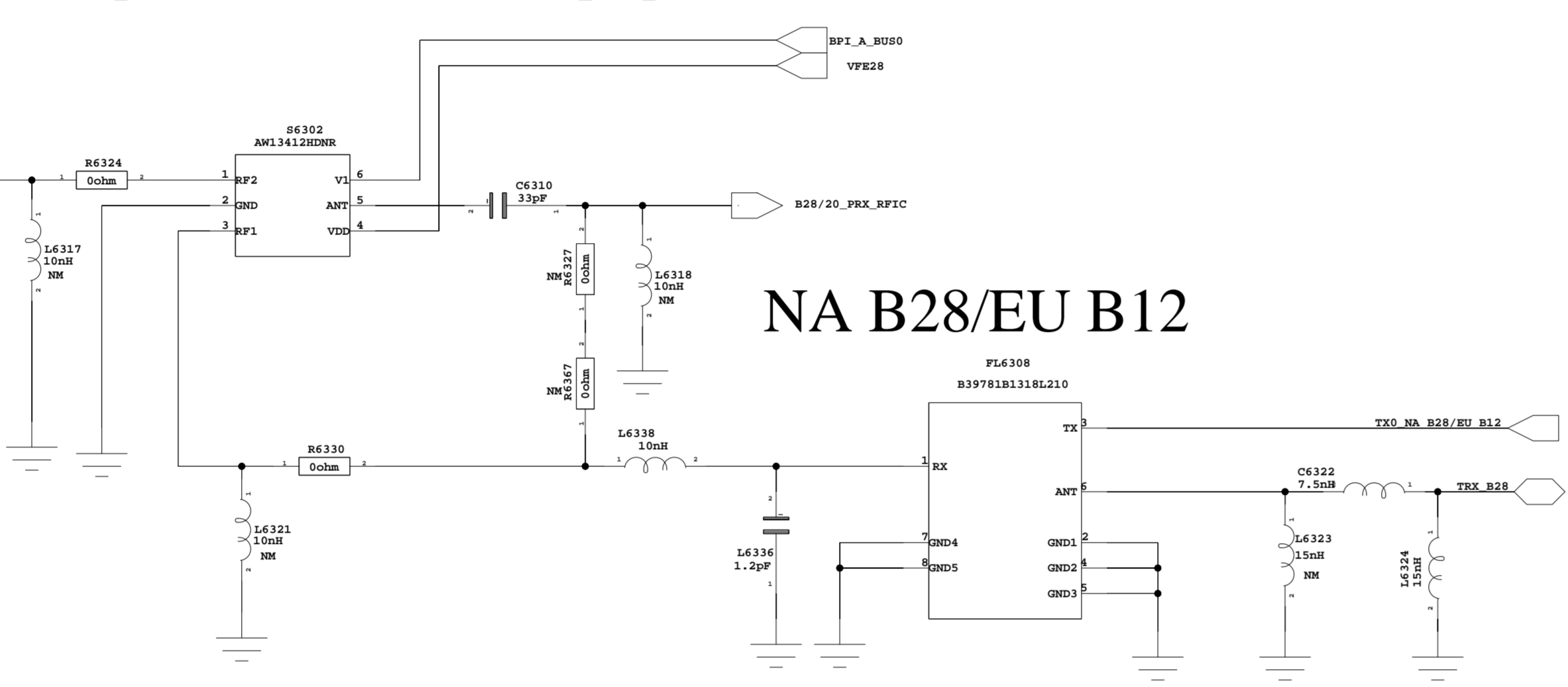
B8 TRX



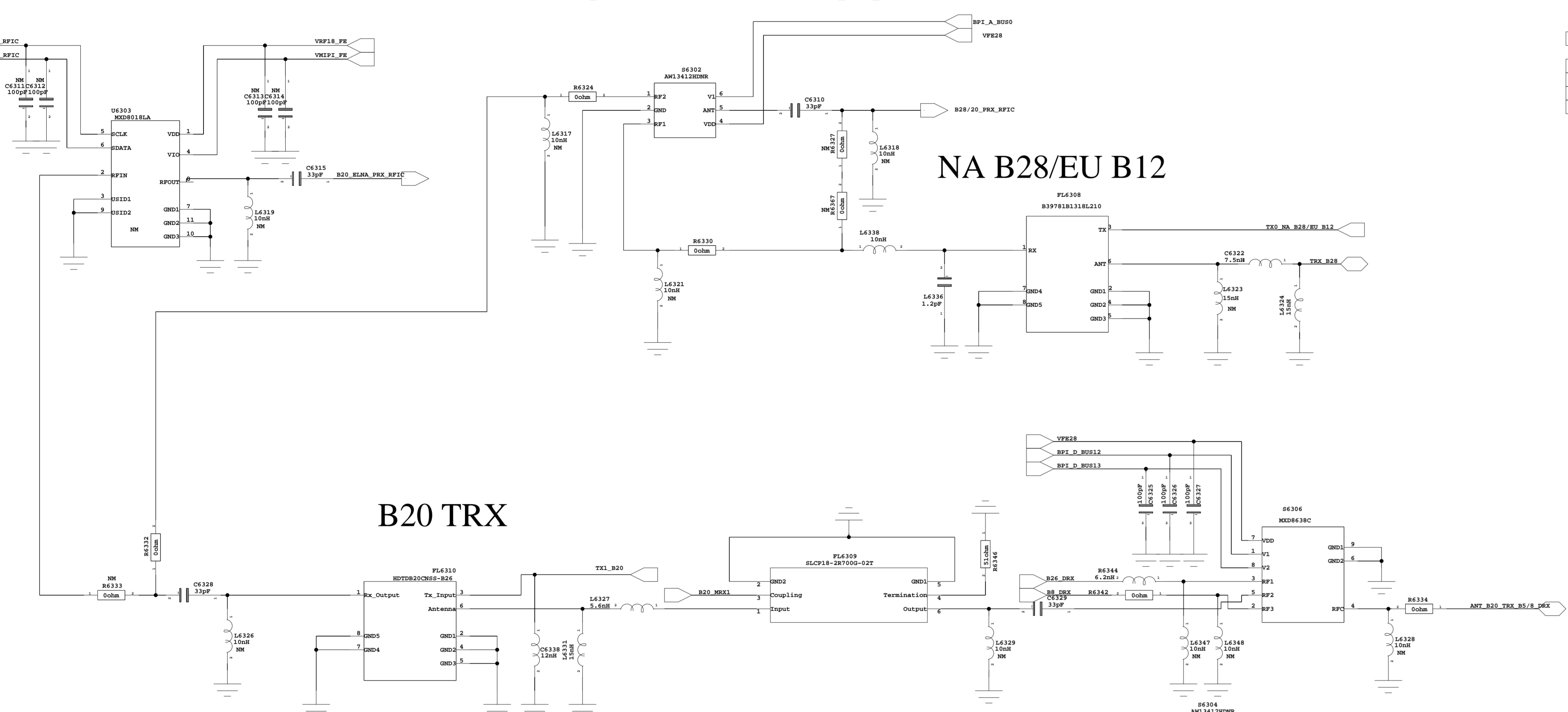
B39/34 TX B34/39 PRX



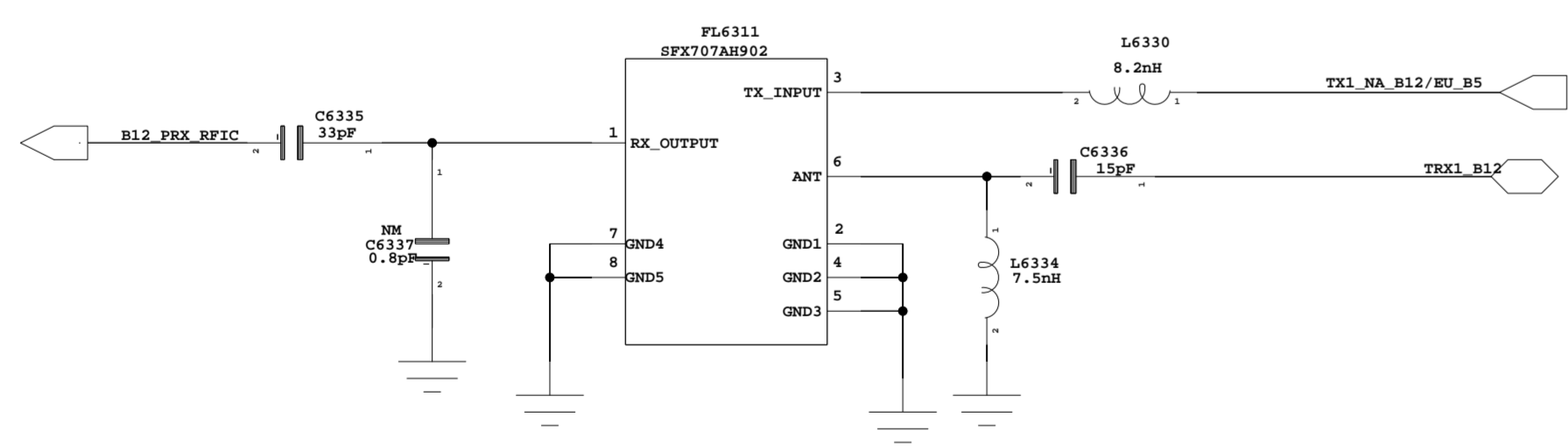
NA B28/EU B12



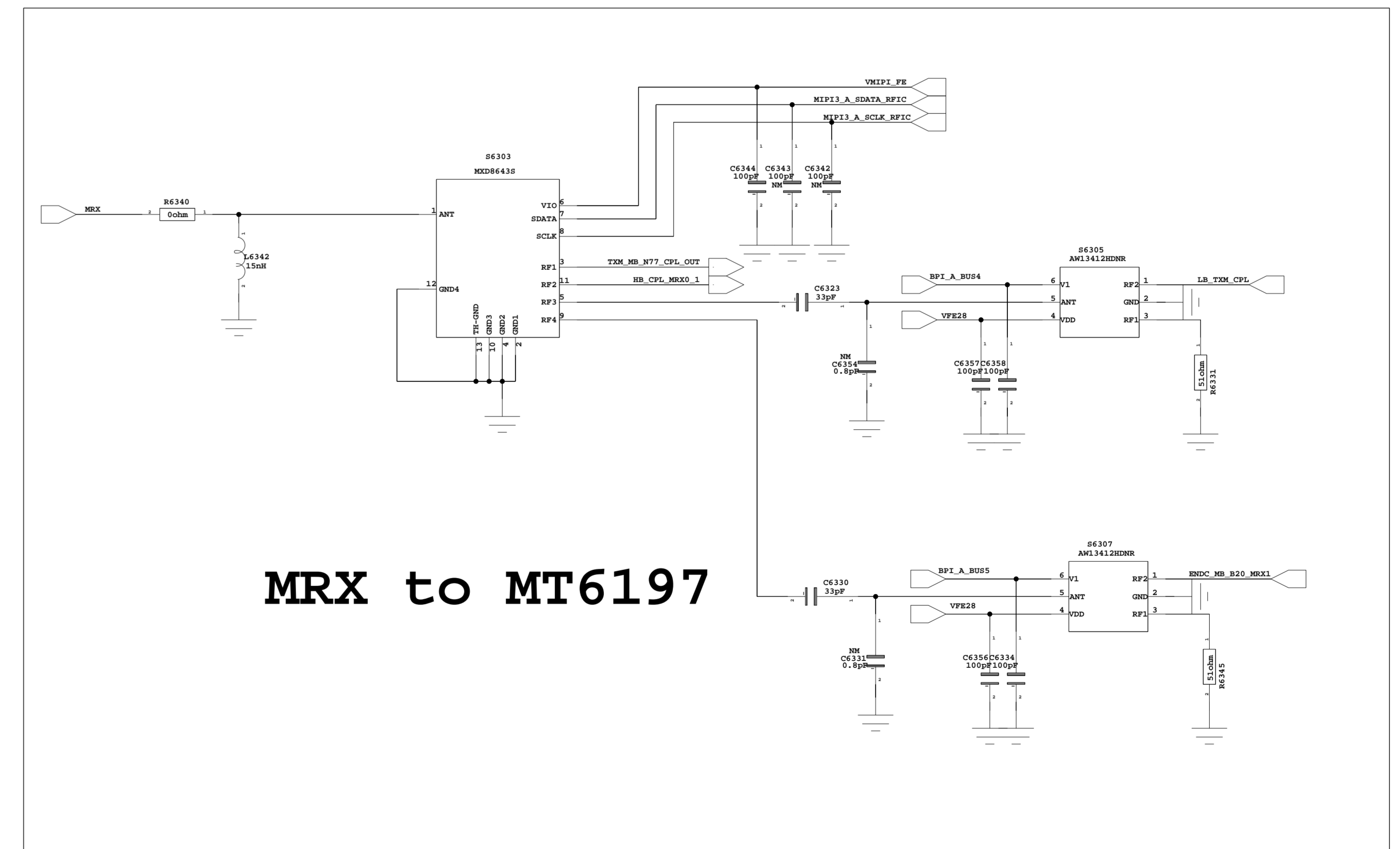
B20 TRX



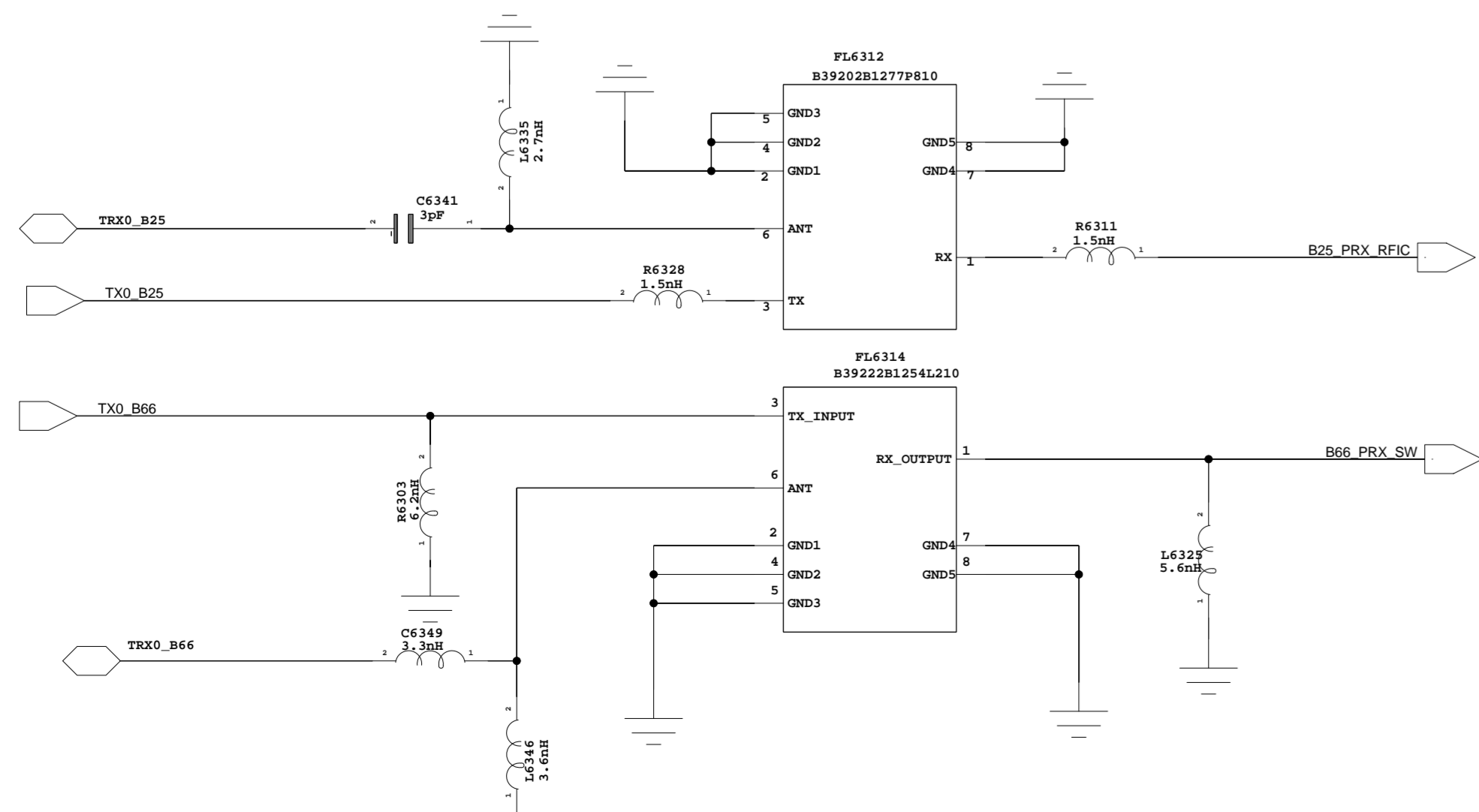
NA B12/EU B5



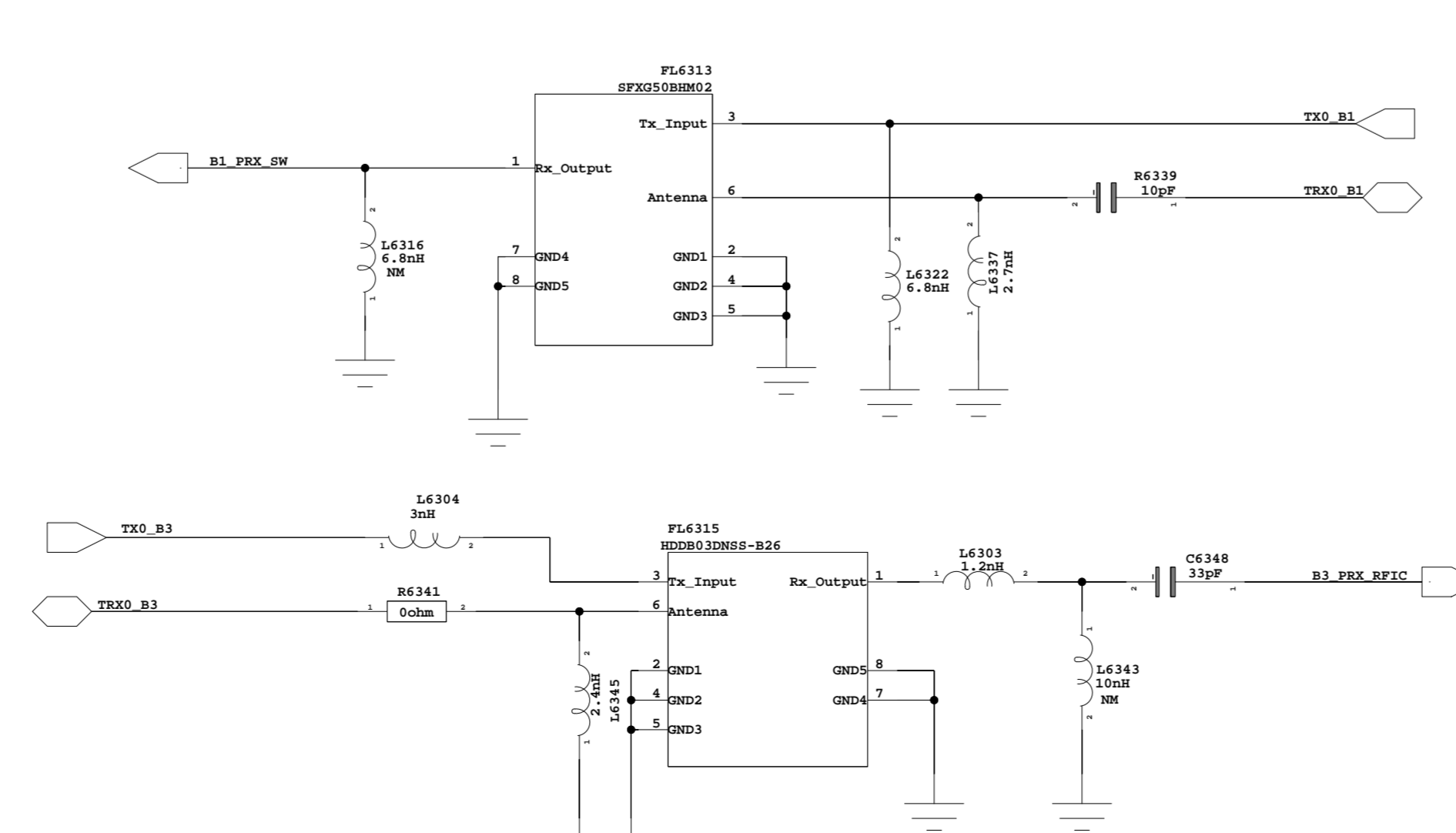
MRX to MT6197

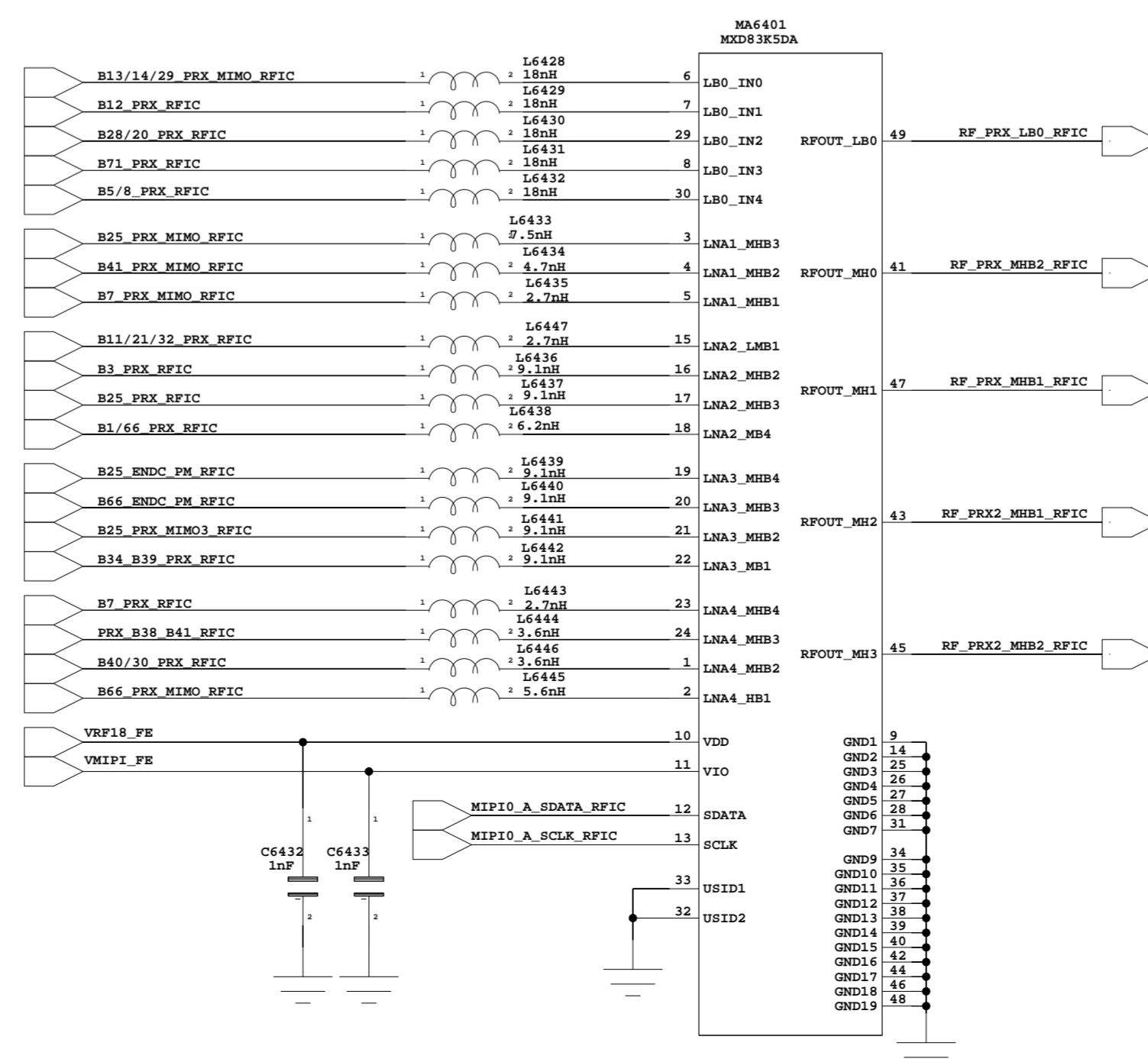
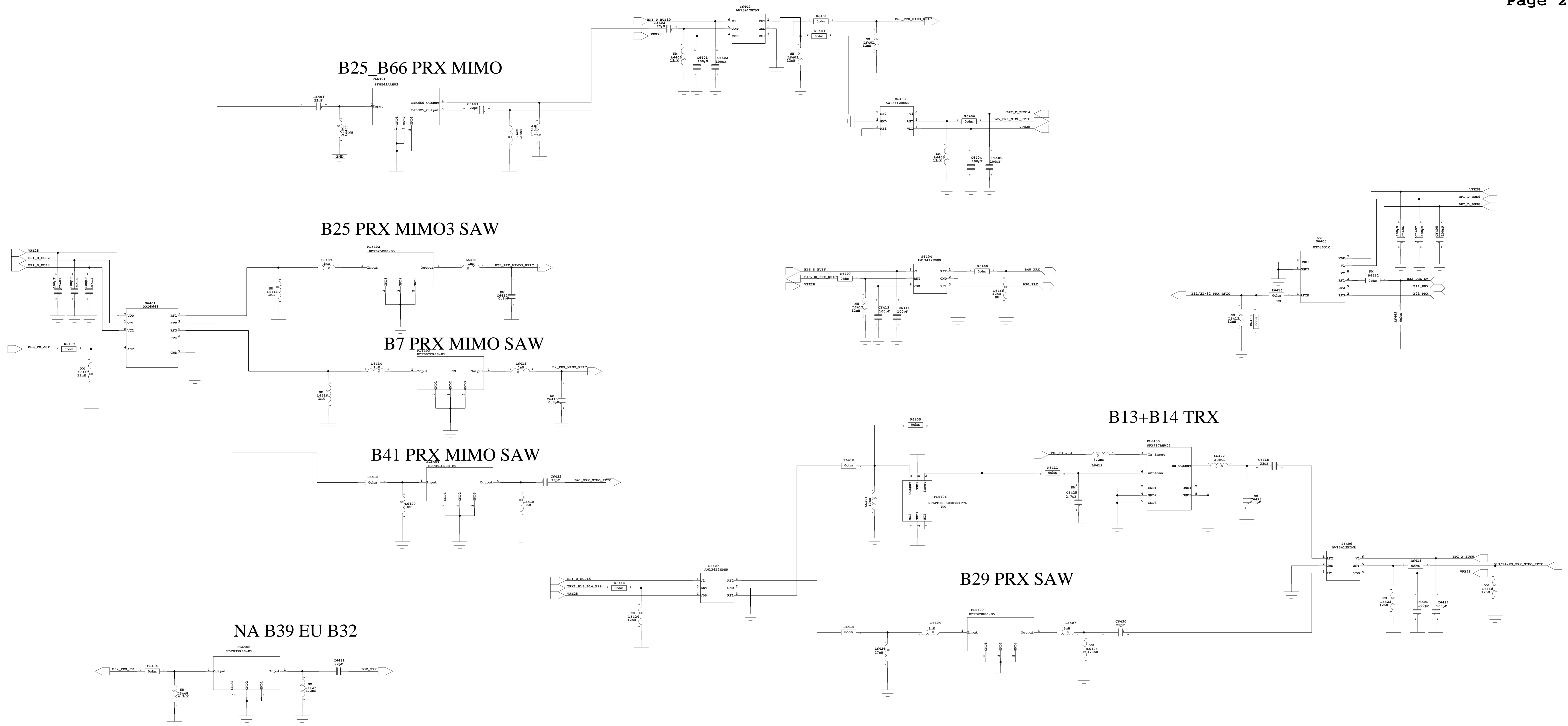


B25_B66 TRX0



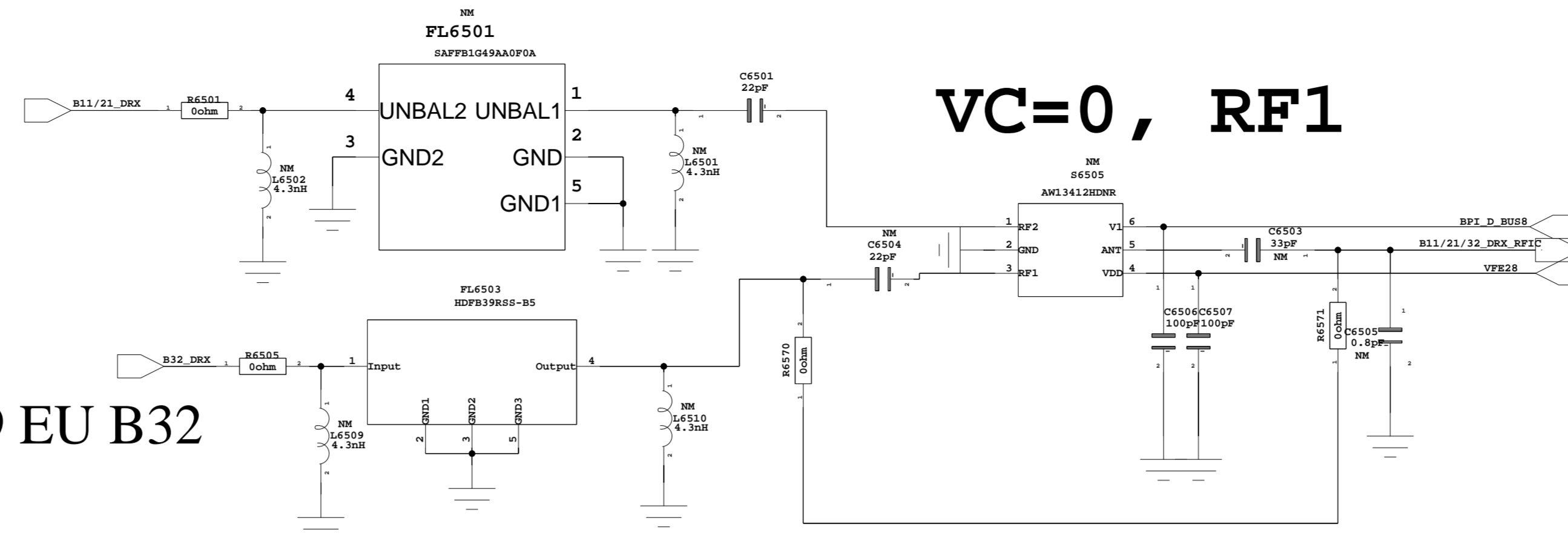
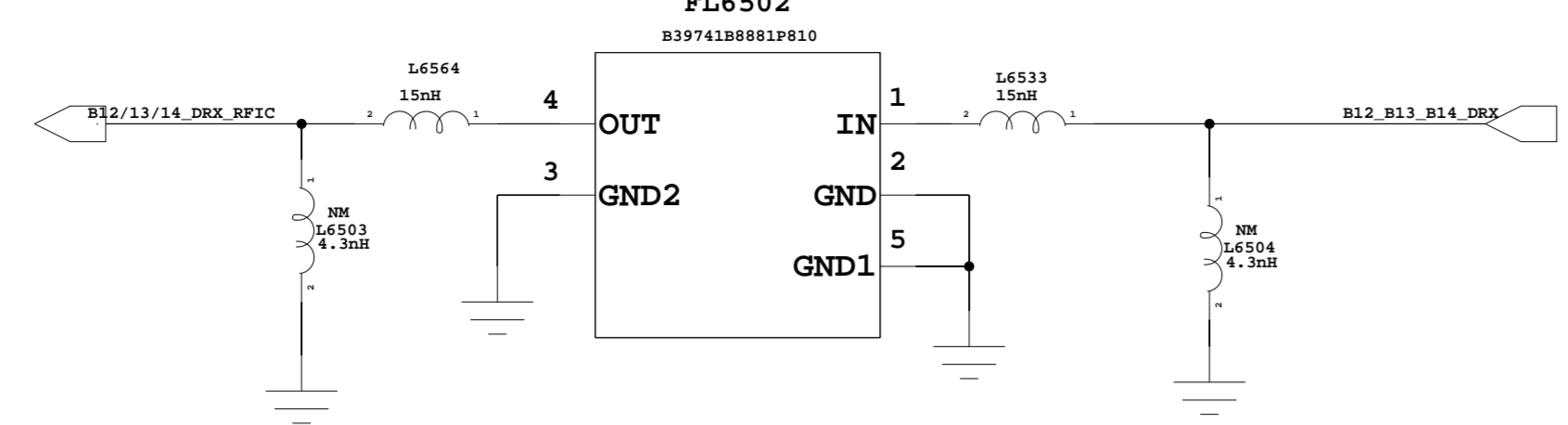
B1_B3 TRX0



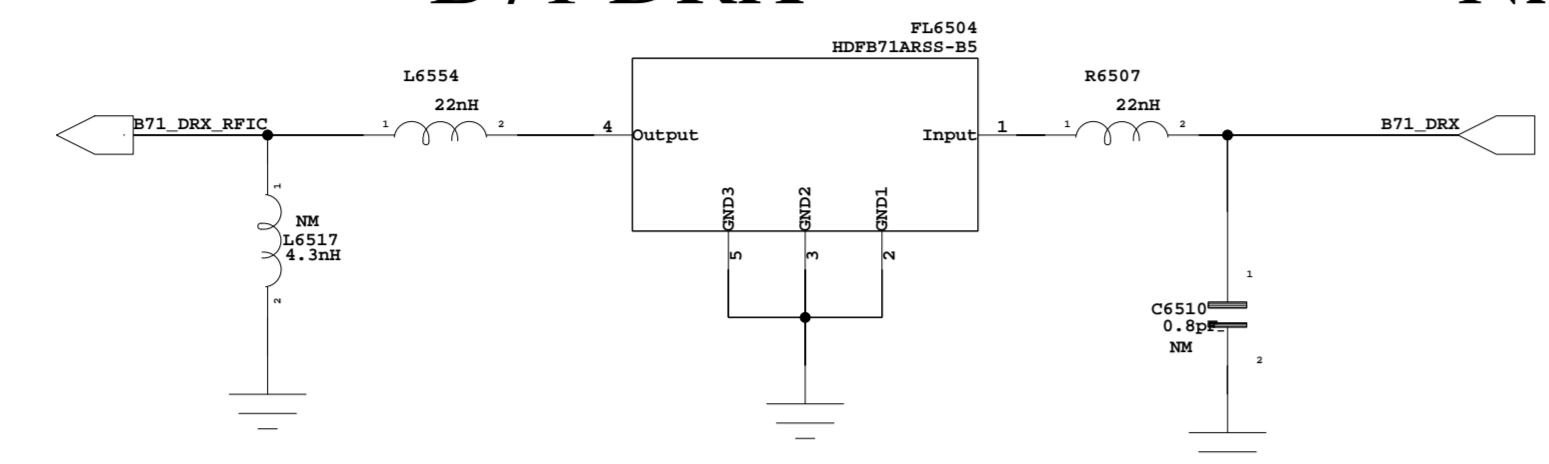


B11/21 DRX

B12/13/14 DRX

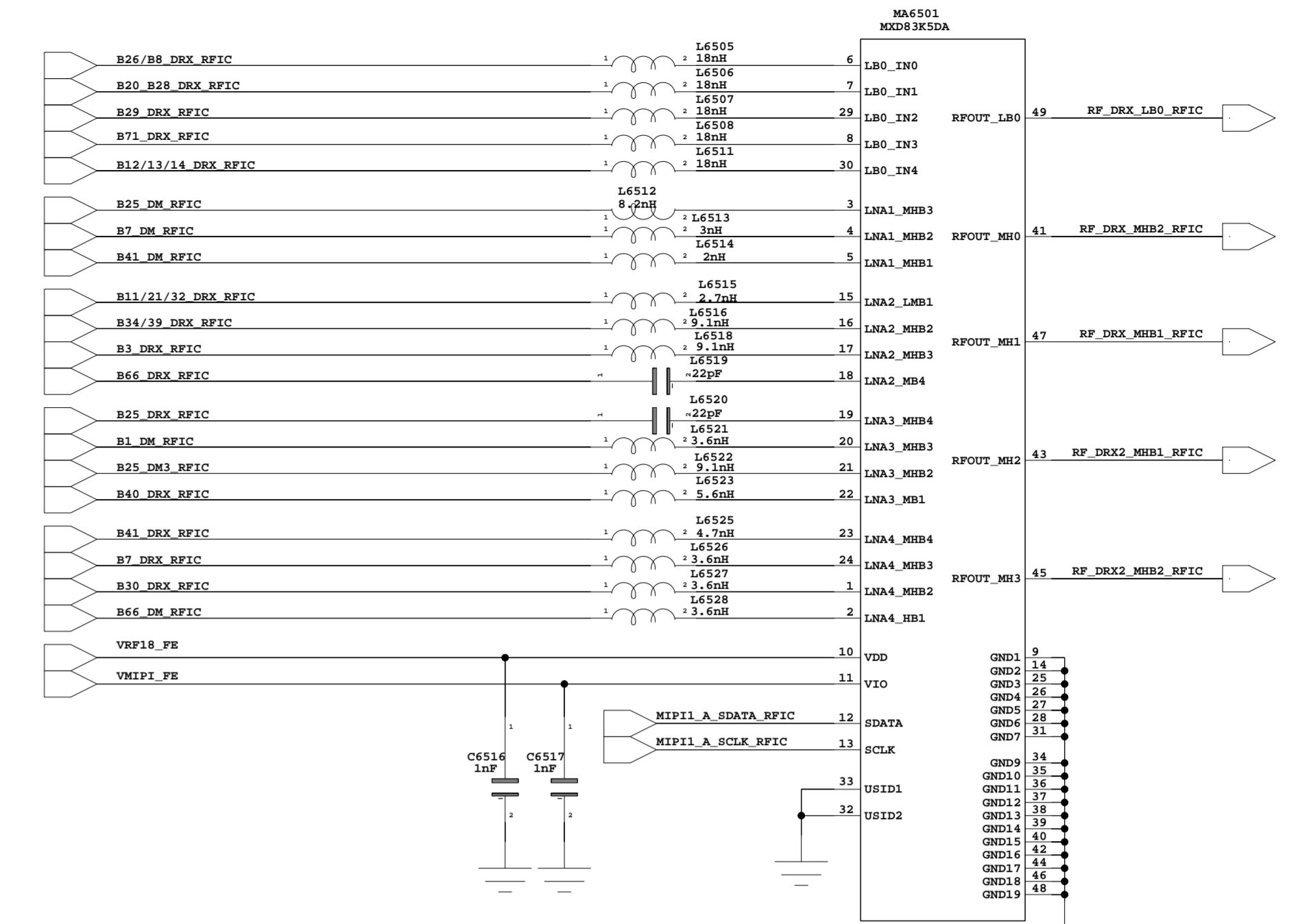
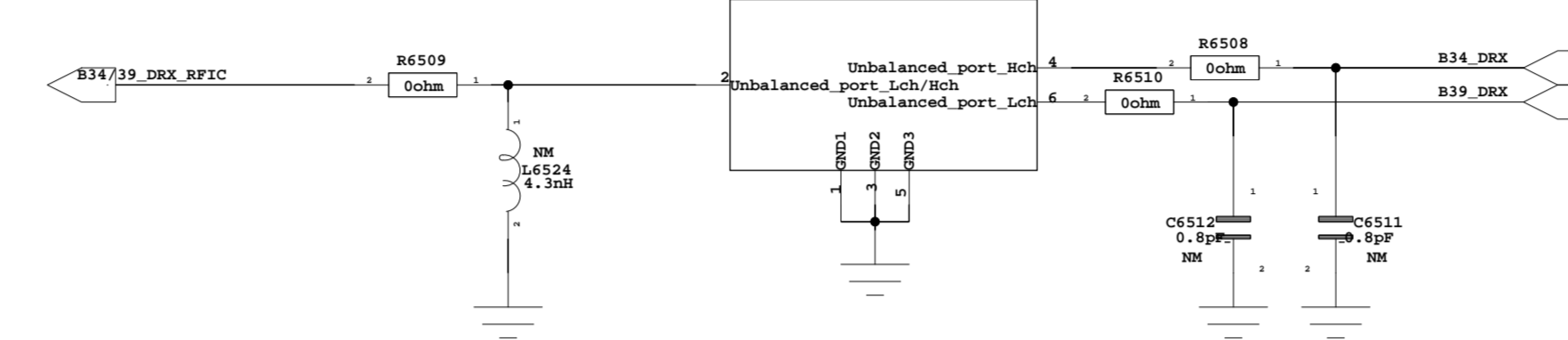


B71 DRX

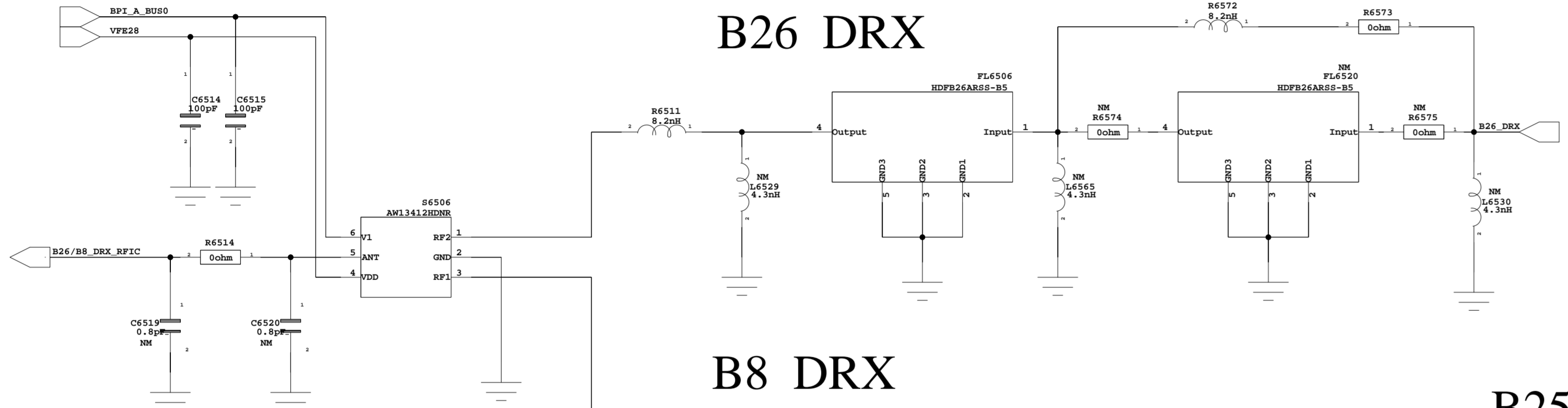


NA B39 EU B32

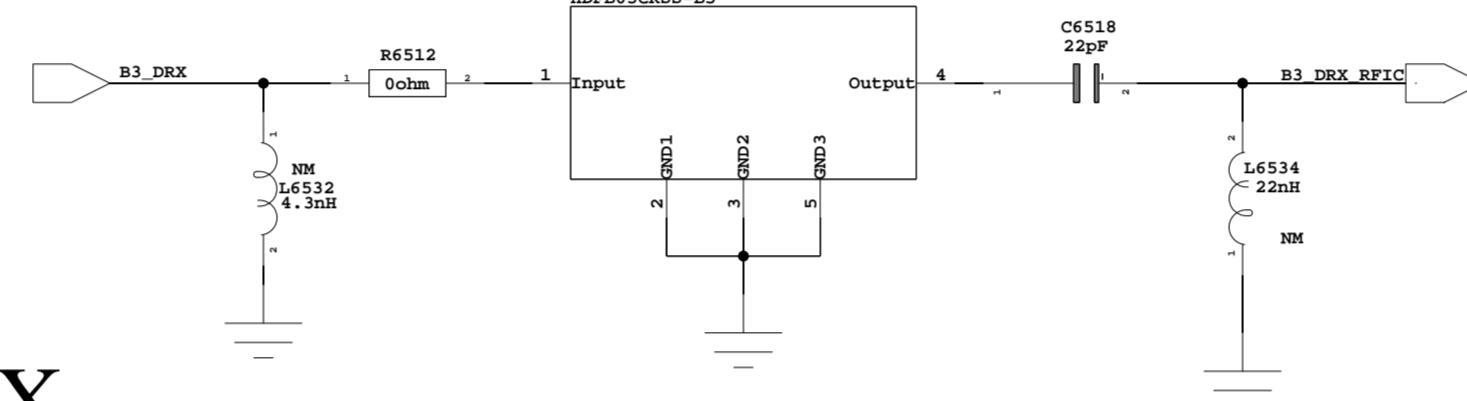
B34/39 DRX



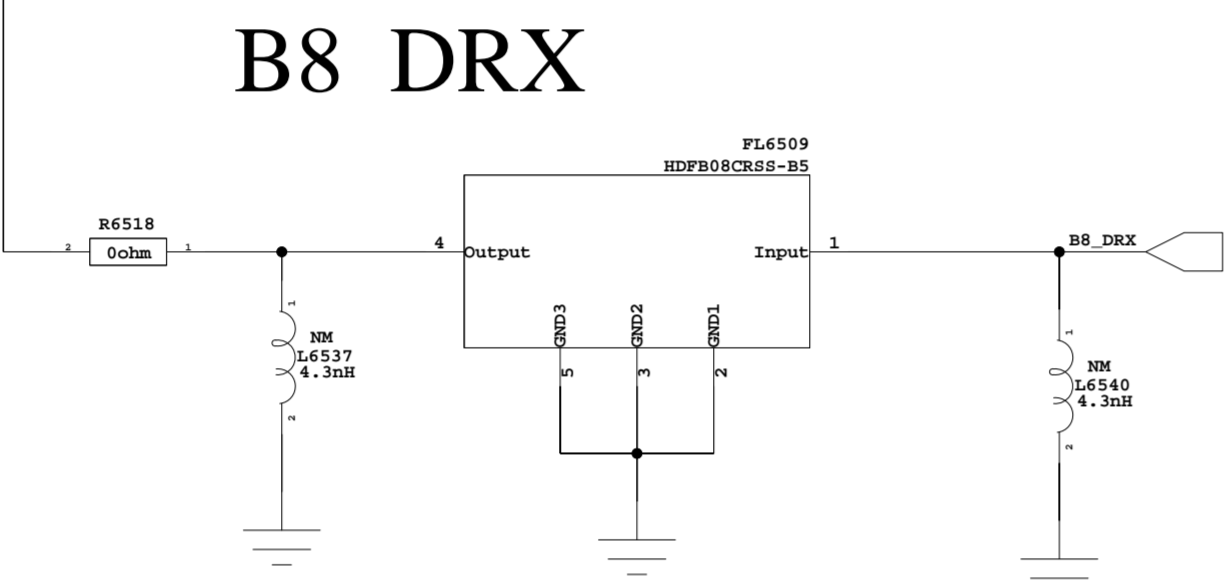
B26 DRX



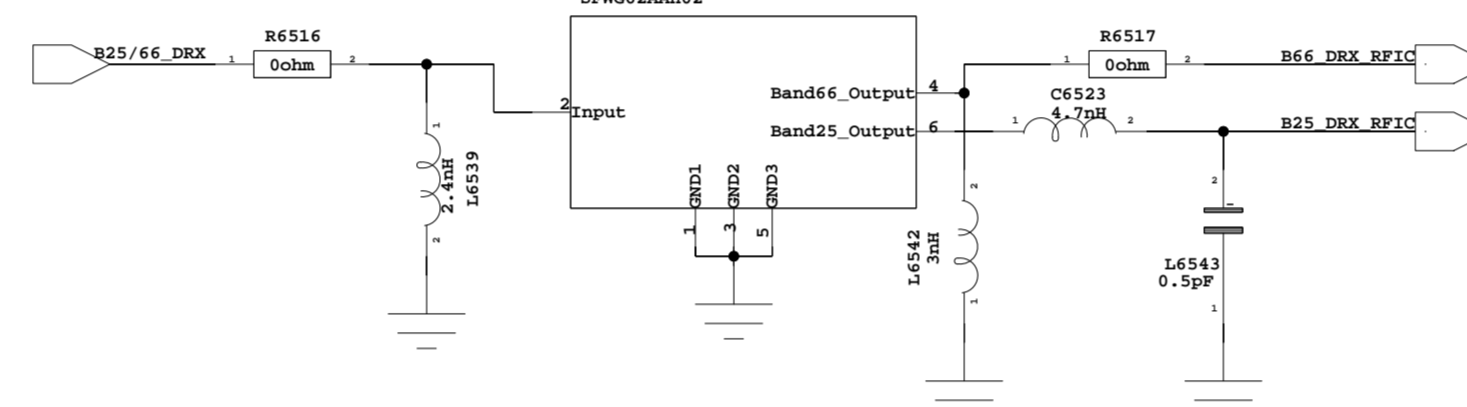
B3 DRX



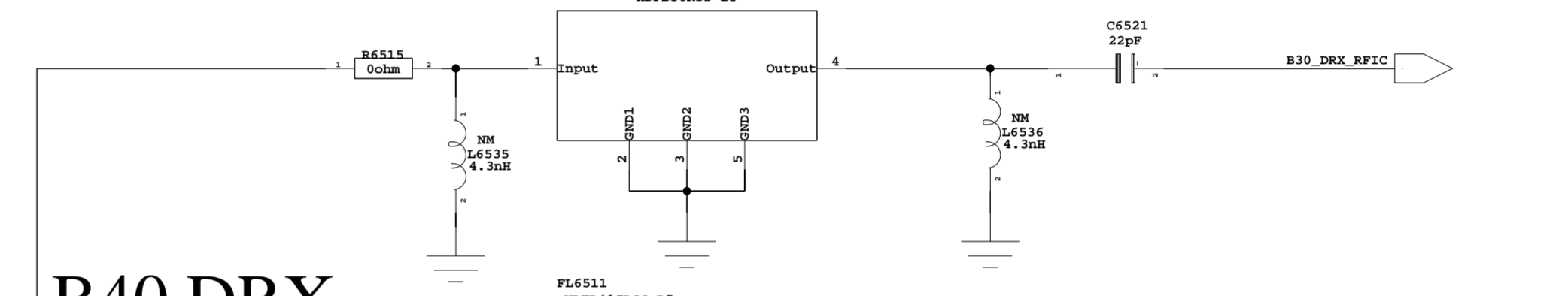
B8 DRX



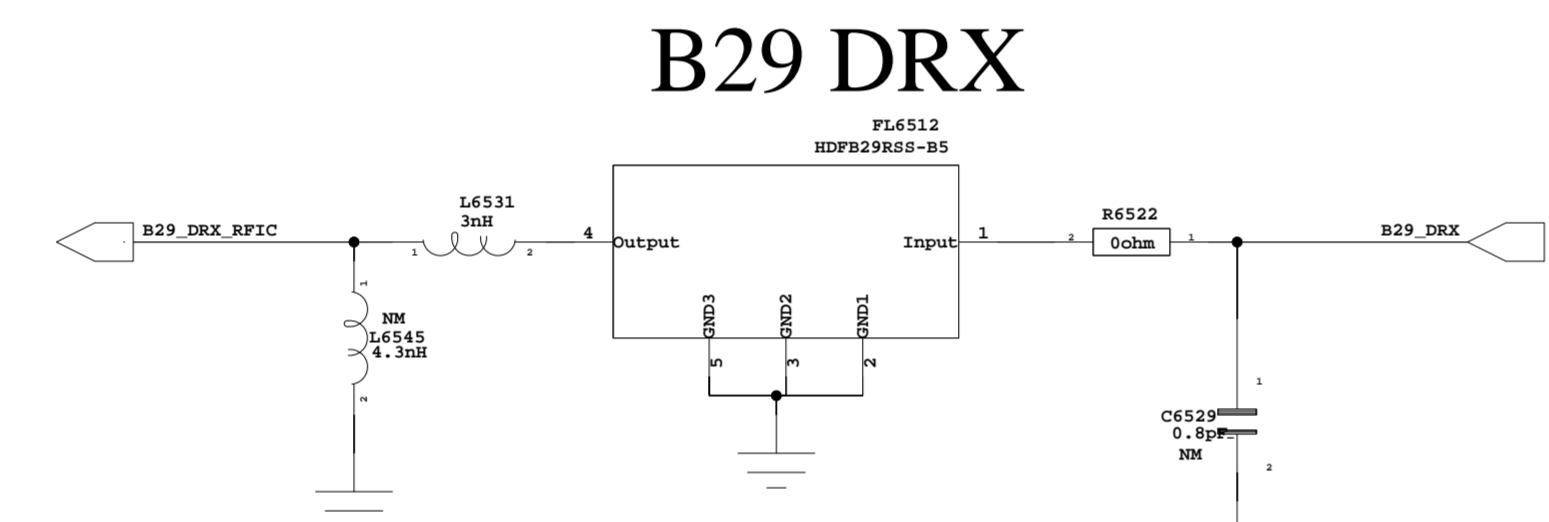
B25/66 DRX



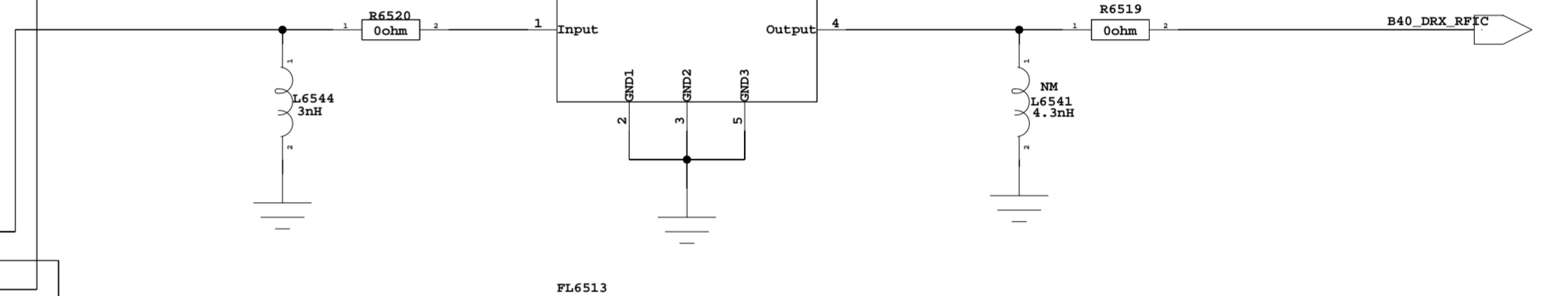
B30 DRX



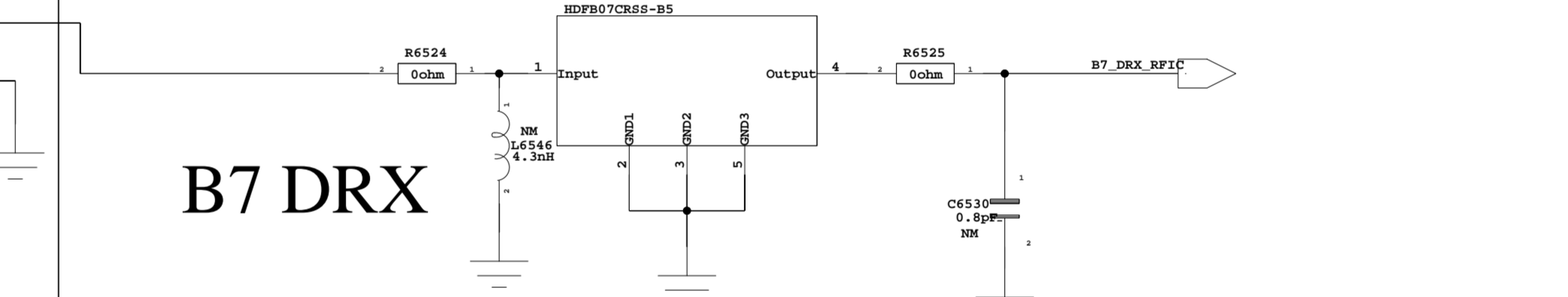
B29 DRX



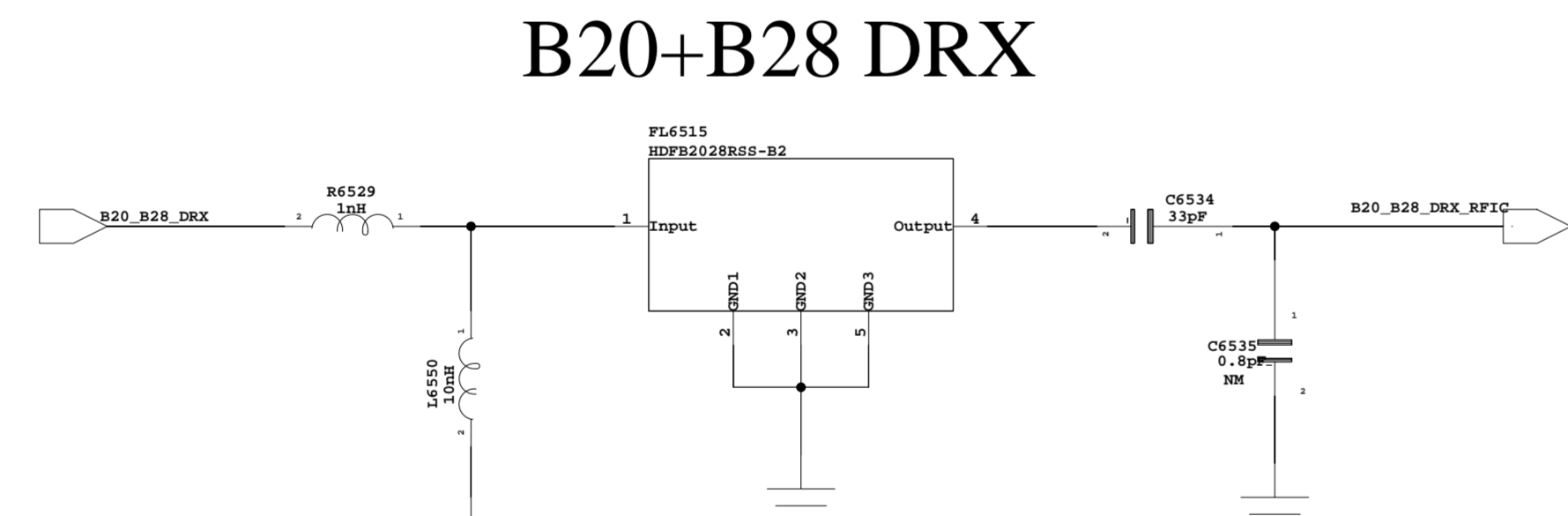
B40 DRX



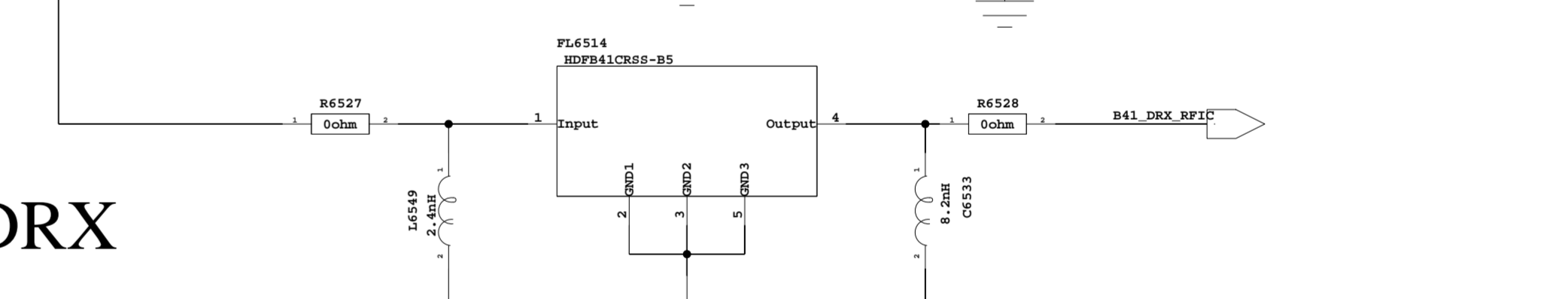
B7 DRX



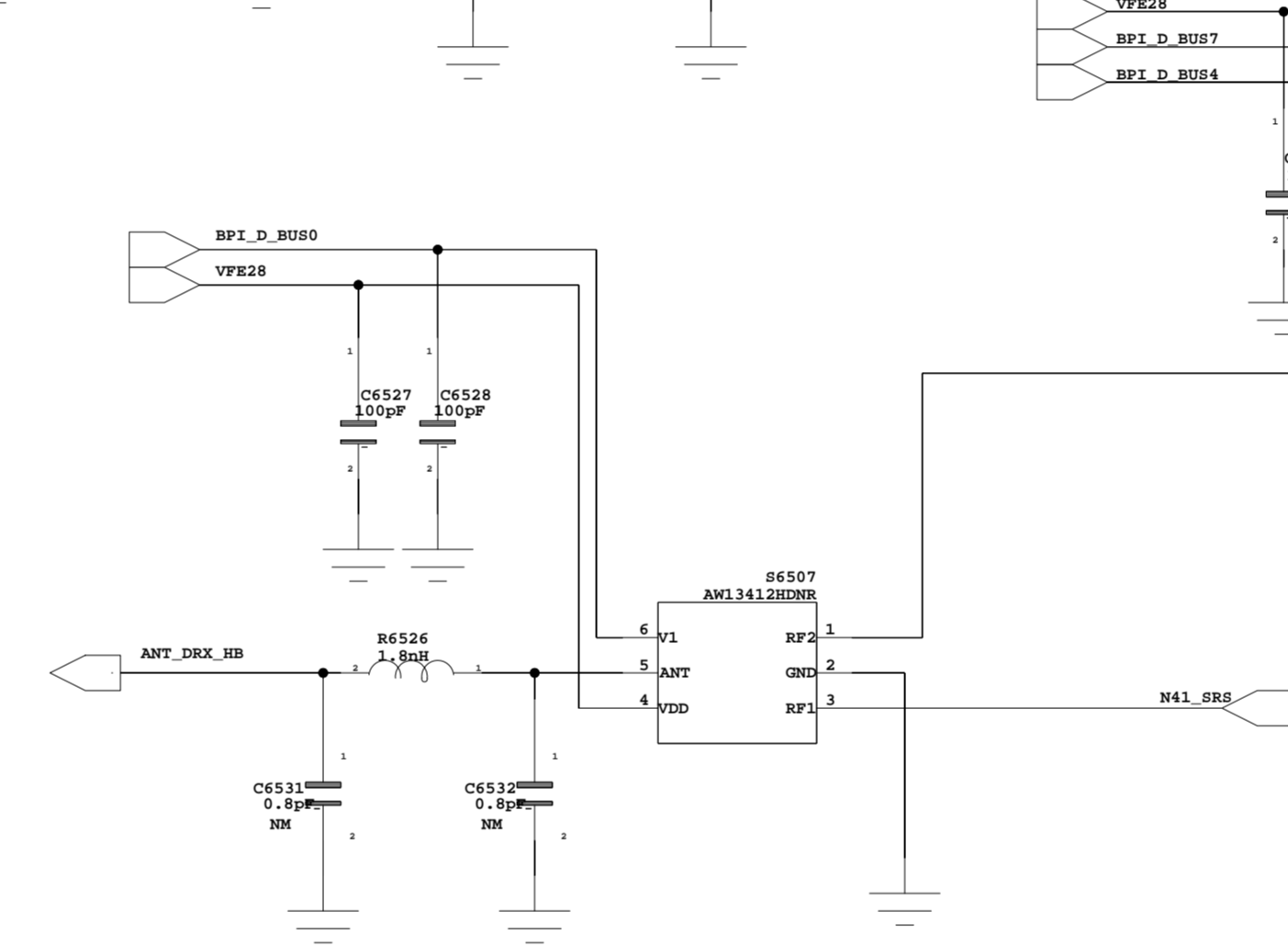
B20+B28 DRX



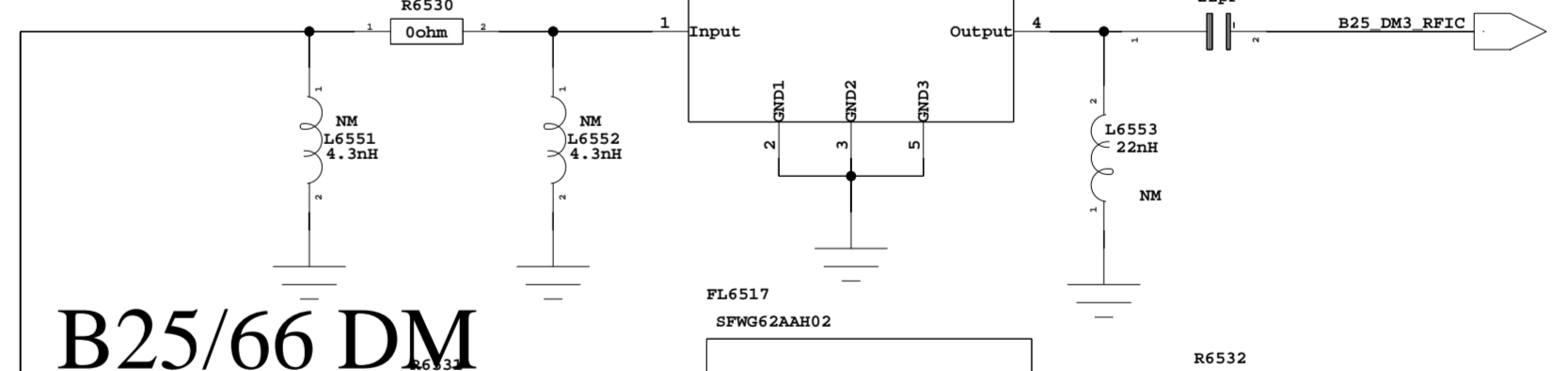
B41 DRX



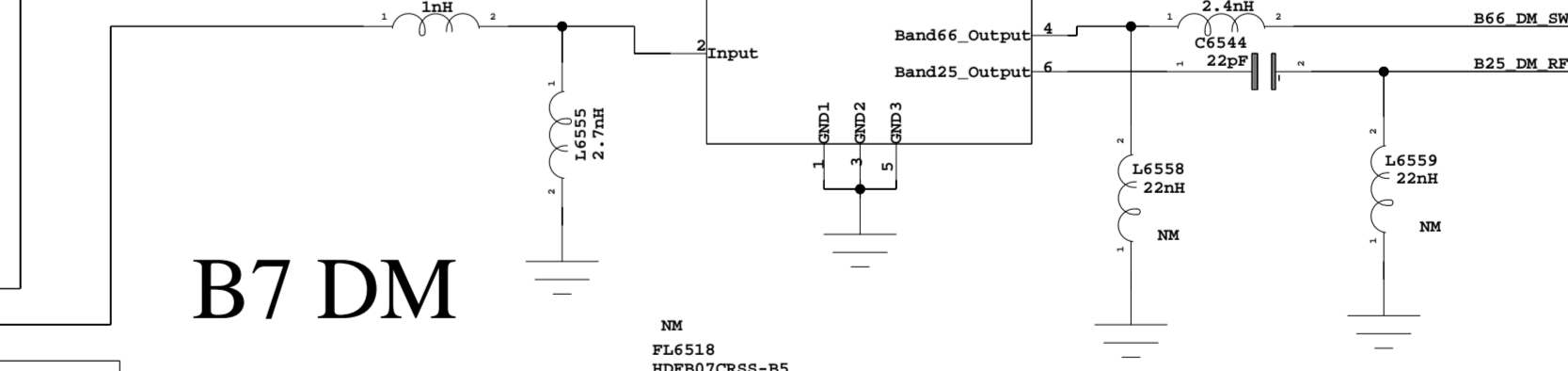
N41 SRS



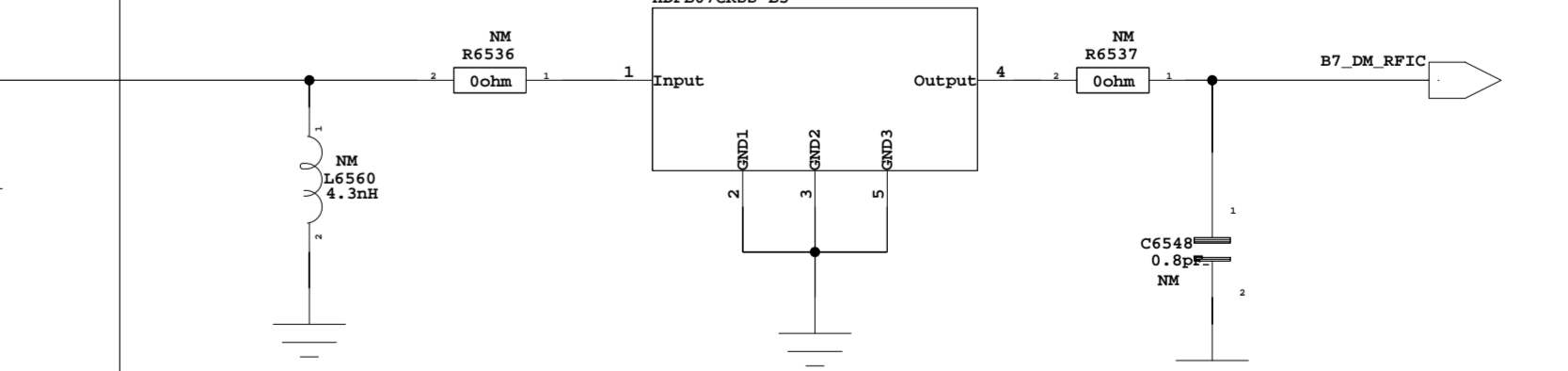
B25 DM3



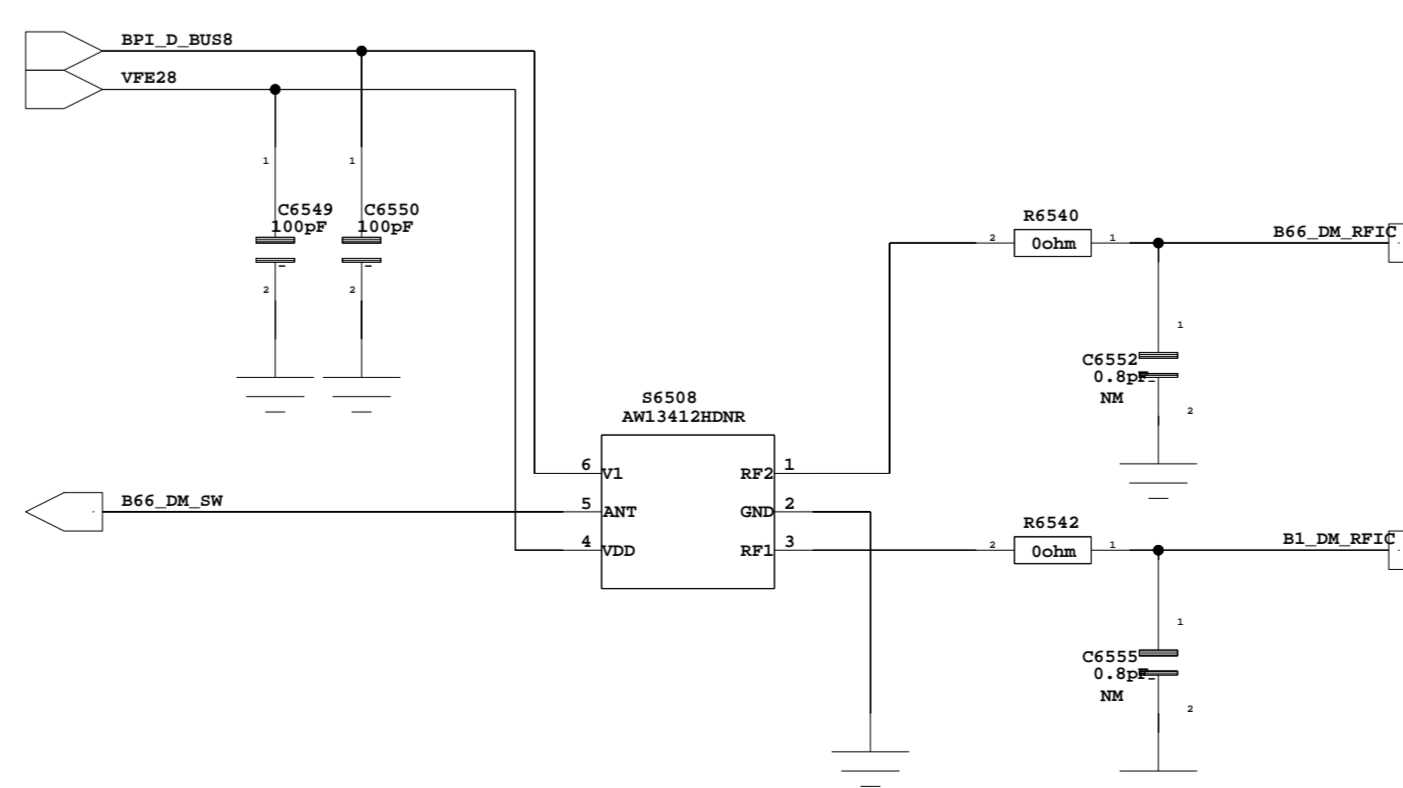
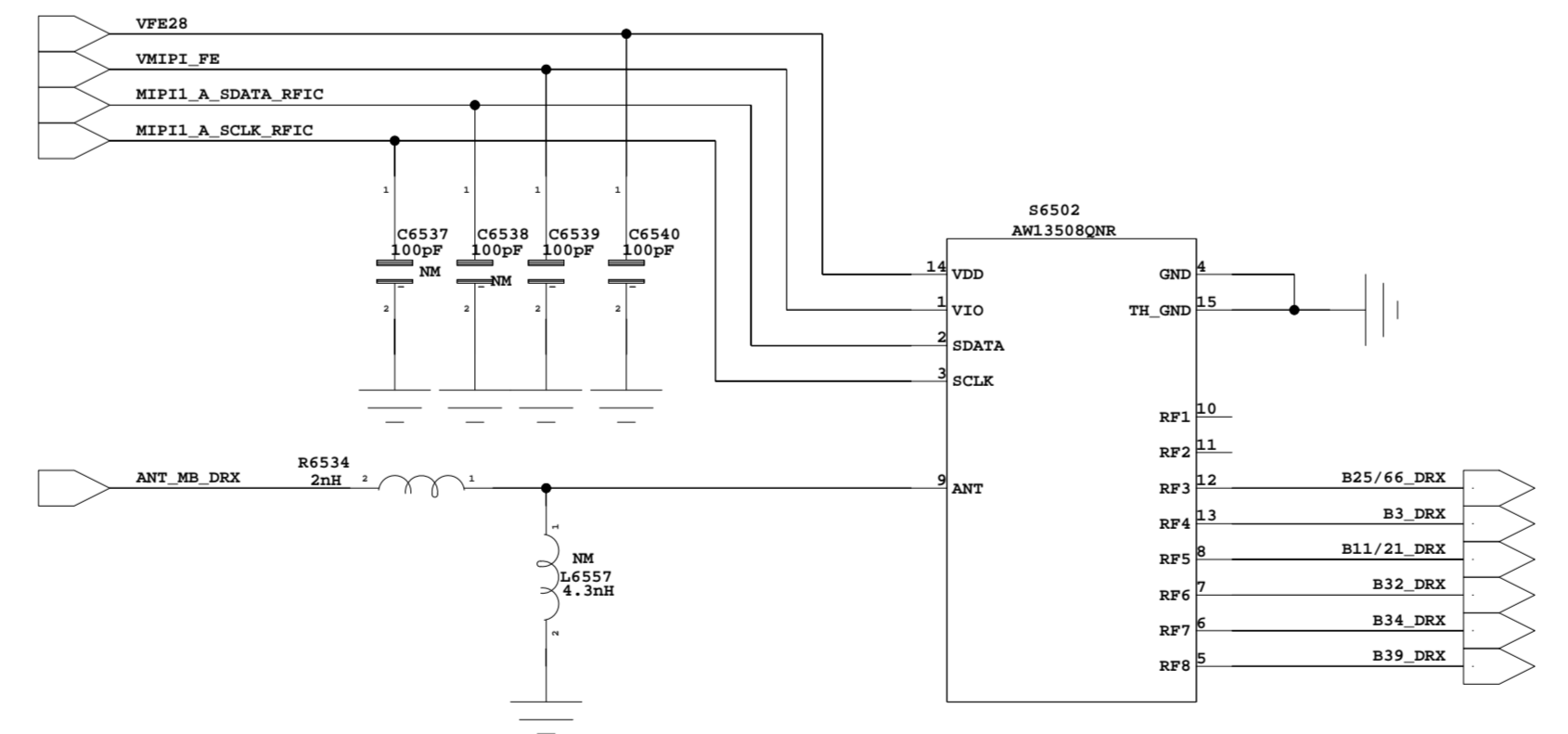
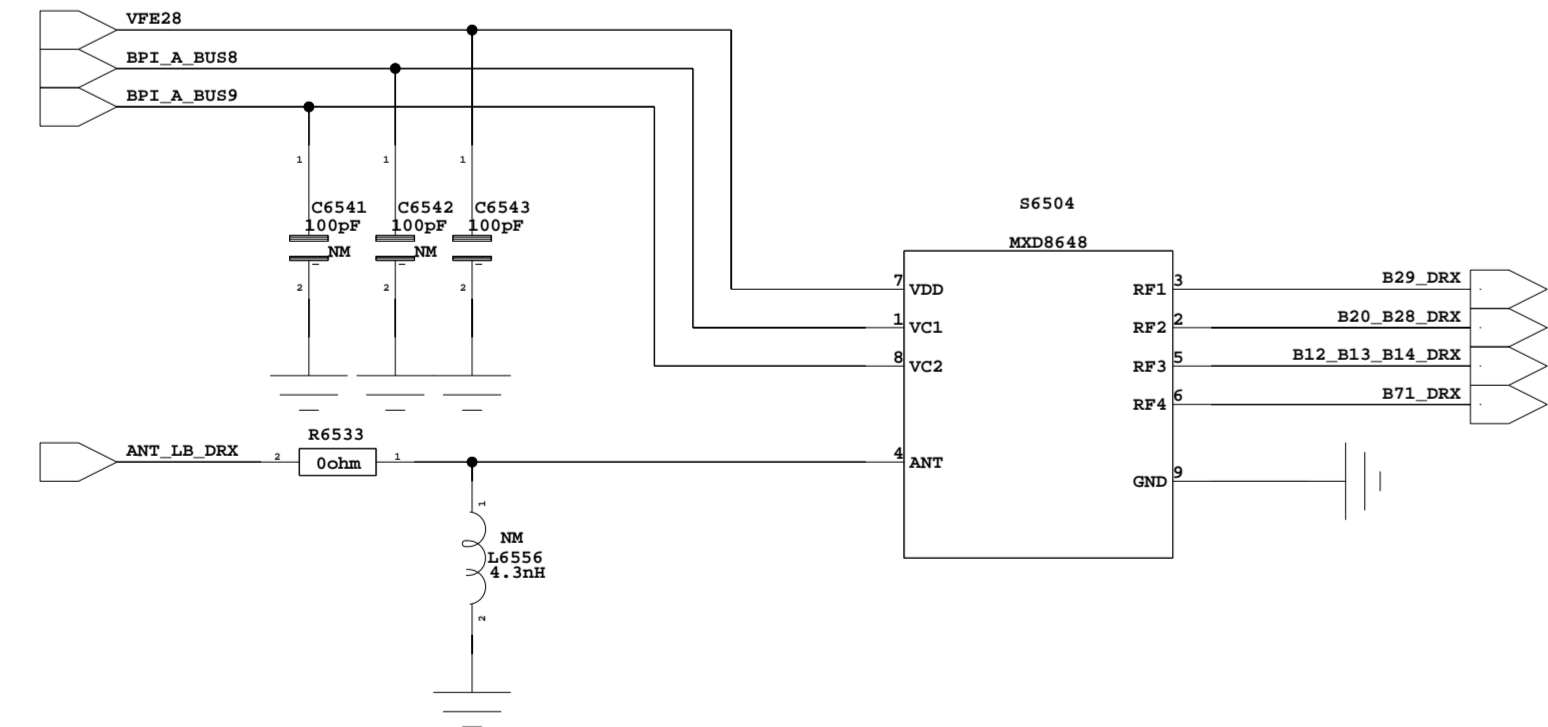
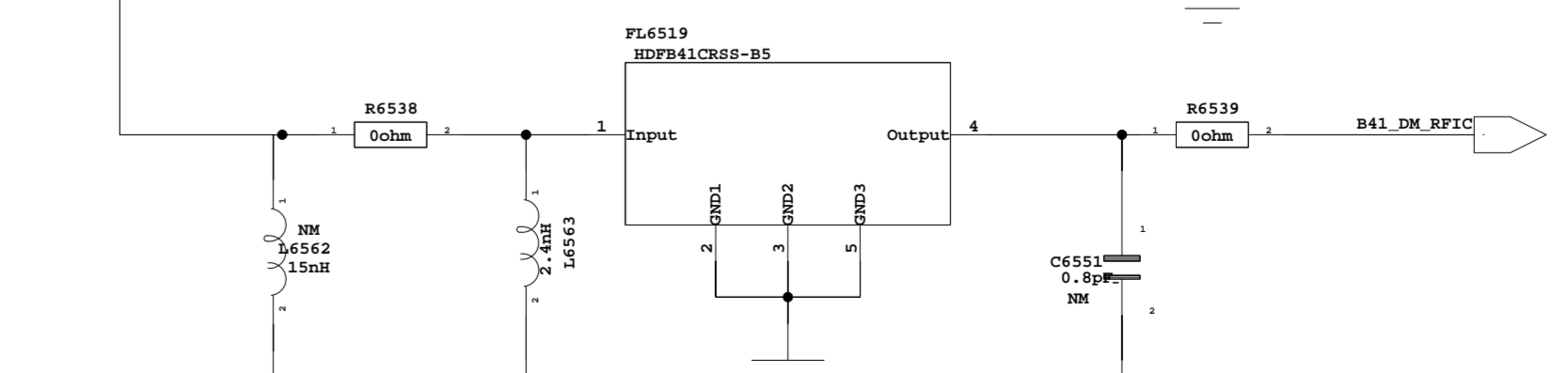
B25/66 DM

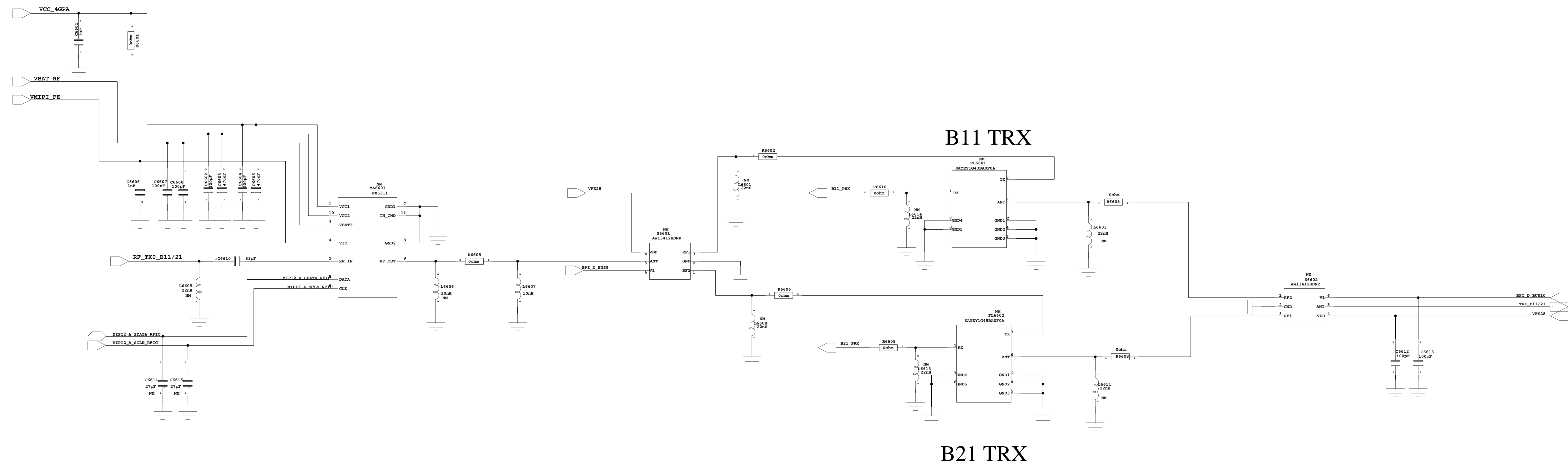


B7 DM

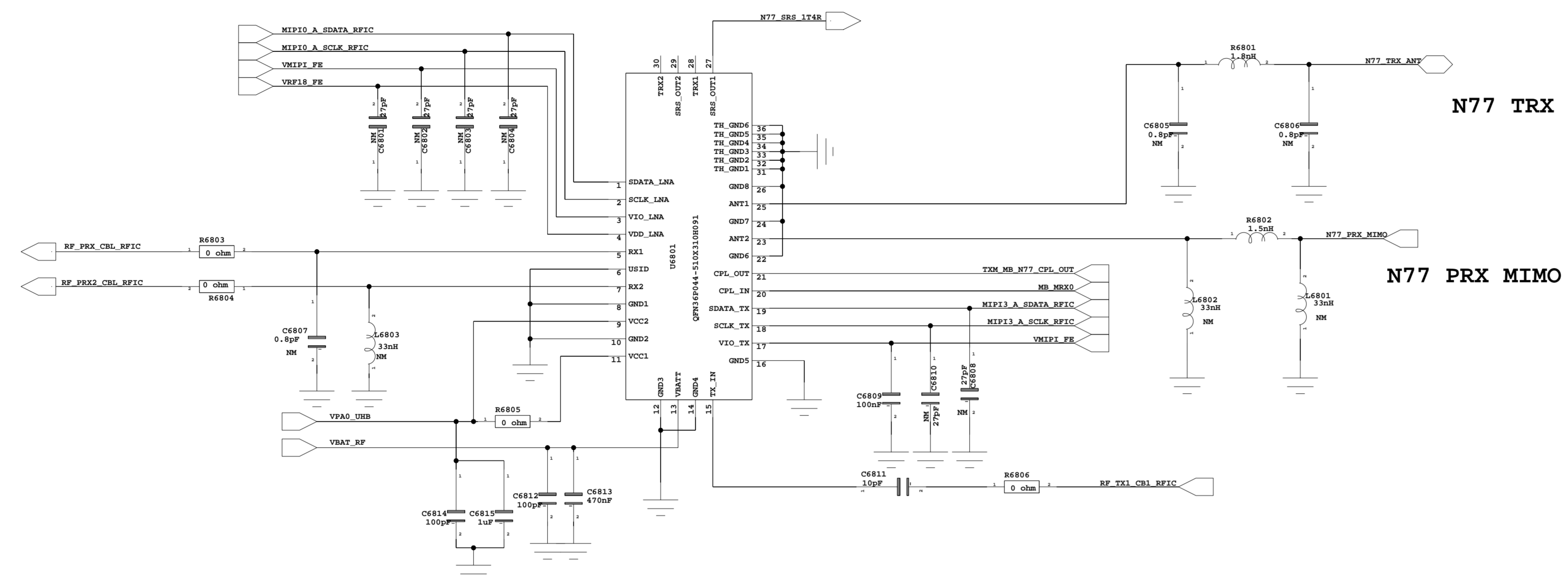


B41 DM

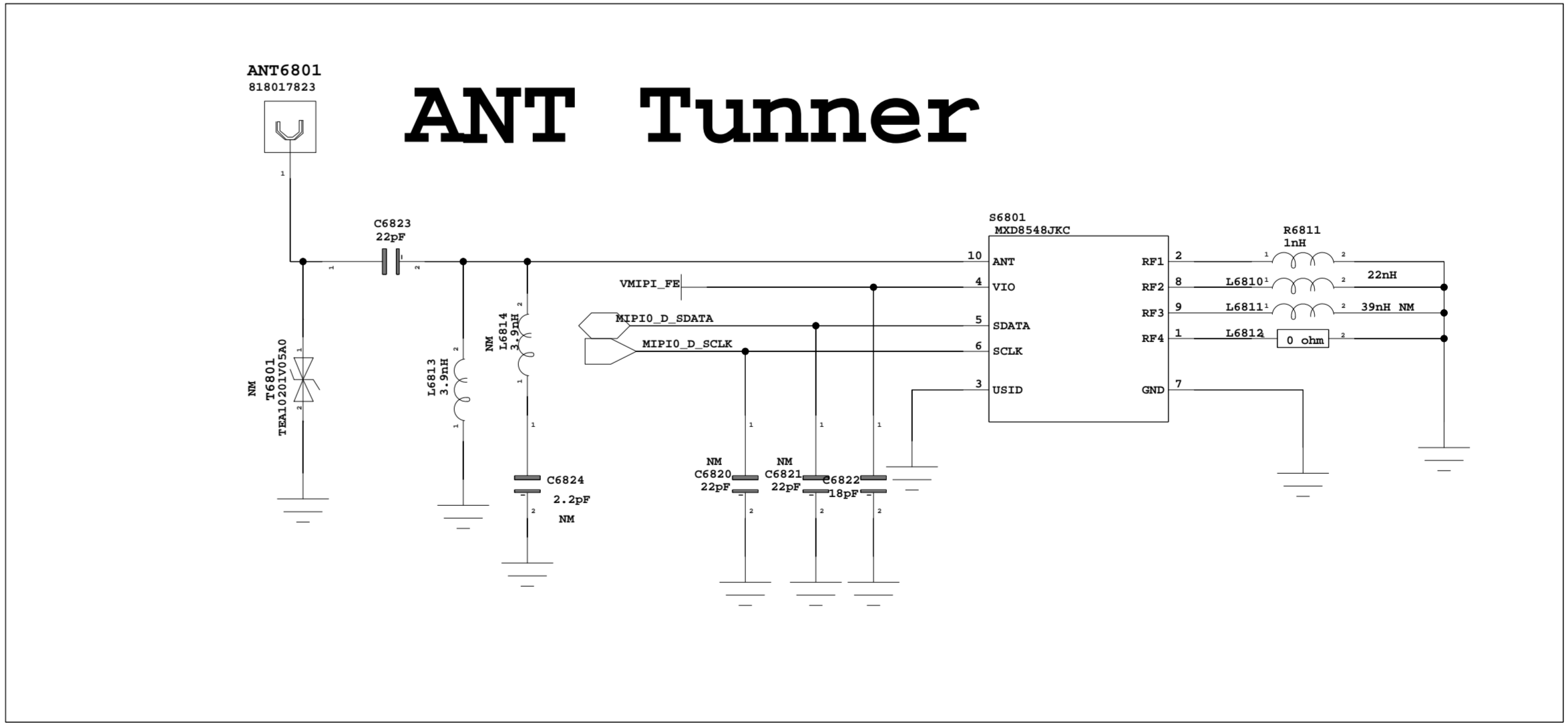
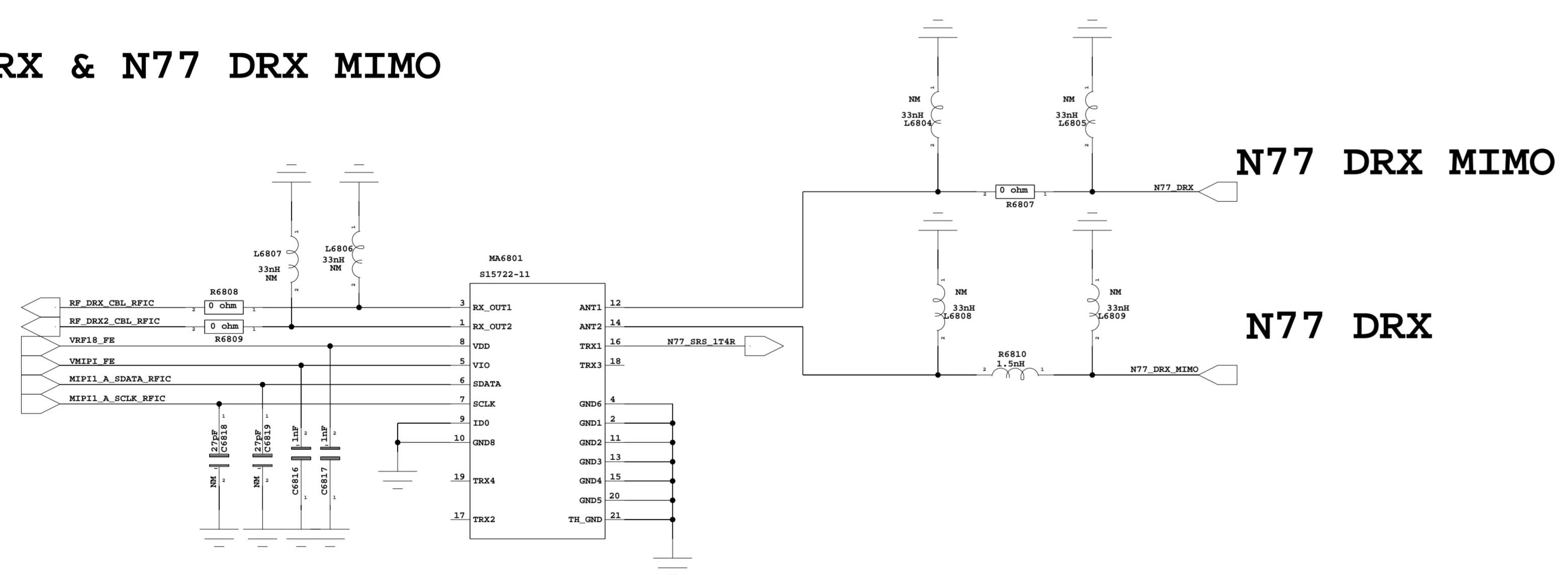


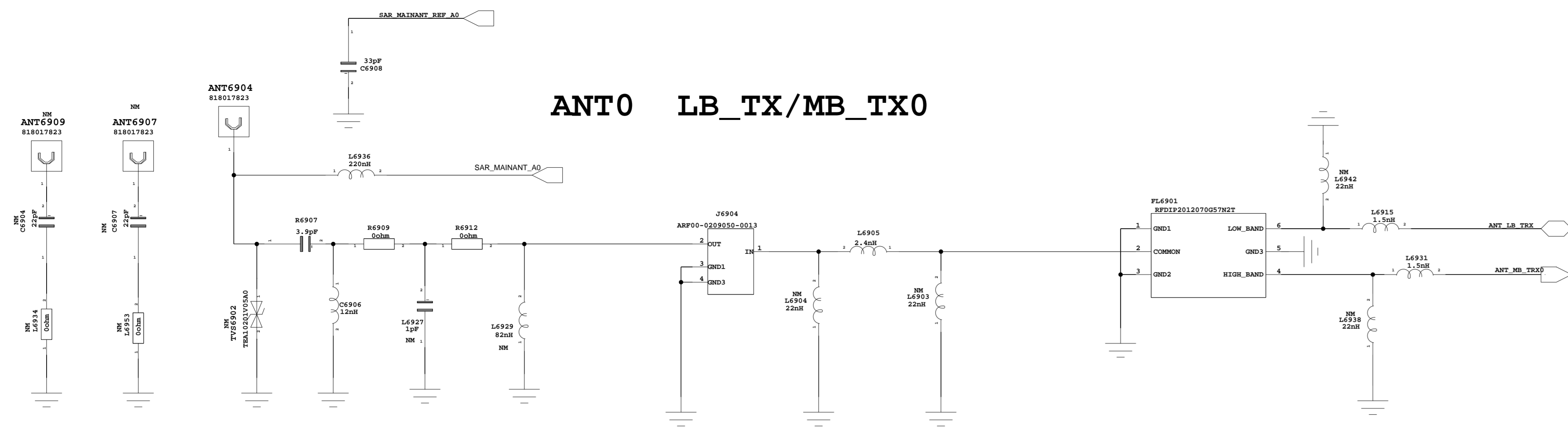


N77 TRX & N77 PRX MIMO

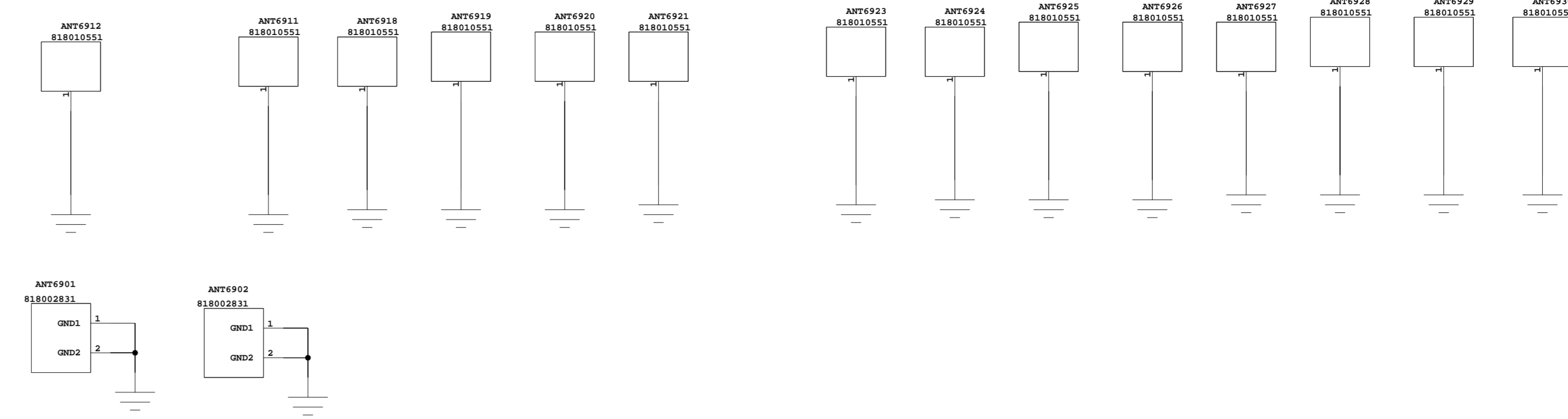


N77 DRX & N77 DRX MIMO

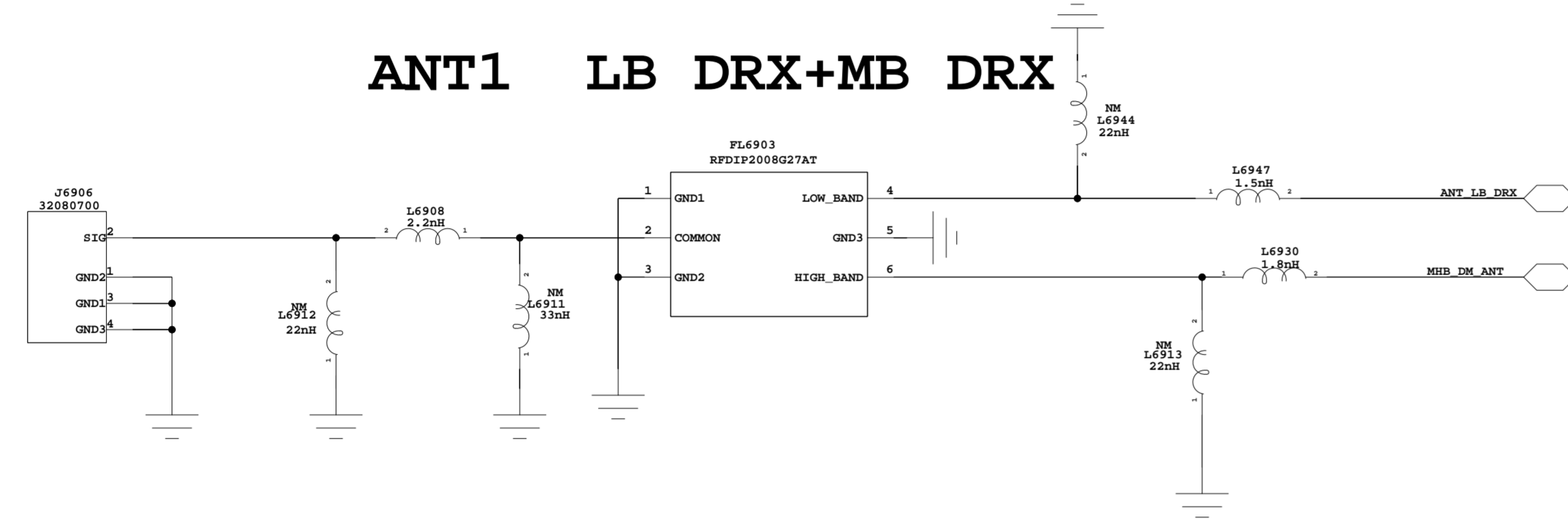




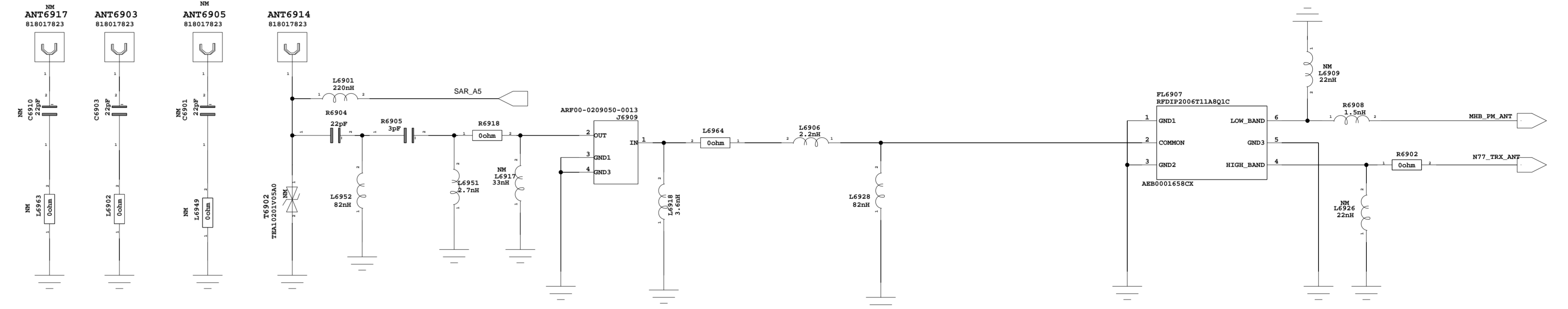
TOP GND



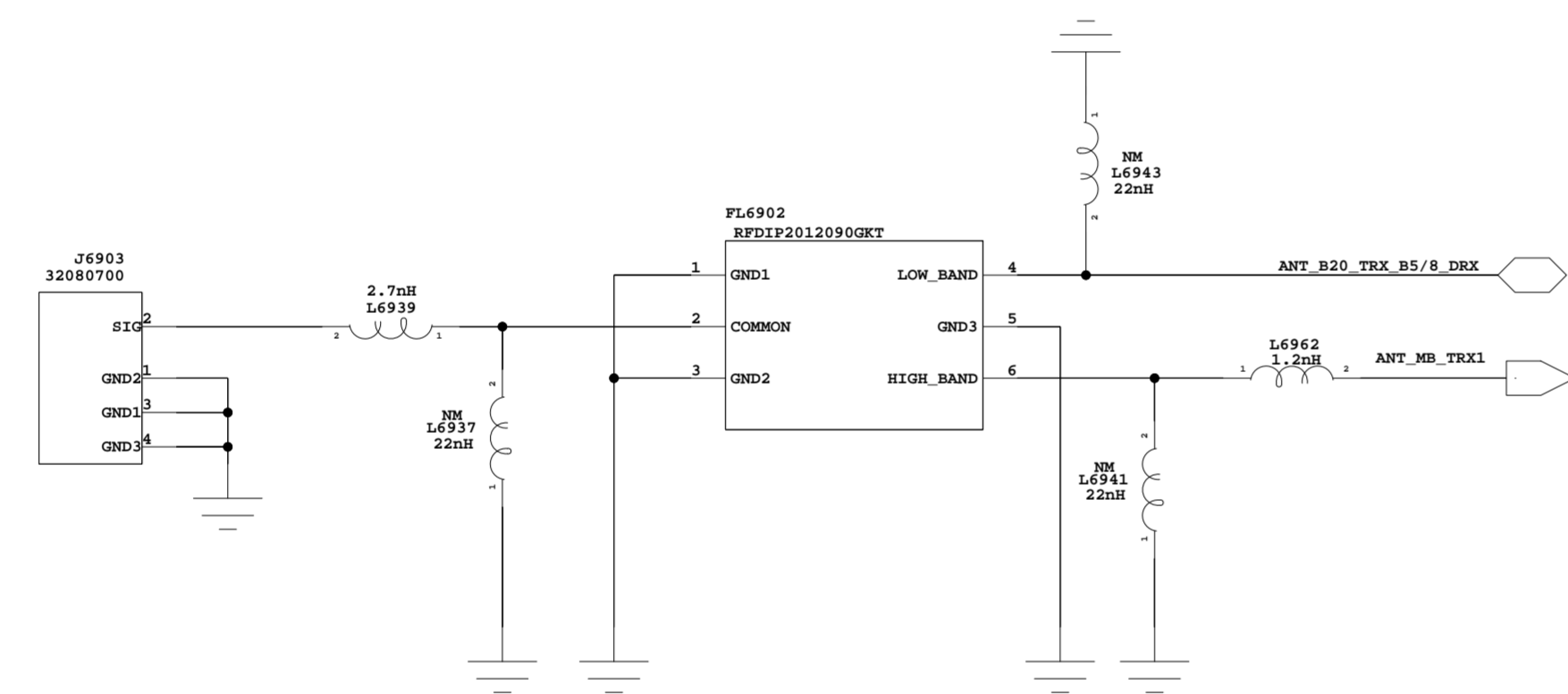
ANT1 LB DRX+MB DRX



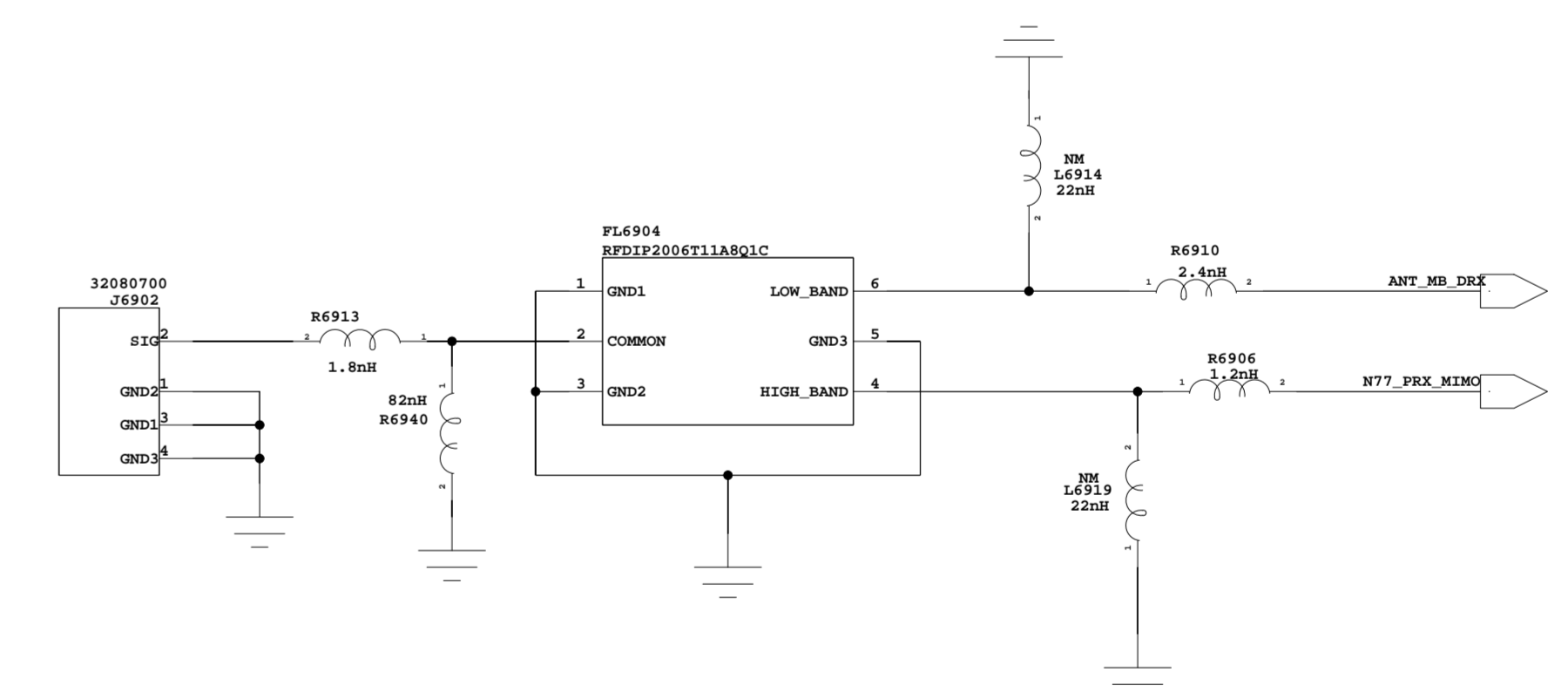
ANT5 MHB_PRX_MIMO/UHB_TX0



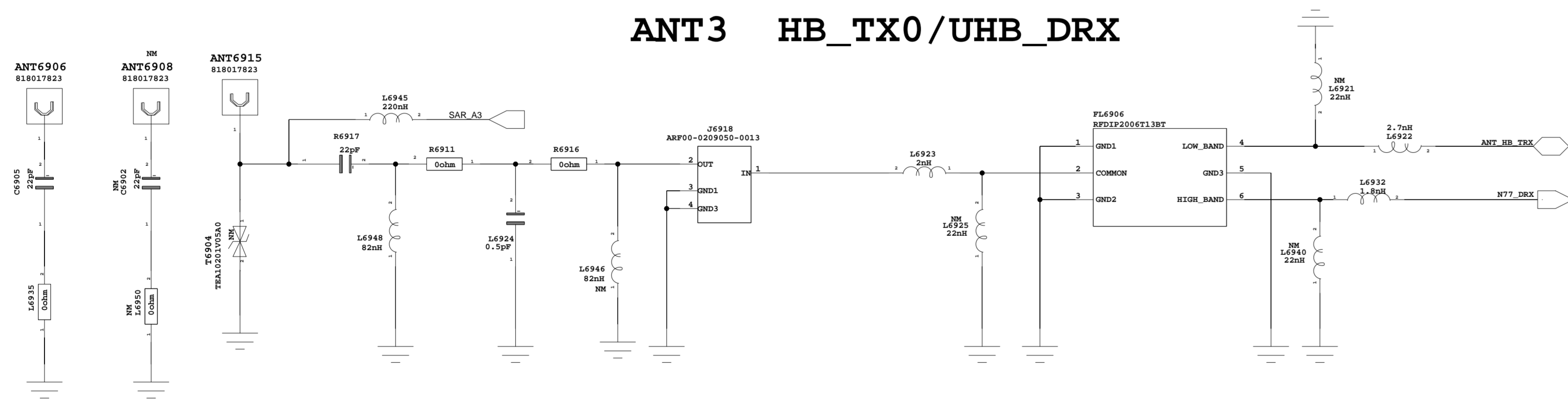
ANT2 MB_TX1/B20_TX/B5/B8_DIV RX



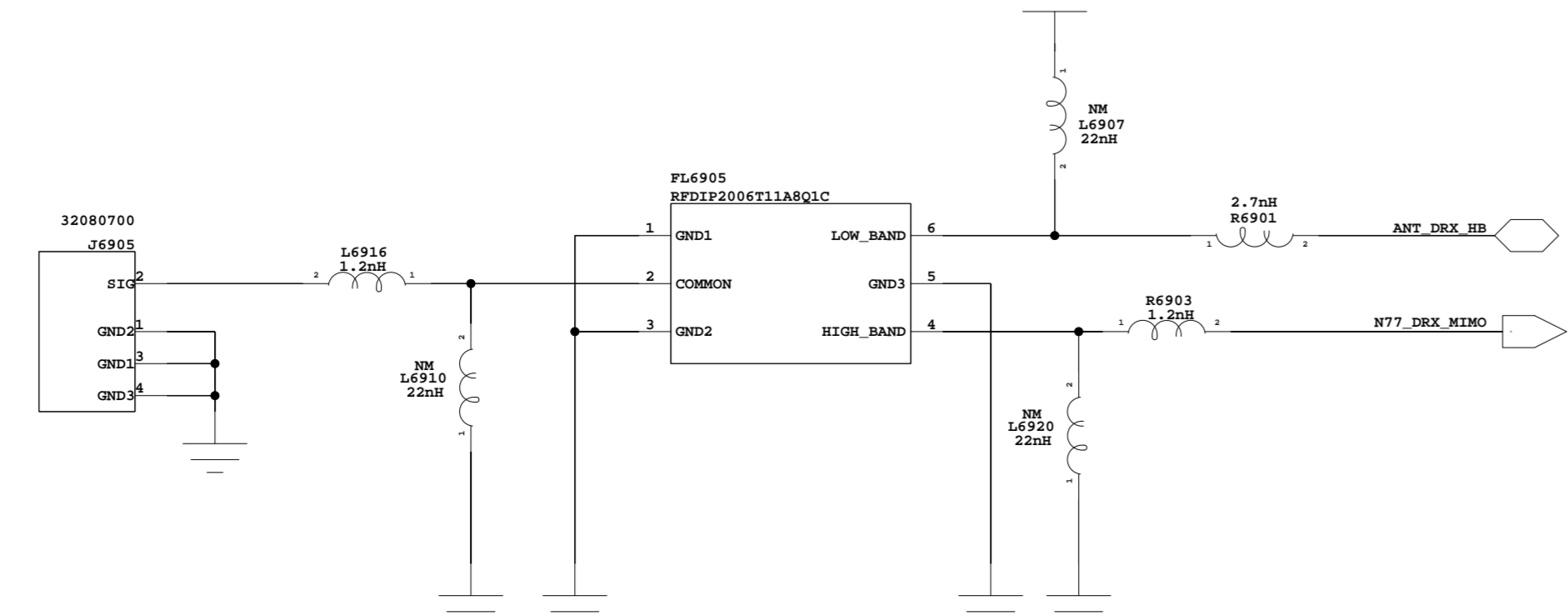
ANT6 MHB_DRX_MIMO/UHB_RX



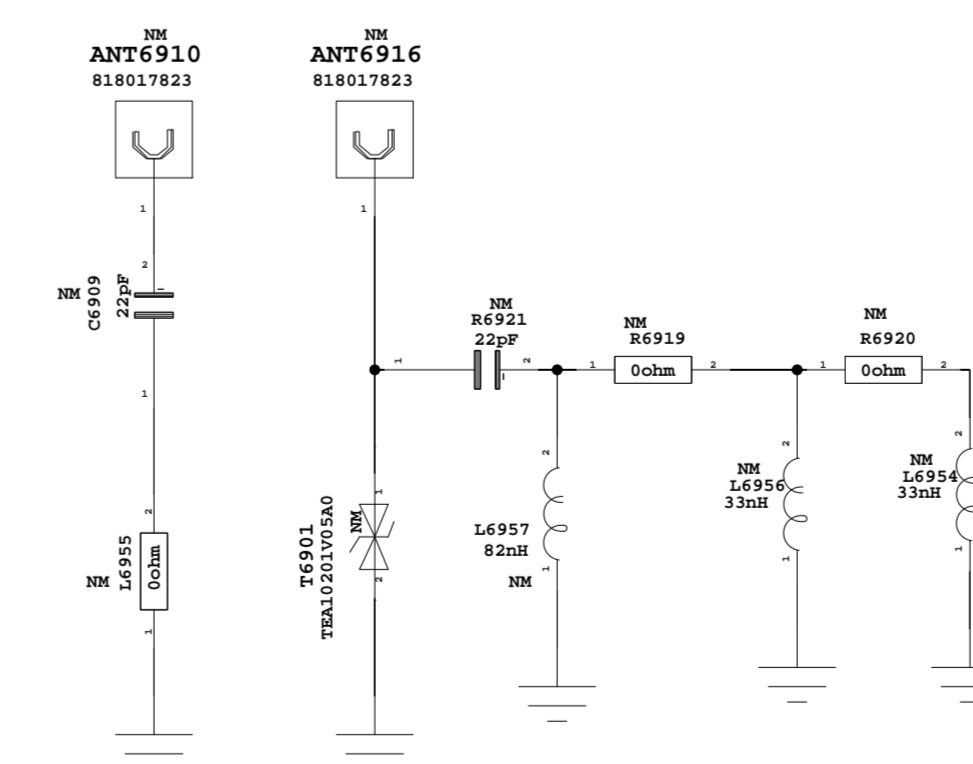
ANT3 HB_TX0/UHB_DRX



ANT4 N77/78 PRX MIMO HB DRX



ANT8



ANT7 GPS 2G/5G WIFI

RF-WIFI/BT

