



TEST REPORT

No. I21Z61036-WMD02

for

TCL Communication Ltd.

Vodafone Gigacube

Model Name: HH500V

FCC ID: 2ACCJB157

with

Hardware Version: HH500_MB_C

Software Version: HH500V_VDF_V2.0.0B01

Issued Date: 2021-08-02

Note:

The test results in this test report relate only to the devices specified in this report. This report shall not be reproduced except in full without the written approval of CTTL.

The report must not be used by the client to claim product certification, approval, or endorsement by NVLAP, NIST, or any agency of the U.S. Government.

Test Laboratory:

CTTL, Telecommunication Technology Labs, CAICT

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REPORT HISTORY

Report Number	Revision	Description	Issue Date
I21Z61036-WMD02	Rev.0	1 st edition	2021-08-02

Note: the latest revision of the test report supersedes all previous version.

CONTENTS

1. Test Laboratory.....	4
1.1. Introduction & Accreditation.....	4
1.2. Testing Location.....	4
1.3. Testing Environment.....	5
1.4. Project Data.....	5
1.5. Signature.....	5
2. Client Information.....	6
2.1. Applicant Information.....	6
2.2. Manufacturer Information.....	6
3. Equipment Under Test (EUT) and Ancillary Equipment (AE).....	7
3.1. About EUT.....	7
3.2. Internal Identification of EUT used during the test.....	8
3.3. Internal Identification of AE used during the test.....	8
4. Reference Documents.....	9
4.1. Documents supplied by applicant.....	9
4.2. Reference Documents for testing.....	9
5. Laboratory Environment.....	10
6. Summary Of Test Result.....	11
7. Test Equipment Utilized.....	12
Annex A: Measurement Results.....	13
A.1 Output Power.....	13
A.2 Emission Limit.....	49
A.3 Frequency Stability.....	53
A.4 Occupied Bandwidth.....	55
A.5 Emission Bandwidth.....	62
A.6 Band Edge Compliance.....	69
A.7 Conducted Spurious Emission.....	76
A.8 Peak-to-Average Power Ratio.....	78
Annex B: Accreditation Certificate.....	79



1. Test Laboratory

1.1. Introduction & Accreditation

Telecommunication Technology Labs, CAICT is an ISO/IEC 17025:2005 accredited test laboratory under NATIONAL VOLUNTARY LABORATORY ACCREDITATION PROGRAM (NVLAP) with lab code 600118-0 and is also an FCC accredited test laboratory (CN5017), and ISED accredited test laboratory (CN0066). The detail accreditation scope can be found on NVLAP website.

1.2. Testing Location

Location 1: CTTL (huayuan North Road)

Address: No. 52, Huayuan North Road, Haidian District, Beijing,
P. R. China 100191

Location 2: CTTL (Shouxiang)

Address: No. 51 Shouxiang Science Building, Xueyuan Road,
Haidian District, Beijing, P. R. China 100191

1.3. Testing Environment

Normal Temperature: 15-35℃
Relative Humidity: 20-75%

1.4. Project Data

Testing Start Date: 2021-06-01
Testing End Date: 2021-07-29

1.5. Signature



Dong Yuan
(Prepared this test report)



Zhou Yu
(Reviewed this test report)



Zhao Hui Lin
Deputy Director of the laboratory
(Approved this test report)



2. Client Information

2.1. Applicant Information

Company Name: TCL Communication Ltd.
Address /Post: 5/F, Building 22E, 22 Science Park East Avenue, Hong Kong Science
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Contact: Gong Zhizhou
Email: zhizhou.gong@tcl.com
Telephone: 0086-755-36611722
Fax: 0086-755-36612000-81722

2.2. Manufacturer Information

Company Name: TCL Communication Ltd.
Address /Post: 5/F, Building 22E, 22 Science Park East Avenue, Hong Kong Science
Park, Shatin, NT, Hong Kong
Contact: Gong Zhizhou
Email: zhizhou.gong@tcl.com
Telephone: 0086-755-36611722
Fax: 0086-755-36612000-81722

3. Equipment Under Test (EUT) and Ancillary Equipment (AE)

3.1. About EUT

Description	Vodafone Gigacube
Model Name	HH500V
FCC ID	2ACCJB157
Antenna	Integral
Output power	27.49dBm maximum EIRP measured for NR B7-n78L
Extreme vol. Limits	10.2VDC to13.8VDC (nominal: 12.0VDC)
Extreme temp. Tolerance	0°C to +45°C
NR Frequency Bands	n1/3/7/8/20/28/38/77/78
5G - LTE DC 1 LTE + NR	DC_1A_n78A, DC_3A_n78A, DC_7A_n78A, DC_8A_n78A, DC_20A_n78A, DC_28A_n78A, DC_1A_n28A, DC_3A_n1A, DC_3A_n28A, DC_7A_n28A, DC_20A_n28A, DC_1A_n77A, DC_3A_n77A, DC_28A_n77A, DC_1A_n7A, DC_28A_n7A, DC_3A_n7A, DC_20A_n7A, DC_28A_n3A, DC_1A_n3A, DC_7A_n3A, DC_20A_n3A, DC_3A_n20A, DC_3A_n1A, DC_7A_n1A, DC_20A_n1A, DC_38A_n78A, DC_7A_n8A, DC_7A_n20A, DC_1A_n20A, DC_8A_n1A, DC_8A_n3A, DC_1A_n38A, DC_3A_n38A
5G - LTE DC 2 LTE + NR	DC_1A-28A_n78A, DC_3C_n28A, DC_3C_n78A, DC_3A-7A_n78A, DC_3A-38A_n78A, DC_1A-3A_n78A, DC_1A-7A_n78A, DC_7C_n78A, DC_1A-3A_n28A, DC_3A-20A_n1A, DC_1A-3A_n7A, DC_3A-20A_n28A, DC_3A-7A_n1A, DC_7A-20A_n78A, DC_3A-20A_n78A, DC_1A-20A_n78A, DC_1A-28A_n78A, DC_7A-20A_n78A, DC_8A-20A_n78A, DC_3C_n1A, DC_3A-20A_n1A, DC_7A-20A_n1A, DC_3A-28A_n78A, DC_20A-38A_n78A, DC_7A-28A_n78A, DC_1A-7A_n28A, DC_3A-7A_n28A, DC_1A-20A_n28A, DC_7A-20A_n28A, DC_1A-28A_n7A, DC_3A-28A_n7A, DC_3A-20A_n7A, DC_1A-7A_n3A, DC_1A-20A_n3A, DC_7A-20A_n3A, DC_1C_n78A, DC_3A-3A_n78A, DC_1A-3A_n77A, DC_3A-7A_n1A, DC_20A-7A_n1A, DC_20A-3A_n1A, DC_1A-7A_n3A, DC_1A-28A_n3A, DC_7A-28A_n3A, DC_1A_8A_n78A, DC_3A_8A_n1A, DC_8A-20A_n1A, DC_1A-3A_n38A, DC_1A-20A_n38A, DC_3C_n38 A, DC_3A-20A_n38A;
5G - LTE DC 3 LTE + NR	DC_1A-3A-7A_n78A, DC_1A-3C_n78A, DC_1A-7C_n78A, DC_1A-3A-7A_n28A, DC_3A-7A-20A_n78A, DC_3A-7C_n78A, DC_1A-3A-20A_n78A, DC_1A-7A-20A_n78A, DC_1A-8A-20A_n78A, DC_3C-7A_n78A, DC_3A-7A-20A_n1A, DC_1A-3A-28A_n78A, DC_1A-7A-28A_n78A,

DC_3A-7A-28A_n78A, DC_1A-3A-28A_n7A , DC_1A-3A-20A_n28A
 DC_3A-7A-20A_n28A, DC_1A-7A-20A_n3A,DC_3C-20A_n78A,
 DC_1A-3C_n28A, DC_3C-20A_n1A, DC_1A-3C_n7A,
 DC_3C-20A_n28A ,DC_1A_3A_8A_n78A, DC_3A_8A_20A_n78A,
 DC_1A_7A_20A_n28A,DC_1A-3A-20A_n38A;
 5G - LTE DC 4 LTE + NR DC_1A-3A-7A-20A_n78A (Layer 12 at LTE), DC_3C-7C_n1A,
 DC_3C-7A-20A_n1A,DC_3C-7C_n78A, DC_1A-3C-7A_n78A,
 DC_1A-3A-7C_n78A, DC_3C-7A-20A_n78A,
 DC_1A-3A-7A-8A_n78A, DC_1A-3A-7A-28A_n78A
 DC_1A-3A-7A-20A_n28A, DC_1A-3A-7C_n28A,
 DC_1A-3C-7A_n28A,DC_3A-7C-20A_n1A,
 DC_1A_7A_3A_20A_n28A,DC_3C-7C_n28A
 5G - LTE DC 5 LTE + NR DC_1A-3C-7C_n78A, DC_1A-3C-7C_n28A (Layer 12 at LTE),
 DC_1A_3C_7A_28A_n78A

Note: Components list, please refer to documents of the manufacturer; it is also included in the original test record of CTTL.

3.2. Internal Identification of EUT used during the test

EUT ID*	IMEI	HW Version	SW Version	Date of receipt
UT19a	350364240200275	HH500_MB_C	HH500V_VDF_V2.0.0B01	2021-06-18
UT04a	350364240200010	HH500_MB_C	HH500V_VDF_V2.0.0B01	2021-05-31
UT20a	350364240200283	HH500_MB_C	HH500V_VDF_V2.0.0B01	2021-06-18

*EUT ID: is used to identify the test sample in the lab internally.

3.3. Internal Identification of AE used during the test

AE ID*	Description
AE1	Charger
AE2	Charger

AE1

Model	CYSE36-120300E
Manufacturer	Jiangsu Chenyang Electron Co.,Ltd
Length of cable	/

AE2

Model	CYSE36-120300UK
Manufacturer	Jiangsu Chenyang Electron Co.,Ltd
Length of cable	/

*AE ID: is used to identify the test sample in the lab internally.

4. Reference Documents

4.1. Documents supplied by applicant

EUT parameters, referring to Annex A for detailed information, is supplied by the client or manufacturer, which is the basis of testing.

4.2. Reference Documents for testing

The following documents listed in this section are referred for testing.

Reference	Title	Version
FCC Part 27	MISCELLANEOUS WIRELESS COMMUNICATIONS SERVICES	10-1-20 Edition
ANSI/TIA-603-E	Land Mobile FM or PM Communications Equipment Measurement and Performance Standards	2016
ANSI C63.26	American National Standard for Compliance Testing of Transmitters Used in Licensed Radio Services	2015
KDB 971168 D01	MEASUREMENT GUIDANCE FOR CERTIFICATION OF LICENSED DIGITAL TRANSMITTERS	v03r01

5. Laboratory Environment

Control room / conducted chamber did not exceed following limits along the EMC testing:

Temperature	Min. = 15 °C, Max. = 35 °C
Relative humidity	Min. =20 %, Max. = 80 %
Shielding effectiveness	> 110 dB
Electrical insulation	>2 M
Ground system resistance	< 0.5

Fully-anechoic chamber 2 (8.6 meters×6.1 meters×3.85 meters) did not exceed following limits along the EMC testing:

Temperature	Min. = 15 °C, Max. = 30 °C
Relative humidity	Min. = 35 %, Max. = 60 %
Shielding effectiveness	> 110 dB
Electrical insulation	>2 M
Ground system resistance	< 1
Site voltage standing-wave ratio (S_{VSWR})	Between 0 and 6 dB, from 1GHz to 18GHz

Semi-anechoic chamber 2 / Fully-anechoic chamber 3 (10 meters×6.7 meters×6.15 meters) did not exceed following limits along the EMC testing:

Temperature	Min. = 15 °C, Max. = 30 °C
Relative humidity	Min. = 35 %, Max. = 60 %
Shielding effectiveness	> 100 dB
Electrical insulation	>2 M
Ground system resistance	< 0.5
Normalised site attenuation (NSA)	<±3.5 dB, 3 m distance
Site voltage standing-wave ratio (S_{VSWR})	Between 0 and 6 dB, from 1GHz to 18GHz

6. Summary Of Test Result

n78L(3450MHz~3550MHz)

Items	Test Name	Clause in FCC rules	Verdict
1	Output Power	27.50	P
2	Emission Limit	2.1051/27.53	P
3	Frequency Stability	2.1055	P
4	Occupied Bandwidth	2.1049	P
5	Emission Bandwidth	27.53	P
6	Band Edge Compliance	27.53	P
7	Conducted Spurious Emission	27.53	P
8	Peak-to-Average Power Ratio	27.50	P

Terms used in Verdict column

P	Pass. The EUT complies with the essential requirements in the standard.
NP	Not Performed. The test was not performed by CTTL.
NA	Not Applicable. The test was not applicable.
BR	Re-use test data from basic model report.
F	Fail. The EUT does not comply with the essential requirements in the standard.

n78L is tested by power class 3.

Explanation of worst-case configuration

NR modulation: DFT-s-OFDM pi/2 BPSK; QPSK; 16QAM; 64QAM; 256QAM

CP-OFDM QPSK; 16QAM; 64QAM; 256QAM

NR BW: 20/40/50/60/80/90MHz for n78L

The test results provided in this report represent the worst case configuration.

For all the NSA cases, LTE Bands are set under the 10MHz bandwidth, middle channel, 50RB and QPSK modulation.

7. Test Equipment Utilized

Description	Type	Series Number	Manufacture	Cal Due Date	Calibration Interval
Radio Communication Test Station	MT8000A	6262093285	Anritsu	2022-01-04	1 year
Radio Communication Analyzer	MT8821C	6201763159	Anritsu	2021-08-12	1 year
Signal&Spectrum Analyzer	FSW	104038	R&S	2022-06-24	1 year
Climate chamber	SH-242	93008556	ESPEC	2023-12-23	3 years
EMI Antenna	VULB9163	9163-301	Schwarzbeck	2021-08-04	1 year
EMI Antenna	3117	00058889	ETS-Lindgren	2021-09-22	1 year
EMI Antenna	3117	00119024	ETS-Lindgren	2022-04-11	1 year
EMI Antenna	9117	167	Schwarzbeck	2021-08-19	1 year
Signal Generator	N5183A	MY49060052	Agilent	2022-07-11	1 year
Test Receiver	E4440A	MY48250642	Agilent	2022-03-04	1 year
Power Amplifier	5S1G4	0341863	AR	/	
UXM 5G Wireless Test Platform	E7515B	MY59020630	Keysight	2022-05-31	1 year

Annex A: Measurement Results

A.1 Output Power

A.1.1 Summary

During the process of testing, the EUT was controlled via communication tester to ensure max power transmission and proper modulation.

In all cases, output power is within the specified limits.

A.1.2 Conducted

LTE Band 7+NR n78L

BAND	BW(MHz)	SCS(kHz)	FREQ(MHz)	OFDM	MODULATION	RB LOCATION	POWER(dBm)
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Inner_Full	23.70
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.51
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Edge_1RB_Right	23.35
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Outer_Full	23.44
B7-n78L	20	30	3460.02	DFT	QPSK	Inner_Full	23.80
B7-n78L	20	30	3460.02	DFT	QPSK	Edge_1RB_Left	23.05
B7-n78L	20	30	3460.02	DFT	QPSK	Edge_1RB_Right	22.88
B7-n78L	20	30	3460.02	DFT	QPSK	Outer_Full	22.89
B7-n78L	20	30	3460.02	DFT	16QAM	Inner_Full	22.93
B7-n78L	20	30	3460.02	DFT	16QAM	Edge_1RB_Left	21.69
B7-n78L	20	30	3460.02	DFT	16QAM	Edge_1RB_Right	21.50
B7-n78L	20	30	3460.02	DFT	16QAM	Outer_Full	21.95
B7-n78L	20	30	3460.02	DFT	64QAM	Inner_Full	21.50
B7-n78L	20	30	3460.02	DFT	64QAM	Edge_1RB_Left	21.22
B7-n78L	20	30	3460.02	DFT	64QAM	Edge_1RB_Right	21.04
B7-n78L	20	30	3460.02	DFT	64QAM	Outer_Full	21.39
B7-n78L	20	30	3460.02	DFT	256QAM	Inner_Full	19.43
B7-n78L	20	30	3460.02	DFT	256QAM	Edge_1RB_Left	18.74
B7-n78L	20	30	3460.02	DFT	256QAM	Edge_1RB_Right	18.57
B7-n78L	20	30	3460.02	DFT	256QAM	Outer_Full	19.42
B7-n78L	20	30	3460.02	CP	QPSK	Inner_Full	22.38
B7-n78L	20	30	3460.02	CP	QPSK	Edge_1RB_Left	21.09
B7-n78L	20	30	3460.02	CP	QPSK	Edge_1RB_Right	20.93
B7-n78L	20	30	3460.02	CP	QPSK	Outer_Full	20.96
B7-n78L	20	30	3460.02	CP	16QAM	Inner_Full	22.03
B7-n78L	20	30	3460.02	CP	16QAM	Edge_1RB_Left	21.01
B7-n78L	20	30	3460.02	CP	16QAM	Edge_1RB_Right	20.80
B7-n78L	20	30	3460.02	CP	16QAM	Outer_Full	20.96
B7-n78L	20	30	3460.02	CP	64QAM	Inner_Full	20.51

B7-n78L	20	30	3460.02	CP	64QAM	Edge_1RB_Left	20.43
B7-n78L	20	30	3460.02	CP	64QAM	Edge_1RB_Right	20.30
B7-n78L	20	30	3460.02	CP	64QAM	Outer_Full	20.41
B7-n78L	20	30	3460.02	CP	256QAM	Inner_Full	17.39
B7-n78L	20	30	3460.02	CP	256QAM	Edge_1RB_Left	16.87
B7-n78L	20	30	3460.02	CP	256QAM	Edge_1RB_Right	16.77
B7-n78L	20	30	3460.02	CP	256QAM	Outer_Full	17.43
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Inner_Full	24.09
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.51
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.68
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.67
B7-n78L	20	30	3500.01	DFT	QPSK	Inner_Full	24.02
B7-n78L	20	30	3500.01	DFT	QPSK	Edge_1RB_Left	23.04
B7-n78L	20	30	3500.01	DFT	QPSK	Edge_1RB_Right	23.24
B7-n78L	20	30	3500.01	DFT	QPSK	Outer_Full	23.11
B7-n78L	20	30	3500.01	DFT	16QAM	Inner_Full	23.13
B7-n78L	20	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.63
B7-n78L	20	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.68
B7-n78L	20	30	3500.01	DFT	16QAM	Outer_Full	22.06
B7-n78L	20	30	3500.01	DFT	64QAM	Inner_Full	21.70
B7-n78L	20	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.89
B7-n78L	20	30	3500.01	DFT	64QAM	Edge_1RB_Right	21.07
B7-n78L	20	30	3500.01	DFT	64QAM	Outer_Full	21.44
B7-n78L	20	30	3500.01	DFT	256QAM	Inner_Full	19.50
B7-n78L	20	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.63
B7-n78L	20	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.75
B7-n78L	20	30	3500.01	DFT	256QAM	Outer_Full	19.58
B7-n78L	20	30	3500.01	CP	QPSK	Inner_Full	22.53
B7-n78L	20	30	3500.01	CP	QPSK	Edge_1RB_Left	20.97
B7-n78L	20	30	3500.01	CP	QPSK	Edge_1RB_Right	21.19
B7-n78L	20	30	3500.01	CP	QPSK	Outer_Full	21.08
B7-n78L	20	30	3500.01	CP	16QAM	Inner_Full	22.05
B7-n78L	20	30	3500.01	CP	16QAM	Edge_1RB_Left	21.44
B7-n78L	20	30	3500.01	CP	16QAM	Edge_1RB_Right	21.52
B7-n78L	20	30	3500.01	CP	16QAM	Outer_Full	21.00
B7-n78L	20	30	3500.01	CP	64QAM	Inner_Full	20.56
B7-n78L	20	30	3500.01	CP	64QAM	Edge_1RB_Left	20.28
B7-n78L	20	30	3500.01	CP	64QAM	Edge_1RB_Right	20.51
B7-n78L	20	30	3500.01	CP	64QAM	Outer_Full	20.50
B7-n78L	20	30	3500.01	CP	256QAM	Inner_Full	17.56
B7-n78L	20	30	3500.01	CP	256QAM	Edge_1RB_Left	16.82

B7-n78L	20	30	3500.01	CP	256QAM	Edge_1RB_Right	17.06
B7-n78L	20	30	3500.01	CP	256QAM	Outer_Full	17.57
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Inner_Full	23.86
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Edge_1RB_Left	23.31
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Edge_1RB_Right	23.62
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Outer_Full	23.51
B7-n78L	20	30	3540	DFT	QPSK	Inner_Full	23.83
B7-n78L	20	30	3540	DFT	QPSK	Edge_1RB_Left	22.90
B7-n78L	20	30	3540	DFT	QPSK	Edge_1RB_Right	23.15
B7-n78L	20	30	3540	DFT	QPSK	Outer_Full	22.98
B7-n78L	20	30	3540	DFT	16QAM	Inner_Full	23.00
B7-n78L	20	30	3540	DFT	16QAM	Edge_1RB_Left	21.81
B7-n78L	20	30	3540	DFT	16QAM	Edge_1RB_Right	21.68
B7-n78L	20	30	3540	DFT	16QAM	Outer_Full	22.01
B7-n78L	20	30	3540	DFT	64QAM	Inner_Full	21.62
B7-n78L	20	30	3540	DFT	64QAM	Edge_1RB_Left	21.09
B7-n78L	20	30	3540	DFT	64QAM	Edge_1RB_Right	21.25
B7-n78L	20	30	3540	DFT	64QAM	Outer_Full	21.47
B7-n78L	20	30	3540	DFT	256QAM	Inner_Full	19.32
B7-n78L	20	30	3540	DFT	256QAM	Edge_1RB_Left	18.62
B7-n78L	20	30	3540	DFT	256QAM	Edge_1RB_Right	18.80
B7-n78L	20	30	3540	DFT	256QAM	Outer_Full	19.47
B7-n78L	20	30	3540	CP	QPSK	Inner_Full	22.56
B7-n78L	20	30	3540	CP	QPSK	Edge_1RB_Left	21.00
B7-n78L	20	30	3540	CP	QPSK	Edge_1RB_Right	21.74
B7-n78L	20	30	3540	CP	QPSK	Outer_Full	21.12
B7-n78L	20	30	3540	CP	16QAM	Inner_Full	22.06
B7-n78L	20	30	3540	CP	16QAM	Edge_1RB_Left	21.35
B7-n78L	20	30	3540	CP	16QAM	Edge_1RB_Right	21.64
B7-n78L	20	30	3540	CP	16QAM	Outer_Full	20.98
B7-n78L	20	30	3540	CP	64QAM	Inner_Full	20.57
B7-n78L	20	30	3540	CP	64QAM	Edge_1RB_Left	20.31
B7-n78L	20	30	3540	CP	64QAM	Edge_1RB_Right	20.58
B7-n78L	20	30	3540	CP	64QAM	Outer_Full	20.50
B7-n78L	20	30	3540	CP	256QAM	Inner_Full	17.64
B7-n78L	20	30	3540	CP	256QAM	Edge_1RB_Left	16.79
B7-n78L	20	30	3540	CP	256QAM	Edge_1RB_Right	17.66
B7-n78L	20	30	3540	CP	256QAM	Outer_Full	17.62
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Inner_Full	23.98
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.54
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.66

B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Outer_Full	23.59
B7-n78L	40	30	3470.01	DFT	QPSK	Inner_Full	23.95
B7-n78L	40	30	3470.01	DFT	QPSK	Edge_1RB_Left	23.33
B7-n78L	40	30	3470.01	DFT	QPSK	Edge_1RB_Right	23.16
B7-n78L	40	30	3470.01	DFT	QPSK	Outer_Full	23.04
B7-n78L	40	30	3470.01	DFT	16QAM	Inner_Full	23.01
B7-n78L	40	30	3470.01	DFT	16QAM	Edge_1RB_Left	21.87
B7-n78L	40	30	3470.01	DFT	16QAM	Edge_1RB_Right	21.86
B7-n78L	40	30	3470.01	DFT	16QAM	Outer_Full	22.09
B7-n78L	40	30	3470.01	DFT	64QAM	Inner_Full	21.55
B7-n78L	40	30	3470.01	DFT	64QAM	Edge_1RB_Left	21.13
B7-n78L	40	30	3470.01	DFT	64QAM	Edge_1RB_Right	21.40
B7-n78L	40	30	3470.01	DFT	64QAM	Outer_Full	21.65
B7-n78L	40	30	3470.01	DFT	256QAM	Inner_Full	19.46
B7-n78L	40	30	3470.01	DFT	256QAM	Edge_1RB_Left	19.00
B7-n78L	40	30	3470.01	DFT	256QAM	Edge_1RB_Right	18.88
B7-n78L	40	30	3470.01	DFT	256QAM	Outer_Full	19.65
B7-n78L	40	30	3470.01	CP	QPSK	Inner_Full	22.56
B7-n78L	40	30	3470.01	CP	QPSK	Edge_1RB_Left	21.41
B7-n78L	40	30	3470.01	CP	QPSK	Edge_1RB_Right	21.22
B7-n78L	40	30	3470.01	CP	QPSK	Outer_Full	21.10
B7-n78L	40	30	3470.01	CP	16QAM	Inner_Full	21.98
B7-n78L	40	30	3470.01	CP	16QAM	Edge_1RB_Left	20.95
B7-n78L	40	30	3470.01	CP	16QAM	Edge_1RB_Right	20.82
B7-n78L	40	30	3470.01	CP	16QAM	Outer_Full	21.04
B7-n78L	40	30	3470.01	CP	64QAM	Inner_Full	20.39
B7-n78L	40	30	3470.01	CP	64QAM	Edge_1RB_Left	20.47
B7-n78L	40	30	3470.01	CP	64QAM	Edge_1RB_Right	20.50
B7-n78L	40	30	3470.01	CP	64QAM	Outer_Full	20.50
B7-n78L	40	30	3470.01	CP	256QAM	Inner_Full	17.60
B7-n78L	40	30	3470.01	CP	256QAM	Edge_1RB_Left	17.11
B7-n78L	40	30	3470.01	CP	256QAM	Edge_1RB_Right	16.91
B7-n78L	40	30	3470.01	CP	256QAM	Outer_Full	17.55
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Inner_Full	24.02
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.55
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.61
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.60
B7-n78L	40	30	3500.01	DFT	QPSK	Inner_Full	24.03
B7-n78L	40	30	3500.01	DFT	QPSK	Edge_1RB_Left	23.08
B7-n78L	40	30	3500.01	DFT	QPSK	Edge_1RB_Right	23.13
B7-n78L	40	30	3500.01	DFT	QPSK	Outer_Full	23.14

B7-n78L	40	30	3500.01	DFT	16QAM	Inner_Full	23.21
B7-n78L	40	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.53
B7-n78L	40	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.66
B7-n78L	40	30	3500.01	DFT	16QAM	Outer_Full	22.16
B7-n78L	40	30	3500.01	DFT	64QAM	Inner_Full	21.60
B7-n78L	40	30	3500.01	DFT	64QAM	Edge_1RB_Left	21.00
B7-n78L	40	30	3500.01	DFT	64QAM	Edge_1RB_Right	21.14
B7-n78L	40	30	3500.01	DFT	64QAM	Outer_Full	21.73
B7-n78L	40	30	3500.01	DFT	256QAM	Inner_Full	19.66
B7-n78L	40	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.72
B7-n78L	40	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.85
B7-n78L	40	30	3500.01	DFT	256QAM	Outer_Full	19.68
B7-n78L	40	30	3500.01	CP	QPSK	Inner_Full	22.63
B7-n78L	40	30	3500.01	CP	QPSK	Edge_1RB_Left	21.05
B7-n78L	40	30	3500.01	CP	QPSK	Edge_1RB_Right	21.16
B7-n78L	40	30	3500.01	CP	QPSK	Outer_Full	21.12
B7-n78L	40	30	3500.01	CP	16QAM	Inner_Full	22.19
B7-n78L	40	30	3500.01	CP	16QAM	Edge_1RB_Left	20.93
B7-n78L	40	30	3500.01	CP	16QAM	Edge_1RB_Right	21.01
B7-n78L	40	30	3500.01	CP	16QAM	Outer_Full	21.13
B7-n78L	40	30	3500.01	CP	64QAM	Inner_Full	20.61
B7-n78L	40	30	3500.01	CP	64QAM	Edge_1RB_Left	20.36
B7-n78L	40	30	3500.01	CP	64QAM	Edge_1RB_Right	20.57
B7-n78L	40	30	3500.01	CP	64QAM	Outer_Full	20.67
B7-n78L	40	30	3500.01	CP	256QAM	Inner_Full	17.73
B7-n78L	40	30	3500.01	CP	256QAM	Edge_1RB_Left	16.83
B7-n78L	40	30	3500.01	CP	256QAM	Edge_1RB_Right	17.07
B7-n78L	40	30	3500.01	CP	256QAM	Outer_Full	17.68
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Inner_Full	24.03
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Edge_1RB_Left	23.70
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Edge_1RB_Right	23.74
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Outer_Full	23.60
B7-n78L	40	30	3529.98	DFT	QPSK	Inner_Full	23.91
B7-n78L	40	30	3529.98	DFT	QPSK	Edge_1RB_Left	23.32
B7-n78L	40	30	3529.98	DFT	QPSK	Edge_1RB_Right	23.36
B7-n78L	40	30	3529.98	DFT	QPSK	Outer_Full	23.10
B7-n78L	40	30	3529.98	DFT	16QAM	Inner_Full	23.09
B7-n78L	40	30	3529.98	DFT	16QAM	Edge_1RB_Left	21.85
B7-n78L	40	30	3529.98	DFT	16QAM	Edge_1RB_Right	21.78
B7-n78L	40	30	3529.98	DFT	16QAM	Outer_Full	22.30
B7-n78L	40	30	3529.98	DFT	64QAM	Inner_Full	21.50

B7-n78L	40	30	3529.98	DFT	64QAM	Edge_1RB_Left	21.17
B7-n78L	40	30	3529.98	DFT	64QAM	Edge_1RB_Right	21.28
B7-n78L	40	30	3529.98	DFT	64QAM	Outer_Full	21.67
B7-n78L	40	30	3529.98	DFT	256QAM	Inner_Full	19.59
B7-n78L	40	30	3529.98	DFT	256QAM	Edge_1RB_Left	18.95
B7-n78L	40	30	3529.98	DFT	256QAM	Edge_1RB_Right	19.05
B7-n78L	40	30	3529.98	DFT	256QAM	Outer_Full	19.70
B7-n78L	40	30	3529.98	CP	QPSK	Inner_Full	22.60
B7-n78L	40	30	3529.98	CP	QPSK	Edge_1RB_Left	21.33
B7-n78L	40	30	3529.98	CP	QPSK	Edge_1RB_Right	21.42
B7-n78L	40	30	3529.98	CP	QPSK	Outer_Full	21.22
B7-n78L	40	30	3529.98	CP	16QAM	Inner_Full	22.05
B7-n78L	40	30	3529.98	CP	16QAM	Edge_1RB_Left	21.05
B7-n78L	40	30	3529.98	CP	16QAM	Edge_1RB_Right	21.26
B7-n78L	40	30	3529.98	CP	16QAM	Outer_Full	21.15
B7-n78L	40	30	3529.98	CP	64QAM	Inner_Full	20.52
B7-n78L	40	30	3529.98	CP	64QAM	Edge_1RB_Left	20.62
B7-n78L	40	30	3529.98	CP	64QAM	Edge_1RB_Right	20.71
B7-n78L	40	30	3529.98	CP	64QAM	Outer_Full	20.63
B7-n78L	40	30	3529.98	CP	256QAM	Inner_Full	17.62
B7-n78L	40	30	3529.98	CP	256QAM	Edge_1RB_Left	16.94
B7-n78L	40	30	3529.98	CP	256QAM	Edge_1RB_Right	17.22
B7-n78L	40	30	3529.98	CP	256QAM	Outer_Full	17.71
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Inner_Full	23.47
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.35
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Edge_1RB_Right	23.19
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Outer_Full	23.13
B7-n78L	50	30	3475.02	DFT	QPSK	Inner_Full	23.56
B7-n78L	50	30	3475.02	DFT	QPSK	Edge_1RB_Left	22.85
B7-n78L	50	30	3475.02	DFT	QPSK	Edge_1RB_Right	22.79
B7-n78L	50	30	3475.02	DFT	QPSK	Outer_Full	22.66
B7-n78L	50	30	3475.02	DFT	16QAM	Inner_Full	22.61
B7-n78L	50	30	3475.02	DFT	16QAM	Edge_1RB_Left	21.45
B7-n78L	50	30	3475.02	DFT	16QAM	Edge_1RB_Right	21.28
B7-n78L	50	30	3475.02	DFT	16QAM	Outer_Full	21.66
B7-n78L	50	30	3475.02	DFT	64QAM	Inner_Full	21.17
B7-n78L	50	30	3475.02	DFT	64QAM	Edge_1RB_Left	20.80
B7-n78L	50	30	3475.02	DFT	64QAM	Edge_1RB_Right	20.79
B7-n78L	50	30	3475.02	DFT	64QAM	Outer_Full	21.19
B7-n78L	50	30	3475.02	DFT	256QAM	Inner_Full	19.16
B7-n78L	50	30	3475.02	DFT	256QAM	Edge_1RB_Left	18.55

B7-n78L	50	30	3475.02	DFT	256QAM	Edge_1RB_Right	18.47
B7-n78L	50	30	3475.02	DFT	256QAM	Outer_Full	19.14
B7-n78L	50	30	3475.02	CP	QPSK	Inner_Full	22.04
B7-n78L	50	30	3475.02	CP	QPSK	Edge_1RB_Left	20.91
B7-n78L	50	30	3475.02	CP	QPSK	Edge_1RB_Right	20.82
B7-n78L	50	30	3475.02	CP	QPSK	Outer_Full	20.73
B7-n78L	50	30	3475.02	CP	16QAM	Inner_Full	21.61
B7-n78L	50	30	3475.02	CP	16QAM	Edge_1RB_Left	21.25
B7-n78L	50	30	3475.02	CP	16QAM	Edge_1RB_Right	21.18
B7-n78L	50	30	3475.02	CP	16QAM	Outer_Full	20.67
B7-n78L	50	30	3475.02	CP	64QAM	Inner_Full	20.04
B7-n78L	50	30	3475.02	CP	64QAM	Edge_1RB_Left	20.21
B7-n78L	50	30	3475.02	CP	64QAM	Edge_1RB_Right	20.26
B7-n78L	50	30	3475.02	CP	64QAM	Outer_Full	20.21
B7-n78L	50	30	3475.02	CP	256QAM	Inner_Full	17.20
B7-n78L	50	30	3475.02	CP	256QAM	Edge_1RB_Left	16.60
B7-n78L	50	30	3475.02	CP	256QAM	Edge_1RB_Right	16.74
B7-n78L	50	30	3475.02	CP	256QAM	Outer_Full	17.20
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.75
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.15
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.15
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.33
B7-n78L	50	30	3500.01	DFT	QPSK	Inner_Full	23.81
B7-n78L	50	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.79
B7-n78L	50	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.73
B7-n78L	50	30	3500.01	DFT	QPSK	Outer_Full	22.83
B7-n78L	50	30	3500.01	DFT	16QAM	Inner_Full	22.88
B7-n78L	50	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.52
B7-n78L	50	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.24
B7-n78L	50	30	3500.01	DFT	16QAM	Outer_Full	21.85
B7-n78L	50	30	3500.01	DFT	64QAM	Inner_Full	21.45
B7-n78L	50	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.84
B7-n78L	50	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.75
B7-n78L	50	30	3500.01	DFT	64QAM	Outer_Full	21.32
B7-n78L	50	30	3500.01	DFT	256QAM	Inner_Full	19.40
B7-n78L	50	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.35
B7-n78L	50	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.40
B7-n78L	50	30	3500.01	DFT	256QAM	Outer_Full	19.57
B7-n78L	50	30	3500.01	CP	QPSK	Inner_Full	22.32
B7-n78L	50	30	3500.01	CP	QPSK	Edge_1RB_Left	20.70
B7-n78L	50	30	3500.01	CP	QPSK	Edge_1RB_Right	20.82

B7-n78L	50	30	3500.01	CP	QPSK	Outer_Full	20.81
B7-n78L	50	30	3500.01	CP	16QAM	Inner_Full	21.86
B7-n78L	50	30	3500.01	CP	16QAM	Edge_1RB_Left	20.31
B7-n78L	50	30	3500.01	CP	16QAM	Edge_1RB_Right	20.32
B7-n78L	50	30	3500.01	CP	16QAM	Outer_Full	20.83
B7-n78L	50	30	3500.01	CP	64QAM	Inner_Full	20.34
B7-n78L	50	30	3500.01	CP	64QAM	Edge_1RB_Left	19.95
B7-n78L	50	30	3500.01	CP	64QAM	Edge_1RB_Right	20.08
B7-n78L	50	30	3500.01	CP	64QAM	Outer_Full	20.35
B7-n78L	50	30	3500.01	CP	256QAM	Inner_Full	17.51
B7-n78L	50	30	3500.01	CP	256QAM	Edge_1RB_Left	16.34
B7-n78L	50	30	3500.01	CP	256QAM	Edge_1RB_Right	16.47
B7-n78L	50	30	3500.01	CP	256QAM	Outer_Full	17.41
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Inner_Full	23.64
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Edge_1RB_Left	23.41
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Edge_1RB_Right	23.41
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Outer_Full	23.26
B7-n78L	50	30	3525	DFT	QPSK	Inner_Full	23.61
B7-n78L	50	30	3525	DFT	QPSK	Edge_1RB_Left	23.09
B7-n78L	50	30	3525	DFT	QPSK	Edge_1RB_Right	22.91
B7-n78L	50	30	3525	DFT	QPSK	Outer_Full	22.80
B7-n78L	50	30	3525	DFT	16QAM	Inner_Full	22.81
B7-n78L	50	30	3525	DFT	16QAM	Edge_1RB_Left	21.60
B7-n78L	50	30	3525	DFT	16QAM	Edge_1RB_Right	21.44
B7-n78L	50	30	3525	DFT	16QAM	Outer_Full	21.89
B7-n78L	50	30	3525	DFT	64QAM	Inner_Full	21.25
B7-n78L	50	30	3525	DFT	64QAM	Edge_1RB_Left	20.92
B7-n78L	50	30	3525	DFT	64QAM	Edge_1RB_Right	21.08
B7-n78L	50	30	3525	DFT	64QAM	Outer_Full	21.31
B7-n78L	50	30	3525	DFT	256QAM	Inner_Full	19.25
B7-n78L	50	30	3525	DFT	256QAM	Edge_1RB_Left	18.63
B7-n78L	50	30	3525	DFT	256QAM	Edge_1RB_Right	18.57
B7-n78L	50	30	3525	DFT	256QAM	Outer_Full	19.33
B7-n78L	50	30	3525	CP	QPSK	Inner_Full	22.22
B7-n78L	50	30	3525	CP	QPSK	Edge_1RB_Left	20.96
B7-n78L	50	30	3525	CP	QPSK	Edge_1RB_Right	21.05
B7-n78L	50	30	3525	CP	QPSK	Outer_Full	20.77
B7-n78L	50	30	3525	CP	16QAM	Inner_Full	21.69
B7-n78L	50	30	3525	CP	16QAM	Edge_1RB_Left	20.49
B7-n78L	50	30	3525	CP	16QAM	Edge_1RB_Right	20.77
B7-n78L	50	30	3525	CP	16QAM	Outer_Full	20.94

B7-n78L	50	30	3525	CP	64QAM	Inner_Full	20.22
B7-n78L	50	30	3525	CP	64QAM	Edge_1RB_Left	20.10
B7-n78L	50	30	3525	CP	64QAM	Edge_1RB_Right	20.20
B7-n78L	50	30	3525	CP	64QAM	Outer_Full	20.30
B7-n78L	50	30	3525	CP	256QAM	Inner_Full	17.34
B7-n78L	50	30	3525	CP	256QAM	Edge_1RB_Left	16.55
B7-n78L	50	30	3525	CP	256QAM	Edge_1RB_Right	16.74
B7-n78L	50	30	3525	CP	256QAM	Outer_Full	17.33
B7-n78L	60	30	3480	DFT	pi/2 BPSK	Inner_Full	23.36
B7-n78L	60	30	3480	DFT	pi/2 BPSK	Edge_1RB_Left	23.24
B7-n78L	60	30	3480	DFT	pi/2 BPSK	Edge_1RB_Right	23.14
B7-n78L	60	30	3480	DFT	pi/2 BPSK	Outer_Full	22.99
B7-n78L	60	30	3480	DFT	QPSK	Inner_Full	23.32
B7-n78L	60	30	3480	DFT	QPSK	Edge_1RB_Left	22.62
B7-n78L	60	30	3480	DFT	QPSK	Edge_1RB_Right	22.65
B7-n78L	60	30	3480	DFT	QPSK	Outer_Full	22.58
B7-n78L	60	30	3480	DFT	16QAM	Inner_Full	22.44
B7-n78L	60	30	3480	DFT	16QAM	Edge_1RB_Left	21.15
B7-n78L	60	30	3480	DFT	16QAM	Edge_1RB_Right	21.21
B7-n78L	60	30	3480	DFT	16QAM	Outer_Full	21.53
B7-n78L	60	30	3480	DFT	64QAM	Inner_Full	20.96
B7-n78L	60	30	3480	DFT	64QAM	Edge_1RB_Left	20.66
B7-n78L	60	30	3480	DFT	64QAM	Edge_1RB_Right	20.69
B7-n78L	60	30	3480	DFT	64QAM	Outer_Full	21.04
B7-n78L	60	30	3480	DFT	256QAM	Inner_Full	19.01
B7-n78L	60	30	3480	DFT	256QAM	Edge_1RB_Left	18.32
B7-n78L	60	30	3480	DFT	256QAM	Edge_1RB_Right	18.33
B7-n78L	60	30	3480	DFT	256QAM	Outer_Full	19.13
B7-n78L	60	30	3480	CP	QPSK	Inner_Full	21.94
B7-n78L	60	30	3480	CP	QPSK	Edge_1RB_Left	20.57
B7-n78L	60	30	3480	CP	QPSK	Edge_1RB_Right	20.82
B7-n78L	60	30	3480	CP	QPSK	Outer_Full	20.57
B7-n78L	60	30	3480	CP	16QAM	Inner_Full	21.46
B7-n78L	60	30	3480	CP	16QAM	Edge_1RB_Left	21.07
B7-n78L	60	30	3480	CP	16QAM	Edge_1RB_Right	21.09
B7-n78L	60	30	3480	CP	16QAM	Outer_Full	20.56
B7-n78L	60	30	3480	CP	64QAM	Inner_Full	20.06
B7-n78L	60	30	3480	CP	64QAM	Edge_1RB_Left	20.01
B7-n78L	60	30	3480	CP	64QAM	Edge_1RB_Right	20.11
B7-n78L	60	30	3480	CP	64QAM	Outer_Full	20.09
B7-n78L	60	30	3480	CP	256QAM	Inner_Full	16.96

B7-n78L	60	30	3480	CP	256QAM	Edge_1RB_Left	16.39
B7-n78L	60	30	3480	CP	256QAM	Edge_1RB_Right	16.55
B7-n78L	60	30	3480	CP	256QAM	Outer_Full	17.10
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.43
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	22.91
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	22.85
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Outer_Full	22.97
B7-n78L	60	30	3500.01	DFT	QPSK	Inner_Full	23.39
B7-n78L	60	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.39
B7-n78L	60	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.39
B7-n78L	60	30	3500.01	DFT	QPSK	Outer_Full	22.52
B7-n78L	60	30	3500.01	DFT	16QAM	Inner_Full	22.52
B7-n78L	60	30	3500.01	DFT	16QAM	Edge_1RB_Left	20.92
B7-n78L	60	30	3500.01	DFT	16QAM	Edge_1RB_Right	20.94
B7-n78L	60	30	3500.01	DFT	16QAM	Outer_Full	21.46
B7-n78L	60	30	3500.01	DFT	64QAM	Inner_Full	21.05
B7-n78L	60	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.33
B7-n78L	60	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.51
B7-n78L	60	30	3500.01	DFT	64QAM	Outer_Full	21.01
B7-n78L	60	30	3500.01	DFT	256QAM	Inner_Full	18.97
B7-n78L	60	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.13
B7-n78L	60	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.22
B7-n78L	60	30	3500.01	DFT	256QAM	Outer_Full	19.08
B7-n78L	60	30	3500.01	CP	QPSK	Inner_Full	22.01
B7-n78L	60	30	3500.01	CP	QPSK	Edge_1RB_Left	20.30
B7-n78L	60	30	3500.01	CP	QPSK	Edge_1RB_Right	20.53
B7-n78L	60	30	3500.01	CP	QPSK	Outer_Full	20.49
B7-n78L	60	30	3500.01	CP	16QAM	Inner_Full	21.51
B7-n78L	60	30	3500.01	CP	16QAM	Edge_1RB_Left	20.79
B7-n78L	60	30	3500.01	CP	16QAM	Edge_1RB_Right	20.73
B7-n78L	60	30	3500.01	CP	16QAM	Outer_Full	20.54
B7-n78L	60	30	3500.01	CP	64QAM	Inner_Full	20.14
B7-n78L	60	30	3500.01	CP	64QAM	Edge_1RB_Left	19.71
B7-n78L	60	30	3500.01	CP	64QAM	Edge_1RB_Right	19.82
B7-n78L	60	30	3500.01	CP	64QAM	Outer_Full	19.97
B7-n78L	60	30	3500.01	CP	256QAM	Inner_Full	17.09
B7-n78L	60	30	3500.01	CP	256QAM	Edge_1RB_Left	16.18
B7-n78L	60	30	3500.01	CP	256QAM	Edge_1RB_Right	16.32
B7-n78L	60	30	3500.01	CP	256QAM	Outer_Full	17.13
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Inner_Full	23.36
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Edge_1RB_Left	22.99

B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Edge_1RB_Right	23.11
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Outer_Full	23.05
B7-n78L	60	30	3519.99	DFT	QPSK	Inner_Full	23.36
B7-n78L	60	30	3519.99	DFT	QPSK	Edge_1RB_Left	22.62
B7-n78L	60	30	3519.99	DFT	QPSK	Edge_1RB_Right	22.69
B7-n78L	60	30	3519.99	DFT	QPSK	Outer_Full	22.53
B7-n78L	60	30	3519.99	DFT	16QAM	Inner_Full	22.44
B7-n78L	60	30	3519.99	DFT	16QAM	Edge_1RB_Left	21.07
B7-n78L	60	30	3519.99	DFT	16QAM	Edge_1RB_Right	21.24
B7-n78L	60	30	3519.99	DFT	16QAM	Outer_Full	21.51
B7-n78L	60	30	3519.99	DFT	64QAM	Inner_Full	21.01
B7-n78L	60	30	3519.99	DFT	64QAM	Edge_1RB_Left	20.32
B7-n78L	60	30	3519.99	DFT	64QAM	Edge_1RB_Right	20.65
B7-n78L	60	30	3519.99	DFT	64QAM	Outer_Full	21.08
B7-n78L	60	30	3519.99	DFT	256QAM	Inner_Full	19.02
B7-n78L	60	30	3519.99	DFT	256QAM	Edge_1RB_Left	18.25
B7-n78L	60	30	3519.99	DFT	256QAM	Edge_1RB_Right	18.39
B7-n78L	60	30	3519.99	DFT	256QAM	Outer_Full	19.17
B7-n78L	60	30	3519.99	CP	QPSK	Inner_Full	21.94
B7-n78L	60	30	3519.99	CP	QPSK	Edge_1RB_Left	20.46
B7-n78L	60	30	3519.99	CP	QPSK	Edge_1RB_Right	20.74
B7-n78L	60	30	3519.99	CP	QPSK	Outer_Full	20.55
B7-n78L	60	30	3519.99	CP	16QAM	Inner_Full	21.44
B7-n78L	60	30	3519.99	CP	16QAM	Edge_1RB_Left	20.92
B7-n78L	60	30	3519.99	CP	16QAM	Edge_1RB_Right	21.05
B7-n78L	60	30	3519.99	CP	16QAM	Outer_Full	20.54
B7-n78L	60	30	3519.99	CP	64QAM	Inner_Full	20.10
B7-n78L	60	30	3519.99	CP	64QAM	Edge_1RB_Left	20.11
B7-n78L	60	30	3519.99	CP	64QAM	Edge_1RB_Right	20.10
B7-n78L	60	30	3519.99	CP	64QAM	Outer_Full	20.05
B7-n78L	60	30	3519.99	CP	256QAM	Inner_Full	17.05
B7-n78L	60	30	3519.99	CP	256QAM	Edge_1RB_Left	16.26
B7-n78L	60	30	3519.99	CP	256QAM	Edge_1RB_Right	16.55
B7-n78L	60	30	3519.99	CP	256QAM	Outer_Full	17.17
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Inner_Full	23.35
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.11
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Edge_1RB_Right	22.87
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Outer_Full	22.97
B7-n78L	80	30	3490.02	DFT	QPSK	Inner_Full	23.35
B7-n78L	80	30	3490.02	DFT	QPSK	Edge_1RB_Left	22.74
B7-n78L	80	30	3490.02	DFT	QPSK	Edge_1RB_Right	22.41

B7-n78L	80	30	3490.02	DFT	QPSK	Outer_Full	22.48
B7-n78L	80	30	3490.02	DFT	16QAM	Inner_Full	22.45
B7-n78L	80	30	3490.02	DFT	16QAM	Edge_1RB_Left	21.88
B7-n78L	80	30	3490.02	DFT	16QAM	Edge_1RB_Right	21.64
B7-n78L	80	30	3490.02	DFT	16QAM	Outer_Full	21.47
B7-n78L	80	30	3490.02	DFT	64QAM	Inner_Full	20.97
B7-n78L	80	30	3490.02	DFT	64QAM	Edge_1RB_Left	20.65
B7-n78L	80	30	3490.02	DFT	64QAM	Edge_1RB_Right	20.64
B7-n78L	80	30	3490.02	DFT	64QAM	Outer_Full	20.97
B7-n78L	80	30	3490.02	DFT	256QAM	Inner_Full	18.93
B7-n78L	80	30	3490.02	DFT	256QAM	Edge_1RB_Left	18.40
B7-n78L	80	30	3490.02	DFT	256QAM	Edge_1RB_Right	18.15
B7-n78L	80	30	3490.02	DFT	256QAM	Outer_Full	19.04
B7-n78L	80	30	3490.02	CP	QPSK	Inner_Full	21.92
B7-n78L	80	30	3490.02	CP	QPSK	Edge_1RB_Left	20.61
B7-n78L	80	30	3490.02	CP	QPSK	Edge_1RB_Right	20.35
B7-n78L	80	30	3490.02	CP	QPSK	Outer_Full	20.51
B7-n78L	80	30	3490.02	CP	16QAM	Inner_Full	21.44
B7-n78L	80	30	3490.02	CP	16QAM	Edge_1RB_Left	20.79
B7-n78L	80	30	3490.02	CP	16QAM	Edge_1RB_Right	20.38
B7-n78L	80	30	3490.02	CP	16QAM	Outer_Full	20.43
B7-n78L	80	30	3490.02	CP	64QAM	Inner_Full	20.03
B7-n78L	80	30	3490.02	CP	64QAM	Edge_1RB_Left	19.84
B7-n78L	80	30	3490.02	CP	64QAM	Edge_1RB_Right	19.71
B7-n78L	80	30	3490.02	CP	64QAM	Outer_Full	20.07
B7-n78L	80	30	3490.02	CP	256QAM	Inner_Full	17.01
B7-n78L	80	30	3490.02	CP	256QAM	Edge_1RB_Left	16.49
B7-n78L	80	30	3490.02	CP	256QAM	Edge_1RB_Right	16.23
B7-n78L	80	30	3490.02	CP	256QAM	Outer_Full	17.09
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.41
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	22.98
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.09
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Outer_Full	22.95
B7-n78L	80	30	3500.01	DFT	QPSK	Inner_Full	23.44
B7-n78L	80	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.66
B7-n78L	80	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.63
B7-n78L	80	30	3500.01	DFT	QPSK	Outer_Full	22.48
B7-n78L	80	30	3500.01	DFT	16QAM	Inner_Full	22.54
B7-n78L	80	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.65
B7-n78L	80	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.72
B7-n78L	80	30	3500.01	DFT	16QAM	Outer_Full	21.48

B7-n78L	80	30	3500.01	DFT	64QAM	Inner_Full	21.10
B7-n78L	80	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.30
B7-n78L	80	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.59
B7-n78L	80	30	3500.01	DFT	64QAM	Outer_Full	21.01
B7-n78L	80	30	3500.01	DFT	256QAM	Inner_Full	19.07
B7-n78L	80	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.09
B7-n78L	80	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.31
B7-n78L	80	30	3500.01	DFT	256QAM	Outer_Full	19.00
B7-n78L	80	30	3500.01	CP	QPSK	Inner_Full	22.02
B7-n78L	80	30	3500.01	CP	QPSK	Edge_1RB_Left	20.62
B7-n78L	80	30	3500.01	CP	QPSK	Edge_1RB_Right	20.64
B7-n78L	80	30	3500.01	CP	QPSK	Outer_Full	20.54
B7-n78L	80	30	3500.01	CP	16QAM	Inner_Full	21.54
B7-n78L	80	30	3500.01	CP	16QAM	Edge_1RB_Left	20.49
B7-n78L	80	30	3500.01	CP	16QAM	Edge_1RB_Right	20.49
B7-n78L	80	30	3500.01	CP	16QAM	Outer_Full	20.48
B7-n78L	80	30	3500.01	CP	64QAM	Inner_Full	20.11
B7-n78L	80	30	3500.01	CP	64QAM	Edge_1RB_Left	19.98
B7-n78L	80	30	3500.01	CP	64QAM	Edge_1RB_Right	19.91
B7-n78L	80	30	3500.01	CP	64QAM	Outer_Full	20.05
B7-n78L	80	30	3500.01	CP	256QAM	Inner_Full	17.11
B7-n78L	80	30	3500.01	CP	256QAM	Edge_1RB_Left	16.14
B7-n78L	80	30	3500.01	CP	256QAM	Edge_1RB_Right	16.42
B7-n78L	80	30	3500.01	CP	256QAM	Outer_Full	17.10
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Inner_Full	23.50
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Edge_1RB_Left	22.90
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Edge_1RB_Right	23.21
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Outer_Full	23.03
B7-n78L	80	30	3510	DFT	QPSK	Inner_Full	23.41
B7-n78L	80	30	3510	DFT	QPSK	Edge_1RB_Left	22.37
B7-n78L	80	30	3510	DFT	QPSK	Edge_1RB_Right	22.73
B7-n78L	80	30	3510	DFT	QPSK	Outer_Full	22.56
B7-n78L	80	30	3510	DFT	16QAM	Inner_Full	22.50
B7-n78L	80	30	3510	DFT	16QAM	Edge_1RB_Left	21.57
B7-n78L	80	30	3510	DFT	16QAM	Edge_1RB_Right	21.82
B7-n78L	80	30	3510	DFT	16QAM	Outer_Full	21.49
B7-n78L	80	30	3510	DFT	64QAM	Inner_Full	21.10
B7-n78L	80	30	3510	DFT	64QAM	Edge_1RB_Left	20.15
B7-n78L	80	30	3510	DFT	64QAM	Edge_1RB_Right	20.58
B7-n78L	80	30	3510	DFT	64QAM	Outer_Full	21.04
B7-n78L	80	30	3510	DFT	256QAM	Inner_Full	19.05

B7-n78L	80	30	3510	DFT	256QAM	Edge_1RB_Left	18.02
B7-n78L	80	30	3510	DFT	256QAM	Edge_1RB_Right	18.39
B7-n78L	80	30	3510	DFT	256QAM	Outer_Full	19.03
B7-n78L	80	30	3510	CP	QPSK	Inner_Full	22.09
B7-n78L	80	30	3510	CP	QPSK	Edge_1RB_Left	20.44
B7-n78L	80	30	3510	CP	QPSK	Edge_1RB_Right	20.75
B7-n78L	80	30	3510	CP	QPSK	Outer_Full	20.57
B7-n78L	80	30	3510	CP	16QAM	Inner_Full	21.52
B7-n78L	80	30	3510	CP	16QAM	Edge_1RB_Left	20.46
B7-n78L	80	30	3510	CP	16QAM	Edge_1RB_Right	20.54
B7-n78L	80	30	3510	CP	16QAM	Outer_Full	20.49
B7-n78L	80	30	3510	CP	64QAM	Inner_Full	20.01
B7-n78L	80	30	3510	CP	64QAM	Edge_1RB_Left	19.58
B7-n78L	80	30	3510	CP	64QAM	Edge_1RB_Right	19.94
B7-n78L	80	30	3510	CP	64QAM	Outer_Full	20.06
B7-n78L	80	30	3510	CP	256QAM	Inner_Full	17.12
B7-n78L	80	30	3510	CP	256QAM	Edge_1RB_Left	16.11
B7-n78L	80	30	3510	CP	256QAM	Edge_1RB_Right	16.48
B7-n78L	80	30	3510	CP	256QAM	Outer_Full	17.09
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Inner_Full	23.46
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Edge_1RB_Left	23.23
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Edge_1RB_Right	23.07
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Outer_Full	23.03
B7-n78L	90	30	3495	DFT	QPSK	Inner_Full	23.55
B7-n78L	90	30	3495	DFT	QPSK	Edge_1RB_Left	22.93
B7-n78L	90	30	3495	DFT	QPSK	Edge_1RB_Right	22.58
B7-n78L	90	30	3495	DFT	QPSK	Outer_Full	22.55
B7-n78L	90	30	3495	DFT	16QAM	Inner_Full	22.50
B7-n78L	90	30	3495	DFT	16QAM	Edge_1RB_Left	21.93
B7-n78L	90	30	3495	DFT	16QAM	Edge_1RB_Right	21.69
B7-n78L	90	30	3495	DFT	16QAM	Outer_Full	21.54
B7-n78L	90	30	3495	DFT	64QAM	Inner_Full	21.14
B7-n78L	90	30	3495	DFT	64QAM	Edge_1RB_Left	20.60
B7-n78L	90	30	3495	DFT	64QAM	Edge_1RB_Right	20.62
B7-n78L	90	30	3495	DFT	64QAM	Outer_Full	21.13
B7-n78L	90	30	3495	DFT	256QAM	Inner_Full	19.14
B7-n78L	90	30	3495	DFT	256QAM	Edge_1RB_Left	18.40
B7-n78L	90	30	3495	DFT	256QAM	Edge_1RB_Right	18.31
B7-n78L	90	30	3495	DFT	256QAM	Outer_Full	19.12
B7-n78L	90	30	3495	CP	QPSK	Inner_Full	22.10
B7-n78L	90	30	3495	CP	QPSK	Edge_1RB_Left	20.78

B7-n78L	90	30	3495	CP	QPSK	Edge_1RB_Right	20.63
B7-n78L	90	30	3495	CP	QPSK	Outer_Full	20.54
B7-n78L	90	30	3495	CP	16QAM	Inner_Full	21.56
B7-n78L	90	30	3495	CP	16QAM	Edge_1RB_Left	20.66
B7-n78L	90	30	3495	CP	16QAM	Edge_1RB_Right	20.44
B7-n78L	90	30	3495	CP	16QAM	Outer_Full	20.57
B7-n78L	90	30	3495	CP	64QAM	Inner_Full	20.12
B7-n78L	90	30	3495	CP	64QAM	Edge_1RB_Left	19.90
B7-n78L	90	30	3495	CP	64QAM	Edge_1RB_Right	19.98
B7-n78L	90	30	3495	CP	64QAM	Outer_Full	20.14
B7-n78L	90	30	3495	CP	256QAM	Inner_Full	17.18
B7-n78L	90	30	3495	CP	256QAM	Edge_1RB_Left	16.46
B7-n78L	90	30	3495	CP	256QAM	Edge_1RB_Right	16.36
B7-n78L	90	30	3495	CP	256QAM	Outer_Full	17.16
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.43
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.08
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.16
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.01
B7-n78L	90	30	3500.01	DFT	QPSK	Inner_Full	23.43
B7-n78L	90	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.76
B7-n78L	90	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.72
B7-n78L	90	30	3500.01	DFT	QPSK	Outer_Full	22.60
B7-n78L	90	30	3500.01	DFT	16QAM	Inner_Full	22.50
B7-n78L	90	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.84
B7-n78L	90	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.85
B7-n78L	90	30	3500.01	DFT	16QAM	Outer_Full	21.59
B7-n78L	90	30	3500.01	DFT	64QAM	Inner_Full	21.15
B7-n78L	90	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.45
B7-n78L	90	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.70
B7-n78L	90	30	3500.01	DFT	64QAM	Outer_Full	21.13
B7-n78L	90	30	3500.01	DFT	256QAM	Inner_Full	19.16
B7-n78L	90	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.35
B7-n78L	90	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.35
B7-n78L	90	30	3500.01	DFT	256QAM	Outer_Full	19.23
B7-n78L	90	30	3500.01	CP	QPSK	Inner_Full	22.07
B7-n78L	90	30	3500.01	CP	QPSK	Edge_1RB_Left	20.56
B7-n78L	90	30	3500.01	CP	QPSK	Edge_1RB_Right	20.78
B7-n78L	90	30	3500.01	CP	QPSK	Outer_Full	20.53
B7-n78L	90	30	3500.01	CP	16QAM	Inner_Full	21.58
B7-n78L	90	30	3500.01	CP	16QAM	Edge_1RB_Left	20.59
B7-n78L	90	30	3500.01	CP	16QAM	Edge_1RB_Right	20.64

B7-n78L	90	30	3500.01	CP	16QAM	Outer_Full	20.60
B7-n78L	90	30	3500.01	CP	64QAM	Inner_Full	20.14
B7-n78L	90	30	3500.01	CP	64QAM	Edge_1RB_Left	19.86
B7-n78L	90	30	3500.01	CP	64QAM	Edge_1RB_Right	20.12
B7-n78L	90	30	3500.01	CP	64QAM	Outer_Full	20.08
B7-n78L	90	30	3500.01	CP	256QAM	Inner_Full	17.10
B7-n78L	90	30	3500.01	CP	256QAM	Edge_1RB_Left	16.35
B7-n78L	90	30	3500.01	CP	256QAM	Edge_1RB_Right	16.50
B7-n78L	90	30	3500.01	CP	256QAM	Outer_Full	17.14
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Inner_Full	23.57
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Edge_1RB_Left	23.05
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Edge_1RB_Right	23.17
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Outer_Full	23.07
B7-n78L	90	30	3504.99	DFT	QPSK	Inner_Full	23.58
B7-n78L	90	30	3504.99	DFT	QPSK	Edge_1RB_Left	22.76
B7-n78L	90	30	3504.99	DFT	QPSK	Edge_1RB_Right	22.74
B7-n78L	90	30	3504.99	DFT	QPSK	Outer_Full	22.55
B7-n78L	90	30	3504.99	DFT	16QAM	Inner_Full	22.65
B7-n78L	90	30	3504.99	DFT	16QAM	Edge_1RB_Left	21.64
B7-n78L	90	30	3504.99	DFT	16QAM	Edge_1RB_Right	21.84
B7-n78L	90	30	3504.99	DFT	16QAM	Outer_Full	21.63
B7-n78L	90	30	3504.99	DFT	64QAM	Inner_Full	21.19
B7-n78L	90	30	3504.99	DFT	64QAM	Edge_1RB_Left	20.36
B7-n78L	90	30	3504.99	DFT	64QAM	Edge_1RB_Right	20.78
B7-n78L	90	30	3504.99	DFT	64QAM	Outer_Full	21.15
B7-n78L	90	30	3504.99	DFT	256QAM	Inner_Full	19.21
B7-n78L	90	30	3504.99	DFT	256QAM	Edge_1RB_Left	18.26
B7-n78L	90	30	3504.99	DFT	256QAM	Edge_1RB_Right	18.46
B7-n78L	90	30	3504.99	DFT	256QAM	Outer_Full	19.18
B7-n78L	90	30	3504.99	CP	QPSK	Inner_Full	22.16
B7-n78L	90	30	3504.99	CP	QPSK	Edge_1RB_Left	20.55
B7-n78L	90	30	3504.99	CP	QPSK	Edge_1RB_Right	20.86
B7-n78L	90	30	3504.99	CP	QPSK	Outer_Full	20.62
B7-n78L	90	30	3504.99	CP	16QAM	Inner_Full	21.65
B7-n78L	90	30	3504.99	CP	16QAM	Edge_1RB_Left	20.47
B7-n78L	90	30	3504.99	CP	16QAM	Edge_1RB_Right	20.70
B7-n78L	90	30	3504.99	CP	16QAM	Outer_Full	20.65
B7-n78L	90	30	3504.99	CP	64QAM	Inner_Full	20.21
B7-n78L	90	30	3504.99	CP	64QAM	Edge_1RB_Left	19.67
B7-n78L	90	30	3504.99	CP	64QAM	Edge_1RB_Right	20.14
B7-n78L	90	30	3504.99	CP	64QAM	Outer_Full	20.14



B7-n78L	90	30	3504.99	CP	256QAM	Inner_Full	17.22
B7-n78L	90	30	3504.99	CP	256QAM	Edge_1RB_Left	16.32
B7-n78L	90	30	3504.99	CP	256QAM	Edge_1RB_Right	16.55
B7-n78L	90	30	3504.99	CP	256QAM	Outer_Full	17.20

A.1.3 Radiated

This is the test for the maximum radiated power from the EUT.

Band 78: 27.50(k)(3) specifies " Mobile devices are limited to 1Watt (30 dBm) EIRP. Mobile devices operating in these bands must employ a means for limiting power to the minimum necessary for successful communications".

A.1.3.2 Method of Measurement

NASI C63.26 chapter 5.2.5.5: when working in decibels (i.e., logarithmic scale), the ERP and EIRP represent the sum of the transmit antenna gain (in dBd or dBi, respectively) and the conducted RF output power (expressed in dB relative to watts or milliwatts).

The relevant equation for determining the maximum ERP or EIRP from the measured RF output power is given in Equation (1) as follows:

$$\text{ERP or EIRP} = P_{\text{Mea}} + G_{\text{T}}$$

Where

ERP or EIRP effective radiated power or equivalent isotropically radiated power, respectively

(expressed in the same units as P_{Mea} , e.g., dBm or dBW)

P_{Mea} measured transmitter output power or PSD, in dBm or dBW

G_{T} gain of the transmitting antenna, in dBd (ERP) or dBi (EIRP)

Band 7_n78L-EIRP
Limits: ≤30dBm (1W)

BAND	BW(MHz)	SCS(kHz)	FREQ(MHz)	OFDM	MODULATION	RB LOCATION	Conducted POWER(dBm)	Radiated POWER(dBm) (GT (dBi)=3.4)
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Inner_Full	23.70	27.10
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.51	26.91
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Edge_1RB_Right	23.35	26.75
B7-n78L	20	30	3460.02	DFT	pi/2 BPSK	Outer_Full	23.44	26.84
B7-n78L	20	30	3460.02	DFT	QPSK	Inner_Full	23.80	27.20
B7-n78L	20	30	3460.02	DFT	QPSK	Edge_1RB_Left	23.05	26.45
B7-n78L	20	30	3460.02	DFT	QPSK	Edge_1RB_Right	22.88	26.28
B7-n78L	20	30	3460.02	DFT	QPSK	Outer_Full	22.89	26.29
B7-n78L	20	30	3460.02	DFT	16QAM	Inner_Full	22.93	26.33
B7-n78L	20	30	3460.02	DFT	16QAM	Edge_1RB_Left	21.69	25.09
B7-n78L	20	30	3460.02	DFT	16QAM	Edge_1RB_Right	21.50	24.90
B7-n78L	20	30	3460.02	DFT	16QAM	Outer_Full	21.95	25.35
B7-n78L	20	30	3460.02	DFT	64QAM	Inner_Full	21.50	24.90
B7-n78L	20	30	3460.02	DFT	64QAM	Edge_1RB_Left	21.22	24.62
B7-n78L	20	30	3460.02	DFT	64QAM	Edge_1RB_Right	21.04	24.44
B7-n78L	20	30	3460.02	DFT	64QAM	Outer_Full	21.39	24.79
B7-n78L	20	30	3460.02	DFT	256QAM	Inner_Full	19.43	22.83
B7-n78L	20	30	3460.02	DFT	256QAM	Edge_1RB_Left	18.74	22.14
B7-n78L	20	30	3460.02	DFT	256QAM	Edge_1RB_Right	18.57	21.97
B7-n78L	20	30	3460.02	DFT	256QAM	Outer_Full	19.42	22.82
B7-n78L	20	30	3460.02	CP	QPSK	Inner_Full	22.38	25.78
B7-n78L	20	30	3460.02	CP	QPSK	Edge_1RB_Left	21.09	24.49
B7-n78L	20	30	3460.02	CP	QPSK	Edge_1RB_Right	20.93	24.33
B7-n78L	20	30	3460.02	CP	QPSK	Outer_Full	20.96	24.36
B7-n78L	20	30	3460.02	CP	16QAM	Inner_Full	22.03	25.43
B7-n78L	20	30	3460.02	CP	16QAM	Edge_1RB_Left	21.01	24.41
B7-n78L	20	30	3460.02	CP	16QAM	Edge_1RB_Right	20.80	24.20
B7-n78L	20	30	3460.02	CP	16QAM	Outer_Full	20.96	24.36
B7-n78L	20	30	3460.02	CP	64QAM	Inner_Full	20.51	23.91
B7-n78L	20	30	3460.02	CP	64QAM	Edge_1RB_Left	20.43	23.83
B7-n78L	20	30	3460.02	CP	64QAM	Edge_1RB_Right	20.30	23.70
B7-n78L	20	30	3460.02	CP	64QAM	Outer_Full	20.41	23.81
B7-n78L	20	30	3460.02	CP	256QAM	Inner_Full	17.39	20.79

B7-n78L	20	30	3460.02	CP	256QAM	Edge_1RB_Left	16.87	20.27
B7-n78L	20	30	3460.02	CP	256QAM	Edge_1RB_Right	16.77	20.17
B7-n78L	20	30	3460.02	CP	256QAM	Outer_Full	17.43	20.83
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Inner_Full	24.09	27.49
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.51	26.91
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.68	27.08
B7-n78L	20	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.67	27.07
B7-n78L	20	30	3500.01	DFT	QPSK	Inner_Full	24.02	27.42
B7-n78L	20	30	3500.01	DFT	QPSK	Edge_1RB_Left	23.04	26.44
B7-n78L	20	30	3500.01	DFT	QPSK	Edge_1RB_Right	23.24	26.64
B7-n78L	20	30	3500.01	DFT	QPSK	Outer_Full	23.11	26.51
B7-n78L	20	30	3500.01	DFT	16QAM	Inner_Full	23.13	26.53
B7-n78L	20	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.63	25.03
B7-n78L	20	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.68	25.08
B7-n78L	20	30	3500.01	DFT	16QAM	Outer_Full	22.06	25.46
B7-n78L	20	30	3500.01	DFT	64QAM	Inner_Full	21.70	25.10
B7-n78L	20	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.89	24.29
B7-n78L	20	30	3500.01	DFT	64QAM	Edge_1RB_Right	21.07	24.47
B7-n78L	20	30	3500.01	DFT	64QAM	Outer_Full	21.44	24.84
B7-n78L	20	30	3500.01	DFT	256QAM	Inner_Full	19.50	22.90
B7-n78L	20	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.63	22.03
B7-n78L	20	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.75	22.15
B7-n78L	20	30	3500.01	DFT	256QAM	Outer_Full	19.58	22.98
B7-n78L	20	30	3500.01	CP	QPSK	Inner_Full	22.53	25.93
B7-n78L	20	30	3500.01	CP	QPSK	Edge_1RB_Left	20.97	24.37
B7-n78L	20	30	3500.01	CP	QPSK	Edge_1RB_Right	21.19	24.59
B7-n78L	20	30	3500.01	CP	QPSK	Outer_Full	21.08	24.48
B7-n78L	20	30	3500.01	CP	16QAM	Inner_Full	22.05	25.45
B7-n78L	20	30	3500.01	CP	16QAM	Edge_1RB_Left	21.44	24.84
B7-n78L	20	30	3500.01	CP	16QAM	Edge_1RB_Right	21.52	24.92
B7-n78L	20	30	3500.01	CP	16QAM	Outer_Full	21.00	24.40
B7-n78L	20	30	3500.01	CP	64QAM	Inner_Full	20.56	23.96
B7-n78L	20	30	3500.01	CP	64QAM	Edge_1RB_Left	20.28	23.68
B7-n78L	20	30	3500.01	CP	64QAM	Edge_1RB_Right	20.51	23.91
B7-n78L	20	30	3500.01	CP	64QAM	Outer_Full	20.50	23.90
B7-n78L	20	30	3500.01	CP	256QAM	Inner_Full	17.56	20.96
B7-n78L	20	30	3500.01	CP	256QAM	Edge_1RB_Left	16.82	20.22

B7-n78L	20	30	3500.01	CP	256QAM	Edge_1RB_Right	17.06	20.46
B7-n78L	20	30	3500.01	CP	256QAM	Outer_Full	17.57	20.97
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Inner_Full	23.86	27.26
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Edge_1RB_Left	23.31	26.71
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Edge_1RB_Right	23.62	27.02
B7-n78L	20	30	3540	DFT	pi/2 BPSK	Outer_Full	23.51	26.91
B7-n78L	20	30	3540	DFT	QPSK	Inner_Full	23.83	27.23
B7-n78L	20	30	3540	DFT	QPSK	Edge_1RB_Left	22.90	26.30
B7-n78L	20	30	3540	DFT	QPSK	Edge_1RB_Right	23.15	26.55
B7-n78L	20	30	3540	DFT	QPSK	Outer_Full	22.98	26.38
B7-n78L	20	30	3540	DFT	16QAM	Inner_Full	23.00	26.40
B7-n78L	20	30	3540	DFT	16QAM	Edge_1RB_Left	21.81	25.21
B7-n78L	20	30	3540	DFT	16QAM	Edge_1RB_Right	21.68	25.08
B7-n78L	20	30	3540	DFT	16QAM	Outer_Full	22.01	25.41
B7-n78L	20	30	3540	DFT	64QAM	Inner_Full	21.62	25.02
B7-n78L	20	30	3540	DFT	64QAM	Edge_1RB_Left	21.09	24.49
B7-n78L	20	30	3540	DFT	64QAM	Edge_1RB_Right	21.25	24.65
B7-n78L	20	30	3540	DFT	64QAM	Outer_Full	21.47	24.87
B7-n78L	20	30	3540	DFT	256QAM	Inner_Full	19.32	22.72
B7-n78L	20	30	3540	DFT	256QAM	Edge_1RB_Left	18.62	22.02
B7-n78L	20	30	3540	DFT	256QAM	Edge_1RB_Right	18.80	22.20
B7-n78L	20	30	3540	DFT	256QAM	Outer_Full	19.47	22.87
B7-n78L	20	30	3540	CP	QPSK	Inner_Full	22.56	25.96
B7-n78L	20	30	3540	CP	QPSK	Edge_1RB_Left	21.00	24.40
B7-n78L	20	30	3540	CP	QPSK	Edge_1RB_Right	21.74	25.14
B7-n78L	20	30	3540	CP	QPSK	Outer_Full	21.12	24.52
B7-n78L	20	30	3540	CP	16QAM	Inner_Full	22.06	25.46
B7-n78L	20	30	3540	CP	16QAM	Edge_1RB_Left	21.35	24.75
B7-n78L	20	30	3540	CP	16QAM	Edge_1RB_Right	21.64	25.04
B7-n78L	20	30	3540	CP	16QAM	Outer_Full	20.98	24.38
B7-n78L	20	30	3540	CP	64QAM	Inner_Full	20.57	23.97
B7-n78L	20	30	3540	CP	64QAM	Edge_1RB_Left	20.31	23.71
B7-n78L	20	30	3540	CP	64QAM	Edge_1RB_Right	20.58	23.98
B7-n78L	20	30	3540	CP	64QAM	Outer_Full	20.50	23.90
B7-n78L	20	30	3540	CP	256QAM	Inner_Full	17.64	21.04
B7-n78L	20	30	3540	CP	256QAM	Edge_1RB_Left	16.79	20.19
B7-n78L	20	30	3540	CP	256QAM	Edge_1RB_Right	17.66	21.06

B7-n78L	20	30	3540	CP	256QAM	Outer_Full	17.62	21.02
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Inner_Full	23.98	27.38
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.54	26.94
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.66	27.06
B7-n78L	40	30	3470.01	DFT	pi/2 BPSK	Outer_Full	23.59	26.99
B7-n78L	40	30	3470.01	DFT	QPSK	Inner_Full	23.95	27.35
B7-n78L	40	30	3470.01	DFT	QPSK	Edge_1RB_Left	23.33	26.73
B7-n78L	40	30	3470.01	DFT	QPSK	Edge_1RB_Right	23.16	26.56
B7-n78L	40	30	3470.01	DFT	QPSK	Outer_Full	23.04	26.44
B7-n78L	40	30	3470.01	DFT	16QAM	Inner_Full	23.01	26.41
B7-n78L	40	30	3470.01	DFT	16QAM	Edge_1RB_Left	21.87	25.27
B7-n78L	40	30	3470.01	DFT	16QAM	Edge_1RB_Right	21.86	25.26
B7-n78L	40	30	3470.01	DFT	16QAM	Outer_Full	22.09	25.49
B7-n78L	40	30	3470.01	DFT	64QAM	Inner_Full	21.55	24.95
B7-n78L	40	30	3470.01	DFT	64QAM	Edge_1RB_Left	21.13	24.53
B7-n78L	40	30	3470.01	DFT	64QAM	Edge_1RB_Right	21.40	24.80
B7-n78L	40	30	3470.01	DFT	64QAM	Outer_Full	21.65	25.05
B7-n78L	40	30	3470.01	DFT	256QAM	Inner_Full	19.46	22.86
B7-n78L	40	30	3470.01	DFT	256QAM	Edge_1RB_Left	19.00	22.40
B7-n78L	40	30	3470.01	DFT	256QAM	Edge_1RB_Right	18.88	22.28
B7-n78L	40	30	3470.01	DFT	256QAM	Outer_Full	19.65	23.05
B7-n78L	40	30	3470.01	CP	QPSK	Inner_Full	22.56	25.96
B7-n78L	40	30	3470.01	CP	QPSK	Edge_1RB_Left	21.41	24.81
B7-n78L	40	30	3470.01	CP	QPSK	Edge_1RB_Right	21.22	24.62
B7-n78L	40	30	3470.01	CP	QPSK	Outer_Full	21.10	24.50
B7-n78L	40	30	3470.01	CP	16QAM	Inner_Full	21.98	25.38
B7-n78L	40	30	3470.01	CP	16QAM	Edge_1RB_Left	20.95	24.35
B7-n78L	40	30	3470.01	CP	16QAM	Edge_1RB_Right	20.82	24.22
B7-n78L	40	30	3470.01	CP	16QAM	Outer_Full	21.04	24.44
B7-n78L	40	30	3470.01	CP	64QAM	Inner_Full	20.39	23.79
B7-n78L	40	30	3470.01	CP	64QAM	Edge_1RB_Left	20.47	23.87
B7-n78L	40	30	3470.01	CP	64QAM	Edge_1RB_Right	20.50	23.90
B7-n78L	40	30	3470.01	CP	64QAM	Outer_Full	20.50	23.90
B7-n78L	40	30	3470.01	CP	256QAM	Inner_Full	17.60	21.00
B7-n78L	40	30	3470.01	CP	256QAM	Edge_1RB_Left	17.11	20.51
B7-n78L	40	30	3470.01	CP	256QAM	Edge_1RB_Right	16.91	20.31
B7-n78L	40	30	3470.01	CP	256QAM	Outer_Full	17.55	20.95

B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Inner_Full	24.02	27.42
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.55	26.95
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.61	27.01
B7-n78L	40	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.60	27.00
B7-n78L	40	30	3500.01	DFT	QPSK	Inner_Full	24.03	27.43
B7-n78L	40	30	3500.01	DFT	QPSK	Edge_1RB_Left	23.08	26.48
B7-n78L	40	30	3500.01	DFT	QPSK	Edge_1RB_Right	23.13	26.53
B7-n78L	40	30	3500.01	DFT	QPSK	Outer_Full	23.14	26.54
B7-n78L	40	30	3500.01	DFT	16QAM	Inner_Full	23.21	26.61
B7-n78L	40	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.53	24.93
B7-n78L	40	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.66	25.06
B7-n78L	40	30	3500.01	DFT	16QAM	Outer_Full	22.16	25.56
B7-n78L	40	30	3500.01	DFT	64QAM	Inner_Full	21.60	25.00
B7-n78L	40	30	3500.01	DFT	64QAM	Edge_1RB_Left	21.00	24.40
B7-n78L	40	30	3500.01	DFT	64QAM	Edge_1RB_Right	21.14	24.54
B7-n78L	40	30	3500.01	DFT	64QAM	Outer_Full	21.73	25.13
B7-n78L	40	30	3500.01	DFT	256QAM	Inner_Full	19.66	23.06
B7-n78L	40	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.72	22.12
B7-n78L	40	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.85	22.25
B7-n78L	40	30	3500.01	DFT	256QAM	Outer_Full	19.68	23.08
B7-n78L	40	30	3500.01	CP	QPSK	Inner_Full	22.63	26.03
B7-n78L	40	30	3500.01	CP	QPSK	Edge_1RB_Left	21.05	24.45
B7-n78L	40	30	3500.01	CP	QPSK	Edge_1RB_Right	21.16	24.56
B7-n78L	40	30	3500.01	CP	QPSK	Outer_Full	21.12	24.52
B7-n78L	40	30	3500.01	CP	16QAM	Inner_Full	22.19	25.59
B7-n78L	40	30	3500.01	CP	16QAM	Edge_1RB_Left	20.93	24.33
B7-n78L	40	30	3500.01	CP	16QAM	Edge_1RB_Right	21.01	24.41
B7-n78L	40	30	3500.01	CP	16QAM	Outer_Full	21.13	24.53
B7-n78L	40	30	3500.01	CP	64QAM	Inner_Full	20.61	24.01
B7-n78L	40	30	3500.01	CP	64QAM	Edge_1RB_Left	20.36	23.76
B7-n78L	40	30	3500.01	CP	64QAM	Edge_1RB_Right	20.57	23.97
B7-n78L	40	30	3500.01	CP	64QAM	Outer_Full	20.67	24.07
B7-n78L	40	30	3500.01	CP	256QAM	Inner_Full	17.73	21.13
B7-n78L	40	30	3500.01	CP	256QAM	Edge_1RB_Left	16.83	20.23
B7-n78L	40	30	3500.01	CP	256QAM	Edge_1RB_Right	17.07	20.47
B7-n78L	40	30	3500.01	CP	256QAM	Outer_Full	17.68	21.08
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Inner_Full	24.03	27.43

B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Edge_1RB_Left	23.70	27.10
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Edge_1RB_Right	23.74	27.14
B7-n78L	40	30	3529.98	DFT	pi/2 BPSK	Outer_Full	23.60	27.00
B7-n78L	40	30	3529.98	DFT	QPSK	Inner_Full	23.91	27.31
B7-n78L	40	30	3529.98	DFT	QPSK	Edge_1RB_Left	23.32	26.72
B7-n78L	40	30	3529.98	DFT	QPSK	Edge_1RB_Right	23.36	26.76
B7-n78L	40	30	3529.98	DFT	QPSK	Outer_Full	23.10	26.50
B7-n78L	40	30	3529.98	DFT	16QAM	Inner_Full	23.09	26.49
B7-n78L	40	30	3529.98	DFT	16QAM	Edge_1RB_Left	21.85	25.25
B7-n78L	40	30	3529.98	DFT	16QAM	Edge_1RB_Right	21.78	25.18
B7-n78L	40	30	3529.98	DFT	16QAM	Outer_Full	22.30	25.70
B7-n78L	40	30	3529.98	DFT	64QAM	Inner_Full	21.50	24.90
B7-n78L	40	30	3529.98	DFT	64QAM	Edge_1RB_Left	21.17	24.57
B7-n78L	40	30	3529.98	DFT	64QAM	Edge_1RB_Right	21.28	24.68
B7-n78L	40	30	3529.98	DFT	64QAM	Outer_Full	21.67	25.07
B7-n78L	40	30	3529.98	DFT	256QAM	Inner_Full	19.59	22.99
B7-n78L	40	30	3529.98	DFT	256QAM	Edge_1RB_Left	18.95	22.35
B7-n78L	40	30	3529.98	DFT	256QAM	Edge_1RB_Right	19.05	22.45
B7-n78L	40	30	3529.98	DFT	256QAM	Outer_Full	19.70	23.10
B7-n78L	40	30	3529.98	CP	QPSK	Inner_Full	22.60	26.00
B7-n78L	40	30	3529.98	CP	QPSK	Edge_1RB_Left	21.33	24.73
B7-n78L	40	30	3529.98	CP	QPSK	Edge_1RB_Right	21.42	24.82
B7-n78L	40	30	3529.98	CP	QPSK	Outer_Full	21.22	24.62
B7-n78L	40	30	3529.98	CP	16QAM	Inner_Full	22.05	25.45
B7-n78L	40	30	3529.98	CP	16QAM	Edge_1RB_Left	21.05	24.45
B7-n78L	40	30	3529.98	CP	16QAM	Edge_1RB_Right	21.26	24.66
B7-n78L	40	30	3529.98	CP	16QAM	Outer_Full	21.15	24.55
B7-n78L	40	30	3529.98	CP	64QAM	Inner_Full	20.52	23.92
B7-n78L	40	30	3529.98	CP	64QAM	Edge_1RB_Left	20.62	24.02
B7-n78L	40	30	3529.98	CP	64QAM	Edge_1RB_Right	20.71	24.11
B7-n78L	40	30	3529.98	CP	64QAM	Outer_Full	20.63	24.03
B7-n78L	40	30	3529.98	CP	256QAM	Inner_Full	17.62	21.02
B7-n78L	40	30	3529.98	CP	256QAM	Edge_1RB_Left	16.94	20.34
B7-n78L	40	30	3529.98	CP	256QAM	Edge_1RB_Right	17.22	20.62
B7-n78L	40	30	3529.98	CP	256QAM	Outer_Full	17.71	21.11
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Inner_Full	23.47	26.87
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.35	26.75

B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Edge_1RB_Right	23.19	26.59
B7-n78L	50	30	3475.02	DFT	pi/2 BPSK	Outer_Full	23.13	26.53
B7-n78L	50	30	3475.02	DFT	QPSK	Inner_Full	23.56	26.96
B7-n78L	50	30	3475.02	DFT	QPSK	Edge_1RB_Left	22.85	26.25
B7-n78L	50	30	3475.02	DFT	QPSK	Edge_1RB_Right	22.79	26.19
B7-n78L	50	30	3475.02	DFT	QPSK	Outer_Full	22.66	26.06
B7-n78L	50	30	3475.02	DFT	16QAM	Inner_Full	22.61	26.01
B7-n78L	50	30	3475.02	DFT	16QAM	Edge_1RB_Left	21.45	24.85
B7-n78L	50	30	3475.02	DFT	16QAM	Edge_1RB_Right	21.28	24.68
B7-n78L	50	30	3475.02	DFT	16QAM	Outer_Full	21.66	25.06
B7-n78L	50	30	3475.02	DFT	64QAM	Inner_Full	21.17	24.57
B7-n78L	50	30	3475.02	DFT	64QAM	Edge_1RB_Left	20.80	24.20
B7-n78L	50	30	3475.02	DFT	64QAM	Edge_1RB_Right	20.79	24.19
B7-n78L	50	30	3475.02	DFT	64QAM	Outer_Full	21.19	24.59
B7-n78L	50	30	3475.02	DFT	256QAM	Inner_Full	19.16	22.56
B7-n78L	50	30	3475.02	DFT	256QAM	Edge_1RB_Left	18.55	21.95
B7-n78L	50	30	3475.02	DFT	256QAM	Edge_1RB_Right	18.47	21.87
B7-n78L	50	30	3475.02	DFT	256QAM	Outer_Full	19.14	22.54
B7-n78L	50	30	3475.02	CP	QPSK	Inner_Full	22.04	25.44
B7-n78L	50	30	3475.02	CP	QPSK	Edge_1RB_Left	20.91	24.31
B7-n78L	50	30	3475.02	CP	QPSK	Edge_1RB_Right	20.82	24.22
B7-n78L	50	30	3475.02	CP	QPSK	Outer_Full	20.73	24.13
B7-n78L	50	30	3475.02	CP	16QAM	Inner_Full	21.61	25.01
B7-n78L	50	30	3475.02	CP	16QAM	Edge_1RB_Left	21.25	24.65
B7-n78L	50	30	3475.02	CP	16QAM	Edge_1RB_Right	21.18	24.58
B7-n78L	50	30	3475.02	CP	16QAM	Outer_Full	20.67	24.07
B7-n78L	50	30	3475.02	CP	64QAM	Inner_Full	20.04	23.44
B7-n78L	50	30	3475.02	CP	64QAM	Edge_1RB_Left	20.21	23.61
B7-n78L	50	30	3475.02	CP	64QAM	Edge_1RB_Right	20.26	23.66
B7-n78L	50	30	3475.02	CP	64QAM	Outer_Full	20.21	23.61
B7-n78L	50	30	3475.02	CP	256QAM	Inner_Full	17.20	20.60
B7-n78L	50	30	3475.02	CP	256QAM	Edge_1RB_Left	16.60	20.00
B7-n78L	50	30	3475.02	CP	256QAM	Edge_1RB_Right	16.74	20.14
B7-n78L	50	30	3475.02	CP	256QAM	Outer_Full	17.20	20.60
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.75	27.15
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.15	26.55
B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.15	26.55

B7-n78L	50	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.33	26.73
B7-n78L	50	30	3500.01	DFT	QPSK	Inner_Full	23.81	27.21
B7-n78L	50	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.79	26.19
B7-n78L	50	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.73	26.13
B7-n78L	50	30	3500.01	DFT	QPSK	Outer_Full	22.83	26.23
B7-n78L	50	30	3500.01	DFT	16QAM	Inner_Full	22.88	26.28
B7-n78L	50	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.52	24.92
B7-n78L	50	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.24	24.64
B7-n78L	50	30	3500.01	DFT	16QAM	Outer_Full	21.85	25.25
B7-n78L	50	30	3500.01	DFT	64QAM	Inner_Full	21.45	24.85
B7-n78L	50	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.84	24.24
B7-n78L	50	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.75	24.15
B7-n78L	50	30	3500.01	DFT	64QAM	Outer_Full	21.32	24.72
B7-n78L	50	30	3500.01	DFT	256QAM	Inner_Full	19.40	22.80
B7-n78L	50	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.35	21.75
B7-n78L	50	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.40	21.80
B7-n78L	50	30	3500.01	DFT	256QAM	Outer_Full	19.57	22.97
B7-n78L	50	30	3500.01	CP	QPSK	Inner_Full	22.32	25.72
B7-n78L	50	30	3500.01	CP	QPSK	Edge_1RB_Left	20.70	24.10
B7-n78L	50	30	3500.01	CP	QPSK	Edge_1RB_Right	20.82	24.22
B7-n78L	50	30	3500.01	CP	QPSK	Outer_Full	20.81	24.21
B7-n78L	50	30	3500.01	CP	16QAM	Inner_Full	21.86	25.26
B7-n78L	50	30	3500.01	CP	16QAM	Edge_1RB_Left	20.31	23.71
B7-n78L	50	30	3500.01	CP	16QAM	Edge_1RB_Right	20.32	23.72
B7-n78L	50	30	3500.01	CP	16QAM	Outer_Full	20.83	24.23
B7-n78L	50	30	3500.01	CP	64QAM	Inner_Full	20.34	23.74
B7-n78L	50	30	3500.01	CP	64QAM	Edge_1RB_Left	19.95	23.35
B7-n78L	50	30	3500.01	CP	64QAM	Edge_1RB_Right	20.08	23.48
B7-n78L	50	30	3500.01	CP	64QAM	Outer_Full	20.35	23.75
B7-n78L	50	30	3500.01	CP	256QAM	Inner_Full	17.51	20.91
B7-n78L	50	30	3500.01	CP	256QAM	Edge_1RB_Left	16.34	19.74
B7-n78L	50	30	3500.01	CP	256QAM	Edge_1RB_Right	16.47	19.87
B7-n78L	50	30	3500.01	CP	256QAM	Outer_Full	17.41	20.81
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Inner_Full	23.64	27.04
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Edge_1RB_Left	23.41	26.81
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Edge_1RB_Right	23.41	26.81
B7-n78L	50	30	3525	DFT	pi/2 BPSK	Outer_Full	23.26	26.66

B7-n78L	50	30	3525	DFT	QPSK	Inner_Full	23.61	27.01
B7-n78L	50	30	3525	DFT	QPSK	Edge_1RB_Left	23.09	26.49
B7-n78L	50	30	3525	DFT	QPSK	Edge_1RB_Right	22.91	26.31
B7-n78L	50	30	3525	DFT	QPSK	Outer_Full	22.80	26.20
B7-n78L	50	30	3525	DFT	16QAM	Inner_Full	22.81	26.21
B7-n78L	50	30	3525	DFT	16QAM	Edge_1RB_Left	21.60	25.00
B7-n78L	50	30	3525	DFT	16QAM	Edge_1RB_Right	21.44	24.84
B7-n78L	50	30	3525	DFT	16QAM	Outer_Full	21.89	25.29
B7-n78L	50	30	3525	DFT	64QAM	Inner_Full	21.25	24.65
B7-n78L	50	30	3525	DFT	64QAM	Edge_1RB_Left	20.92	24.32
B7-n78L	50	30	3525	DFT	64QAM	Edge_1RB_Right	21.08	24.48
B7-n78L	50	30	3525	DFT	64QAM	Outer_Full	21.31	24.71
B7-n78L	50	30	3525	DFT	256QAM	Inner_Full	19.25	22.65
B7-n78L	50	30	3525	DFT	256QAM	Edge_1RB_Left	18.63	22.03
B7-n78L	50	30	3525	DFT	256QAM	Edge_1RB_Right	18.57	21.97
B7-n78L	50	30	3525	DFT	256QAM	Outer_Full	19.33	22.73
B7-n78L	50	30	3525	CP	QPSK	Inner_Full	22.22	25.62
B7-n78L	50	30	3525	CP	QPSK	Edge_1RB_Left	20.96	24.36
B7-n78L	50	30	3525	CP	QPSK	Edge_1RB_Right	21.05	24.45
B7-n78L	50	30	3525	CP	QPSK	Outer_Full	20.77	24.17
B7-n78L	50	30	3525	CP	16QAM	Inner_Full	21.69	25.09
B7-n78L	50	30	3525	CP	16QAM	Edge_1RB_Left	20.49	23.89
B7-n78L	50	30	3525	CP	16QAM	Edge_1RB_Right	20.77	24.17
B7-n78L	50	30	3525	CP	16QAM	Outer_Full	20.94	24.34
B7-n78L	50	30	3525	CP	64QAM	Inner_Full	20.22	23.62
B7-n78L	50	30	3525	CP	64QAM	Edge_1RB_Left	20.10	23.50
B7-n78L	50	30	3525	CP	64QAM	Edge_1RB_Right	20.20	23.60
B7-n78L	50	30	3525	CP	64QAM	Outer_Full	20.30	23.70
B7-n78L	50	30	3525	CP	256QAM	Inner_Full	17.34	20.74
B7-n78L	50	30	3525	CP	256QAM	Edge_1RB_Left	16.55	19.95
B7-n78L	50	30	3525	CP	256QAM	Edge_1RB_Right	16.74	20.14
B7-n78L	50	30	3525	CP	256QAM	Outer_Full	17.33	20.73
B7-n78L	60	30	3480	DFT	$\pi/2$ BPSK	Inner_Full	23.36	26.76
B7-n78L	60	30	3480	DFT	$\pi/2$ BPSK	Edge_1RB_Left	23.24	26.64
B7-n78L	60	30	3480	DFT	$\pi/2$ BPSK	Edge_1RB_Right	23.14	26.54
B7-n78L	60	30	3480	DFT	$\pi/2$ BPSK	Outer_Full	22.99	26.39
B7-n78L	60	30	3480	DFT	QPSK	Inner_Full	23.32	26.72

B7-n78L	60	30	3480	DFT	QPSK	Edge_1RB_Left	22.62	26.02
B7-n78L	60	30	3480	DFT	QPSK	Edge_1RB_Right	22.65	26.05
B7-n78L	60	30	3480	DFT	QPSK	Outer_Full	22.58	25.98
B7-n78L	60	30	3480	DFT	16QAM	Inner_Full	22.44	25.84
B7-n78L	60	30	3480	DFT	16QAM	Edge_1RB_Left	21.15	24.55
B7-n78L	60	30	3480	DFT	16QAM	Edge_1RB_Right	21.21	24.61
B7-n78L	60	30	3480	DFT	16QAM	Outer_Full	21.53	24.93
B7-n78L	60	30	3480	DFT	64QAM	Inner_Full	20.96	24.36
B7-n78L	60	30	3480	DFT	64QAM	Edge_1RB_Left	20.66	24.06
B7-n78L	60	30	3480	DFT	64QAM	Edge_1RB_Right	20.69	24.09
B7-n78L	60	30	3480	DFT	64QAM	Outer_Full	21.04	24.44
B7-n78L	60	30	3480	DFT	256QAM	Inner_Full	19.01	22.41
B7-n78L	60	30	3480	DFT	256QAM	Edge_1RB_Left	18.32	21.72
B7-n78L	60	30	3480	DFT	256QAM	Edge_1RB_Right	18.33	21.73
B7-n78L	60	30	3480	DFT	256QAM	Outer_Full	19.13	22.53
B7-n78L	60	30	3480	CP	QPSK	Inner_Full	21.94	25.34
B7-n78L	60	30	3480	CP	QPSK	Edge_1RB_Left	20.57	23.97
B7-n78L	60	30	3480	CP	QPSK	Edge_1RB_Right	20.82	24.22
B7-n78L	60	30	3480	CP	QPSK	Outer_Full	20.57	23.97
B7-n78L	60	30	3480	CP	16QAM	Inner_Full	21.46	24.86
B7-n78L	60	30	3480	CP	16QAM	Edge_1RB_Left	21.07	24.47
B7-n78L	60	30	3480	CP	16QAM	Edge_1RB_Right	21.09	24.49
B7-n78L	60	30	3480	CP	16QAM	Outer_Full	20.56	23.96
B7-n78L	60	30	3480	CP	64QAM	Inner_Full	20.06	23.46
B7-n78L	60	30	3480	CP	64QAM	Edge_1RB_Left	20.01	23.41
B7-n78L	60	30	3480	CP	64QAM	Edge_1RB_Right	20.11	23.51
B7-n78L	60	30	3480	CP	64QAM	Outer_Full	20.09	23.49
B7-n78L	60	30	3480	CP	256QAM	Inner_Full	16.96	20.36
B7-n78L	60	30	3480	CP	256QAM	Edge_1RB_Left	16.39	19.79
B7-n78L	60	30	3480	CP	256QAM	Edge_1RB_Right	16.55	19.95
B7-n78L	60	30	3480	CP	256QAM	Outer_Full	17.10	20.50
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.43	26.83
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	22.91	26.31
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	22.85	26.25
B7-n78L	60	30	3500.01	DFT	pi/2 BPSK	Outer_Full	22.97	26.37
B7-n78L	60	30	3500.01	DFT	QPSK	Inner_Full	23.39	26.79
B7-n78L	60	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.39	25.79

B7-n78L	60	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.39	25.79
B7-n78L	60	30	3500.01	DFT	QPSK	Outer_Full	22.52	25.92
B7-n78L	60	30	3500.01	DFT	16QAM	Inner_Full	22.52	25.92
B7-n78L	60	30	3500.01	DFT	16QAM	Edge_1RB_Left	20.92	24.32
B7-n78L	60	30	3500.01	DFT	16QAM	Edge_1RB_Right	20.94	24.34
B7-n78L	60	30	3500.01	DFT	16QAM	Outer_Full	21.46	24.86
B7-n78L	60	30	3500.01	DFT	64QAM	Inner_Full	21.05	24.45
B7-n78L	60	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.33	23.73
B7-n78L	60	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.51	23.91
B7-n78L	60	30	3500.01	DFT	64QAM	Outer_Full	21.01	24.41
B7-n78L	60	30	3500.01	DFT	256QAM	Inner_Full	18.97	22.37
B7-n78L	60	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.13	21.53
B7-n78L	60	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.22	21.62
B7-n78L	60	30	3500.01	DFT	256QAM	Outer_Full	19.08	22.48
B7-n78L	60	30	3500.01	CP	QPSK	Inner_Full	22.01	25.41
B7-n78L	60	30	3500.01	CP	QPSK	Edge_1RB_Left	20.30	23.70
B7-n78L	60	30	3500.01	CP	QPSK	Edge_1RB_Right	20.53	23.93
B7-n78L	60	30	3500.01	CP	QPSK	Outer_Full	20.49	23.89
B7-n78L	60	30	3500.01	CP	16QAM	Inner_Full	21.51	24.91
B7-n78L	60	30	3500.01	CP	16QAM	Edge_1RB_Left	20.79	24.19
B7-n78L	60	30	3500.01	CP	16QAM	Edge_1RB_Right	20.73	24.13
B7-n78L	60	30	3500.01	CP	16QAM	Outer_Full	20.54	23.94
B7-n78L	60	30	3500.01	CP	64QAM	Inner_Full	20.14	23.54
B7-n78L	60	30	3500.01	CP	64QAM	Edge_1RB_Left	19.71	23.11
B7-n78L	60	30	3500.01	CP	64QAM	Edge_1RB_Right	19.82	23.22
B7-n78L	60	30	3500.01	CP	64QAM	Outer_Full	19.97	23.37
B7-n78L	60	30	3500.01	CP	256QAM	Inner_Full	17.09	20.49
B7-n78L	60	30	3500.01	CP	256QAM	Edge_1RB_Left	16.18	19.58
B7-n78L	60	30	3500.01	CP	256QAM	Edge_1RB_Right	16.32	19.72
B7-n78L	60	30	3500.01	CP	256QAM	Outer_Full	17.13	20.53
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Inner_Full	23.36	26.76
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Edge_1RB_Left	22.99	26.39
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Edge_1RB_Right	23.11	26.51
B7-n78L	60	30	3519.99	DFT	pi/2 BPSK	Outer_Full	23.05	26.45
B7-n78L	60	30	3519.99	DFT	QPSK	Inner_Full	23.36	26.76
B7-n78L	60	30	3519.99	DFT	QPSK	Edge_1RB_Left	22.62	26.02
B7-n78L	60	30	3519.99	DFT	QPSK	Edge_1RB_Right	22.69	26.09

B7-n78L	60	30	3519.99	DFT	QPSK	Outer_Full	22.53	25.93
B7-n78L	60	30	3519.99	DFT	16QAM	Inner_Full	22.44	25.84
B7-n78L	60	30	3519.99	DFT	16QAM	Edge_1RB_Left	21.07	24.47
B7-n78L	60	30	3519.99	DFT	16QAM	Edge_1RB_Right	21.24	24.64
B7-n78L	60	30	3519.99	DFT	16QAM	Outer_Full	21.51	24.91
B7-n78L	60	30	3519.99	DFT	64QAM	Inner_Full	21.01	24.41
B7-n78L	60	30	3519.99	DFT	64QAM	Edge_1RB_Left	20.32	23.72
B7-n78L	60	30	3519.99	DFT	64QAM	Edge_1RB_Right	20.65	24.05
B7-n78L	60	30	3519.99	DFT	64QAM	Outer_Full	21.08	24.48
B7-n78L	60	30	3519.99	DFT	256QAM	Inner_Full	19.02	22.42
B7-n78L	60	30	3519.99	DFT	256QAM	Edge_1RB_Left	18.25	21.65
B7-n78L	60	30	3519.99	DFT	256QAM	Edge_1RB_Right	18.39	21.79
B7-n78L	60	30	3519.99	DFT	256QAM	Outer_Full	19.17	22.57
B7-n78L	60	30	3519.99	CP	QPSK	Inner_Full	21.94	25.34
B7-n78L	60	30	3519.99	CP	QPSK	Edge_1RB_Left	20.46	23.86
B7-n78L	60	30	3519.99	CP	QPSK	Edge_1RB_Right	20.74	24.14
B7-n78L	60	30	3519.99	CP	QPSK	Outer_Full	20.55	23.95
B7-n78L	60	30	3519.99	CP	16QAM	Inner_Full	21.44	24.84
B7-n78L	60	30	3519.99	CP	16QAM	Edge_1RB_Left	20.92	24.32
B7-n78L	60	30	3519.99	CP	16QAM	Edge_1RB_Right	21.05	24.45
B7-n78L	60	30	3519.99	CP	16QAM	Outer_Full	20.54	23.94
B7-n78L	60	30	3519.99	CP	64QAM	Inner_Full	20.10	23.50
B7-n78L	60	30	3519.99	CP	64QAM	Edge_1RB_Left	20.11	23.51
B7-n78L	60	30	3519.99	CP	64QAM	Edge_1RB_Right	20.10	23.50
B7-n78L	60	30	3519.99	CP	64QAM	Outer_Full	20.05	23.45
B7-n78L	60	30	3519.99	CP	256QAM	Inner_Full	17.05	20.45
B7-n78L	60	30	3519.99	CP	256QAM	Edge_1RB_Left	16.26	19.66
B7-n78L	60	30	3519.99	CP	256QAM	Edge_1RB_Right	16.55	19.95
B7-n78L	60	30	3519.99	CP	256QAM	Outer_Full	17.17	20.57
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Inner_Full	23.35	26.75
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Edge_1RB_Left	23.11	26.51
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Edge_1RB_Right	22.87	26.27
B7-n78L	80	30	3490.02	DFT	pi/2 BPSK	Outer_Full	22.97	26.37
B7-n78L	80	30	3490.02	DFT	QPSK	Inner_Full	23.35	26.75
B7-n78L	80	30	3490.02	DFT	QPSK	Edge_1RB_Left	22.74	26.14
B7-n78L	80	30	3490.02	DFT	QPSK	Edge_1RB_Right	22.41	25.81
B7-n78L	80	30	3490.02	DFT	QPSK	Outer_Full	22.48	25.88

B7-n78L	80	30	3490.02	DFT	16QAM	Inner_Full	22.45	25.85
B7-n78L	80	30	3490.02	DFT	16QAM	Edge_1RB_Left	21.88	25.28
B7-n78L	80	30	3490.02	DFT	16QAM	Edge_1RB_Right	21.64	25.04
B7-n78L	80	30	3490.02	DFT	16QAM	Outer_Full	21.47	24.87
B7-n78L	80	30	3490.02	DFT	64QAM	Inner_Full	20.97	24.37
B7-n78L	80	30	3490.02	DFT	64QAM	Edge_1RB_Left	20.65	24.05
B7-n78L	80	30	3490.02	DFT	64QAM	Edge_1RB_Right	20.64	24.04
B7-n78L	80	30	3490.02	DFT	64QAM	Outer_Full	20.97	24.37
B7-n78L	80	30	3490.02	DFT	256QAM	Inner_Full	18.93	22.33
B7-n78L	80	30	3490.02	DFT	256QAM	Edge_1RB_Left	18.40	21.80
B7-n78L	80	30	3490.02	DFT	256QAM	Edge_1RB_Right	18.15	21.55
B7-n78L	80	30	3490.02	DFT	256QAM	Outer_Full	19.04	22.44
B7-n78L	80	30	3490.02	CP	QPSK	Inner_Full	21.92	25.32
B7-n78L	80	30	3490.02	CP	QPSK	Edge_1RB_Left	20.61	24.01
B7-n78L	80	30	3490.02	CP	QPSK	Edge_1RB_Right	20.35	23.75
B7-n78L	80	30	3490.02	CP	QPSK	Outer_Full	20.51	23.91
B7-n78L	80	30	3490.02	CP	16QAM	Inner_Full	21.44	24.84
B7-n78L	80	30	3490.02	CP	16QAM	Edge_1RB_Left	20.79	24.19
B7-n78L	80	30	3490.02	CP	16QAM	Edge_1RB_Right	20.38	23.78
B7-n78L	80	30	3490.02	CP	16QAM	Outer_Full	20.43	23.83
B7-n78L	80	30	3490.02	CP	64QAM	Inner_Full	20.03	23.43
B7-n78L	80	30	3490.02	CP	64QAM	Edge_1RB_Left	19.84	23.24
B7-n78L	80	30	3490.02	CP	64QAM	Edge_1RB_Right	19.71	23.11
B7-n78L	80	30	3490.02	CP	64QAM	Outer_Full	20.07	23.47
B7-n78L	80	30	3490.02	CP	256QAM	Inner_Full	17.01	20.41
B7-n78L	80	30	3490.02	CP	256QAM	Edge_1RB_Left	16.49	19.89
B7-n78L	80	30	3490.02	CP	256QAM	Edge_1RB_Right	16.23	19.63
B7-n78L	80	30	3490.02	CP	256QAM	Outer_Full	17.09	20.49
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.41	26.81
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	22.98	26.38
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.09	26.49
B7-n78L	80	30	3500.01	DFT	pi/2 BPSK	Outer_Full	22.95	26.35
B7-n78L	80	30	3500.01	DFT	QPSK	Inner_Full	23.44	26.84
B7-n78L	80	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.66	26.06
B7-n78L	80	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.63	26.03
B7-n78L	80	30	3500.01	DFT	QPSK	Outer_Full	22.48	25.88
B7-n78L	80	30	3500.01	DFT	16QAM	Inner_Full	22.54	25.94

B7-n78L	80	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.65	25.05
B7-n78L	80	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.72	25.12
B7-n78L	80	30	3500.01	DFT	16QAM	Outer_Full	21.48	24.88
B7-n78L	80	30	3500.01	DFT	64QAM	Inner_Full	21.10	24.50
B7-n78L	80	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.30	23.70
B7-n78L	80	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.59	23.99
B7-n78L	80	30	3500.01	DFT	64QAM	Outer_Full	21.01	24.41
B7-n78L	80	30	3500.01	DFT	256QAM	Inner_Full	19.07	22.47
B7-n78L	80	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.09	21.49
B7-n78L	80	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.31	21.71
B7-n78L	80	30	3500.01	DFT	256QAM	Outer_Full	19.00	22.40
B7-n78L	80	30	3500.01	CP	QPSK	Inner_Full	22.02	25.42
B7-n78L	80	30	3500.01	CP	QPSK	Edge_1RB_Left	20.62	24.02
B7-n78L	80	30	3500.01	CP	QPSK	Edge_1RB_Right	20.64	24.04
B7-n78L	80	30	3500.01	CP	QPSK	Outer_Full	20.54	23.94
B7-n78L	80	30	3500.01	CP	16QAM	Inner_Full	21.54	24.94
B7-n78L	80	30	3500.01	CP	16QAM	Edge_1RB_Left	20.49	23.89
B7-n78L	80	30	3500.01	CP	16QAM	Edge_1RB_Right	20.49	23.89
B7-n78L	80	30	3500.01	CP	16QAM	Outer_Full	20.48	23.88
B7-n78L	80	30	3500.01	CP	64QAM	Inner_Full	20.11	23.51
B7-n78L	80	30	3500.01	CP	64QAM	Edge_1RB_Left	19.98	23.38
B7-n78L	80	30	3500.01	CP	64QAM	Edge_1RB_Right	19.91	23.31
B7-n78L	80	30	3500.01	CP	64QAM	Outer_Full	20.05	23.45
B7-n78L	80	30	3500.01	CP	256QAM	Inner_Full	17.11	20.51
B7-n78L	80	30	3500.01	CP	256QAM	Edge_1RB_Left	16.14	19.54
B7-n78L	80	30	3500.01	CP	256QAM	Edge_1RB_Right	16.42	19.82
B7-n78L	80	30	3500.01	CP	256QAM	Outer_Full	17.10	20.50
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Inner_Full	23.50	26.90
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Edge_1RB_Left	22.90	26.30
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Edge_1RB_Right	23.21	26.61
B7-n78L	80	30	3510	DFT	pi/2 BPSK	Outer_Full	23.03	26.43
B7-n78L	80	30	3510	DFT	QPSK	Inner_Full	23.41	26.81
B7-n78L	80	30	3510	DFT	QPSK	Edge_1RB_Left	22.37	25.77
B7-n78L	80	30	3510	DFT	QPSK	Edge_1RB_Right	22.73	26.13
B7-n78L	80	30	3510	DFT	QPSK	Outer_Full	22.56	25.96
B7-n78L	80	30	3510	DFT	16QAM	Inner_Full	22.50	25.90
B7-n78L	80	30	3510	DFT	16QAM	Edge_1RB_Left	21.57	24.97

B7-n78L	80	30	3510	DFT	16QAM	Edge_1RB_Right	21.82	25.22
B7-n78L	80	30	3510	DFT	16QAM	Outer_Full	21.49	24.89
B7-n78L	80	30	3510	DFT	64QAM	Inner_Full	21.10	24.50
B7-n78L	80	30	3510	DFT	64QAM	Edge_1RB_Left	20.15	23.55
B7-n78L	80	30	3510	DFT	64QAM	Edge_1RB_Right	20.58	23.98
B7-n78L	80	30	3510	DFT	64QAM	Outer_Full	21.04	24.44
B7-n78L	80	30	3510	DFT	256QAM	Inner_Full	19.05	22.45
B7-n78L	80	30	3510	DFT	256QAM	Edge_1RB_Left	18.02	21.42
B7-n78L	80	30	3510	DFT	256QAM	Edge_1RB_Right	18.39	21.79
B7-n78L	80	30	3510	DFT	256QAM	Outer_Full	19.03	22.43
B7-n78L	80	30	3510	CP	QPSK	Inner_Full	22.09	25.49
B7-n78L	80	30	3510	CP	QPSK	Edge_1RB_Left	20.44	23.84
B7-n78L	80	30	3510	CP	QPSK	Edge_1RB_Right	20.75	24.15
B7-n78L	80	30	3510	CP	QPSK	Outer_Full	20.57	23.97
B7-n78L	80	30	3510	CP	16QAM	Inner_Full	21.52	24.92
B7-n78L	80	30	3510	CP	16QAM	Edge_1RB_Left	20.46	23.86
B7-n78L	80	30	3510	CP	16QAM	Edge_1RB_Right	20.54	23.94
B7-n78L	80	30	3510	CP	16QAM	Outer_Full	20.49	23.89
B7-n78L	80	30	3510	CP	64QAM	Inner_Full	20.01	23.41
B7-n78L	80	30	3510	CP	64QAM	Edge_1RB_Left	19.58	22.98
B7-n78L	80	30	3510	CP	64QAM	Edge_1RB_Right	19.94	23.34
B7-n78L	80	30	3510	CP	64QAM	Outer_Full	20.06	23.46
B7-n78L	80	30	3510	CP	256QAM	Inner_Full	17.12	20.52
B7-n78L	80	30	3510	CP	256QAM	Edge_1RB_Left	16.11	19.51
B7-n78L	80	30	3510	CP	256QAM	Edge_1RB_Right	16.48	19.88
B7-n78L	80	30	3510	CP	256QAM	Outer_Full	17.09	20.49
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Inner_Full	23.46	26.86
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Edge_1RB_Left	23.23	26.63
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Edge_1RB_Right	23.07	26.47
B7-n78L	90	30	3495	DFT	pi/2 BPSK	Outer_Full	23.03	26.43
B7-n78L	90	30	3495	DFT	QPSK	Inner_Full	23.55	26.95
B7-n78L	90	30	3495	DFT	QPSK	Edge_1RB_Left	22.93	26.33
B7-n78L	90	30	3495	DFT	QPSK	Edge_1RB_Right	22.58	25.98
B7-n78L	90	30	3495	DFT	QPSK	Outer_Full	22.55	25.95
B7-n78L	90	30	3495	DFT	16QAM	Inner_Full	22.50	25.90
B7-n78L	90	30	3495	DFT	16QAM	Edge_1RB_Left	21.93	25.33
B7-n78L	90	30	3495	DFT	16QAM	Edge_1RB_Right	21.69	25.09

B7-n78L	90	30	3495	DFT	16QAM	Outer_Full	21.54	24.94
B7-n78L	90	30	3495	DFT	64QAM	Inner_Full	21.14	24.54
B7-n78L	90	30	3495	DFT	64QAM	Edge_1RB_Left	20.60	24.00
B7-n78L	90	30	3495	DFT	64QAM	Edge_1RB_Right	20.62	24.02
B7-n78L	90	30	3495	DFT	64QAM	Outer_Full	21.13	24.53
B7-n78L	90	30	3495	DFT	256QAM	Inner_Full	19.14	22.54
B7-n78L	90	30	3495	DFT	256QAM	Edge_1RB_Left	18.40	21.80
B7-n78L	90	30	3495	DFT	256QAM	Edge_1RB_Right	18.31	21.71
B7-n78L	90	30	3495	DFT	256QAM	Outer_Full	19.12	22.52
B7-n78L	90	30	3495	CP	QPSK	Inner_Full	22.10	25.50
B7-n78L	90	30	3495	CP	QPSK	Edge_1RB_Left	20.78	24.18
B7-n78L	90	30	3495	CP	QPSK	Edge_1RB_Right	20.63	24.03
B7-n78L	90	30	3495	CP	QPSK	Outer_Full	20.54	23.94
B7-n78L	90	30	3495	CP	16QAM	Inner_Full	21.56	24.96
B7-n78L	90	30	3495	CP	16QAM	Edge_1RB_Left	20.66	24.06
B7-n78L	90	30	3495	CP	16QAM	Edge_1RB_Right	20.44	23.84
B7-n78L	90	30	3495	CP	16QAM	Outer_Full	20.57	23.97
B7-n78L	90	30	3495	CP	64QAM	Inner_Full	20.12	23.52
B7-n78L	90	30	3495	CP	64QAM	Edge_1RB_Left	19.90	23.30
B7-n78L	90	30	3495	CP	64QAM	Edge_1RB_Right	19.98	23.38
B7-n78L	90	30	3495	CP	64QAM	Outer_Full	20.14	23.54
B7-n78L	90	30	3495	CP	256QAM	Inner_Full	17.18	20.58
B7-n78L	90	30	3495	CP	256QAM	Edge_1RB_Left	16.46	19.86
B7-n78L	90	30	3495	CP	256QAM	Edge_1RB_Right	16.36	19.76
B7-n78L	90	30	3495	CP	256QAM	Outer_Full	17.16	20.56
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Inner_Full	23.43	26.83
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Left	23.08	26.48
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Edge_1RB_Right	23.16	26.56
B7-n78L	90	30	3500.01	DFT	pi/2 BPSK	Outer_Full	23.01	26.41
B7-n78L	90	30	3500.01	DFT	QPSK	Inner_Full	23.43	26.83
B7-n78L	90	30	3500.01	DFT	QPSK	Edge_1RB_Left	22.76	26.16
B7-n78L	90	30	3500.01	DFT	QPSK	Edge_1RB_Right	22.72	26.12
B7-n78L	90	30	3500.01	DFT	QPSK	Outer_Full	22.60	26.00
B7-n78L	90	30	3500.01	DFT	16QAM	Inner_Full	22.50	25.90
B7-n78L	90	30	3500.01	DFT	16QAM	Edge_1RB_Left	21.84	25.24
B7-n78L	90	30	3500.01	DFT	16QAM	Edge_1RB_Right	21.85	25.25
B7-n78L	90	30	3500.01	DFT	16QAM	Outer_Full	21.59	24.99

B7-n78L	90	30	3500.01	DFT	64QAM	Inner_Full	21.15	24.55
B7-n78L	90	30	3500.01	DFT	64QAM	Edge_1RB_Left	20.45	23.85
B7-n78L	90	30	3500.01	DFT	64QAM	Edge_1RB_Right	20.70	24.10
B7-n78L	90	30	3500.01	DFT	64QAM	Outer_Full	21.13	24.53
B7-n78L	90	30	3500.01	DFT	256QAM	Inner_Full	19.16	22.56
B7-n78L	90	30	3500.01	DFT	256QAM	Edge_1RB_Left	18.35	21.75
B7-n78L	90	30	3500.01	DFT	256QAM	Edge_1RB_Right	18.35	21.75
B7-n78L	90	30	3500.01	DFT	256QAM	Outer_Full	19.23	22.63
B7-n78L	90	30	3500.01	CP	QPSK	Inner_Full	22.07	25.47
B7-n78L	90	30	3500.01	CP	QPSK	Edge_1RB_Left	20.56	23.96
B7-n78L	90	30	3500.01	CP	QPSK	Edge_1RB_Right	20.78	24.18
B7-n78L	90	30	3500.01	CP	QPSK	Outer_Full	20.53	23.93
B7-n78L	90	30	3500.01	CP	16QAM	Inner_Full	21.58	24.98
B7-n78L	90	30	3500.01	CP	16QAM	Edge_1RB_Left	20.59	23.99
B7-n78L	90	30	3500.01	CP	16QAM	Edge_1RB_Right	20.64	24.04
B7-n78L	90	30	3500.01	CP	16QAM	Outer_Full	20.60	24.00
B7-n78L	90	30	3500.01	CP	64QAM	Inner_Full	20.14	23.54
B7-n78L	90	30	3500.01	CP	64QAM	Edge_1RB_Left	19.86	23.26
B7-n78L	90	30	3500.01	CP	64QAM	Edge_1RB_Right	20.12	23.52
B7-n78L	90	30	3500.01	CP	64QAM	Outer_Full	20.08	23.48
B7-n78L	90	30	3500.01	CP	256QAM	Inner_Full	17.10	20.50
B7-n78L	90	30	3500.01	CP	256QAM	Edge_1RB_Left	16.35	19.75
B7-n78L	90	30	3500.01	CP	256QAM	Edge_1RB_Right	16.50	19.90
B7-n78L	90	30	3500.01	CP	256QAM	Outer_Full	17.14	20.54
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Inner_Full	23.57	26.97
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Edge_1RB_Left	23.05	26.45
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Edge_1RB_Right	23.17	26.57
B7-n78L	90	30	3504.99	DFT	pi/2 BPSK	Outer_Full	23.07	26.47
B7-n78L	90	30	3504.99	DFT	QPSK	Inner_Full	23.58	26.98
B7-n78L	90	30	3504.99	DFT	QPSK	Edge_1RB_Left	22.76	26.16
B7-n78L	90	30	3504.99	DFT	QPSK	Edge_1RB_Right	22.74	26.14
B7-n78L	90	30	3504.99	DFT	QPSK	Outer_Full	22.55	25.95
B7-n78L	90	30	3504.99	DFT	16QAM	Inner_Full	22.65	26.05
B7-n78L	90	30	3504.99	DFT	16QAM	Edge_1RB_Left	21.64	25.04
B7-n78L	90	30	3504.99	DFT	16QAM	Edge_1RB_Right	21.84	25.24
B7-n78L	90	30	3504.99	DFT	16QAM	Outer_Full	21.63	25.03
B7-n78L	90	30	3504.99	DFT	64QAM	Inner_Full	21.19	24.59

B7-n78L	90	30	3504.99	DFT	64QAM	Edge_1RB_Left	20.36	23.76
B7-n78L	90	30	3504.99	DFT	64QAM	Edge_1RB_Right	20.78	24.18
B7-n78L	90	30	3504.99	DFT	64QAM	Outer_Full	21.15	24.55
B7-n78L	90	30	3504.99	DFT	256QAM	Inner_Full	19.21	22.61
B7-n78L	90	30	3504.99	DFT	256QAM	Edge_1RB_Left	18.26	21.66
B7-n78L	90	30	3504.99	DFT	256QAM	Edge_1RB_Right	18.46	21.86
B7-n78L	90	30	3504.99	DFT	256QAM	Outer_Full	19.18	22.58
B7-n78L	90	30	3504.99	CP	QPSK	Inner_Full	22.16	25.56
B7-n78L	90	30	3504.99	CP	QPSK	Edge_1RB_Left	20.55	23.95
B7-n78L	90	30	3504.99	CP	QPSK	Edge_1RB_Right	20.86	24.26
B7-n78L	90	30	3504.99	CP	QPSK	Outer_Full	20.62	24.02
B7-n78L	90	30	3504.99	CP	16QAM	Inner_Full	21.65	25.05
B7-n78L	90	30	3504.99	CP	16QAM	Edge_1RB_Left	20.47	23.87
B7-n78L	90	30	3504.99	CP	16QAM	Edge_1RB_Right	20.70	24.10
B7-n78L	90	30	3504.99	CP	16QAM	Outer_Full	20.65	24.05
B7-n78L	90	30	3504.99	CP	64QAM	Inner_Full	20.21	23.61
B7-n78L	90	30	3504.99	CP	64QAM	Edge_1RB_Left	19.67	23.07
B7-n78L	90	30	3504.99	CP	64QAM	Edge_1RB_Right	20.14	23.54
B7-n78L	90	30	3504.99	CP	64QAM	Outer_Full	20.14	23.54
B7-n78L	90	30	3504.99	CP	256QAM	Inner_Full	17.22	20.62
B7-n78L	90	30	3504.99	CP	256QAM	Edge_1RB_Left	16.32	19.72
B7-n78L	90	30	3504.99	CP	256QAM	Edge_1RB_Right	16.55	19.95
B7-n78L	90	30	3504.99	CP	256QAM	Outer_Full	17.20	20.60

Sample: 3504.99MHz

$$\text{EIRP} = P_{\text{Mea}} + G_{\text{T}}$$

$$20.6\text{dBm} = 17.2\text{dBm} + (3.4\text{dBi})$$

A.2 Emission Limit

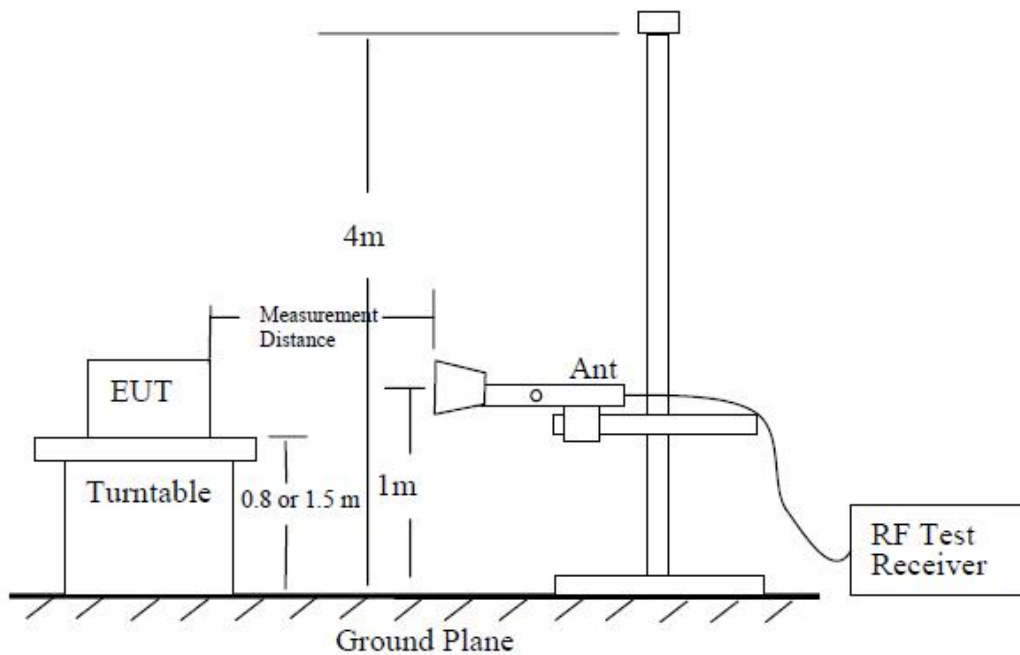
A.2.1 Measurement Method

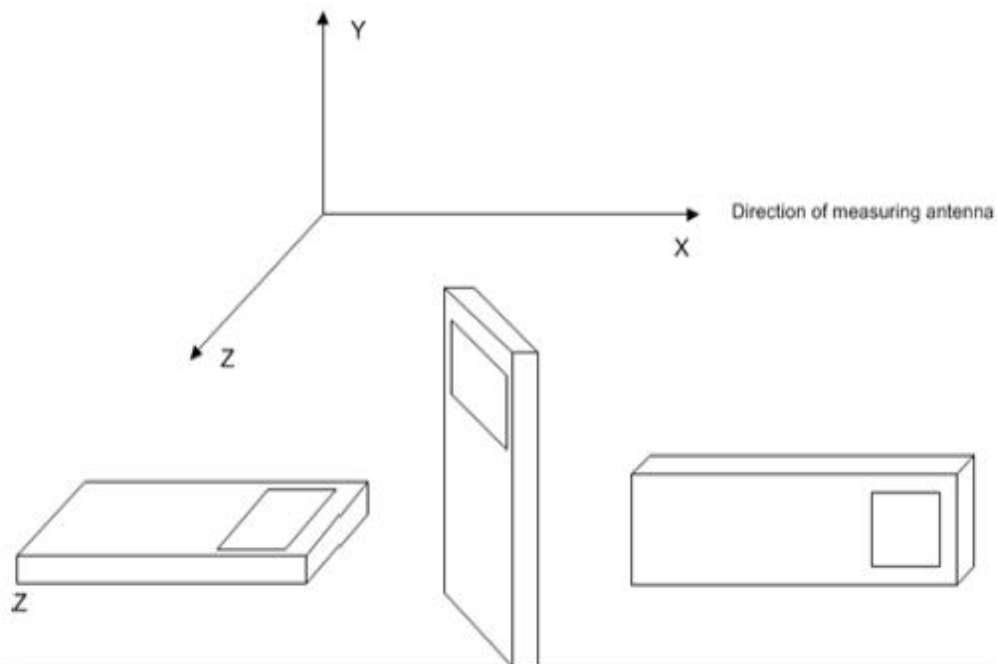
The measurements procedures in C63.26 are used. This measurement is carried out in fully anechoic chamber FAC-3.

The spectrum was scanned from 30 MHz to the 10th harmonic of the highest frequency generated within the equipment, which is the transmitted carrier. The resolution bandwidth is set 1MHz. The spectrum was scanned with the mobile station transmitting at carrier frequencies that pertain to low, mid and high channels of each LTE Band.

The procedure of radiated spurious emissions is as follows:

Using the test configuration as follow, measure the radiated emissions directly from the EUT and convert the measured field strength or received power to ERP or EIRP, as required, for comparison to the applicable limits.





The emission characteristics of the EUT can be identified from the pre-scan measurement information.

Exploratory radiated measurements (pre-scans) may be performed to determine the general EUT radiated emissions characteristics and, when necessary, the EUT-to-measurement antenna orientation that produces the maximum emission amplitude. Pre-scans shall only be used to determine the emission frequencies (i.e., not amplitude levels). The information garnered from a pre-scan can then be used to perform final compliance measurements using either the substitution or direct field strength method.

For radiated emissions measurements performed at frequencies less than or equal to 1 GHz, the EUT shall be placed on a RF-transparent table or support at a nominal height of 80 cm above the reference ground plane. Radiated measurements shall be made with the measurement antenna positioned in both horizontal and vertical polarization. The measurement antenna shall be varied from 1 m to 4 m in height above the reference ground in a search for the relative positioning that produces the maximum radiated signal level (i.e., field strength or received power). When orienting the measurement antenna in vertical polarization, the minimum height of the lowest element of the antenna shall clear the site reference ground plane by at least 25 cm.

The radiated emission measurements of all non-harmonic and harmonics of the transmit frequency through the 10th harmonic were measured with peak detector.

For radiated measurements performed at frequencies above 1 GHz, the EUT shall be placed on an RF transparent table or support at a nominal height of 1.5 m above the ground plane. When maximizing the emissions from the EUT for measurement, the EUT and its transmitting antenna(s) shall be rotated through 360°. For each mode of operation to be tested, the frequency spectrum (based on findings from exploratory measurements) shall be monitored. Final measurements shall be performed for the worst case combination(s) of variable technical parameters that result in the maximum measured emission amplitude, record the frequency and amplitude of the highest fundamental emission (if applicable), and the frequency and amplitude data for the six highest-amplitude spurious emissions.

A.2.2 Measurement Limit

Band 7_n78: 27.53(n) (2) specifies " For mobile operations in the 3450-3550 MHz band, the conducted power of any emission outside the licensee's authorized bandwidth shall not exceed -13 dBm/MHz. Compliance with this paragraph (n)(2) is based on the use of measurement instrumentation employing a resolution bandwidth of 1 megahertz or greater. However, in the 1 megahertz bands immediately outside and adjacent to the licensee's frequency block, a resolution bandwidth of at least one percent of the emission bandwidth of the fundamental emission of the transmitter may be employed, but limited to a maximum of 200 kHz. In the bands between 1 and 5 MHz removed from the licensee's frequency block, the minimum resolution bandwidth for the measurement shall be 500 kHz. The emission bandwidth is defined as the width of the signal between two points, one below the carrier center frequency and one above the carrier center frequency, outside of which all emissions are attenuated at least 26 dB below the transmitter power. "

A.2.3 Measurement Results

Radiated emissions measurements were made only at the upper, middle, and lower carrier frequencies of each LTE Band. It was decided that measurements at these three carrier frequencies would be sufficient to demonstrate compliance with emissions limits because it was seen that all the significant spurs occur well outside the band and no radiation was seen from a carrier in one block of each LTE Band into any of the other blocks. The equipment must still, however, meet emissions requirements with the carrier at all frequencies over which it is capable of operating and it is the manufacturer's responsibility to verify this. The range of evaluated frequency is from 30MHz to 26GHz.

LTE Band 7+NR n78L, 40 MHz, QPSK, 3470.01MHz

Frequency (MHz)	P _{Mea} (dBm)	Path Loss(dB)	Antenna Gain(dBi)	Peak EIRP (dBm)	Limit (dBm)	Margin (dB)	Polarization
7583.00	-54.84	-8.04	12.27	-50.61	-13.00	37.61	V
10167.00	-52.26	-9.35	12.97	-48.64	-13.00	35.64	V
12662.00	-48.18	-10.36	13.30	-45.24	-13.00	32.24	H
6620.00	-44.56	-7.88	11.14	-41.30	-13.00	28.30	H
9915.00	-53.58	-9.10	12.99	-49.69	-13.00	36.69	V
13244.00	-48.42	-10.53	13.84	-45.11	-13.00	32.11	H

LTE Band 7+NR n78L, 40 MHz, QPSK, 3500.01MHz

Frequency (MHz)	P _{Mea} (dBm)	Path Loss(dB)	Antenna Gain(dBi)	Peak EIRP (dBm)	Limit (dBm)	Margin (dB)	Polarization
7628.00	-53.58	-8.10	12.30	-49.38	-13.00	36.38	H
10153.00	-51.74	-9.38	12.96	-48.16	-13.00	35.16	H
12653.00	-48.32	-10.38	13.29	-45.41	-13.00	32.41	V
7101.00	-54.08	-8.16	11.72	-50.52	-13.00	37.52	V
10642.00	-51.16	-9.29	13.13	-47.32	-13.00	34.32	H
14207.00	-46.96	-10.88	14.46	-43.38	-13.00	30.38	H

LTE Band 7+NR n78L, 40 MHz, QPSK, 3529.98MHz

Frequency (MHz)	P _{Mea} (dBm)	Path Loss(dB)	Antenna Gain(dBi)	Peak EIRP (dBm)	Limit (dBm)	Margin (dB)	Polarization
7581.00	-51.99	-8.05	12.26	-47.78	-13.00	34.78	H
10121.00	-51.77	-9.43	12.95	-48.25	-13.00	35.25	V
12660.00	-48.86	-10.37	13.30	-45.93	-13.00	32.93	H
7581.00	-51.99	-8.05	12.26	-47.78	-13.00	34.78	H
11376.00	-50.63	-10.05	13.12	-47.56	-13.00	34.56	V
15153.00	-46.07	-11.38	13.91	-43.54	-13.00	30.54	H

Sample: 7581.00MHz

$$\text{Power (EIRP)} = P_{\text{Mea}} + P_{\text{pl}} + G_{\text{a}}$$

$$\text{Power (-47.78dBm)} = P_{\text{Mea}} (-51.99\text{dBm}) + P_{\text{pl}} (-8.05\text{dB}) + G_{\text{a}} (12.26 \text{ dBi})$$

Note: Expanded measurement uncertainty is U = 5.16 dB, k = 2.

A.3 Frequency Stability

A.3.1 Method of Measurement

Frequency stability is a measure of the frequency drift due to temperature and supply voltage variations, with reference to the frequency measured at +20 °C and rated supply voltage. Two reference points are established at the applicable unwanted emissions limit using a RBW equal to the RBW required by the unwanted emissions specification of the applicable regulatory standard. These reference points measured using the lowest and highest channel of operation shall be identified as F_L and F_H respectively.

In order to measure the carrier frequency under the condition of AFC lock, it is necessary to make measurements with the EUT in a “call mode”. This is accomplished with the use of 8000A.

1. Measure the carrier frequency at room temperature.
2. Subject the EUT to overnight soak at -30°C.
3. With the EUT, powered via nominal voltage, connected to the 8000A, and in a simulated call on middle channel for each NR band, measure the carrier frequency. These measurements should be made within 2 minutes of Powering up the EUT, to prevent significant self-warming.
4. Repeat the above measurements at 10°C increments from -30°C to +50°C. Allow at least 1.5 hours at each temperature, unpowered, before making measurements.
5. Re-measure carrier frequency at room temperature with nominal voltage. Vary supply voltage from minimum voltage to maximum voltage, in 0.1Volt increments re-measuring carrier frequency at each voltage. Pause at nominal voltage for 1.5 hours unpowered, to allow any self-heating to stabilize, before continuing.
6. Subject the EUT to overnight soak at +50°C.
7. With the EUT, powered via nominal voltage, connected to the 8000A and in a simulated call on the center channel, measure the carrier frequency. These measurements should be made within 2 minutes of Powering up the EUT, to prevent significant self-warming.
8. Repeat the above measurements at 10 °C increments from -30°C to +50°C. Allow at least 1.5 hours at each temperature, unpowered, before making measurements.
9. At all temperature levels hold the temperature to +/- 0.5°C during the measurement procedure.

The frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. This transceiver is specified to operate with an input voltage of the lower, higher and nominal voltage. Operation above or below these voltage limits is prohibited by transceiver software in order to prevent improper operation as well as to protect components from overstress.

A.3.2 Measurement results

LTE Band 7+NR n78L

Frequency Error vs Voltage

Temperature(°C)	Voltage(V)	F _L (MHz)	F _H (MHz)	Offset(Hz)	Frequency error(ppm)
20	12.0	3450.600	3547.440		
50				-24.50	0.0070
40				-28.40	0.0081
30				-33.30	0.0095
10				-14.70	0.0042
0				-12.60	0.0036
-10				-20.80	0.0059
-20				-40.40	0.0115
-30				-21.40	0.0061

Frequency Error vs Voltage

Voltage(V)	Temperature(°C)	F _L (MHz)	F _H (MHz)	Offset(Hz)	Frequency error(ppm)
10.2	20	3450.600	3547.440	-20.40	0.0058
13.8				-10.60	0.0030

A.4 Occupied Bandwidth

Occupied bandwidth measurements are only provided for selected frequencies in order to reduce the amount of submitted data. Data were taken at the mid frequencies frequency. The table below lists the measured 99% BW. Spectrum analyzer plots are included on the following pages.

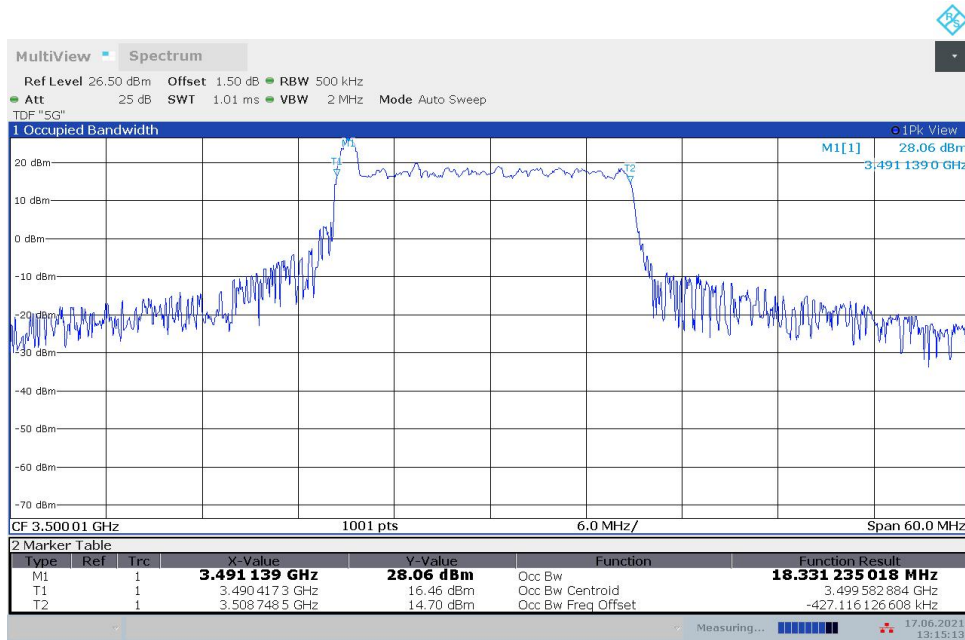
The measurement method is from ANSI C63.26:

- a) The spectrum analyzer center frequency is set to the nominal EUT channel center frequency. The frequency span for the spectrum analyzer shall be set wide enough to capture all modulation products including the emission skirts.
- b) The nominal IF filter 3 dB bandwidth (RBW) shall be in the range of 1% to 5% of the anticipated OBW, and the VBW shall be set $\geq 3 \times$ RBW.
- c) Set the reference level of the instrument as required to prevent the signal amplitude from exceeding the maximum spectrum analyzer input mixer level for linear operation.
- d) Set the detection mode to peak, and the trace mode to max-hold.

LTE Band 7+NR n78L
n78L,20MHz(99%)

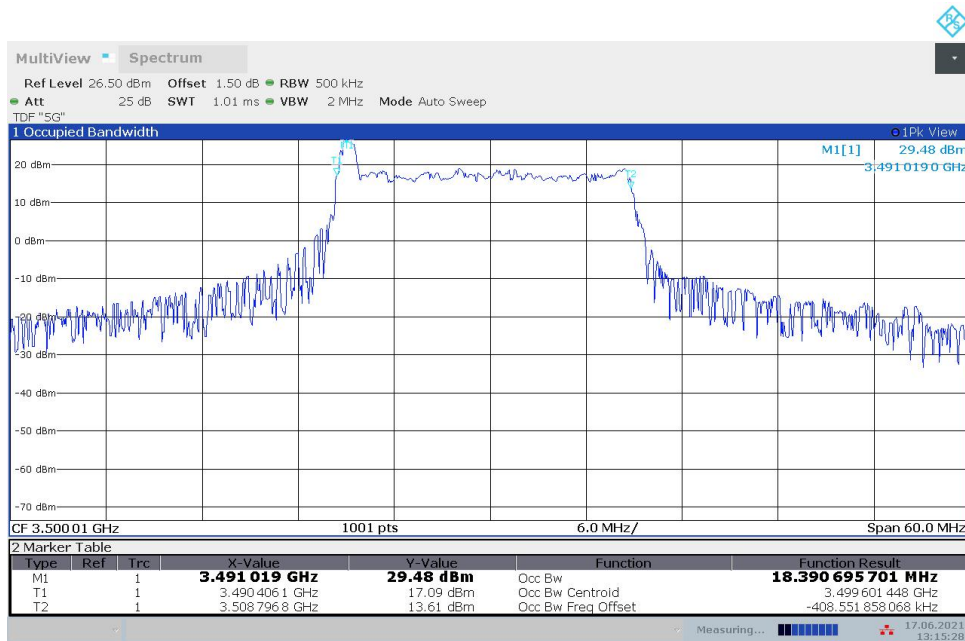
Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	18.331	18.391

n78L,20MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



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n78L,20MHz Bandwidth,DFT-s-QPSK (99% BW)

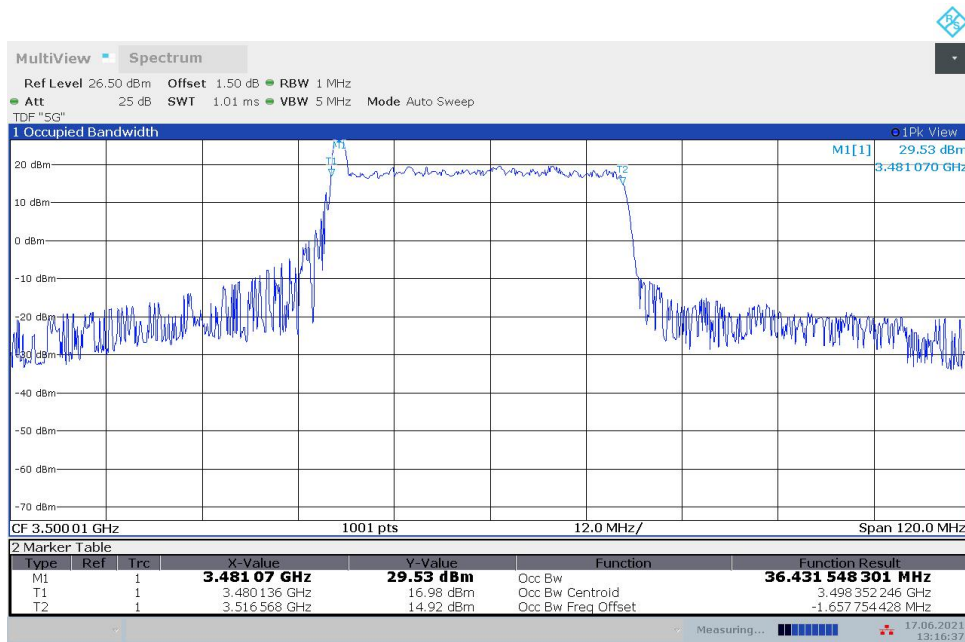


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LTE Band 7+NR n78L
n78L,40MHz(99%)

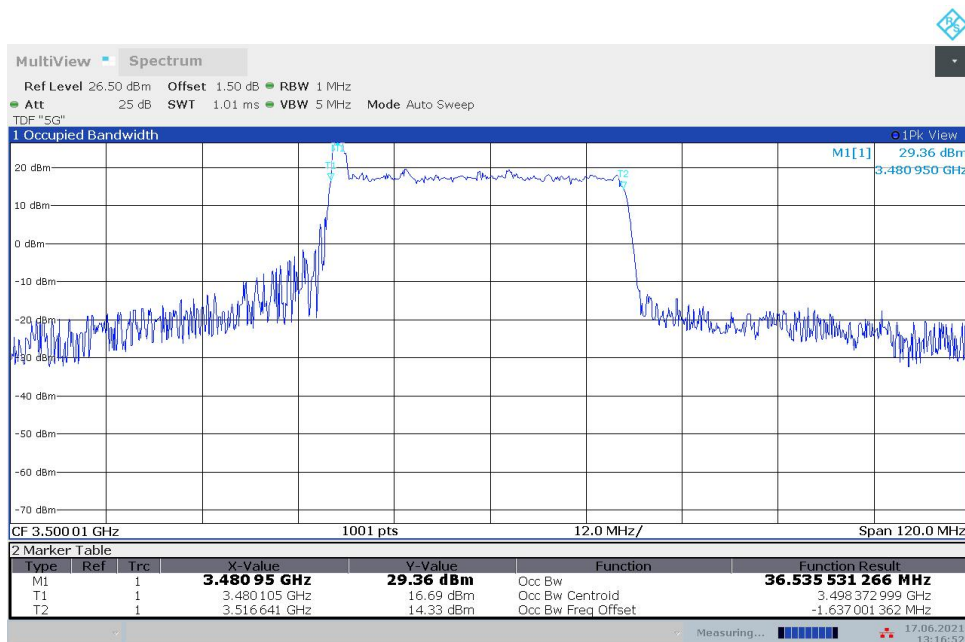
Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	36.432	36.536

n78L,40MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



Date:17 JUN 2021 13:16:37

n78L,40MHz Bandwidth,DFT-s-QPSK (99% BW)

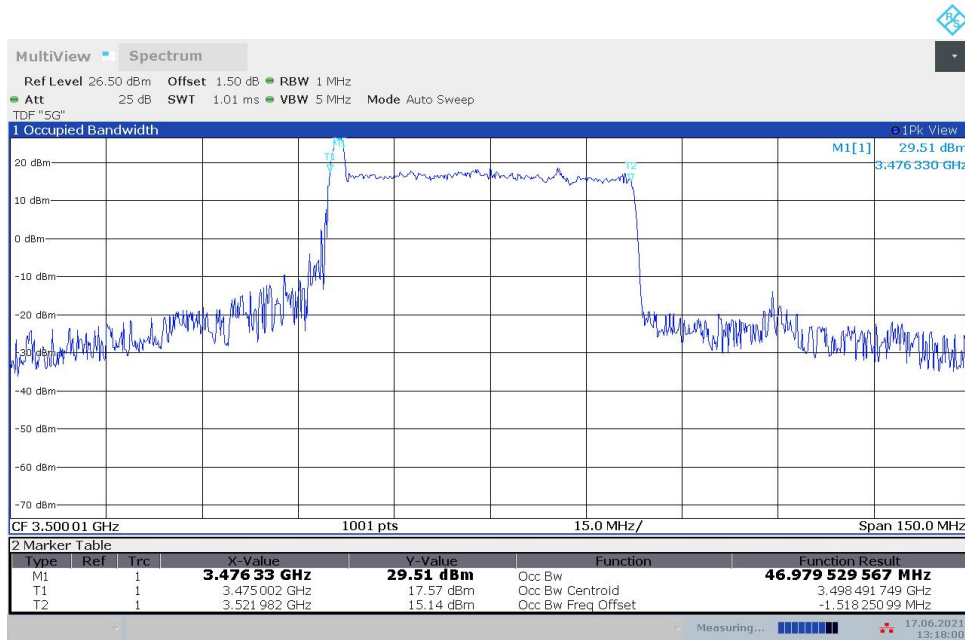


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LTE Band 7+NR n78L
n78L,50MHz(99%)

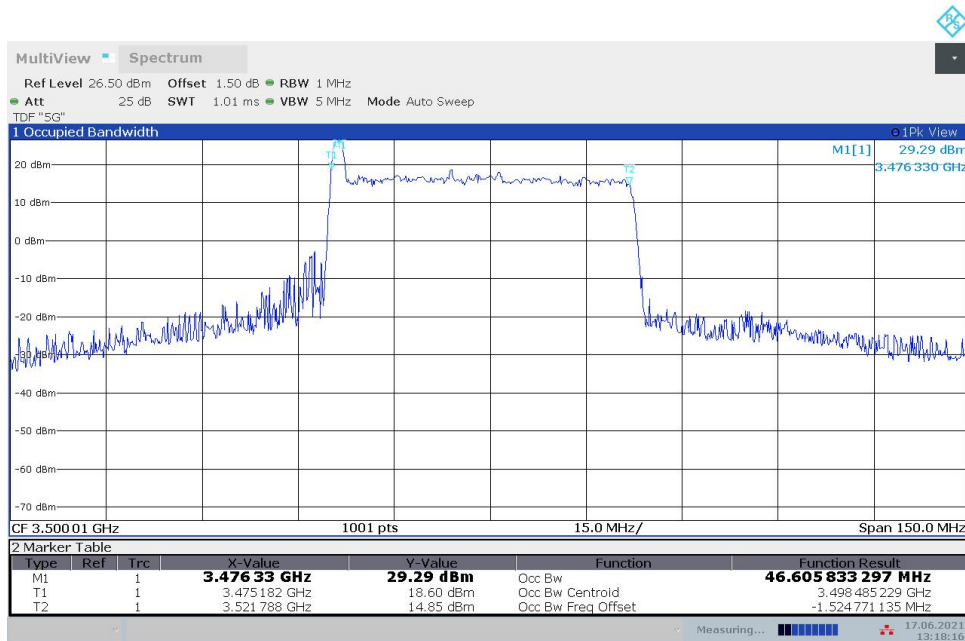
Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	46.980	46.606

n78L,50MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



Date:17 JUN 2021 13:18:01

n78L,50MHz Bandwidth,DFT-s-QPSK (99% BW)

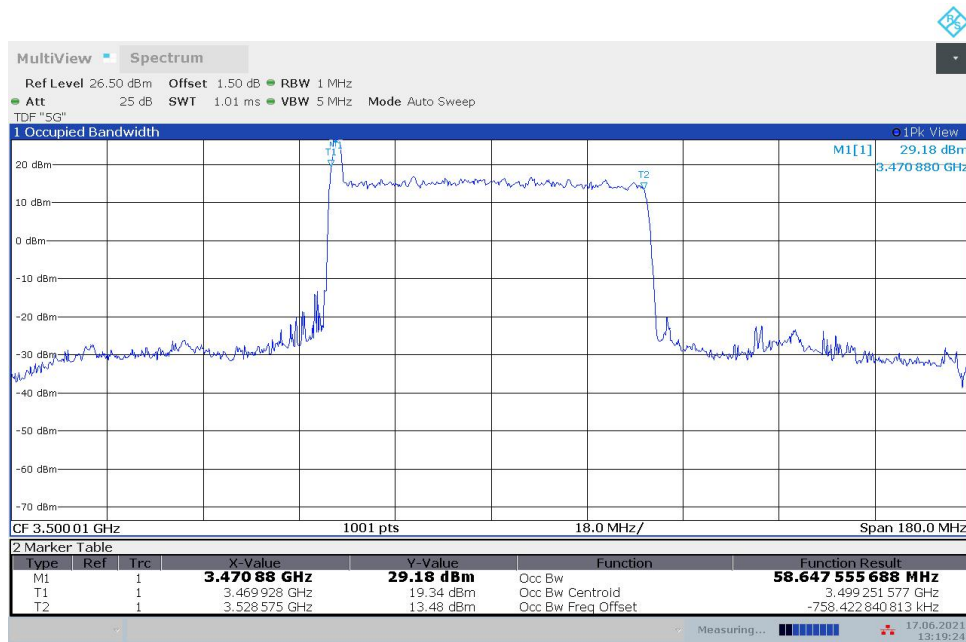


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LTE Band 7+NR n78L
n78L,60MHz(99%)

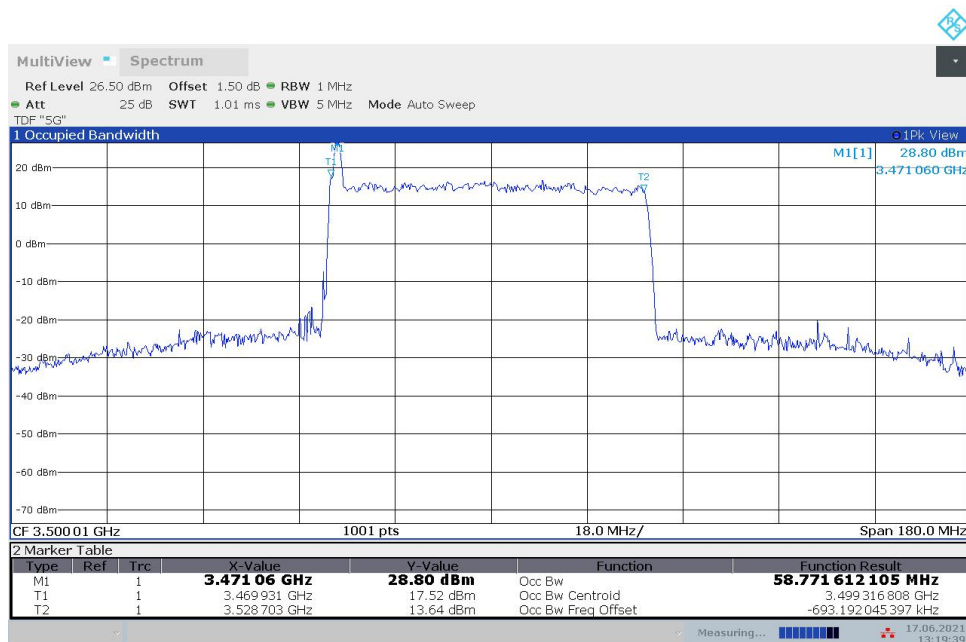
Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	58.648	58.772

n78L,60MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



Date:17 JUN 2021 13:19:24

n78L,60MHz Bandwidth,DFT-s-QPSK (99% BW)

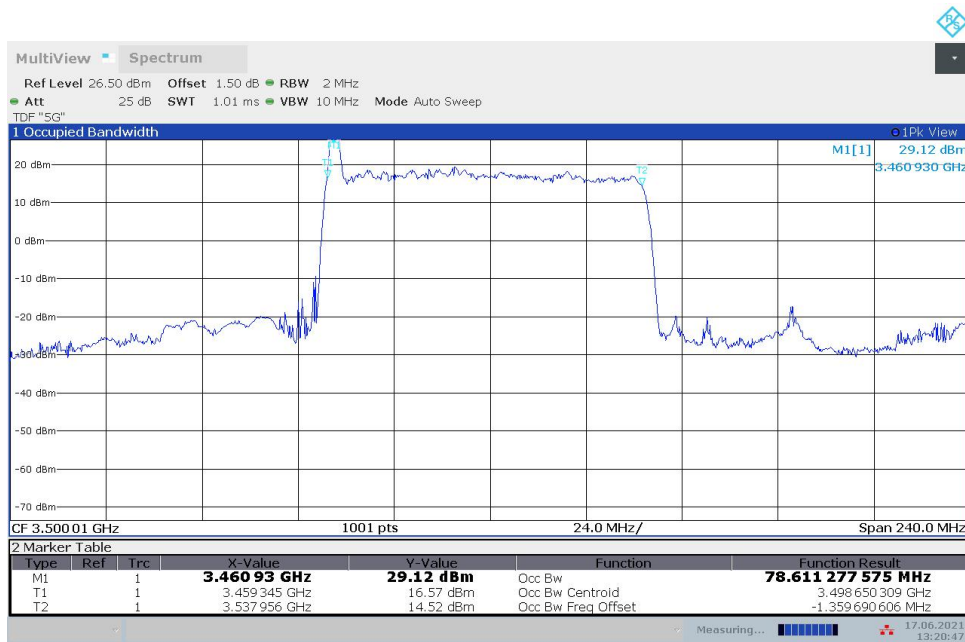


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LTE Band 7+NR n78L
n78L,80MHz(99%)

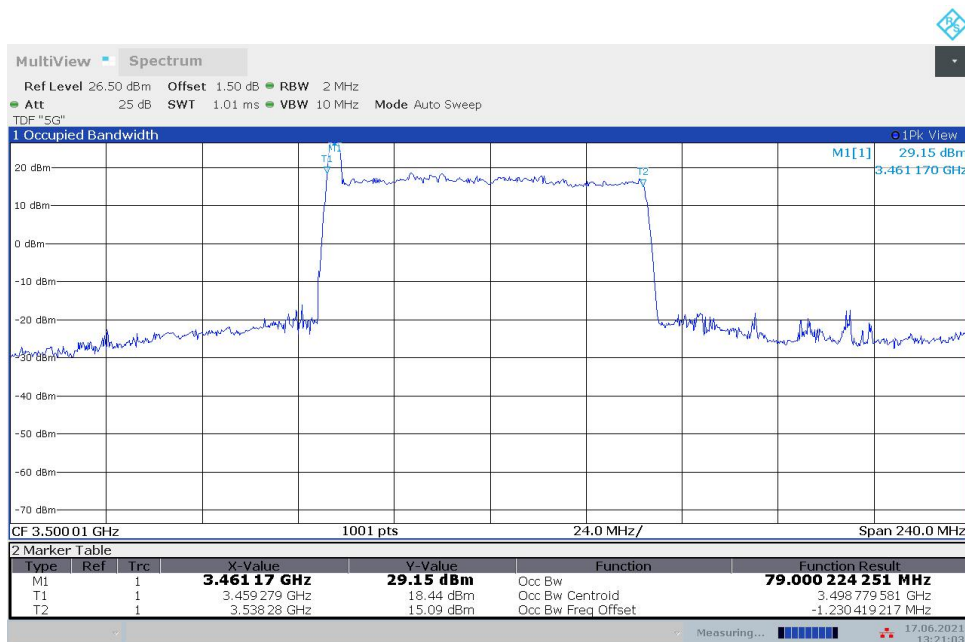
Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	78.611	79.000

n78L,80MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



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n78L,80MHz Bandwidth,DFT-s-QPSK (99% BW)

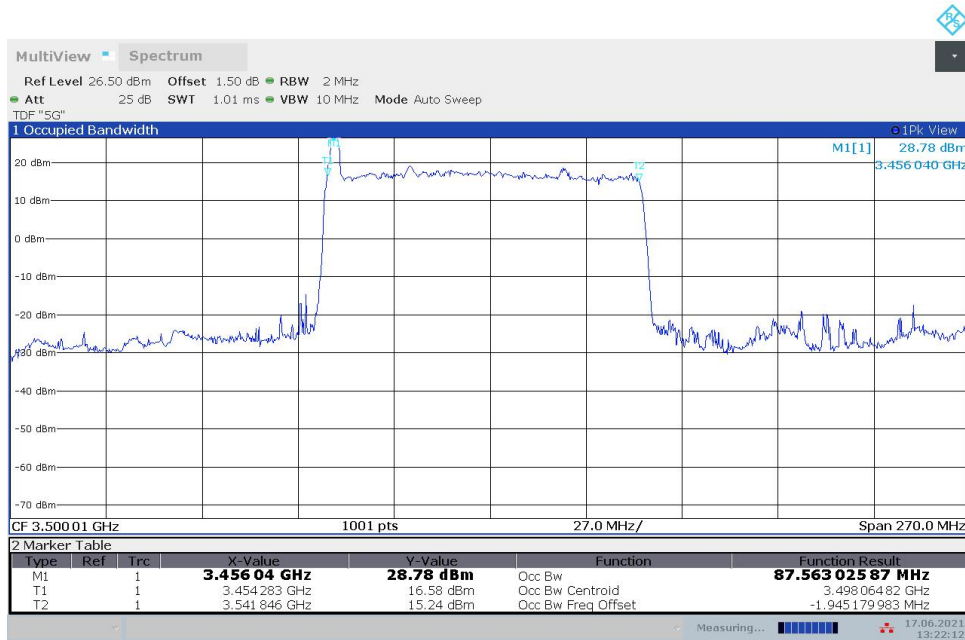


Date:17 JUN 2021 13:21:03

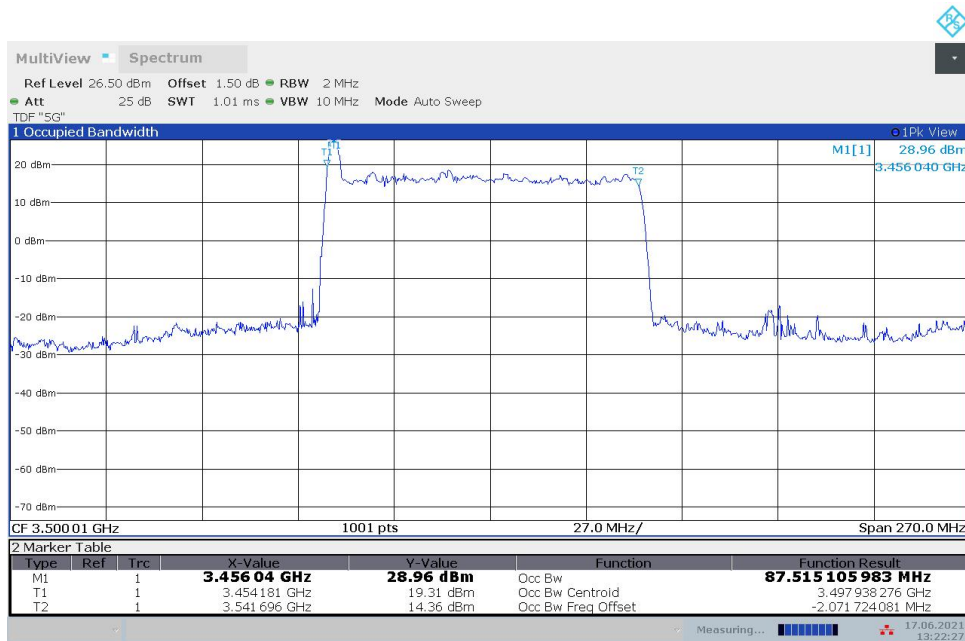
LTE Band 7+NR n78L
n78L,90MHz(99%)

Frequency (MHz)	Occupied Bandwidth (99%) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	87.563	87.515

n78L,90MHz Bandwidth,DFT-s-pi/2 BPSK (99% BW)



n78L,90MHz Bandwidth,DFT-s-QPSK (99% BW)



A.5 Emission Bandwidth

The emission bandwidth is defined as the width of the signal between two points, one below the carrier center frequency and one above the carrier center frequency, outside of which all emissions are attenuated at least 26 dB below the transmitter power. Table below lists the measured -26dBc BW. Spectrum analyzer plots are included on the following pages.

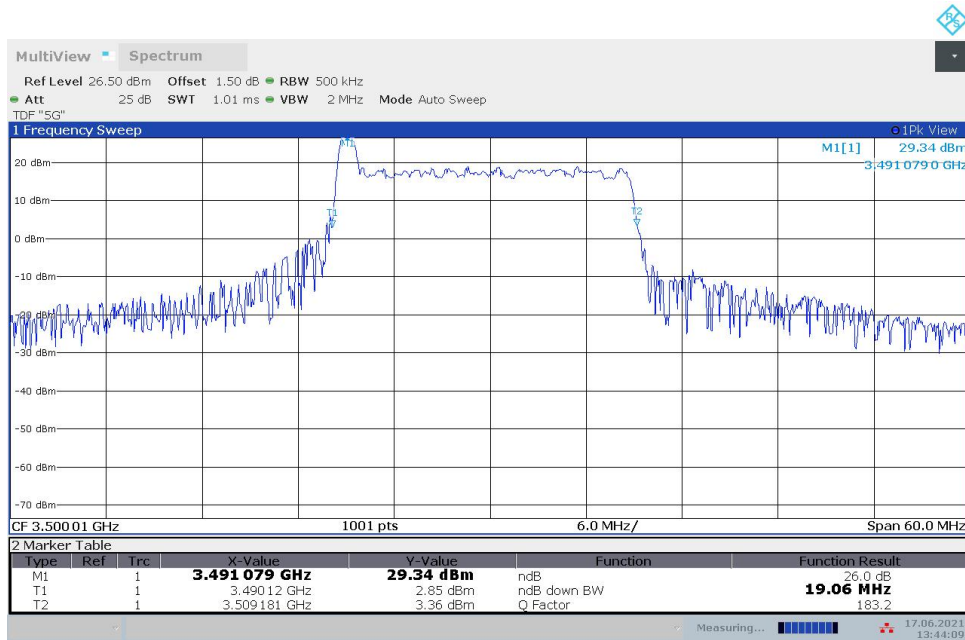
The measurement method is from ANSI C63.26:

- a) The spectrum analyzer center frequency is set to the nominal EUT channel center frequency. The span range for the spectrum analyzer shall be wide enough to see sufficient roll off of the signal to make the measurement.
- b) The nominal RBW shall be in the range of 1% to 5% of the anticipated OBW, and the VBW shall be set $\geq 3 \times$ RBW.
- c) Set the reference level of the instrument as required to prevent the signal amplitude from exceeding the maximum spectrum analyzer input mixer level for linear operation.
- d) The dynamic range of the spectrum analyzer at the selected RBW shall be more than 10 dB below the target “-X dB” requirement, i.e., if the requirement calls for measuring the -26 dB OBW, the spectrum analyzer noise floor at the selected RBW shall be at least 36 dB below the reference level.
- e) Set spectrum analyzer detection mode to peak, and the trace mode to max hold.

LTE Band 7+NR n78L
n78L,20MHz(-26dBc)

Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	19.061	19.181

n78L,20MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



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n78L,20MHz Bandwidth,DFT-s-QPSK (-26dBc BW)

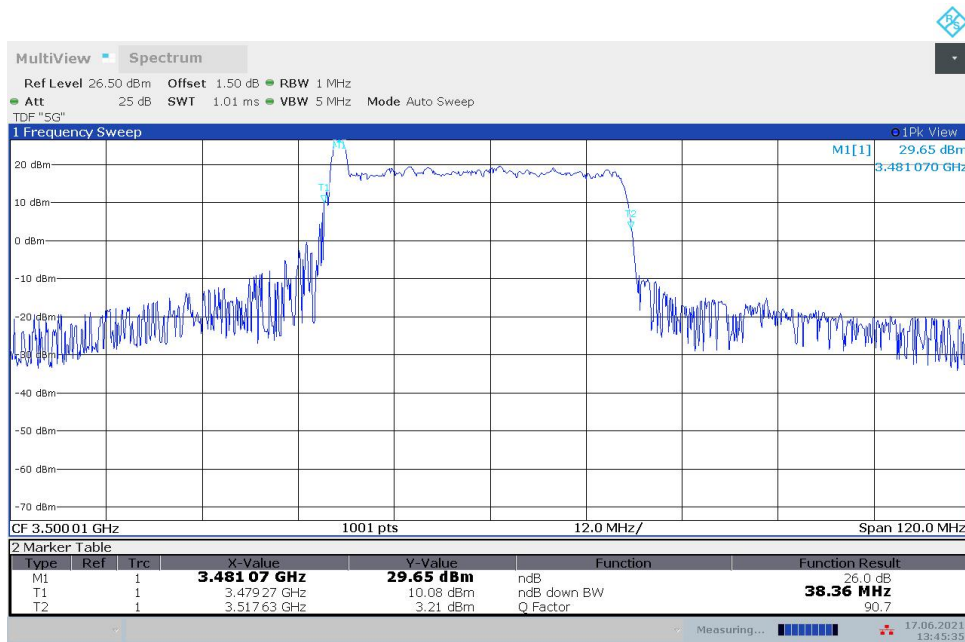


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LTE Band 7+NR n78L
n78L,40MHz(-26dBc)

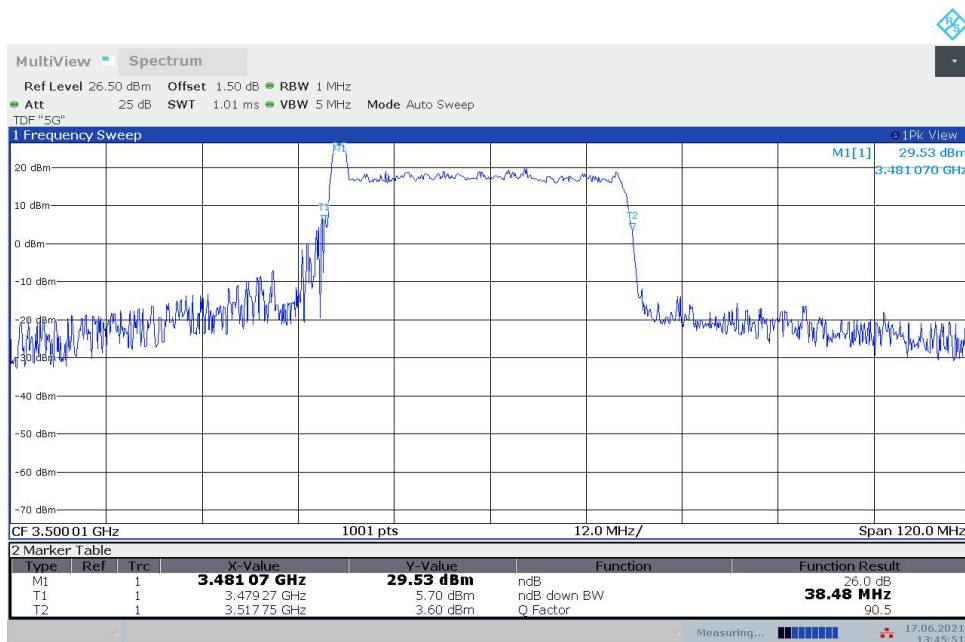
Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	38.360	38.480

n78L,40MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



Date:17 JUN 2021 13:45:35

n78L,40MHz Bandwidth,DFT-s-QPSK (-26dBc BW)

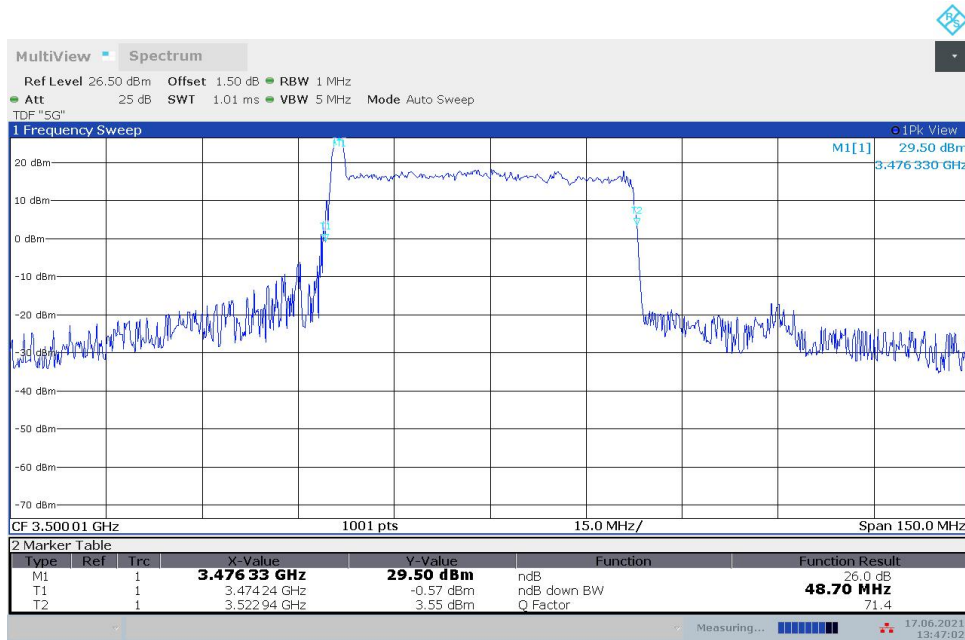


Date:17 JUN 2021 13:45:51

LTE Band 7+NR n78L
n78L,50MHz(-26dBc)

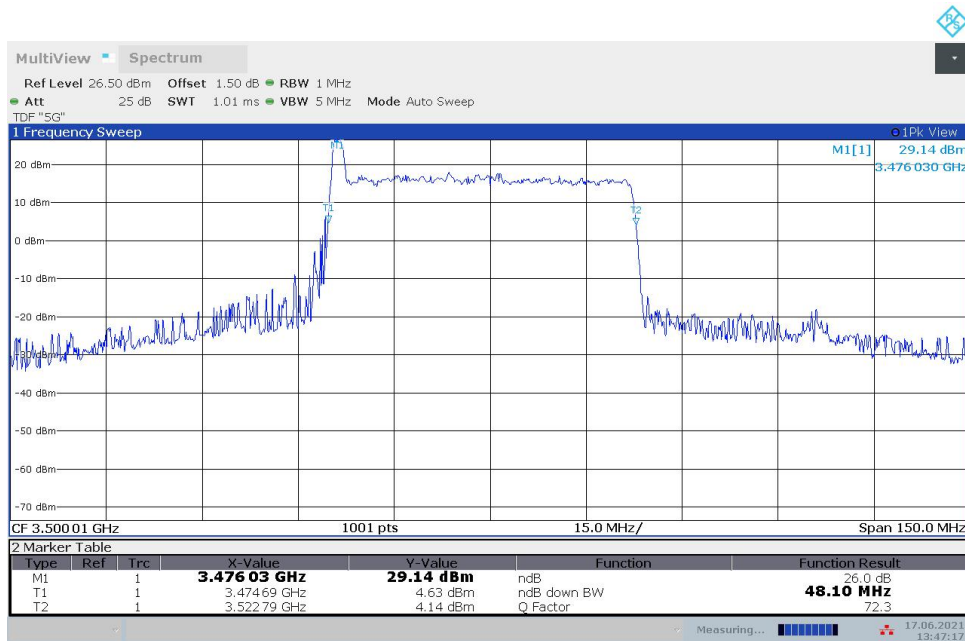
Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	48.700	48.100

n78L,50MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



Date:17 JUN 2021 13:47:02

n78L,50MHz Bandwidth,DFT-s-QPSK (-26dBc BW)

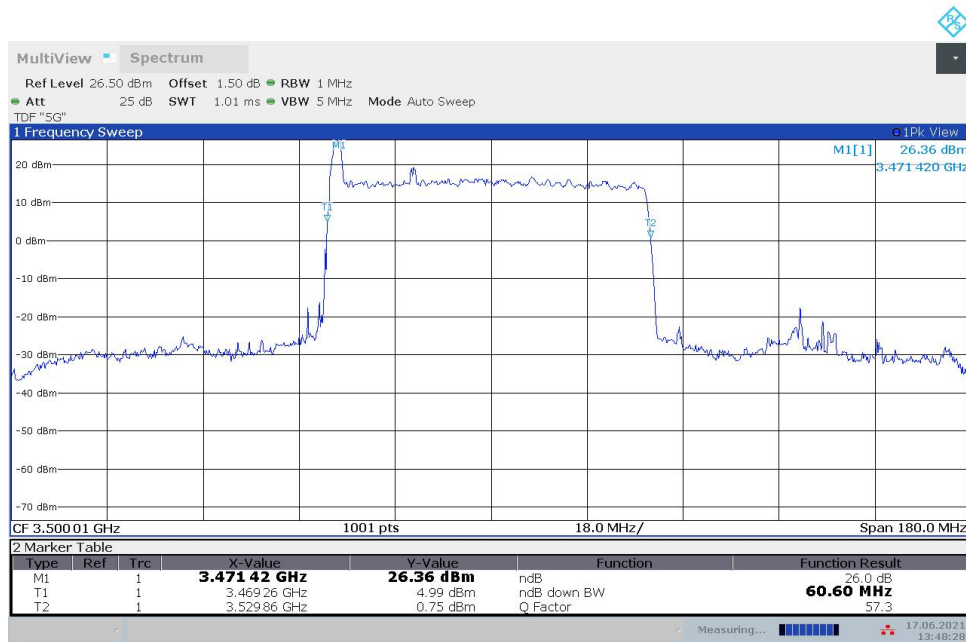


Date:17 JUN 2021 13:47:18

LTE Band 7+NR n78L
n78L,60MHz(-26dBc)

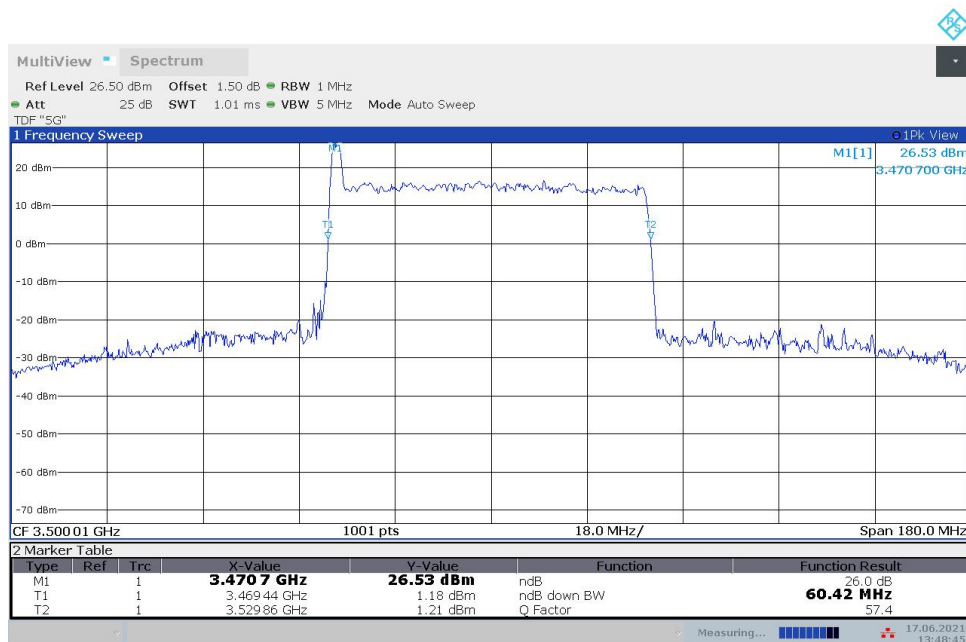
Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	60.600	60.420

n78L,60MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



Date:17 JUN 2021 13:48:29

n78L,60MHz Bandwidth,DFT-s-QPSK (-26dBc BW)

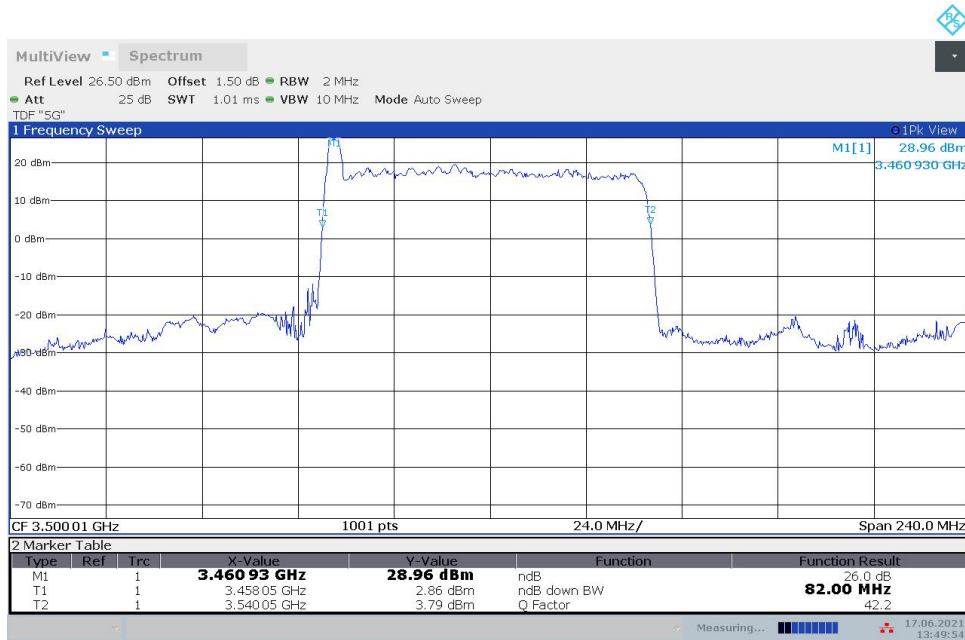


Date:17 JUN 2021 13:48:44

LTE Band 7+NR n78L
n78L,80MHz(-26dBc)

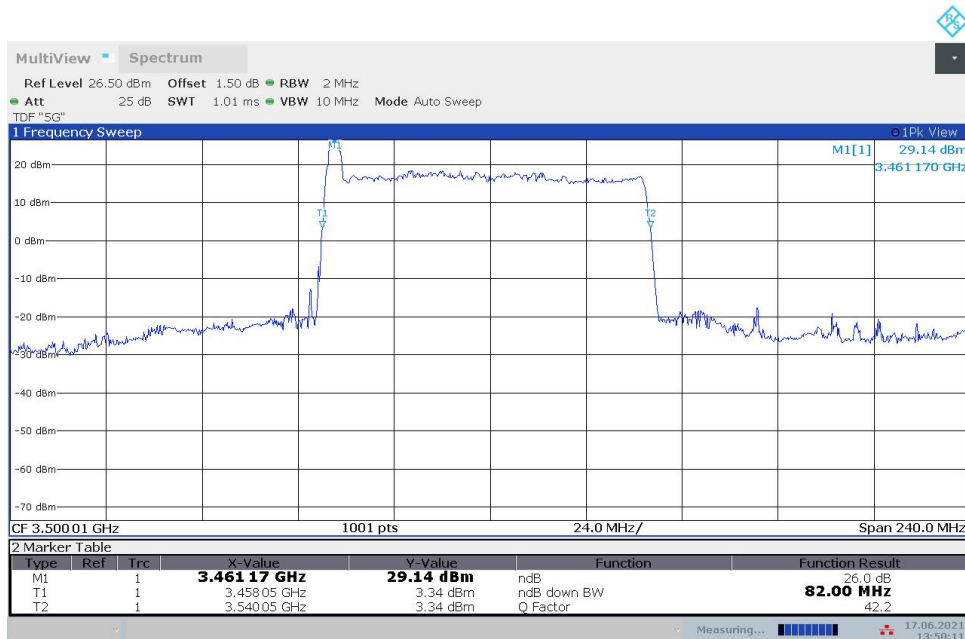
Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	82.000	82.000

n78L,80MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



Date:17.JUN.2021 13:49:55

n78L,80MHz Bandwidth,DFT-s-QPSK (-26dBc BW)

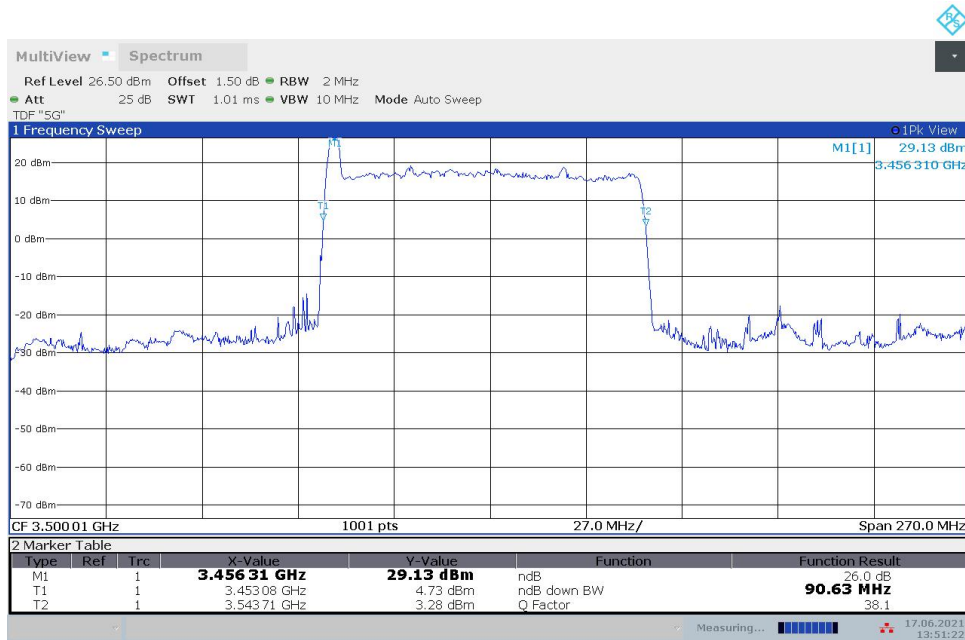


Date:17.JUN.2021 13:50:11

LTE Band 7+NR n78L
n78L,90MHz(-26dBc)

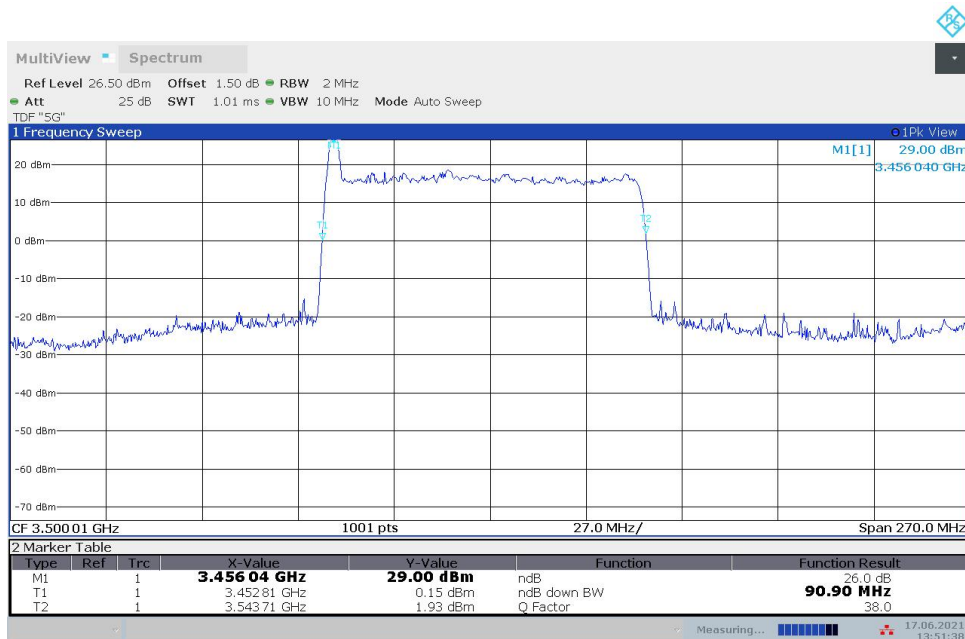
Frequency (MHz)	Emission Bandwidth (-26dBc) (MHz)	
	DFT-s-pi/2 BPSK	DFT-s-QPSK
3500.01	90.630	90.900

n78L,90MHz Bandwidth,DFT-s-pi/2 BPSK (-26dBc BW)



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n78L,90MHz Bandwidth,DFT-s-QPSK (-26dBc BW)



Date:17 JUN 2021 13:51:38

A.6 Band Edge Compliance

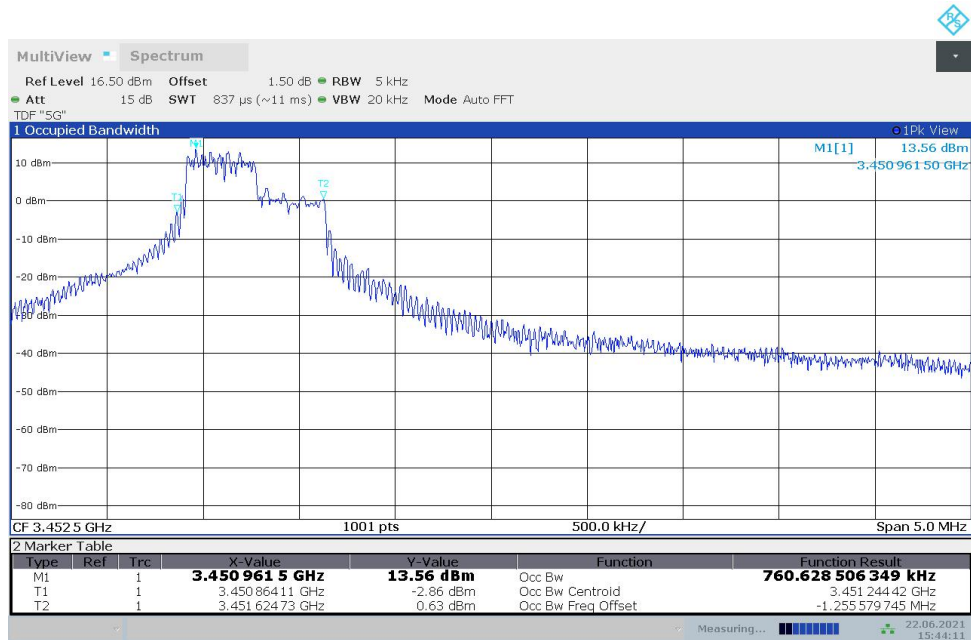
A.6.1 Measurement limit

Part 27.53(n) states for mobile operations in the 3450-3550 MHz band, the conducted power of any emission outside the licensee's authorized bandwidth shall not exceed -13 dBm/MHz. Compliance with this paragraph (n)(2) is based on the use of measurement instrumentation employing a resolution bandwidth of 1 megahertz or greater. However, in the 1 megahertz bands immediately outside and adjacent to the licensee's frequency block, a resolution bandwidth of at least one percent of the emission bandwidth of the fundamental emission of the transmitter may be employed, but limited to a maximum of 200 kHz. In the bands between 1 and 5 MHz removed from the licensee's frequency block, the minimum resolution bandwidth for the measurement shall be 500 kHz. The emission bandwidth is defined as the width of the signal between two points, one below the carrier center frequency and one above the carrier center frequency, outside of which all emissions are attenuated at least 26 dB below the transmitter power.

A.6.2 Measurement result

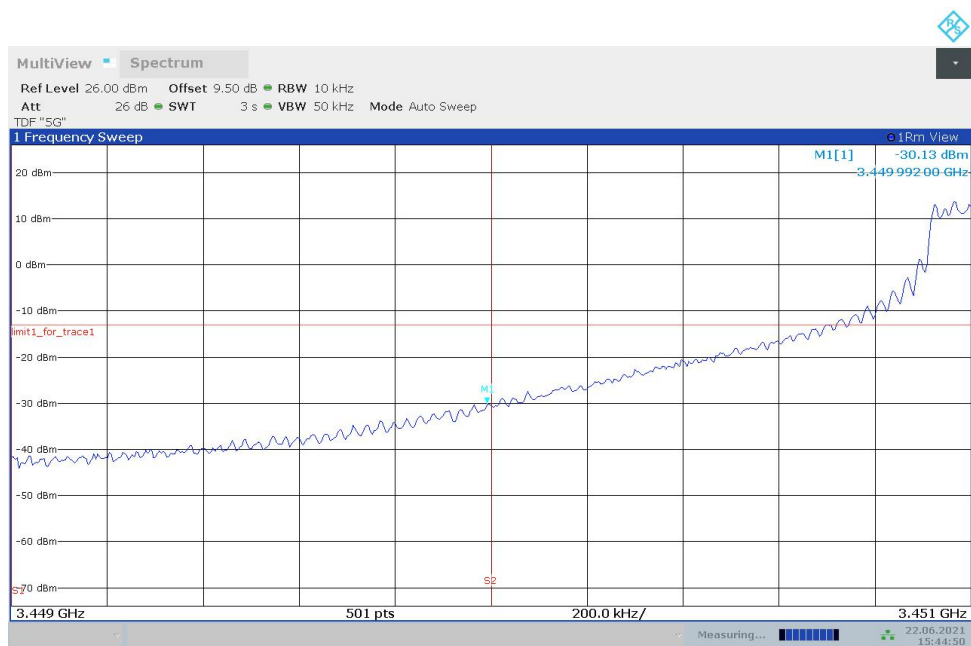
LTE Band 7+NR n78L

OBW: 1RB-LOW_offset



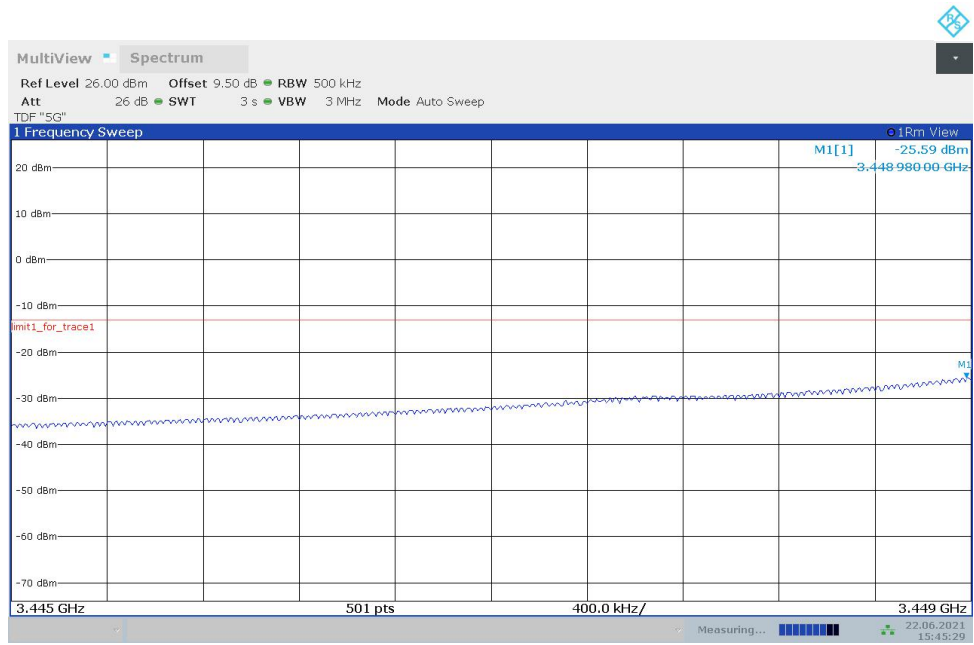
Date:22 JUN 2021 15:44:11

LOW BAND EDGE BLOCK-1RB-LOW_offset



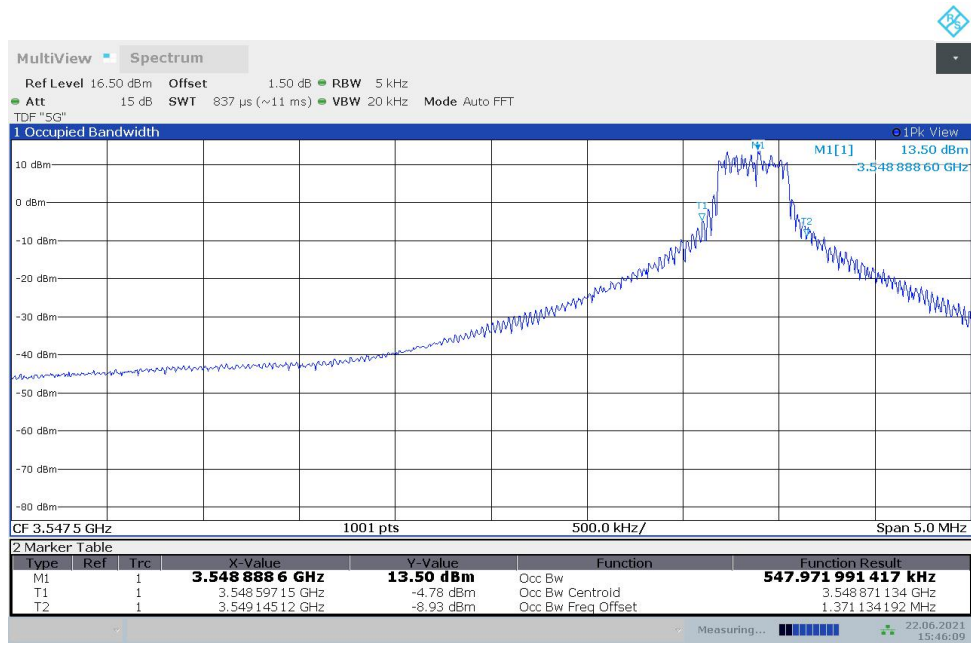
Date:22 JUN 2021 15:44:51

LOW BAND EDGE BLOCK-1RB-LOW_offset

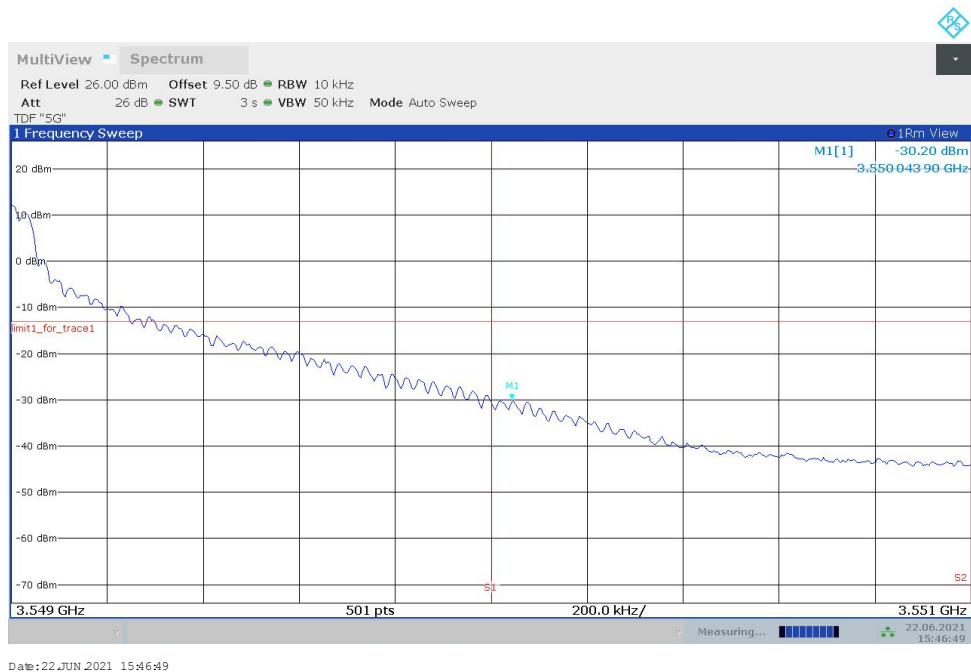


Date: 22 JUN 2021 15:45:29

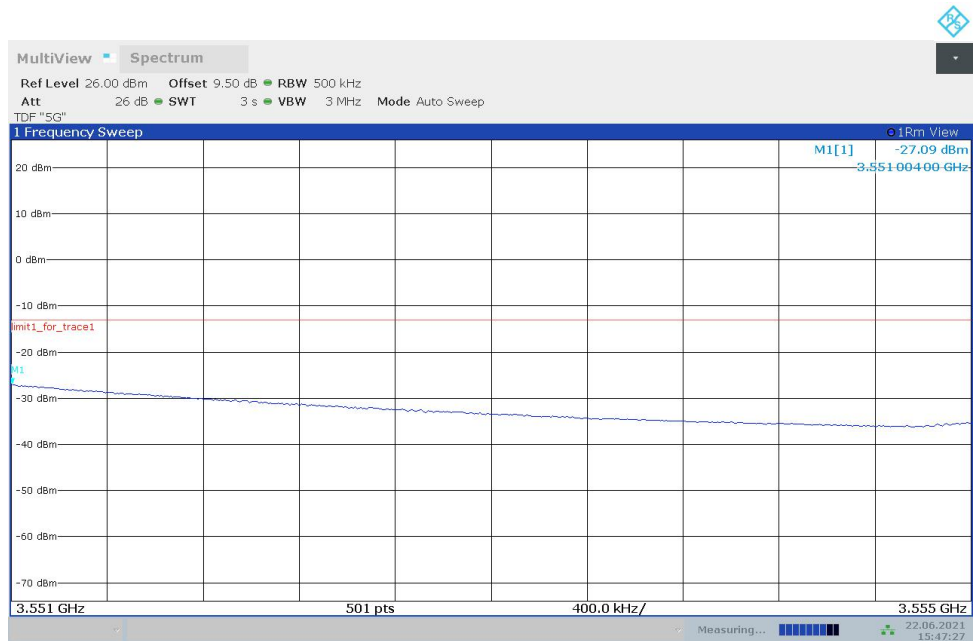
OBW: 1RB-HIGH_offset



HIGH BAND EDGE BLOCK-1RB-HIGH_offset

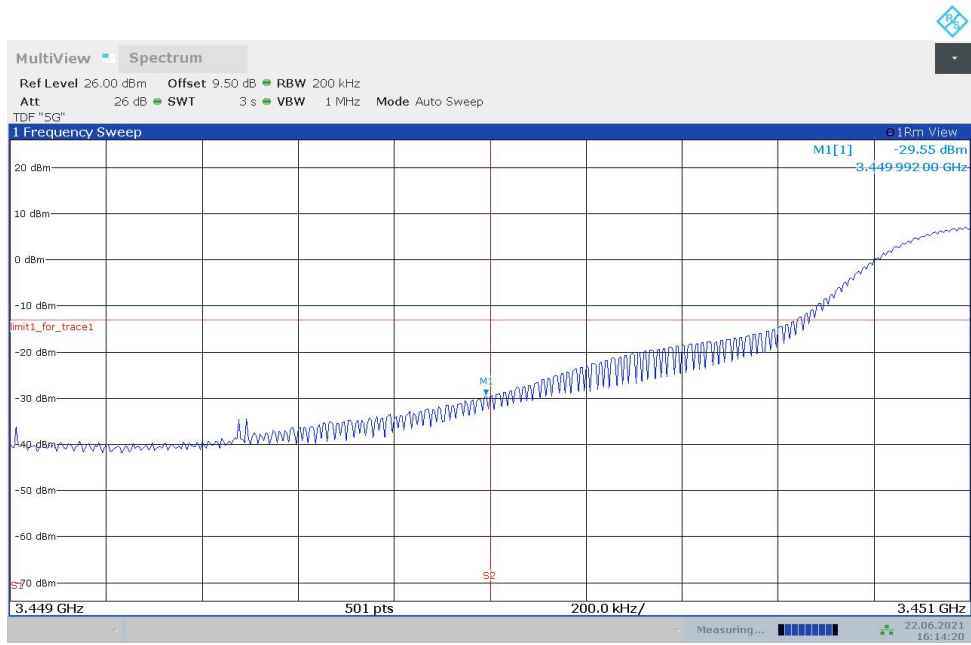


HIGH BAND EDGE BLOCK-1RB-HIGH_offset

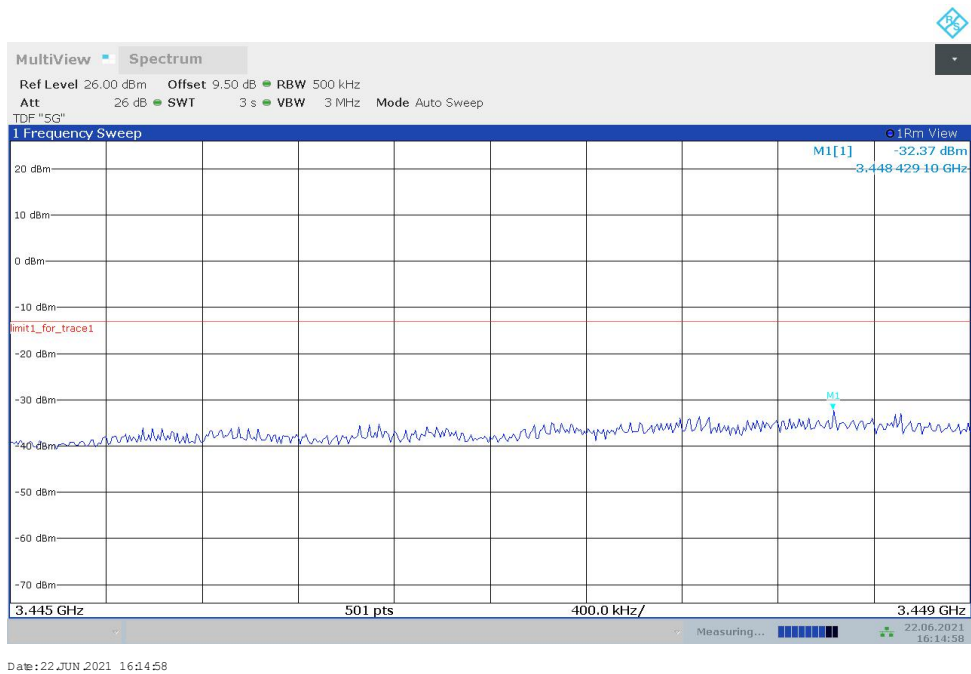


Date: 22 JUN 2021 15:47:27

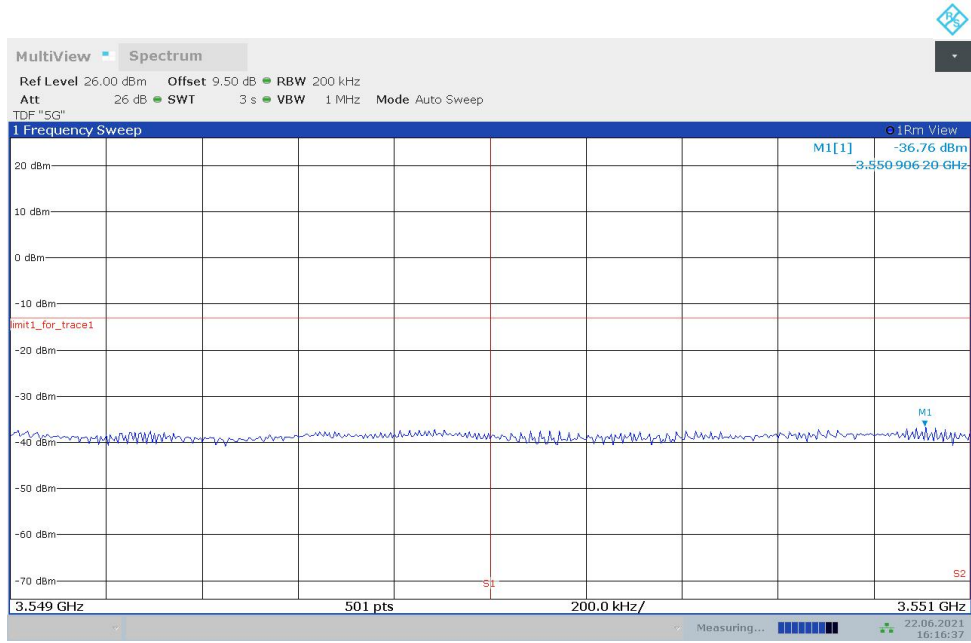
LOW BAND EDGE BLOCK-90M-100%RB



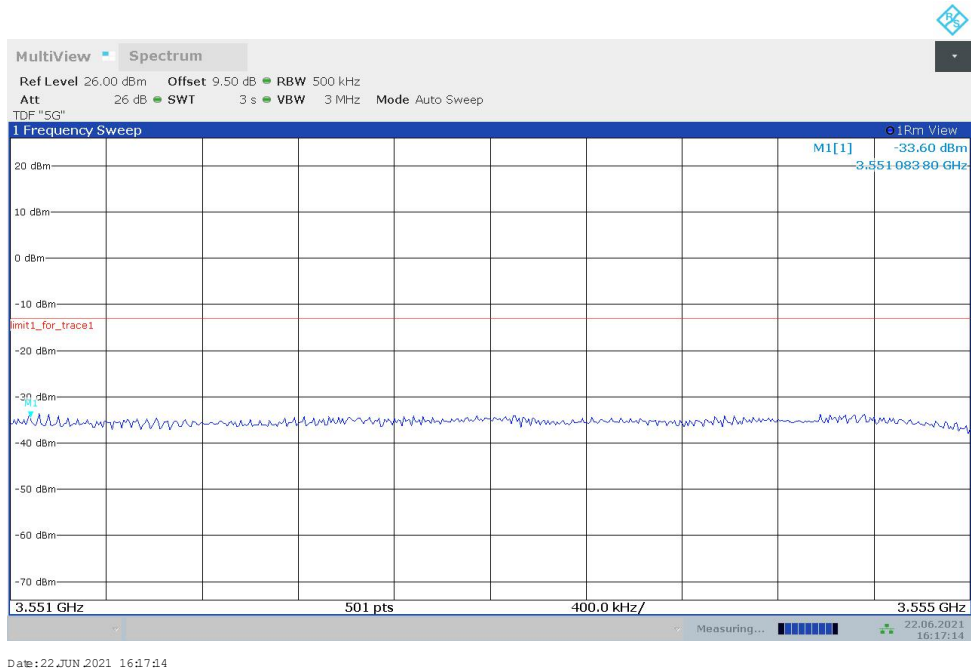
LOW BAND EDGE BLOCK-90M-100%RB



HIGH BAND EDGE BLOCK-90M-100%RB



HIGH BAND EDGE BLOCK-90M-100%RB



A.7 Conducted Spurious Emission

A.7.1 Measurement Method

The following steps outline the procedure used to measure the conducted emissions from the EUT.

1. In measuring unwanted emissions, the spectrum shall be investigated from 30 MHz or the lowest radio frequency signal generated in the equipment, whichever is lower, without going below 9 kHz, up to at least the frequency given below:
 - (a) If the equipment operates below 10 GHz: to the tenth harmonic of the highest fundamental frequency or to 40 GHz, whichever is lower.
 - (b) If the equipment operates at or above 10 GHz: to the fifth harmonic of the highest fundamental frequency or to 100 GHz, whichever is lower.
2. Determine EUT transmit frequencies: below outlines the band edge frequencies pertinent to conducted emissions testing.
3. The number of sweep points of spectrum analyzer is set to 30001 which is greater than span/RBW.

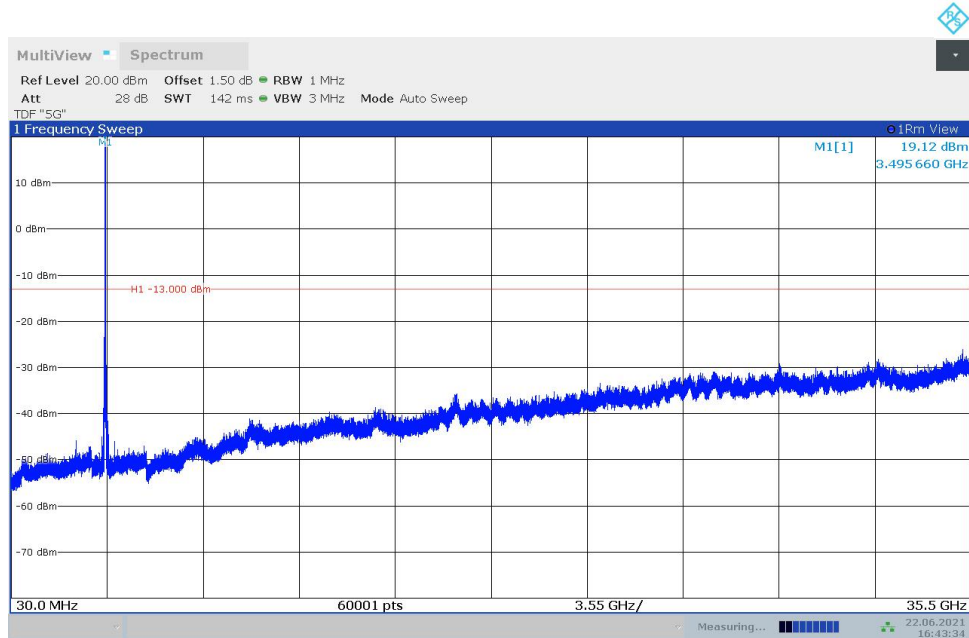
A. 7.2 Measurement Limit

Part 27.53(n) states for mobile operations in the 3450-3550 MHz band, the conducted power of any emission outside the licensee's authorized bandwidth shall not exceed -13 dBm/MHz. Compliance with this paragraph (n)(2) is based on the use of measurement instrumentation employing a resolution bandwidth of 1 megahertz or greater. However, in the 1 megahertz bands immediately outside and adjacent to the licensee's frequency block, a resolution bandwidth of at least one percent of the emission bandwidth of the fundamental emission of the transmitter may be employed, but limited to a maximum of 200 kHz. In the bands between 1 and 5 MHz removed from the licensee's frequency block, the minimum resolution bandwidth for the measurement shall be 500 kHz. The emission bandwidth is defined as the width of the signal between two points, one below the carrier center frequency and one above the carrier center frequency, outside of which all emissions are attenuated at least 26 dB below the transmitter power.

A. 7.3 Measurement result

LTE Band 7+NR n78L

NOTE: peak above the limit line is the carrier frequency.



A.8 Peak-to-Average Power Ratio

The peak-to-average ratio (PAR) of the transmission may not exceed 13 dB

- a) Refer to instrument's analyzer instruction manual for details on how to use the power statistics/CCDF function;
- b) Set resolution/measurement bandwidth \geq signal's occupied bandwidth;
- c) Set the number of counts to a value that stabilizes the measured CCDF curve;
- d) Record the maximum PAPR level associated with a probability of 0.1%.

Measurement results

LTE Band 7+NR n78L,90MHz

Frequency (MHz)	PAPR (dB)								
	DFT-s-pi/2 BPSK	DFT-s-QPSK	DFT-s-16QAM	DFT-s-64QAM	DFT-s-256QAM	CP-QPSK	CP-16QAM	CP-64QAM	CP-256QAM
3500.01	6.41	7.15	7.66	8.63	7.99	9.81	10.89	10.01	9.75

Annex B: Accreditation Certificate

**United States Department of Commerce
National Institute of Standards and Technology**

Certificate of Accreditation to ISO/IEC 17025:2017

NVLAP LAB CODE: 600118-0

Telecommunication Technology Labs, CAICT
Beijing
China

*is accredited by the National Voluntary Laboratory Accreditation Program for specific services,
listed on the Scope of Accreditation, for:*

Electromagnetic Compatibility & Telecommunications

*This laboratory is accredited in accordance with the recognized International Standard ISO/IEC 17025:2017.
This accreditation demonstrates technical competence for a defined scope and the operation of a laboratory quality
management system (refer to joint ISO-ILAC-IAF Communique dated January 2009).*

2020-09-29 through 2021-09-30
Effective Dates




For the National Voluntary Laboratory Accreditation Program

END OF REPORT