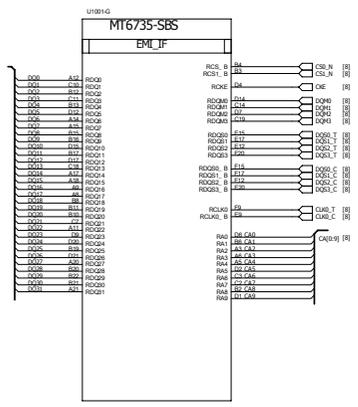


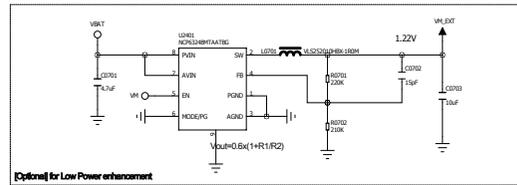
Schematic design notice of "10_BB_POWER" page.

Note 10-1: 4 mil GND trace with good shielding to PMIC (Differential)

Note 10-3: FSOURCE_P(FUSE)
 (1)FSOURCE_P FUSE power(VEFUSE) should be only for FUSE usage(not share with other application)
 (2)W/O FUSE program, VEFUSE need 1uF bypass cap (pls refer to i sLDO output voltage/current table)
 (3)W/O FUSE program, VEFUSE bypass cap should be NC.



External Buck for DRAM



DRAM Buck Purpose: Reserved for low power performance

THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR DISCLOSED TO OTHERS WITHOUT THE PRIOR WRITTEN PERMISSION OF ZTE.			
DRAWN: xuxuemei	DATE: <Drawn Date>	TITLE: <Title>	
CHECKED: Chris Fu	DATE: <Checked Date>	SIZE: A1	Sheet Name: 7_POWER_THIRD_PART
RELEASED: xuxuemei	DATE: <Release Date>	SCALE: 1:1	SHEET: 7 OF 19 REV: V1.0

ZHEPHONE

eMMC+LPDDR3
221 Ball, 0.5mm pitch

VDD1=1.8V
VDD2=1.20V
VDDCA=1.2V
VDDQ= 1.20V

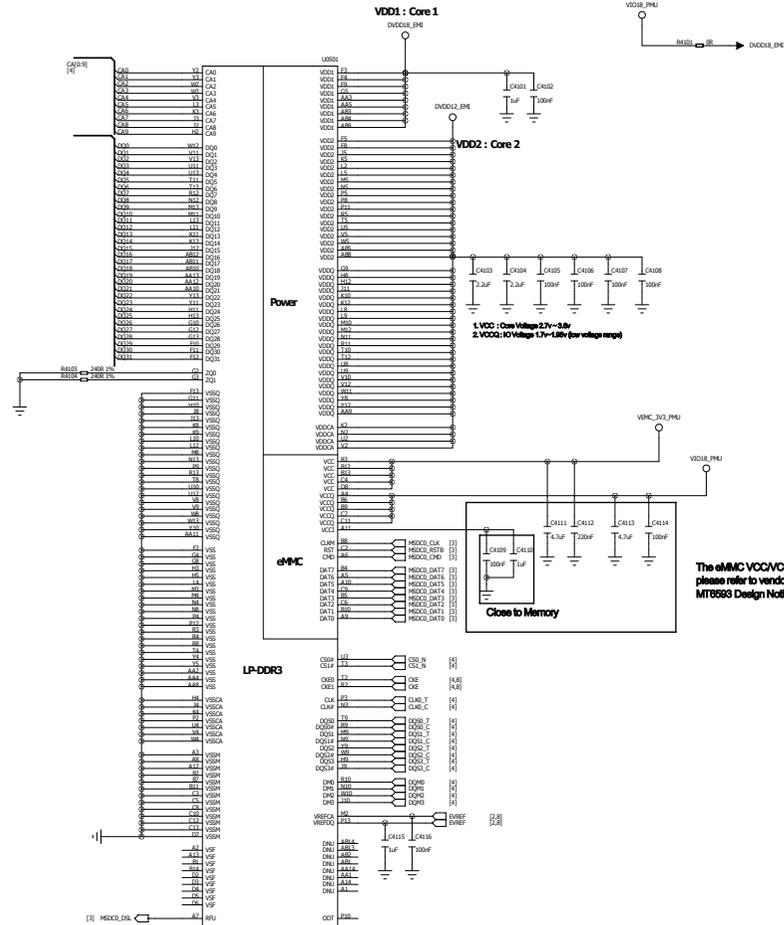
8Gb eMMC + 8Gb LPDDR3
Hynix/H9TQ64A8GTMCUR-KUM

Connect to AP

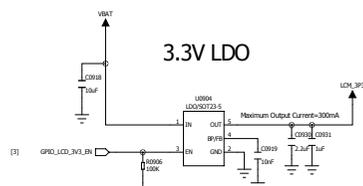
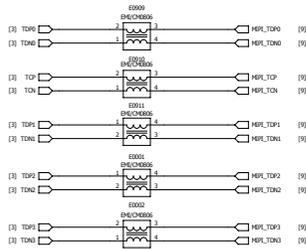
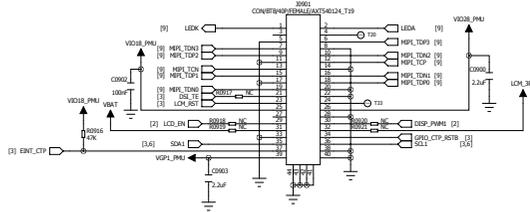
DQ0_31	DQ0_31
CA0_9	CA0_9
CS0_N	CS0_N
CS1_N	CS1_N
CKE	CKE
DQM0	DQM0
DQM1	DQM1
DQM2	DQM2
DQM3	DQM3
DQS0_C	DQS0_C
DQS1_C	DQS1_C
DQS2_C	DQS2_C
DQS3_C	DQS3_C
DQS0_T	DQS0_T
DQS1_T	DQS1_T
DQS2_T	DQS2_T
DQS3_T	DQS3_T
CLK0_T	CLK0_T
CLK0_C	CLK0_C
VREF_CA	VREF_CA
VREF_DQ	VREF_DQ
MSDC0_RSTB	MSDC0_RSTB
MSDC0_CMD	MSDC0_CMD
MSDC0_CLK	MSDC0_CLK
MSDC0_DSL	MSDC0_DSL
MSDC0_DAT0	MSDC0_DAT0
MSDC0_DAT1	MSDC0_DAT1
MSDC0_DAT2	MSDC0_DAT2
MSDC0_DAT3	MSDC0_DAT3
MSDC0_DAT4	MSDC0_DAT4
MSDC0_DAT5	MSDC0_DAT5
MSDC0_DAT6	MSDC0_DAT6
MSDC0_DAT7	MSDC0_DAT7

Power I/F

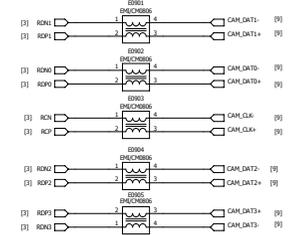
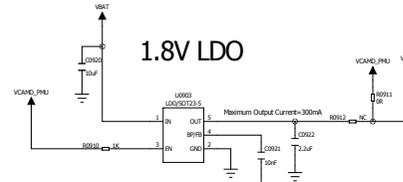
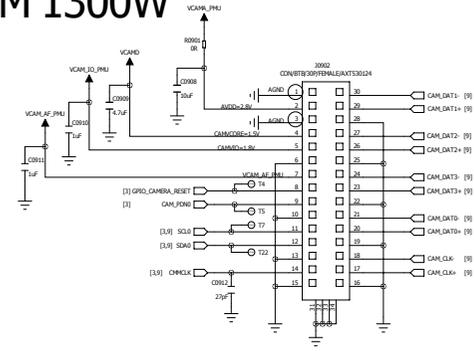
VI018_PMU	VI018_PMU
VEMC_3V3_PMU	VEMC_3V3_PMU
DVDD12_EM	DVDD12_EM



LCM+TP

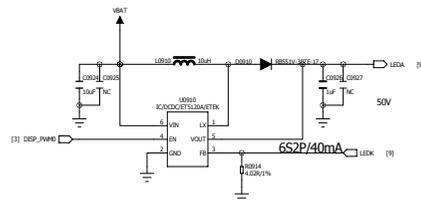


Rear CAM 1300W

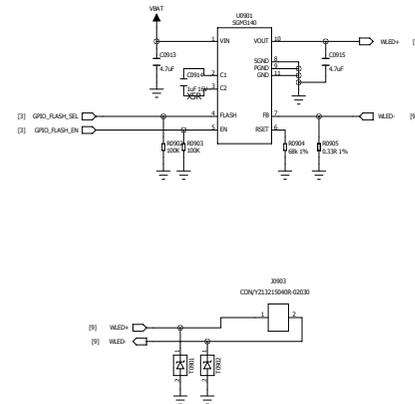


Backlight LED Driver

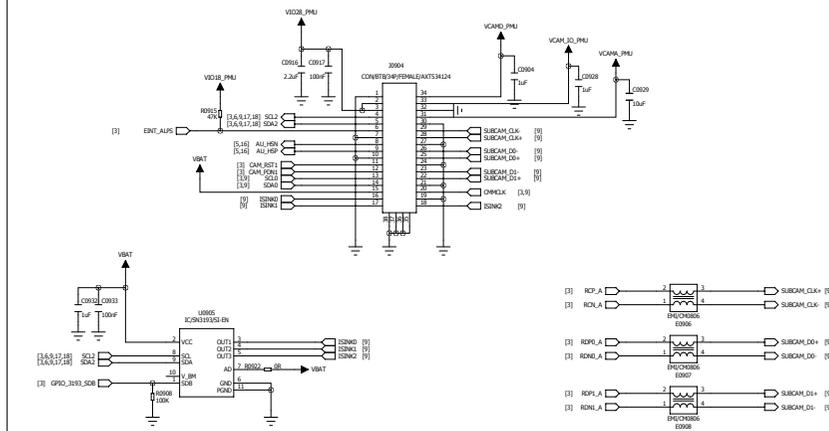
VOUT max. : 37V
Switch Frequency : 5KHz~100KHz
VFB : 200mV

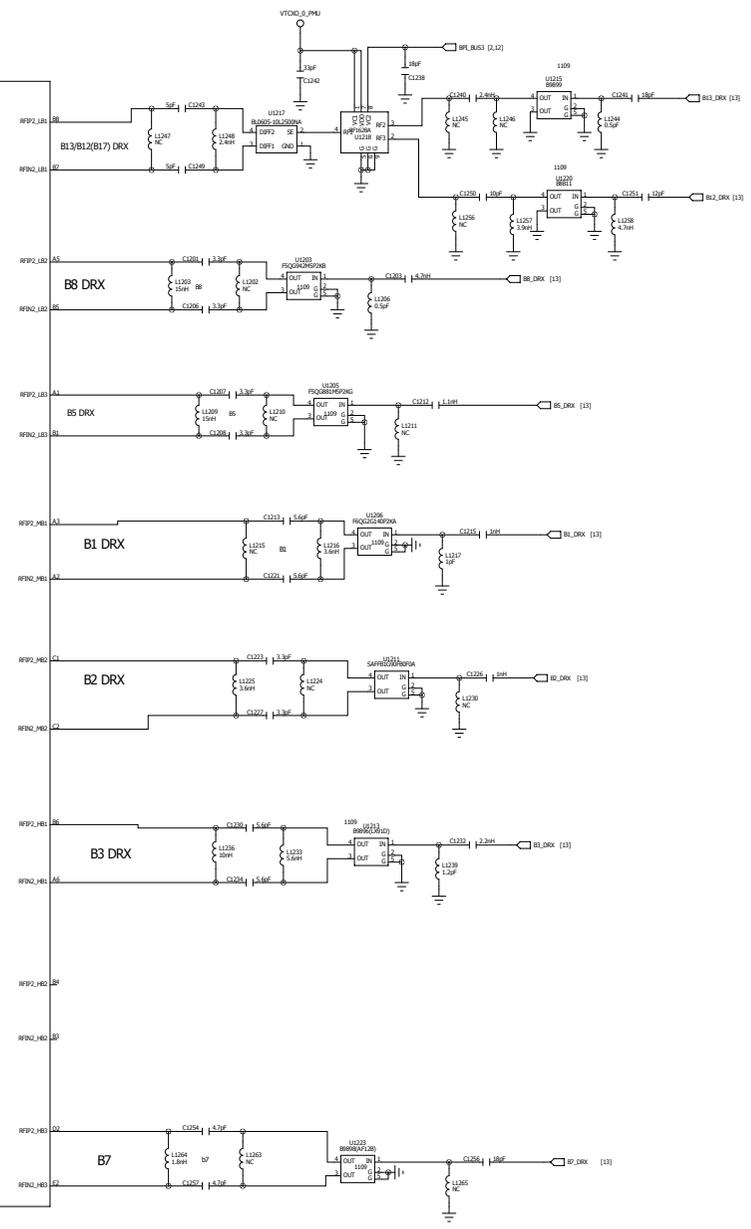
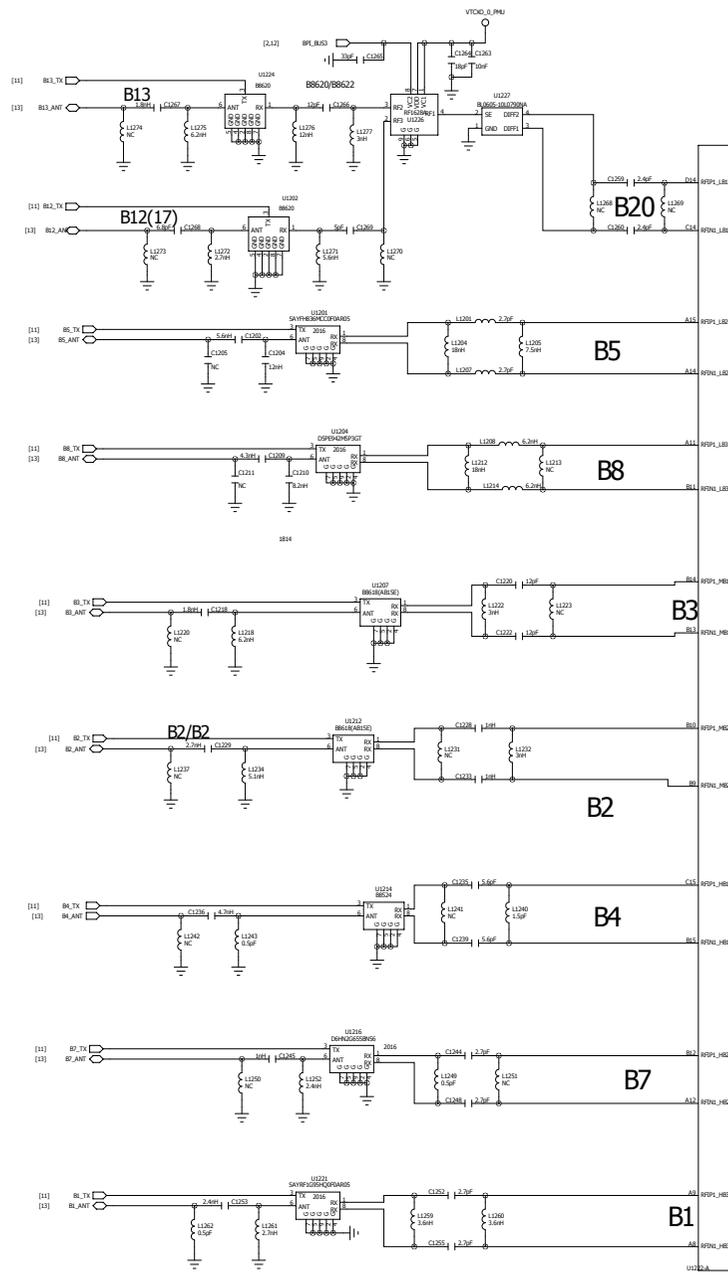


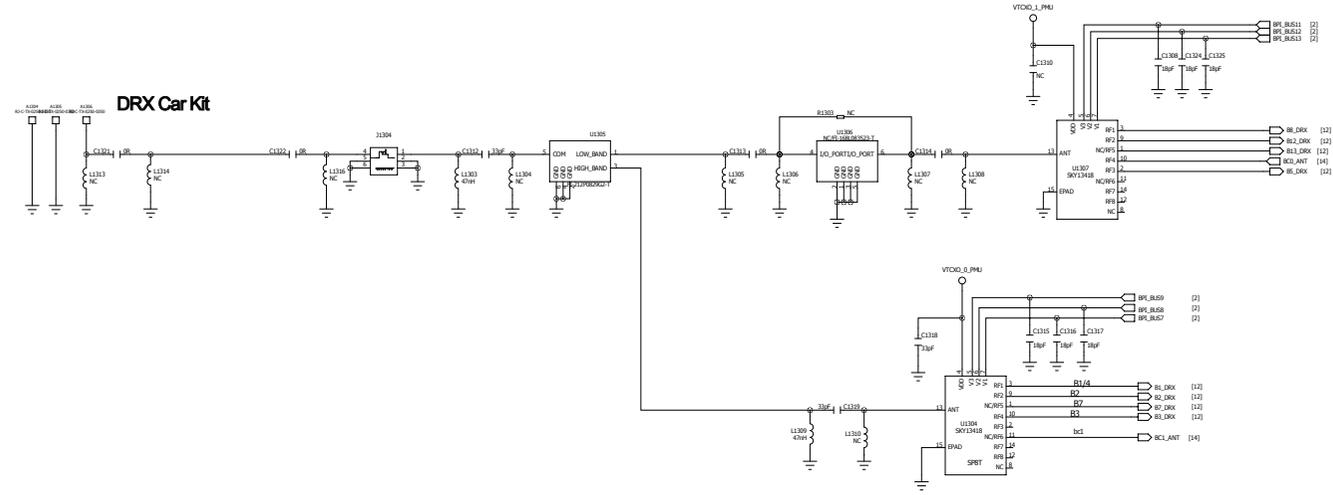
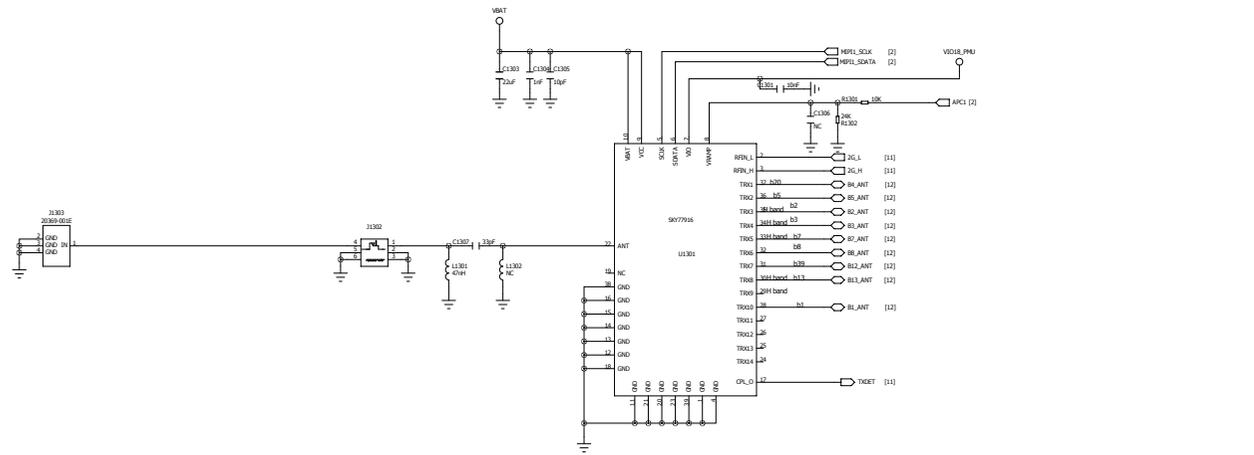
Flash LED

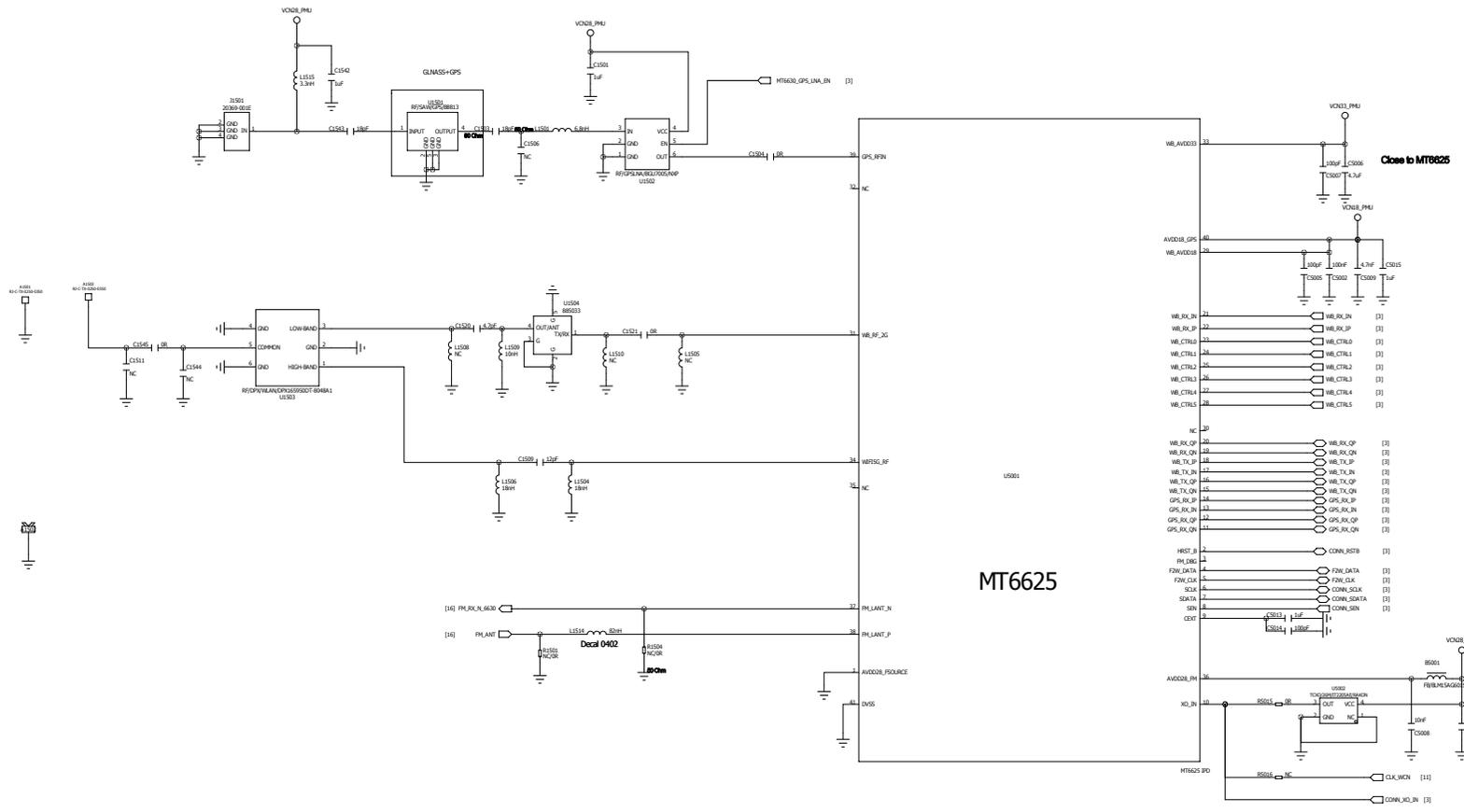


REC+ALPS+Indicator+SUB CAM 200W

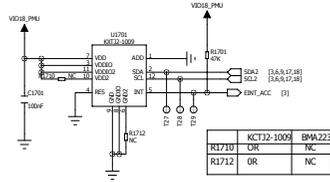




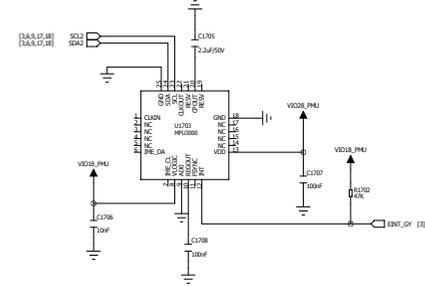




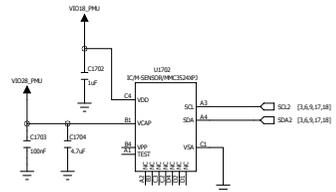
G-sensor



Gyro Sensor

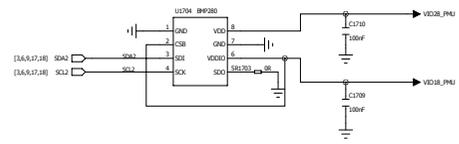


M-Sensor



Pressure Sensor

Baro I2C address: 0x77 (Wils:0xEE, Read:0xEF)



C1211, C1212, C1213, C1214, C1215, C1216, C1217, C1218, C1219 need to use 2% accuracy and 50V tolerance capacitor
PS: 0201 cap can't tolerance 50V

Components in this region use 5% accuracy

ENB (NFC_VEN)
1. Input pin
2. Internal pull low
3. Low active
4. If default NFC would like to disable, please configure to high

SYSRST_B (NFC_RST)
1. Input pin
2. Internal pull high
3. Low active

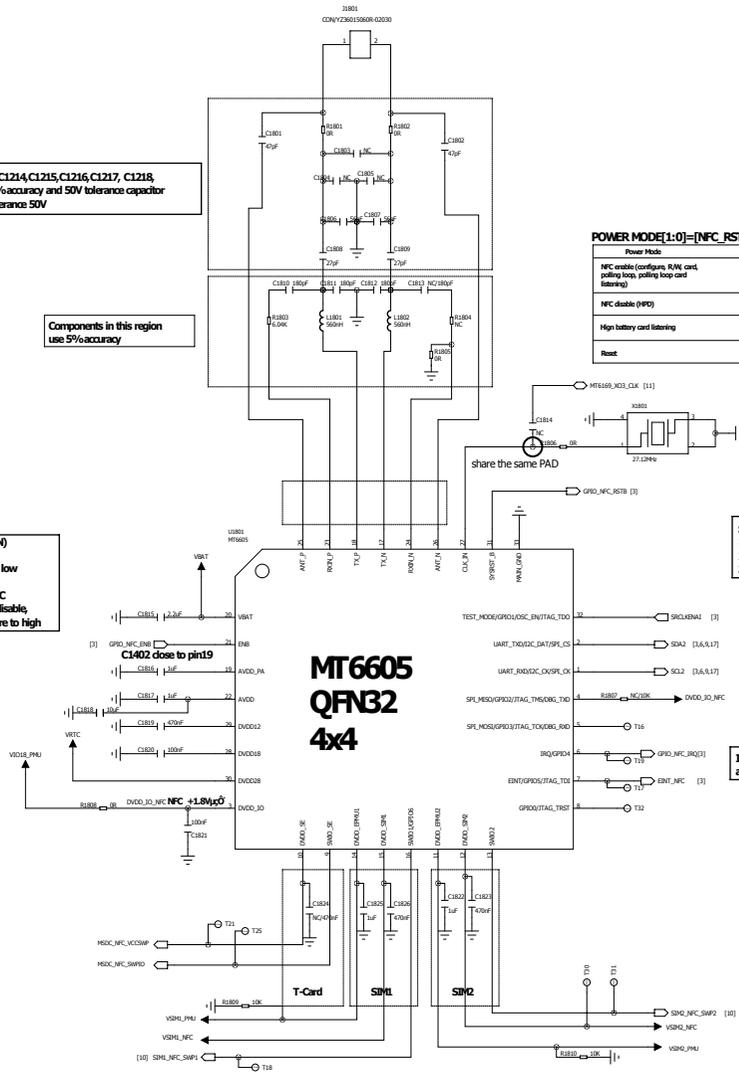
Only can use HW I2C.
SW I2C is not allowed.

R1206 NC : XTAL MODE
R1206 10K : Co-Clock

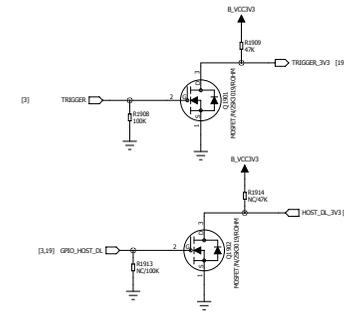
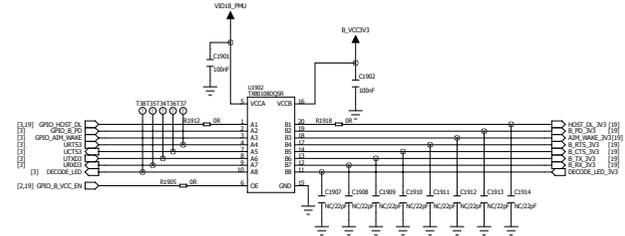
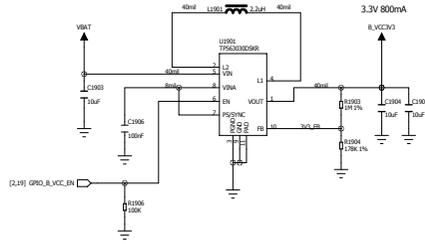
IRQ(NFC_IRQ), OSC_EN(NFC_OSC_EN) are output pin, and both are High active

POWER MODE[1:0]=[NFC_RST:NFC_VENB]

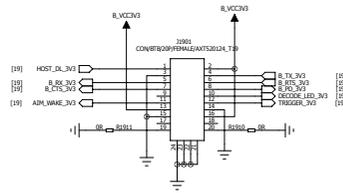
Power Mode	NFC_RST	NFC_VENB
NFC enable (configure, R/W card, polling loop, polling loop card listening)	1	0
NFC disable (PWR)	0	1
High battery card listening	1	1
Reset	0	0



Barcode 3.3V



Barcode CON



Barcode Decoder Board Interface

1	HOST_DOWNLOAD	3.3V GPIO
2	HOST_3P3	3.3V 800mA
3	GND	
4	MT8735_TXD	3.3V 接MT8735平台TX
5	MT8735_RXD	
6	MT8735_RTS	
7	MT8735_CTS	
8	POWER_DOWN	3.3V GPIO/EINT
10	DECODE_LRD*	3.3V GPIO Input
11	ATM_WAKE*	3.3V GPIO
12	TRIGGER*	3.3V GPO
13	HOST_3P3	
14	GND	
15	GND	
16	HOST_3P3	
17	N.C	
18		
19	HOST_SYS_CFG0 (Connect to GND for UART)	OR GND
20	HOST_SYS_CFG1 (Connect to GND for UART)	OR GND