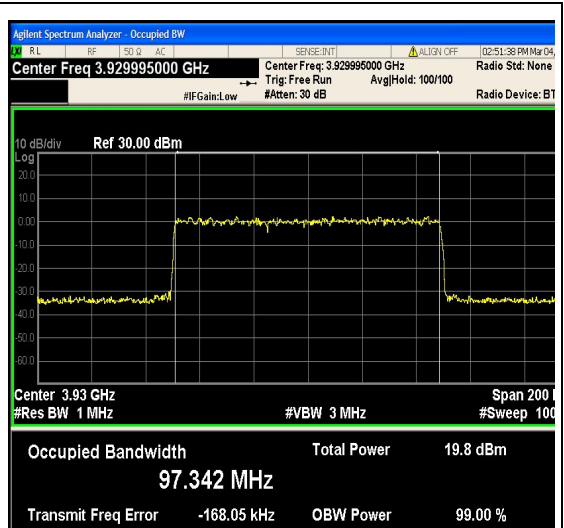
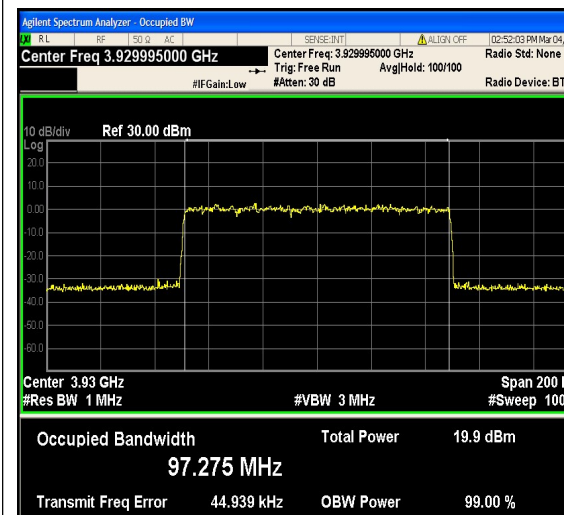


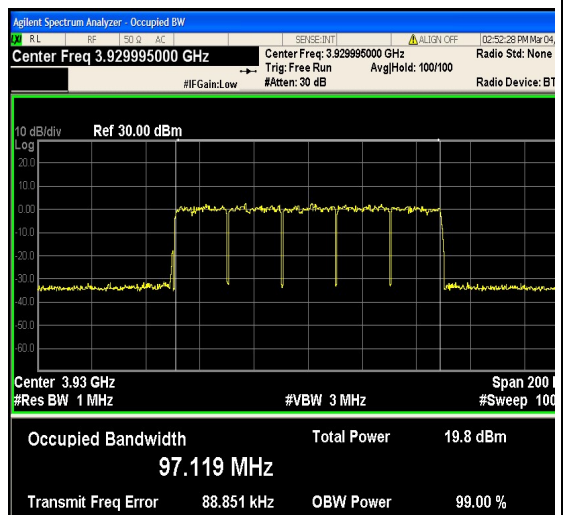
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +H-5-N/A-N/A-N/A-CP-QPSK-Outer_Full-273
 @0-Ant1-96.487-99.59-≤100-PASS



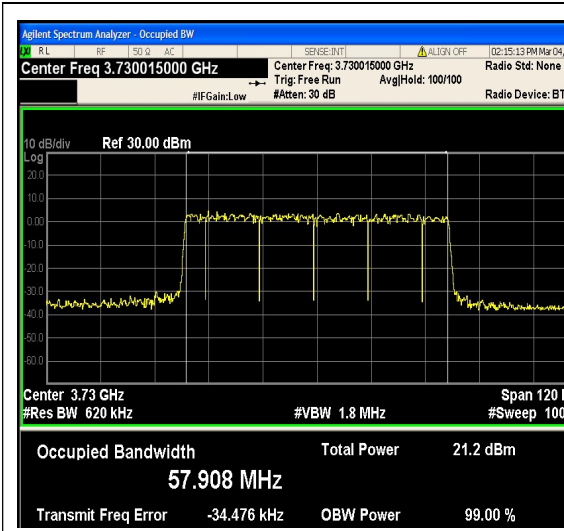
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 +H-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-27
 3@0-Ant1-97.342-100.7-≤100-PASS



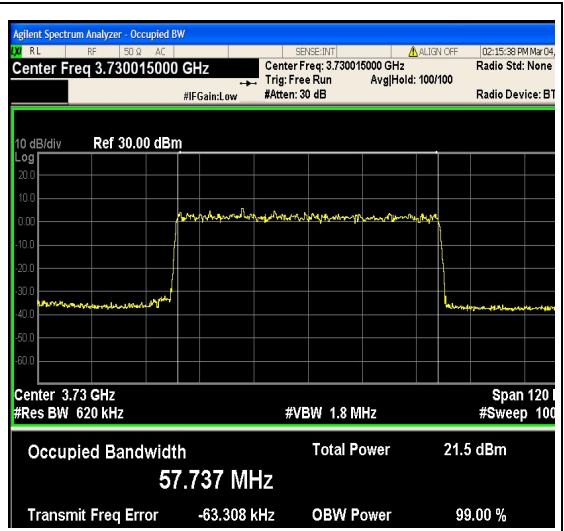
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 +H-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-27
 3@0-Ant1-97.275-100.6-≤100-PASS



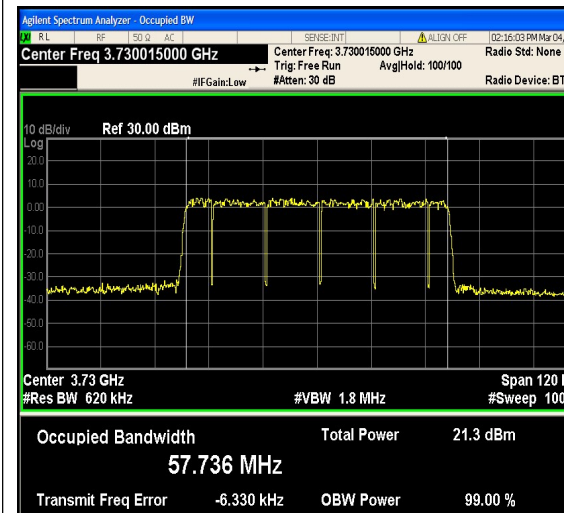
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +H-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-2
 73@0-Ant1-97.119-100.5-≤100-PASS



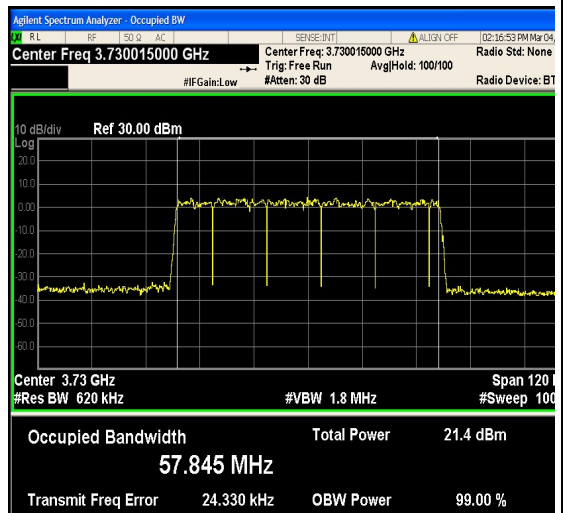
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
 @0-Ant1-57.908-59.95-≤60-PASS



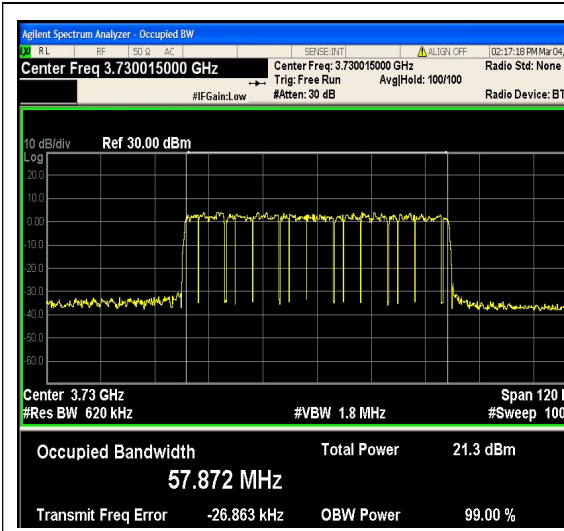
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-2-N/A-N/A-N/A-DFT-Pi2BPSK-Outer_Full-1
 62@0-Ant1-57.737-59.88-≤60-PASS



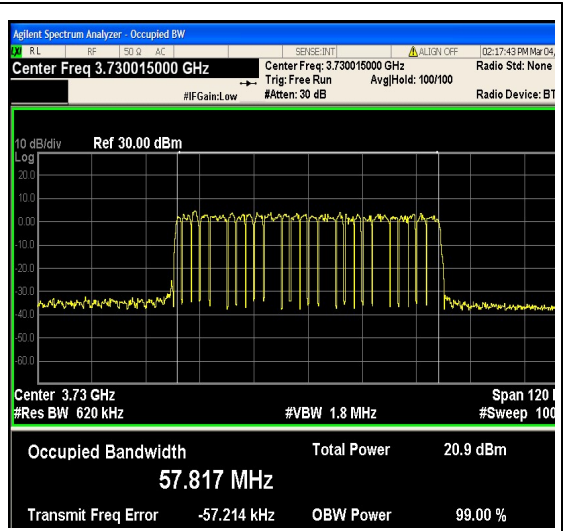
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
 2@0-Ant1-57.736-60.22-≤60-PASS



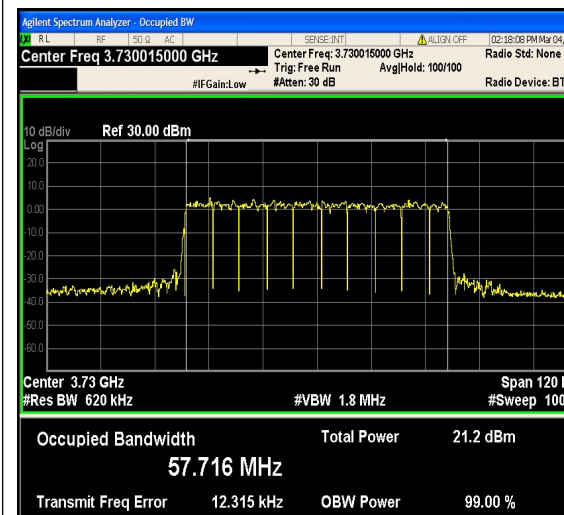
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
 2@0-Ant1-57.845-60.29-≤60-PASS



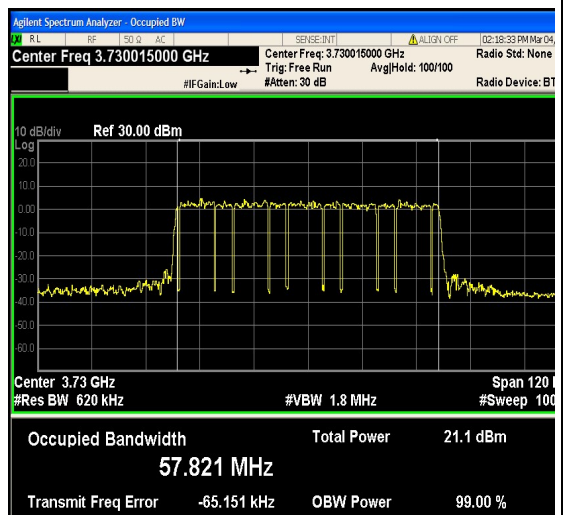
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
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@0-Ant1-57.872-59.80-≤60-PASS



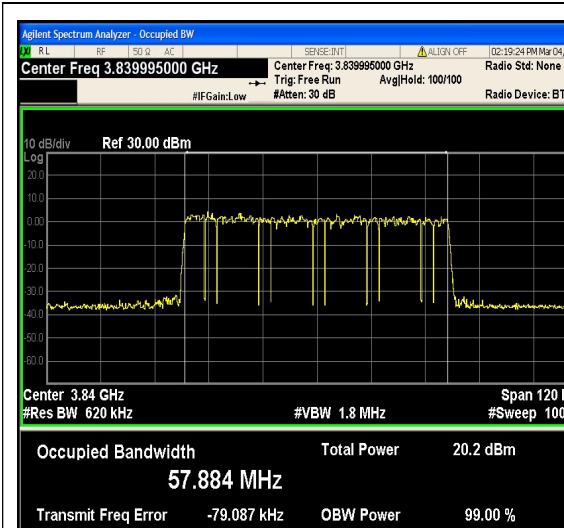
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
@0-Ant1-57.817-59.70-≤60-PASS



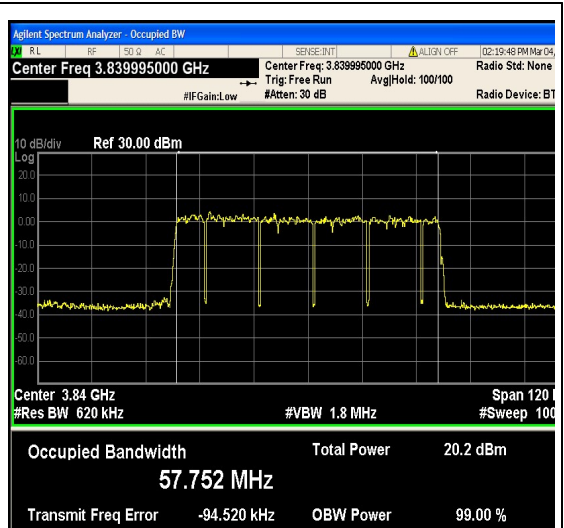
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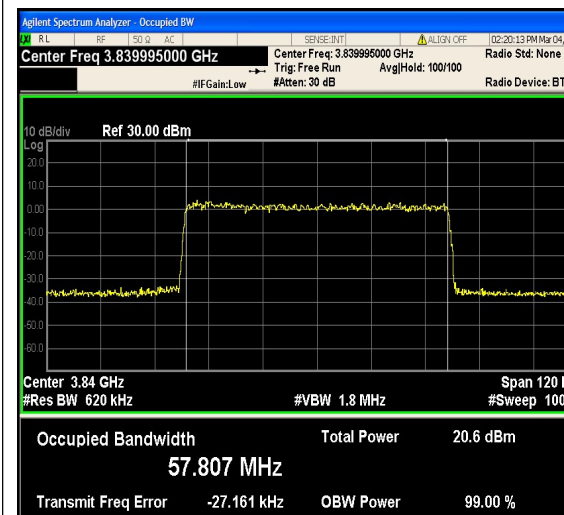
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L-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
2@0-Ant1-57.821-60.05-≤60-PASS



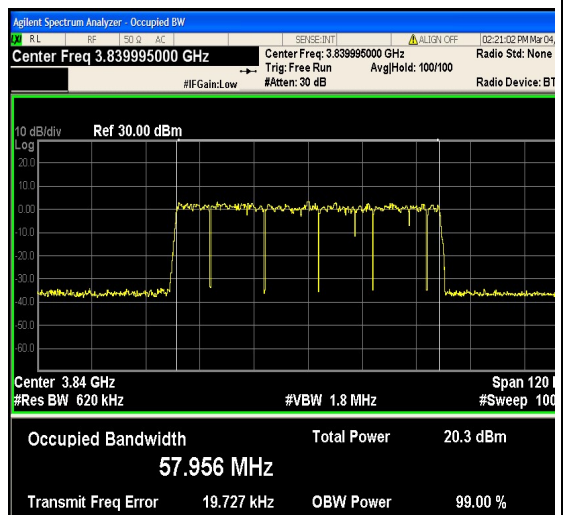
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
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@0-Ant1-57.884-60.00-≤60-PASS



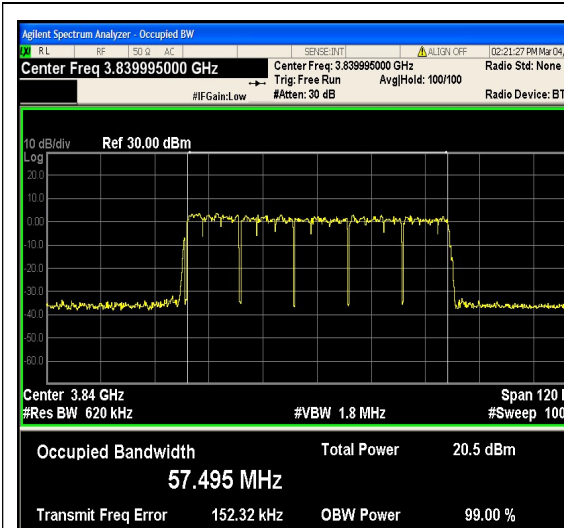
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
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162@0-Ant1-57.752-60.04-≤60-PASS



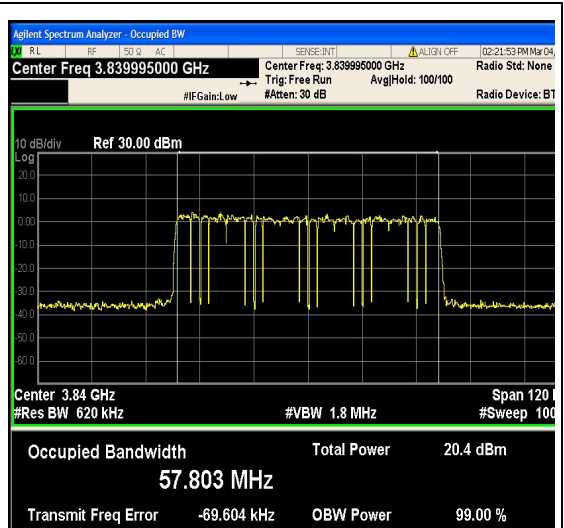
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
2@0-Ant1-57.807-60.21-≤60-PASS



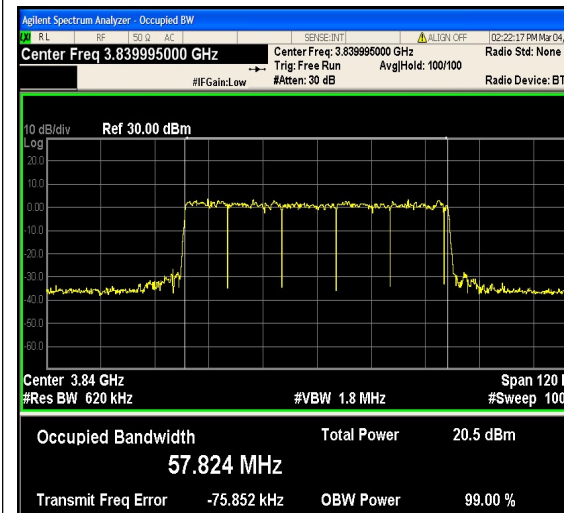
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
2@0-Ant1-57.956-60.23-≤60-PASS



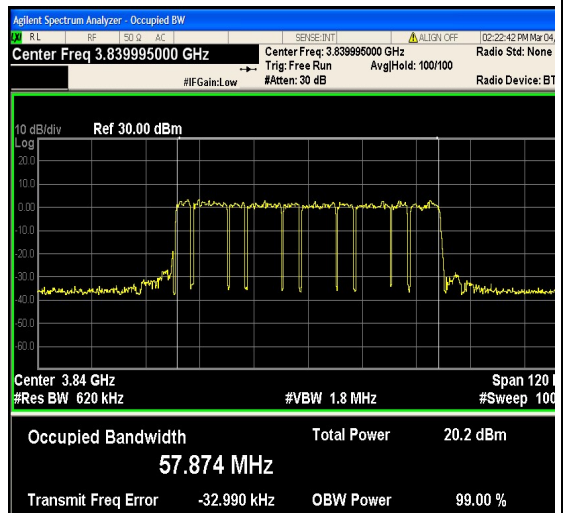
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 @0-Ant1-57.495-60.19-≤60-PASS



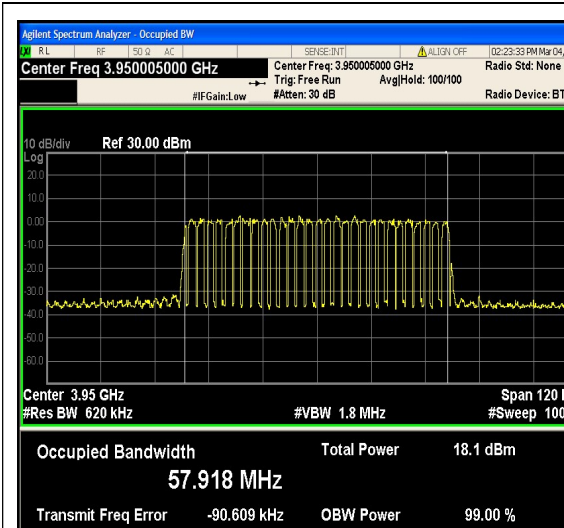
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 M-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
 @0-Ant1-57.803-59.79-≤60-PASS



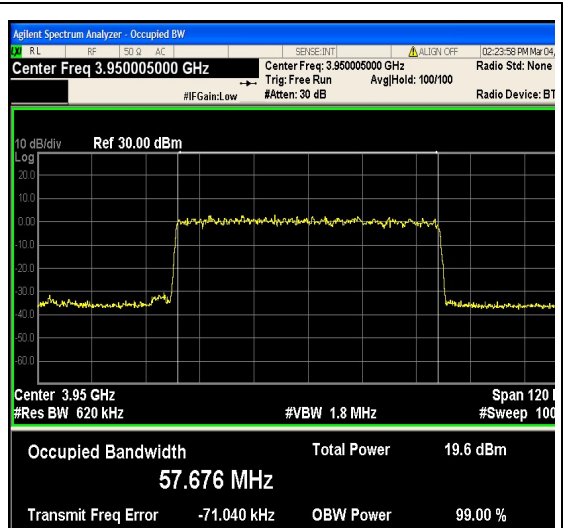
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 M-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
 @0-Ant1-57.824-60.01-≤60-PASS



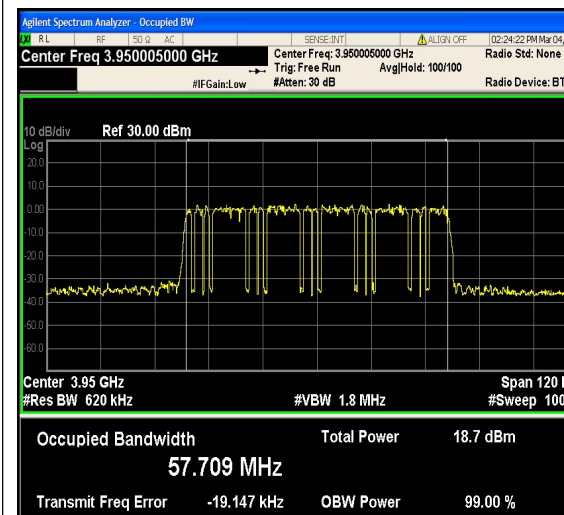
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 M-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
 2@0-Ant1-57.874-60.05-≤60-PASS



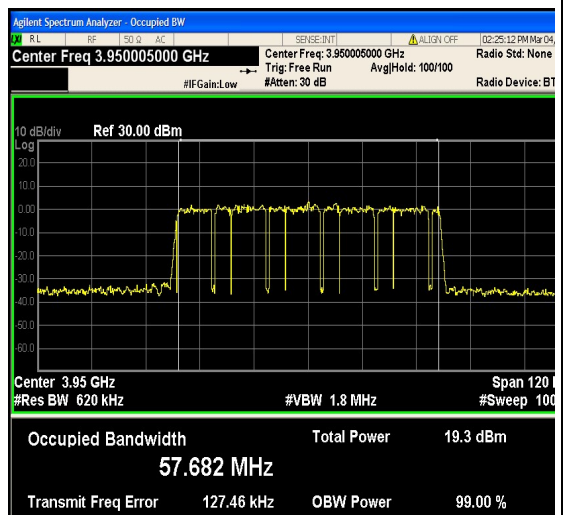
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
 @0-Ant1-57.918-60.10-≤60-PASS



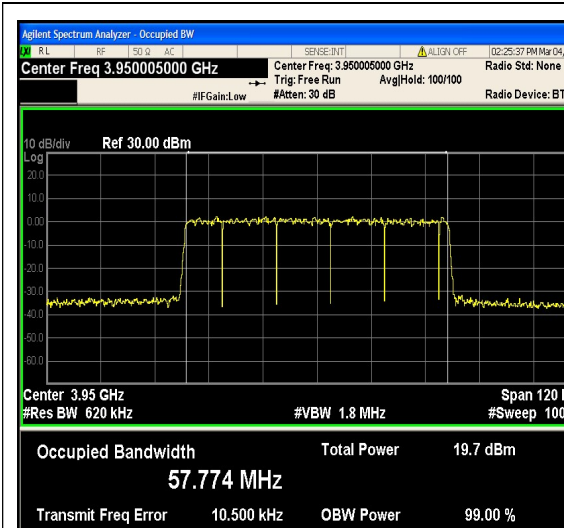
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 162@0-Ant1-57.676-59.97-≤60-PASS



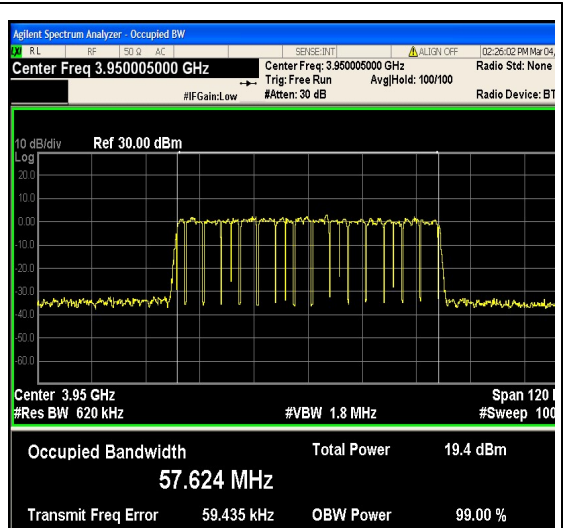
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
 2@0-Ant1-57.709-60.22-≤60-PASS



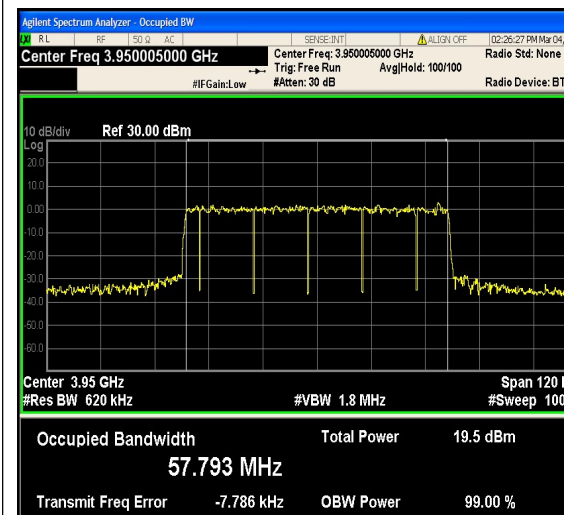
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
 2@0-Ant1-57.682-60.13-≤60-PASS



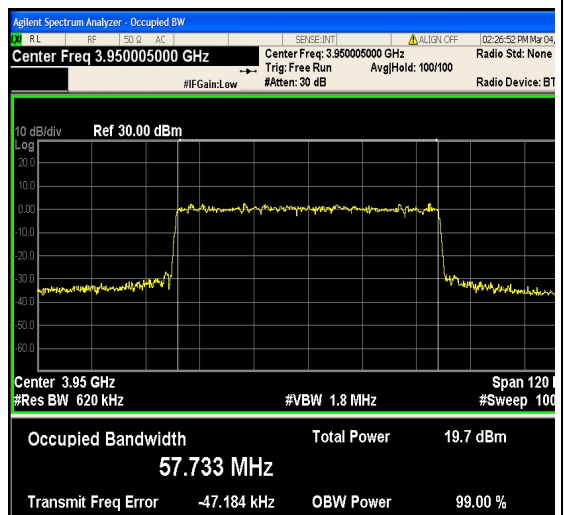
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-5-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
 @0-Ant1-57.774-59.99-<=60-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
 @0-Ant1-57.624-60.07-<=60-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
 @0-Ant1-57.793-59.79-<=60-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
 2@0-Ant1-57.733-60.12-<=60-PASS

Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS

DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	see graph	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	see graph	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS

DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	see graph	PASS

DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	see graph	PASS

DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	see graph	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	see graph	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Left	see graph	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Edge_1RB_Right	see graph	PASS

DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS

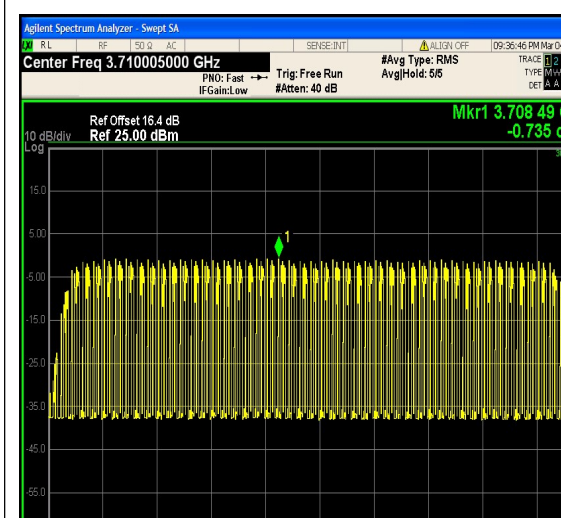
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Outer_Full	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Outer_Full	see graph	PASS

Test Graphs

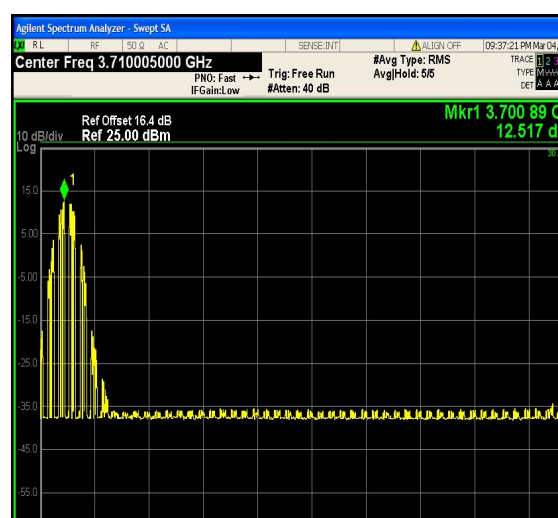


1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Left-1@0-3700.005~3720.005(3701.045MHz,
13.271MHz,Limit30,Margin[16.729])-PASS-se
e graph

1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Right-1@50-3700.005~3720.005(3719.145M
Hz,12.656MHz,Limit30,Margin[17.344])-PAS
S-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-5
0@0-3700.005~3720.005(3708.485MHz,-0.7
35MHz,Limit30,Margin[30.735])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Left
-1@0-3700.005~3720.005(3700.885MHz,12.
517MHz,Limit30,Margin[17.483])-PASS-see
graph